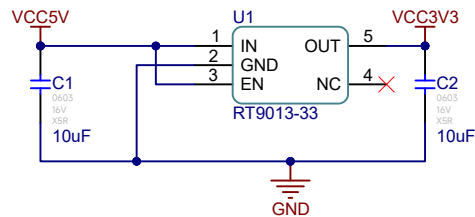
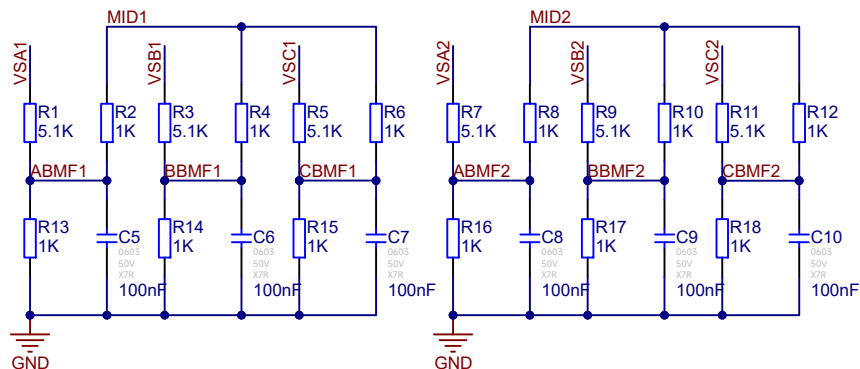


1			2			3			4																																						
A	版本号	修订日期	修订记录																																												
	V1.0.0.0	2025-03-05	初始版本。																																												
	V1.0.1.0	2025-03-14	添加了三个运放； 把两路过零检测集成在一块板子。																																												
	V1.0.2.0	2025-04-29	调整PCB布局； 改变PCB形状。																																												
B																																															
C																																															
D																																															
1																																					2			3			4				

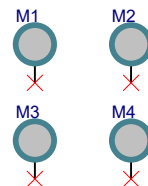
3.3V稳压



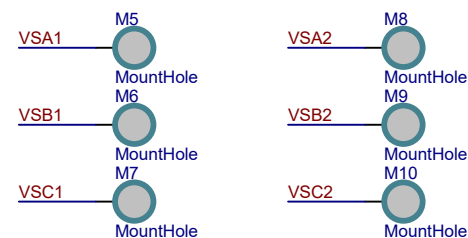
换相检测



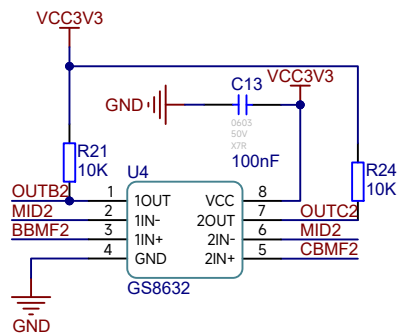
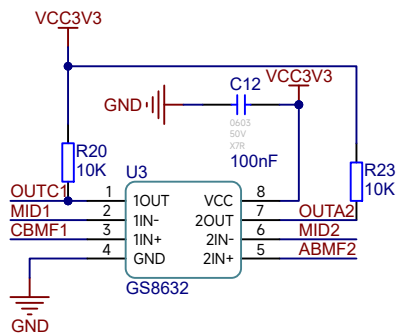
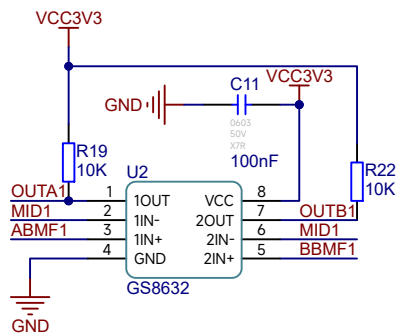
定位孔



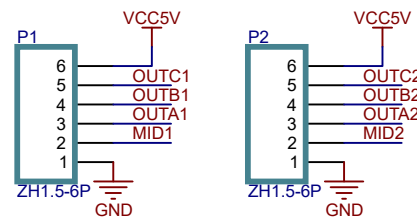
电机三相接口



过零检测



输出信号接口



Title: CYT2BL3过零检测转接板

Author: 贝果

Subtitle: Main

Revision: V1.0.2.0

Size: A4

Date: 2025/5/10

Time: 16:06:06

Sheet: 2

of: 2

