Instructions	Supported	File	Comments
LUI	yes	arithmetic_unit.bsv	
AUIPC	yes	arithmetic_unit.bsv	
JAL	yes	branch_unit.bsv	
JALR	yes	branch_unit.bsv	
BEQ	yes	branch_unit.bsv	
BNE	yes	branch_unit.bsv	
BLT	yes	branch_unit.bsv	
BGE	yes	branch_unit.bsv	
BLTU	yes	branch_unit.bsv	
BGEU	yes	branch_unit.bsv	
LB	yes	memory_unit.bsv	
LH	yes	memory_unit.bsv	
LW	yes	memory_unit.bsv	
LBU	yes	memory_unit.bsv	
LHU	yes	memory_unit.bsv	
SB	yes	memory_unit.bsv	
SH	yes	memory_unit.bsv	
SW	yes	memory_unit.bsv	
ADDI	yes	arithmetic_unit.bsv	
SLTI	yes	arithmetic_unit.bsv	
SLTIU	yes	arithmetic_unit.bsv	
XORI	yes	arithmetic_unit.bsv	
ORI	yes	arithmetic_unit.bsv	
ANDI	yes	arithmetic_unit.bsv	
SLLI	yes	arithmetic_unit.bsv	
SRLI	yes	arithmetic_unit.bsv	
SRAI	yes	arithmetic_unit.bsv	
ADD	yes	arithmetic_unit.bsv	
SUB	yes	arithmetic_unit.bsv	
SLL	yes	arithmetic_unit.bsv	
SLT	yes	arithmetic_unit.bsv	
SLTU	yes	arithmetic_unit.bsv	
XOR	yes	arithmetic_unit.bsv	
SRL	yes	arithmetic_unit.bsv	
SRA	yes	arithmetic_unit.bsv	
OR	yes	arithmetic_unit.bsv	
AND	yes	arithmetic_unit.bsv	
MUL	yes	arithmetic_unit.bsv	
MULH	yes	arithmetic_unit.bsv	
MULSHU	yes	arithmetic_unit.bsv	
MULHU	yes	arithmetic_unit.bsv	
DIV	no		Combinational circuit required
DIVU	no		Combinational circuit required
REM	no		Combinational circuit required
REMU	no		Combinational circuit required
Floating Point	no		
Atomic	no		
64bit	no		