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Lab 1: Introduction to digital circuits

Objectives

The purpose of this laboratory exercise is to learn how to use the git versioning system, the Linux console terminal to work in the lab, and to verify basic logic functions using an online simulator.

1 Preparation tasks (done before the lab at home)

1. Create an account on GitHub server.

2 GitHub

GitHub is a code hosting platform for collaboration and version control. GitHub lets you (and others) work together on projects.

1. In GitHub, create a new public repository titled **Digital-electronics-1**. Initialize a README and MIT license.
2. Use How to make the perfect Readme.md on GitHub, Basic writing and formatting syntax or Mastering Markdown articles and add the following parts in your README file.

• Headers

• Emphasis

• List

• Link

• Table

3 Linux terminal

A Linux console terminal is one of the system consoles provided in the Linux kernel. The Linux console terminal acts as the medium for input and output operations for a Linux system. A Linux console terminal is similar to command line in Microsoft Windows but it differs in that it can perform any operation on the system. (Cited from Technopedia.)

1. Try basic commands in the Linux terminal:

Command	Description
Ctrl+Alt+T	Open Linux terminal in Ubuntu based distributions
Tab	Automatic completion what you are typing or suggest options to you
Up/Down	Browse command history
ls	List directory contents
cd	Change the current directory (change to parent directory: <code>cd ..</code>)
mkdir	Make directories
pwd	Print name of current/working directory
clear	Clear the terminal screen
touch	Create an empty file (if it does not exist)
cp	Copy files and directories
mc	GNU Midnight Commander (file manager)

2. In the lab, make your own home folder within **Documents**, and with help of **git** command create a local copy of your public repository:

```

$ cd
$ pwd
/home/lab661

$ cd Documents/
$ mkdir your-name
$ cd your-name/
$ pwd
/home/lab661/Documents/your-name

$ git clone https://github.com/your-github-account/Digital-electronics-1
$ cd Digital-electronics-1/
$ ls
LICENSE  README.md

```

- Download Docs folder from teacher's repository and copy it to your Digital-electronics-1 local repository.

```

$ ls
Docs  LICENSE  README.md

```

- Make a new folder Images.

```

$ mkdir Images
$ ls
Docs  Images  LICENSE  README.md

```

- Make a new folder Labs/01-gates and create an empty file README.md.

```

$ mkdir Labs
$ cd Labs/
$ mkdir 01-gates
$ cd 01-gates/
$ touch README.md
$ ls
README.md

```

4 Online simulator

- Use online digital circuit simulator CircuitVerse, launch the simulator, draw and verify basic two-input logic gates.

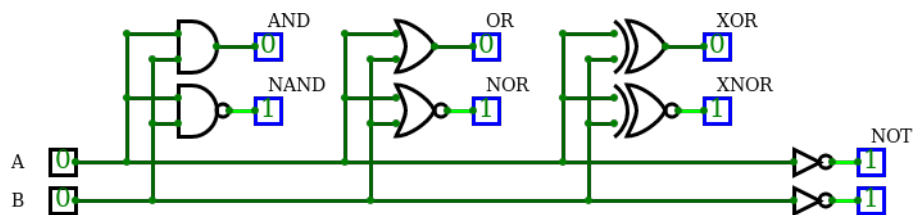


Figure 1: and_gates

- Run any text editor, such as Visual Studio Code, open Digital-electronics-1/Labs/01-gates/README.md **local** file (not on GitHub), create/complete tables with logical values, and add a screenshot from the simulator.

A	NOT
0	
1	

A	B	AND	NAND	OR	NOR	XOR	XNOR
0	0	0	1	0	1	0	1
0	1						
1	0						

A	B	AND	NAND	OR	NOR	XOR	XNOR
1	1						

3. Use De Morgan's law and modify logic function to form with (N)AND and (N)OR gates only. Verify all three functions in online simulator.

$$f(c, b, a) = (\bar{b}a) + (\bar{c}b)$$

Figure 2: equation

![equation](https://latex.codecogs.com/gif.latex?f%28c%2Cb%2Ca%29_%7BAND%7D%20%3D)

![equation](https://latex.codecogs.com/gif.latex?f%28c%2Cb%2Ca%29_%7BOR%7D%20%3D)

A	B	C	f(A,B,C)
0	0	0	0
0	0	1	0
0	1	0	0
0	1	1	1
1	0	0	0
1	0	1	0
1	1	0	1
1	1	1	1

> Equations and symbols were generated by [Online LaTeX Equation Editor](https://www.codecogs.com/latex/eqneditor.php)
>

5 Synchronize git

1. Use `cd ..` command in Linux terminal and change the working directory to `Digital-electronics-1`. Then use git commands to add, commit, and push all local changes to your remote repository. Check the repository at GitHub web page for changes.

```
$ pwd
/home/lab661/Documents/your-name/Digital-electronics-1/Labs/01-gates

$ cd ..
$ cd ..
$ pwd
/home/lab661/Documents/your-name/Digital-electronics-1

$ git status
$ git add <your-modified-files>
$ git status
$ git commit -m "[LAB] Adding 01-gates lab"
$ git status
$ git push
$ git status
```

Experiments on your own

1. Use digital circuit simulator and verify basic Boolean postulates:

$$x \cdot \overline{x} = 0$$

Figure 3: equation

$$x + \overline{x} = 1$$

Figure 4: equation

and Distributive laws:

2. Use online digital circuit simulator, draw the logic diagram according to figure, complete the truth table, and determine the circuit function.

A	B	Q3	Q2	Q1	Q0
0	0				
0	1				
1	0				
1	1				

3. Try different online simulators, such as Logicley, CircuitLab, simulatorIO, LogicEmu, or find others, and compare them.

$$x + x + x = x$$

Figure 5: equation

$$x \cdot x \cdot x = x$$

Figure 6: equation

$$x \cdot y + x \cdot z = x(y + z)$$

Figure 7: equation

$$(x + y) \cdot (x + z) = x + (y \cdot z)$$

Figure 8: equation

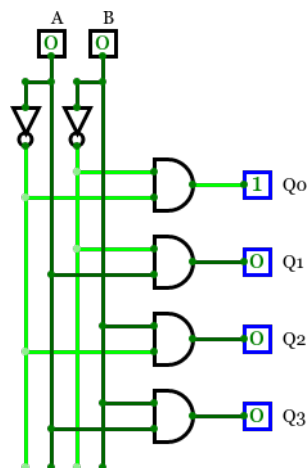


Figure 9: logic