

| | | | | COURSE COMPLETION CERTIFICATE | | | |

The certificate is awarded to

saikumar Gunnam

for successfully completing the course

VLSI Digital Design - Chip Design and Verilog Programming

on April 16, 2025



Congratulations! You make us proud!

Issued on: Sunday, April 20, 2025
To verify, scan the QR code at https://verify.onwingspan.com

Sende

Thirumala Arohi
Executive Vice President and Global Head
Education, Training & Assessment (ETA)
Infosys Limited