DECODER
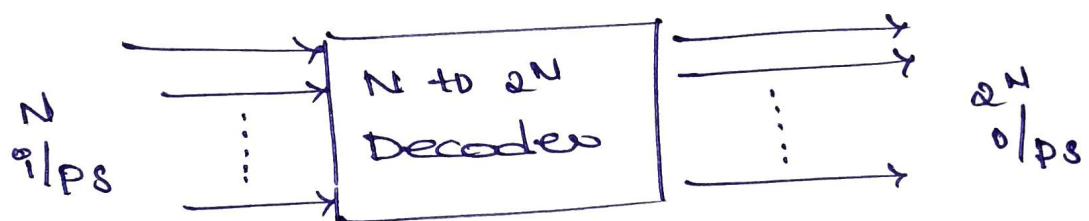
A combinational logic circuit that converts an N-bit binary input code into M o/p channels in such a way that only one o/p channel is activated for each one of possible combinations of i/ps is known as a decoder.

A combinational logic circuit which converts N i/p lines into a maximum of 2^N o/p lines is called as decoder.

A decoder is a combination logic circuit that is capable of identifying or detecting a particular code. the operation that a decoder performs is referred to as decoding

circuit Diagram of 2 to 4 Decoder :-