




# Report Generated by Test Manager

**Title:** Automation Simulink Test  
**Author:** Sung Hyun Park  
**Date:** 08-Dec-2023 14:31:55

## Test Environment

Platform: PCWIN64  
MATLAB: (R2023b)


## Summary

Name	Outcome	Duration (Seconds)
<a href="#">Results: 2023-Dec-08 14:31:38</a>	✓	13.774
 <a href="#">Simulink_TestManager</a>	✓	13.459
 <a href="#">New Test Suite 1</a>	✓	13.458
 <a href="#">New Test Case 1</a>	✓	13.459

## Results: 2023-Dec-08 14:31:38

Result Type: Result Set  
Parent: None  
Start Time: 2023-12-08 14:31:40  
End Time: 2023-12-08 14:31:53  
Outcome: Total: 1, **Passed: 1**

### Aggregated Coverage Results

Analyzed Model	Sim Mode	Complexity	Decision	Execution
 <a href="#">Example</a>	Normal	7	70%	100%

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## Simulink\_TestManager

### Test Result Information

Result Type: Test File Result  
Parent: [Results: 2023-Dec-08 14:31:38](#)  
Start Time: 2023-12-08 14:31:40  
End Time: 2023-12-08 14:31:53  
Outcome: Total: 1, **Passed: 1**

### Test Suite Information

Name: Simulink\_TestManager

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## New Test Suite 1

### Test Result Information

Result Type: Test Suite Result  
Parent: [Simulink\\_TestManager](#)  
Start Time: 2023-12-08 14:31:40  
End Time: 2023-12-08 14:31:53  
Outcome: Total: 1, **Passed: 1**

## Test Suite Information

Name: New Test Suite 1

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## New Test Case 1

### Test Result Information

Result Type: Test Case Result

Parent: [New Test Suite 1](#)

Start Time: 2023-12-08 14:31:40

End Time: 2023-12-08 14:31:53

Outcome: Passed

### Test Case Information


Name: New Test Case 1

Type: Baseline Test

Baseline Name: Test\_Baseline.mat

Baseline File: C:\Users\Administrator\MATLAB\Projects\Simulink\_Test\Test\_Baseline.mat

### Baseline Comparison

Name	Abs Tol	Rel Tol	Lead Tol	Lag Tol	Max Diff	Data Type 1	Units 1	Sample Time 1	Data Type 2	Units 2	Sample Time 2	Interp	Sync
 tgtGear	0	0	0	0	0	uint8			uint8			zoh	union

## Test\_Baseline.mat

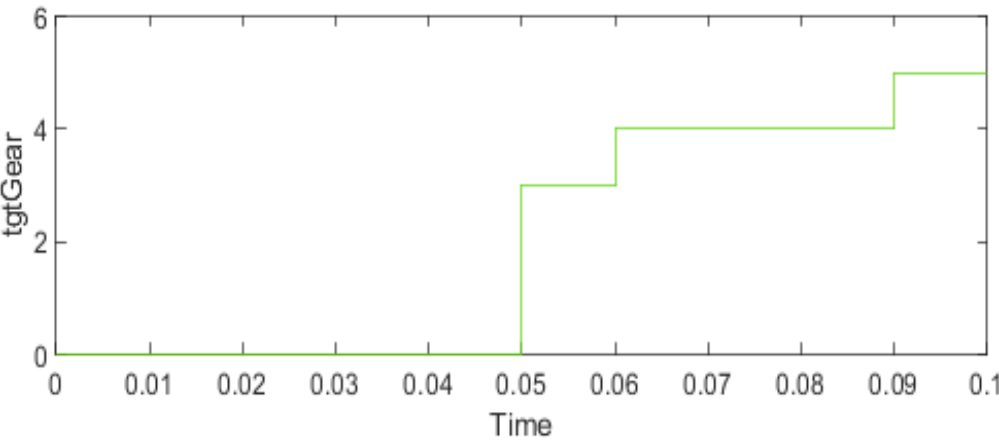
### Baseline Information

Baseline Name: Test\_Baseline.mat

Baseline File: C:\Users\Administrator\MATLAB\Projects\Simulink\_Test\Test\_Baseline.mat

Name	Data Type	Units	Sample Time	Interp	Sync	Link to Plot
tgtGear	uint8			zoh	union	<a href="#">Link</a>

Name	Data Type	Units	Sample Time	Interp	Sync
tgtGear	uint8			zoh	union



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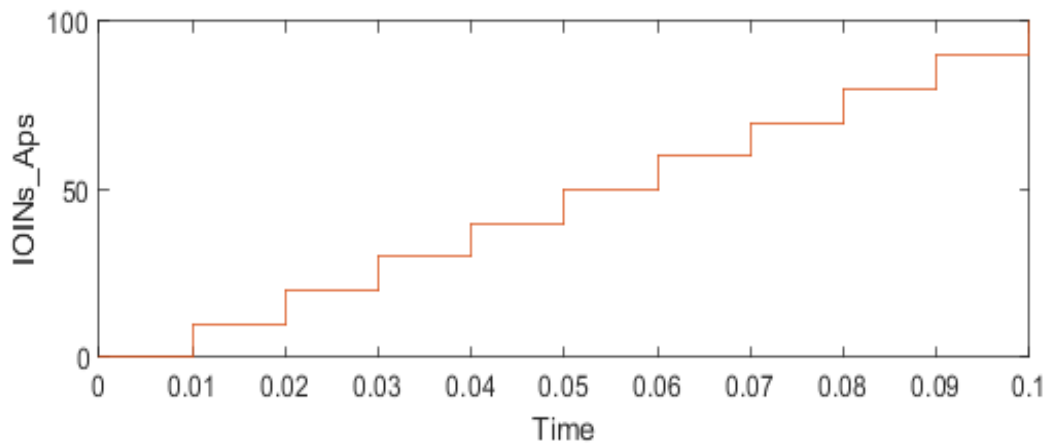
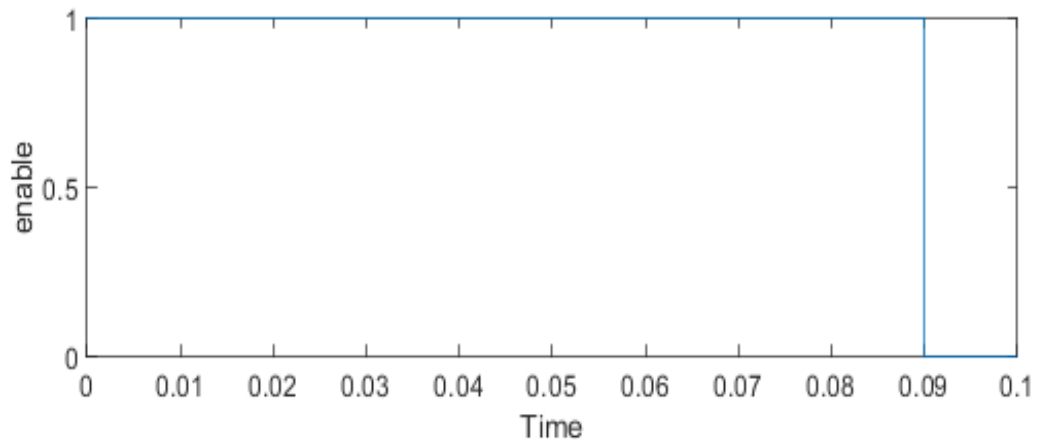
Input Data

Input Information

External Input      Test\_Input2.mat  
Name:  
External Input File: C:\Users\Administrator\MATLAB\Projects\Simulink\_Test\Test\_Input2.mat

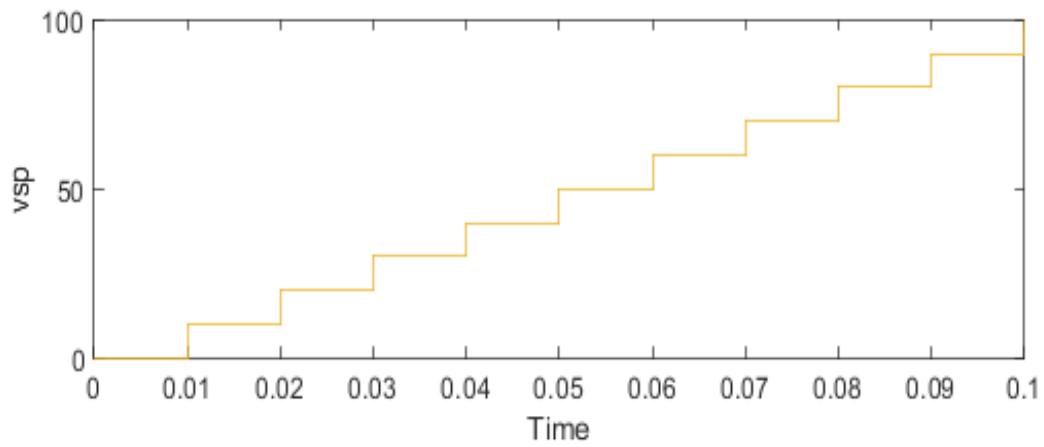
Name	Data Type	Units	Sample Time	Interp	Sync	Link to Plot
enable	boolean			zoh	union	<a href="#">Link</a>
IOINs_Aps	uint8			zoh	union	<a href="#">Link</a>
vsp	uint8			zoh	union	<a href="#">Link</a>

Name	Data Type	Units	Sample Time	Interp	Sync
enable	boolean			zoh	union
IOINs_Aps	uint8			zoh	union



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Name	Data Type	Units	Sample Time	Interp	Sync
vsp	uint8			zoh	union



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## Simulation

### System Under Test Information

Model:	Example
Release:	Current
Simulation Mode:	normal
Override SIL or PIL Mode:	0
Configuration Set:	Configuration



External Input Name: Test\_Input2.mat  
 External Input File: C:\Users\Administrator\MATLAB\Projects\Simulink\_Test\Test\_Input2.mat  
 Start Time: 0  
 Stop Time: 0.10000000000000001  
 Checksum: 2929015004 1177147957 2237092558 1165275227  
 Simulink Version: 23.2  
 Model Version: 7.5  
 Model Author: 22011083  
 Date: Fri Dec 08 10:52:55 2023  
 User ID: 22011083  
 Model Path: C:\Users\Administrator\MATLAB\Projects\Simulink\_Test\Example.slx  
 Machine Name: GUMD00067  
 Solver Name: FixedStepDiscrete  
 Solver Type: Fixed-Step  
 Fixed Step Size: 0.01  
 Simulation Start Time: 2023-12-08 14:31:43  
 Simulation Stop Time: 2023-12-08 14:31:50  
 Platform: PCWIN64

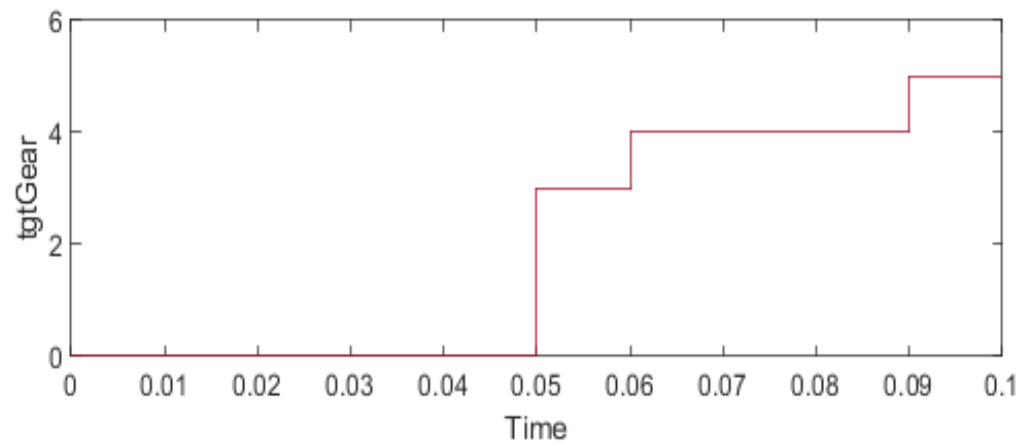
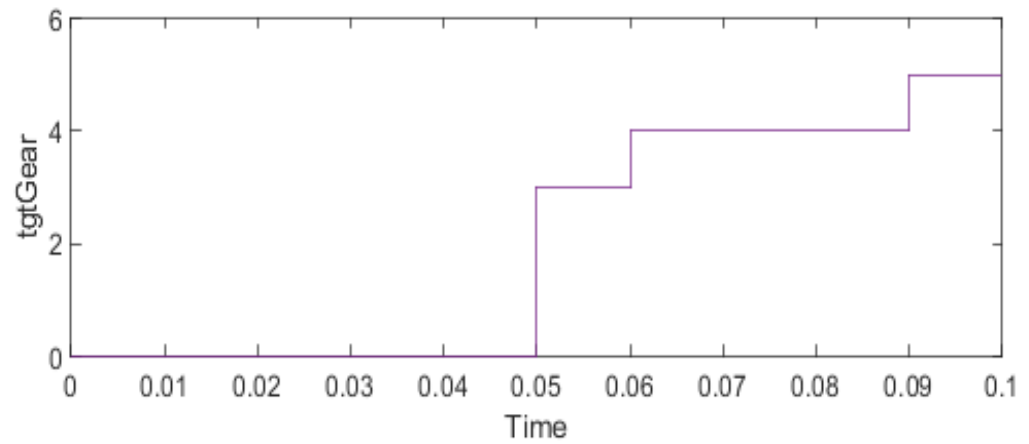
### Parameter Overrides

Workspace Variable	Value	Source	Model Element
<a href="#">C:\Users\Administrator\MATLAB\Projects\Simulink_Test\Test_Param.m</a>			
Aps_val1	60	base workspace	Example/TCU/Chart
Aps_val2	40	base workspace	Example/TCU/Chart
Aps_val3	20	base workspace	Example/TCU/Chart
Default_Value	5	base workspace	Example/TCU/Constant
Gear_Val1	4	base workspace	Example/TCU/Chart
Gear_Val2	3	base workspace	Example/TCU/Chart
Gear_Val3	2	base workspace	Example/TCU/Chart
Gear_Val4	1	base workspace	Example/TCU/Chart
vsp_val	50	base workspace	Example/TCU/Chart

### Simulation Output

Name	Data Type	Units	Sample Time	Interp	Sync	Link to Plot
tgtGear	uint8	1	0.01	zoh	union	<a href="#">Link</a>
tgtGear	uint8			zoh	union	<a href="#">Link</a>

Name	Data Type	Units	Sample Time	Interp	Sync
tgtGear	uint8	1	0.01	zoh	union
tgtGear	uint8			zoh	union



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Simulation Logs:

Simulation stopped at '0.10000000000000001' because there is no input data after this time point.

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