

Report Generated by Test Manager

Title: Automation Simulink Test
Author: Sung Hyun Park
Date: 08-Dec-2023 14:30:04

Test Environment

Platform: PCWIN64
MATLAB: (R2023b)


Summary

Name	Outcome	Duration (Seconds)
Results: 2023-Dec-08 14:29:48	✓	13.905
 Simulink_TestManager	✓	13.583
 New Test Suite 1	✓	13.583
 New Test Case 1	✓	13.583

Results: 2023-Dec-08 14:29:48

Result Type: Result Set
Parent: None
Start Time: 2023-12-08 14:29:49
End Time: 2023-12-08 14:30:03
Outcome: Total: 1, **Passed: 1**

Aggregated Coverage Results

Analyzed Model	Sim Mode	Complexity	Decision	Execution
 Example	Normal	7	70%	100%

[Back to Report Summary](#)

Simulink_TestManager

Test Result Information

Result Type: Test File Result
Parent: [Results: 2023-Dec-08 14:29:48](#)
Start Time: 2023-12-08 14:29:49
End Time: 2023-12-08 14:30:03
Outcome: Total: 1, **Passed: 1**

Test Suite Information

Name: Simulink_TestManager

[Back to Report Summary](#)

New Test Suite 1

Test Result Information

Result Type: Test Suite Result
Parent: [Simulink_TestManager](#)
Start Time: 2023-12-08 14:29:49
End Time: 2023-12-08 14:30:03
Outcome: Total: 1, **Passed: 1**

Test Suite Information

Name: New Test Suite 1

[Back to Report Summary](#)

New Test Case 1

Test Result Information

Result Type: Test Case Result

Parent: [New Test Suite 1](#)

Start Time: 2023-12-08 14:29:49

End Time: 2023-12-08 14:30:03

Outcome: Passed

Test Case Information


Name: New Test Case 1

Type: Baseline Test

Baseline Name: Test_Baseline.mat

Baseline File: C:\Users\Administrator\MATLAB\Projects\Simulink_Test\Test_Baseline.mat

Baseline Comparison

Name	Abs Tol	Rel Tol	Lead Tol	Lag Tol	Max Diff	Data Type 1	Units 1	Sample Time 1	Data Type 2	Units 2	Sample Time 2	Interp	Sync
 tgtGear	0	0	0	0	0	uint8			uint8			zoh	union

Test_Baseline.mat

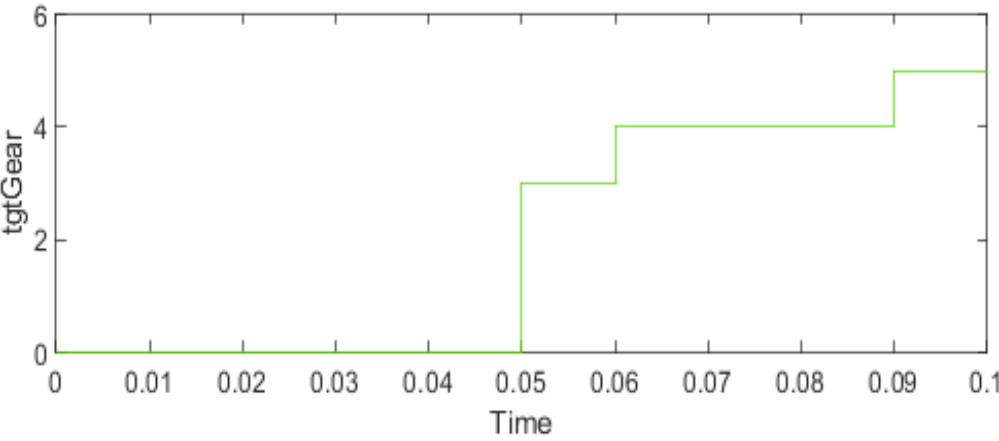
Baseline Information

Baseline Name: Test_Baseline.mat

Baseline File: C:\Users\Administrator\MATLAB\Projects\Simulink_Test\Test_Baseline.mat

Name	Data Type	Units	Sample Time	Interp	Sync	Link to Plot
tgtGear	uint8			zoh	union	Link

Name	Data Type	Units	Sample Time	Interp	Sync
tgtGear	uint8			zoh	union



[Back to Report Summary](#)[Back to Signal Summary](#)

Input Data

Input Information

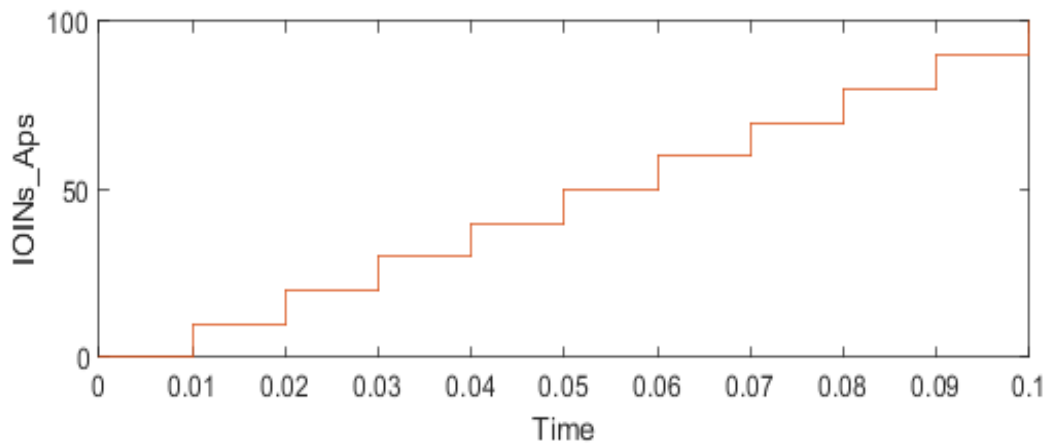
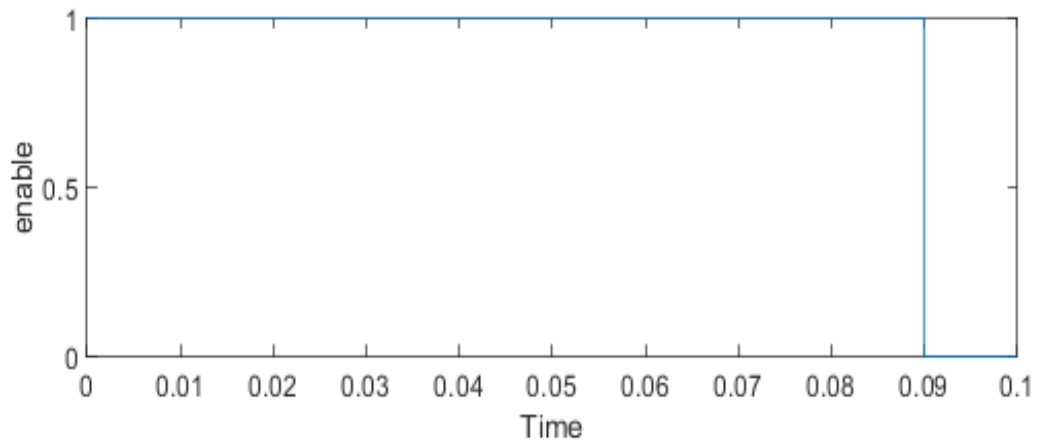
External Input Test_Input2.mat

Name:

External Input File: C:\Users\Administrator\MATLAB\Projects\Simulink_Test\Test_Input2.mat

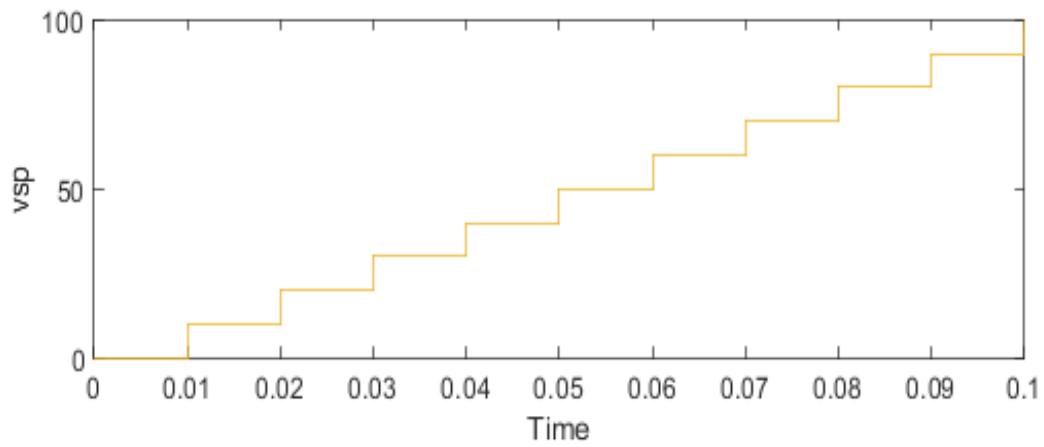
Name	Data Type	Units	Sample Time	Interp	Sync	Link to Plot
enable	boolean			zoh	union	Link
IOINs_Aps	uint8			zoh	union	Link
vsp	uint8			zoh	union	Link

Name	Data Type	Units	Sample Time	Interp	Sync
enable	boolean			zoh	union
IOINs_Aps	uint8			zoh	union



[Back to Report Summary](#)[Back to Signal Summary](#)

Name	Data Type	Units	Sample Time	Interp	Sync
vsp	uint8			zoh	union



[Back to Report Summary](#)[Back to Signal Summary](#)

Simulation

System Under Test Information

Model:	Example
Release:	Current
Simulation Mode:	normal
Override SIL or PIL Mode:	0
Configuration Set:	Configuration

External Input Name: Test_Input2.mat
 External Input File: C:\Users\Administrator\MATLAB\Projects\Simulink_Test\Test_Input2.mat
 Start Time: 0
 Stop Time: 0.10000000000000001
 Checksum: 2929015004 1177147957 2237092558 1165275227
 Simulink Version: 23.2
 Model Version: 7.5
 Model Author: 22011083
 Date: Fri Dec 08 10:52:55 2023
 User ID: 22011083
 Model Path: C:\Users\Administrator\MATLAB\Projects\Simulink_Test\Example.slx
 Machine Name: GUMD00067
 Solver Name: FixedStepDiscrete
 Solver Type: Fixed-Step
 Fixed Step Size: 0.01
 Simulation Start Time: 2023-12-08 14:29:53
 Simulation Stop Time: 2023-12-08 14:30:00
 Platform: PCWIN64

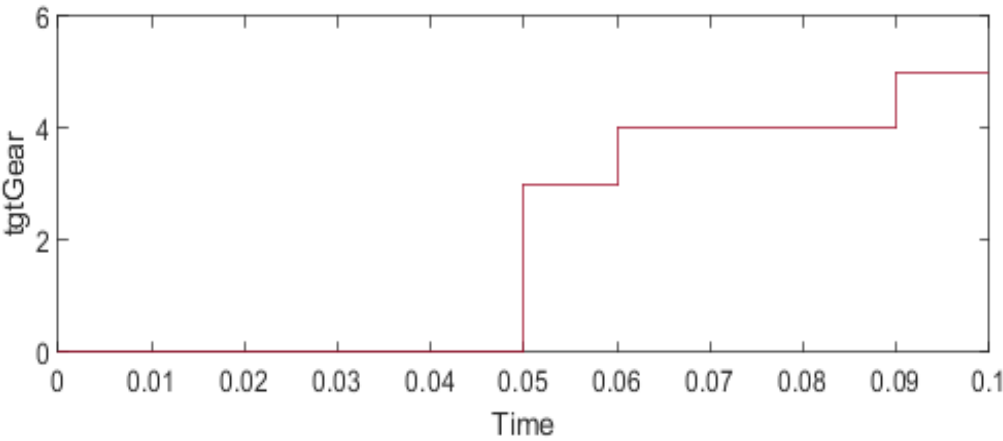
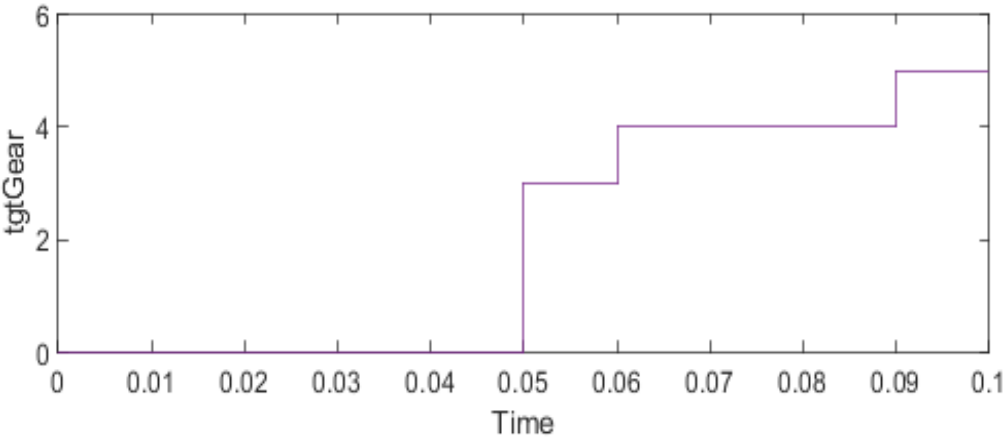
Parameter Overrides

Workspace Variable	Value	Source	Model Element
C:\Users\Administrator\MATLAB\Projects\Simulink_Test\Test_Param.m			
Aps_val1	60	base workspace	Example/TCU/Chart
Aps_val2	40	base workspace	Example/TCU/Chart
Aps_val3	20	base workspace	Example/TCU/Chart
Default_Value	5	base workspace	Example/TCU/Constant
Gear_Val1	4	base workspace	Example/TCU/Chart
Gear_Val2	3	base workspace	Example/TCU/Chart
Gear_Val3	2	base workspace	Example/TCU/Chart
Gear_Val4	1	base workspace	Example/TCU/Chart
vsp_val	50	base workspace	Example/TCU/Chart

Simulation Output

Name	Data Type	Units	Sample Time	Interp	Sync	Link to Plot
tgtGear	uint8	1	0.01	zoh	union	Link
tgtGear	uint8			zoh	union	Link

Name	Data Type	Units	Sample Time	Interp	Sync
tgtGear	uint8	1	0.01	zoh	union
tgtGear	uint8			zoh	union



[Back to Report Summary](#)[Back to Signal Summary](#)

Simulation Logs:

Simulation stopped at '0.10000000000000001' because there is no input data after this time point.

[Back to Report Summary](#)