Report Generated by Test Manager

Title: Automation Simulink Test

Author: Sung Hyun Park

Date: 08-Dec-2023 13:32:43

Test Environment

Platform: PCWIN64 MATLAB: (R2023b)

Summary

Name	Outcome	Duration (Seconds)
Results: 2023-Dec-08 13:32:26	Ø	14.415
Simulink_TestManager	Ø	14.075
New Test Suite 1	Ø	14.075
New Test Case 1	⊘	14.075

Results: 2023-Dec-08 13:32:26

Result Type: Result Set Parent: None

Start Time: 2023-12-08 13:32:27 End Time: 2023-12-08 13:32:42 Outcome: Total: 1, Passed: 1

Aggregated Coverage Results

Analyzed Model	Sim Mode	Complexity	Decision	Execution
Example	Normal	7	70%	100%

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Simulink_TestManager

Test Result Information

Result Type: Test File Result

Parent: Results: 2023-Dec-08 13:32:26

Start Time: 2023-12-08 13:32:27 End Time: 2023-12-08 13:32:41 Outcome: Total: 1, Passed: 1

Test Suite Information

Name: Simulink_TestManager

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New Test Suite 1

Test Result Information

Result Type: Test Suite Result

Parent: Simulink TestManager
Start Time: 2023-12-08 13:32:27
End Time: 2023-12-08 13:32:41
Outcome: Total: 1, Passed: 1

Test Suite Information

Name: New Test Suite 1

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New Test Case 1

Test Result Information

Result Type: Test Case Result
Parent: New Test Suite 1
Start Time: 2023-12-08 13:32:27
End Time: 2023-12-08 13:32:41

Outcome: Passed

Test Case Information

Name: New Test Case 1
Type: Baseline Test
Baseline Name: Test_Baseline.mat

Baseline File: C:\Users\Administrator\MATLAB\Projects\Simulin

k_Test\Test_Baseline.mat

Baseline Comparison

Name	Abs T	Rel To l	Lead T ol	Lag T ol	Max Di ff	Data Type 1	Sample Ti me 1	Data Type 2	Sample Ti me 2	Interp Sync
gtGear	. 0	. 0	0	0	0	uint8		uint8		zoh union

Test Baseline.mat

Baseline Information

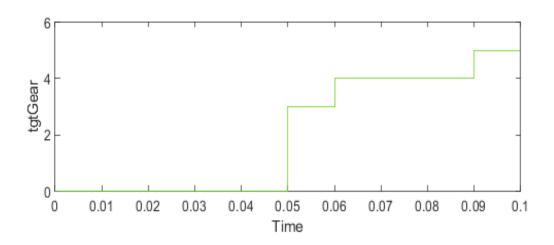
Baseline Name: Test_Baseline.mat

Baseline File: C:\Users\Administrator\MATLAB\Projects\Simulin

k_Test\Test_Baseline.mat

Name	Data Type	Units	Sample Time	Interp	Sync	Link to Plo t	
tgtGear	uint8			zoh	union	<u>Link</u>	

Name	Data Type	Units	Sample Time	Interp	Sync
tgtGear	uint8			zoh	union



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Input Data

Input Information

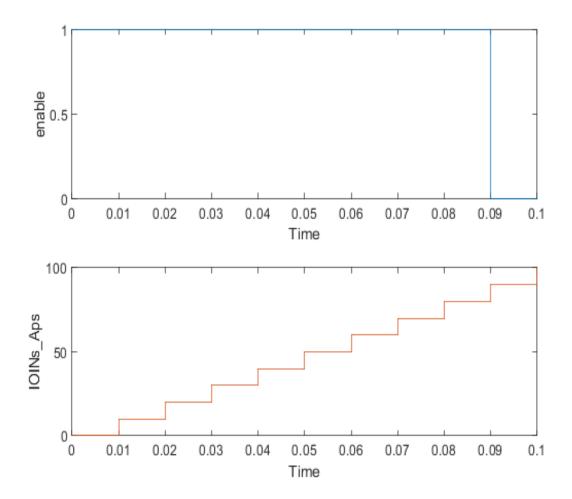
External Input Test_Input2.mat

Name:

External Input File: C:\Users\Administrator\MATLAB\Projects\Simulin k_Test\Test_Input2.mat

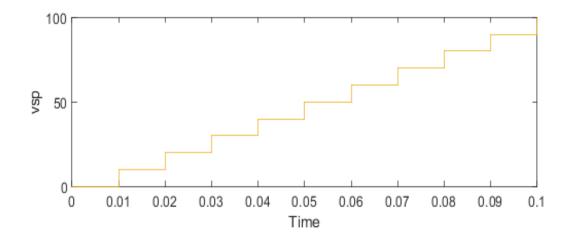
Name	Data Type	Units	Sample Time	Interp	Sync	Link to Plo t
enable	boolean		 	zoh	union	<u>Link</u>
IOINs_Aps	uint8		 	zoh	union	<u>Link</u>
vsp	uint8			zoh	union	<u>Link</u>

Name	Data Type	Units	Sample Time	Interp	Sync
enable	boolean			zoh	union
IOINs_Aps	uint8			zoh	union



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Name	Data Type	Units	Sample Time	Interp	Sync
vsp	uint8			zoh	union



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Simulation

System Under Test Information

Model: Example Release: Current Simulation Mode: normal

Override SIL or PIL Mode: 0

Configuration Set: Configuration

External Input Name: Test_Input2.mat

External Input File: C:\Users\Administrator\MATLAB\Projects\Simulin

k_Test\Test_Input2.mat

Start Time: 0

Stop Time: 0.1000000000000001

Checksum: 2929015004 1177147957 2237092558 1165275227

Simulink Version: 23.2 Model Version: 7.5

Model Author: 22011083

Date: Fri Dec 08 10:52:55 2023

User ID: 22011083

Model Path: C:\Users\Administrator\MATLAB\Projects\Simulin

k_Test\Example.slx

Machine Name: GUMD00067

Solver Name: FixedStepDiscrete

Solver Type: Fixed-Step

Fixed Step Size: 0.01

Simulation Start Time: 2023-12-08 13:32:32 Simulation Stop Time: 2023-12-08 13:32:39

Platform: PCWIN64

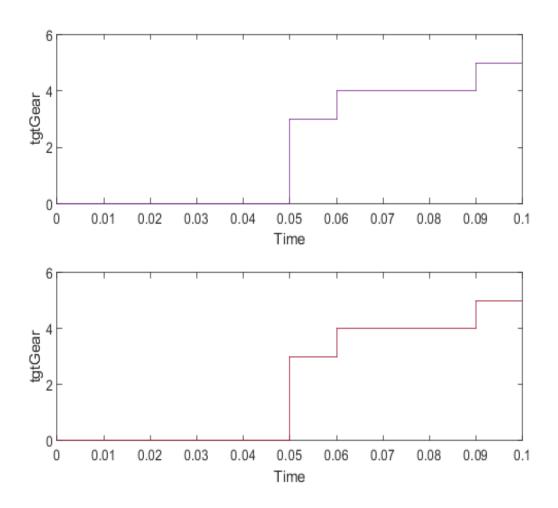
Parameter Overrides

i di diliotor o voltido									
Workspace Variab	Value	Source	Model Element						
le									
C:\Users\Administrator\MATLAB\Projects\Simulink_Test\Test_Param.m									
Aps_val1	60	base workspace	Example/TCU/Chart						
Aps_val2	40	base workspace	Example/TCU/Chart						
Aps_val3	20	base workspace	Example/TCU/Chart						
Default_Value	5	base workspace	Example/TCU/Constant						
Gear_Val1	4	base workspace	Example/TCU/Chart						
Gear_Val2	3	base workspace	Example/TCU/Chart						
Gear_Val3	2	base workspace	Example/TCU/Chart						
Gear_Val4	1	base workspace	Example/TCU/Chart						
vsp_val	50	base workspace	Example/TCU/Chart						

Simulation Output

Name	Data Type	Units	Sample Time	Interp	Sync	Link to Plo t
tgtGear	uint8	1	0.01	zoh	union	<u>Link</u>
tgtGear	uint8	 		zoh	union	<u>Link</u>

Name	Data Type	Units	Sample Time	Interp	Sync
tgtGear	uint8	1	0.01	zoh	union
tgtGear	uint8			zoh	union



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Simulation Logs: Simulation stopped at '0.1000000000000001' because there is no input data after this time point.

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