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Assignment-5 (con)

1) An operating system is a courcial software component that manages Ocomputor hardroare and software resources. It acts as an intermediary between the user and the computer hardware, providing I an interface for user interactions and managing touts such as memory allocation, process scheduling, file management and device management.

2) Here are some key services provided by an operating system.

· Proces management - It involves creating, scheduling and

terminating processes. The os allocates susources to processes, manage their execution, and faciliates communication and synchronication between them.

· Memory Management - This service handles memory allocation and terminating, deallocation for processes. It also manages virtual memory, swapping data between RAM and secondary storage when needed:

· file management - The OS provides services for creating, deleting reading and writing files. It manages directories and control access to files through permissions.

· Device Management - It control and coordinates the use of harder and olevices such as printers, keyboards, disks and network interfaces.

· Security and protection- The OS enfances security policies and mech-

anisms to protect the system and user data.

· User Interface - It provides a user-friendly interface for interacting with the computer system. This can be a command-line interface on a graphical user interface (CLI) on a graphical user interface (CLI) on a graphical user interface (GUI), allowing users to perform tasks efficiency.

3/ Major types of operating system are-· Single-wer, single-Tasking os - This type of as allow only one user to execute one program at a time. · Single-user, multi-Tasking os - It permits a single-user to sun multiple programs simultaneously. Mast modern desktop and laptop operating systems like windows, macos, and linear fall into • Multi-Vser as - Designed to allow multiple users to access a computer system concurrently, each with their own user account. · Real-Time 05 - RTOS is disigned to handle tooks with specific timing constraints. It guarantees a response to events within a fixed time frame. · Distributed as - This type of as suns on a network of interconnected computers and provides a single interface to users. It manages nesources acrows multiple machine, allowing for distributed computing • Embedded OS - Operating systems designed to operate an embedded devices with limited resource such as smootphones, 507 clevices and industrial machinery 4) Here's a breakdown of the 0's's role as a resource manager in computer organication and eventeelwie: • CPU management - The OS schedule processes and threads to run on the CPU, determining which takes should be executed and for how long. · Memory management - The OS manages system memorial by allocating and dellocating memory space for processes and applications. · Device management - The OS controls communication b/w software processes and peripheral devices such as printers, keyboards and peripheral devices such as nitwork interfaces. file system management - The as manages storage viesouvice through the file system, organizing and storing data on the disk drives. · I/o management - The OS faciliates I/o operation by managing communication between software processes and external devices.

5) In computer organization and everhitecture, a resident monitoring typically resides in the lowest partion of memory, often starting at address o. It's loaded into memory during system boot and remains the throughout the system's boperation. The memory layout usually includes the monitoris cocle, data structures, and interrupts handlus, allowing it to manage system resources, provide services and control the execution of other programs.

6) Uniprogramming

· Also known as single-tasking on single-programming

• In this CPV executes only one program at a time writil it completes.

• CPV is idle dwing \$10 operations on when waiting for user input.

multiprogramming

· Also kmown as multitasking or multi-pregramming.

· In this multiple programs reside in main memory at the same time.

one program is waiting for 310 on other resources.

7) Operand 1: 00000010 = 2 Operand 2: 00000011 = 3 Subtract Operand 2 from Operand 1 = 2-3=-1.

1. <u>Caving</u>—In a subtraction operation, the caving flag is set if their a boverow. Since there's no barrow in this case, the caving flag would not be set.

2. Zoro - The zoro flag is set if the result of the subtraction is zoro. In this case, the result is -1, so the zoro flag would not be set.

3. Negative - The negative flag is set if the result is negative. since the result is -1, the negative flag would be set.

4. Overflow-Since we're working with 32-bit operands, there's no overflow as-I as within the representable range of a 32-bit signed integer.

After subtraction- Cowey flag-Not set Toro flag-Not set Negative flag-set Overflow flag-Not set

- 8/a/Clock cycle = 1 = 0.2 ns per cycle
- b) If a machine instruction consist of three dock cycle, its direction would be 3x0.2 = 0.6 ns.
- 9) ARM processor typically have three main operating modes:
- · User mode This mode is the least privileged mode where most applications sun. It provide access to a restricted set of resources and instructions, ensuring that applications cannot interfere with critical system functions.

• Supervisor Mode (Svc) - Also known as "kornel mode" on "pravileged mode", this mode is used by the operating system kernel to execute

prieviliged instructions and access system rusources.

About mode - These are specialized modes used for handling specific events ar exceptions.

- 10) The CRR (current program status Register) is a special register in ARM processor that holds information about the current state of the processor is to particular mode, whether such as whether the processor is in particular mode, whether interrupts are enable or disabled, and the conclition code flags resulting from withmetic and logical operations. It's crucial for controlling program execution and handling exceptions in ARM architecture.
- 11) Grenoral-Purpose Registers These are used for general data processing touchs and over often numbered from RO to R15. RO to R12 are accessible by the programmer, while R13 is the Stack Pointer (SP), R14 is the link Register (IR), and R15 is the program Counter (PC).

Status Registers- These include the Current program Status Register and the Saved Program Status Register.

Special-Rupose Registers-These include negisters like the Base Register (R13), link register (R14), and Program Counter (R15), which some specific functions within the processor's execution flow.

Floating-point Registers- In processors that support foating-Point operations, there are dedicated floating-point registers for storing floating-point numbers and performing floating-point withmetic operations. 12) Immediate addressing - The operand is a constant value specified in the instruction itself. Eg- MOV RO, Register addressing - The operand is located in a register. Eg - ADD RI, RZ, R3; Base addressing - The operands address is calculated by adding an offset to a base negister. Eg- LDR RO, [RI, Indexed adobusing - Similar to base adobusing, but the offset us added to a base register and an index integer. Eg - LDR RO [RI, R2] Base with immediate offset addressing - Similar to base addressing, but the offset is specified in the instruction itself. Eg- LDR RO, [R] PC-relative addressing- The operand's address is earlied relative to the program counter. Eg- LDR RO, [PC ; 13/ DR, RO = 0 X 4532 ABCD: · Content of RO: 'OX4532ABCD' mov R1, · Content of RI: '0×40' ADD RO, RI, RO: · RO = R1 + RO = 'OX 4532ABCD' + OX 40 = OX 4532ACOD' STR RO, [R1]: · memory location 0x40; fox4532ACOD' MY-EXTIBMY_EXIT:

14) . selection text · global count-zeros count_zeros; м1, mov H2, mou loop: LSR HO, HO, BCC increment SUBS HZ, HZ, BNE loop BX be increment: ADDS HI, HI, SUBS HZ, HZ, loop BNE BX

This code counts the number of zeros in the input number 'no' and retwens the result in 'ni'.