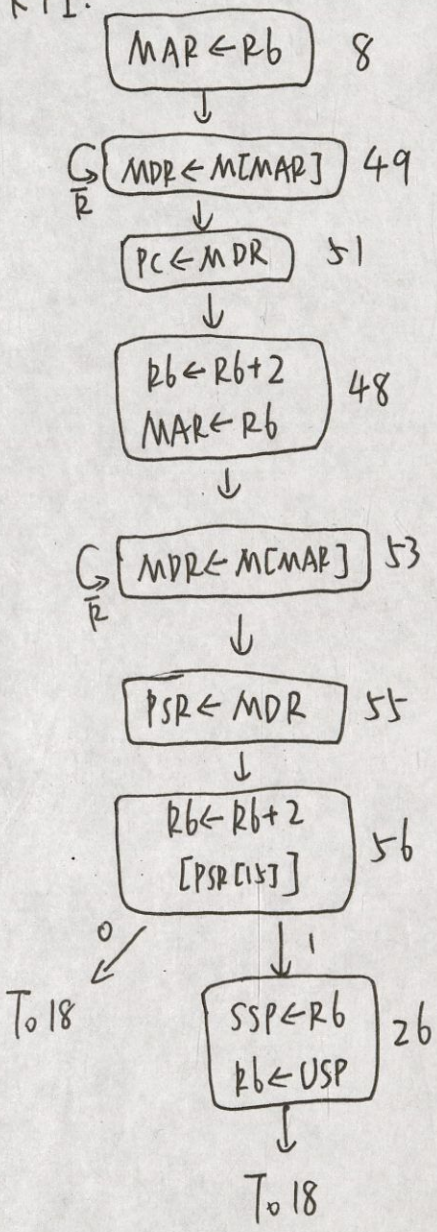
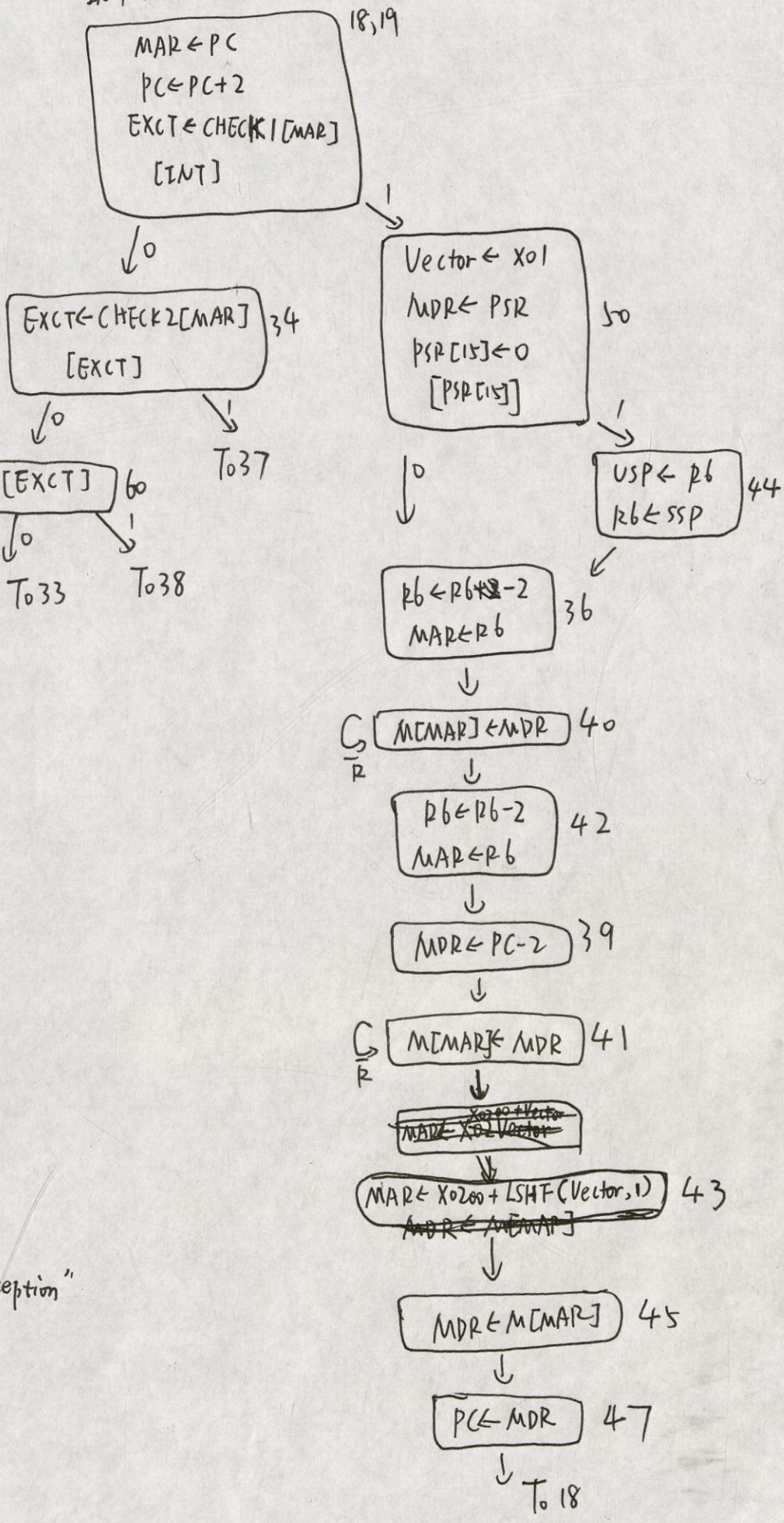
RTI:



INT:



Note: CHECK1[MAR]:

If "Protection Exception",

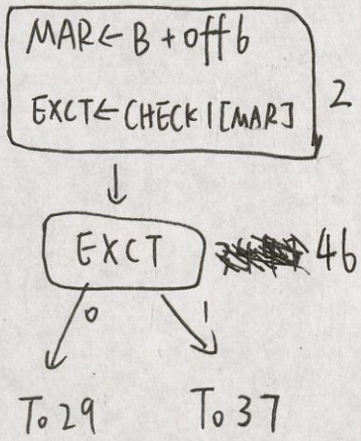
set EXCT=1;

CHECK2[MAR]:

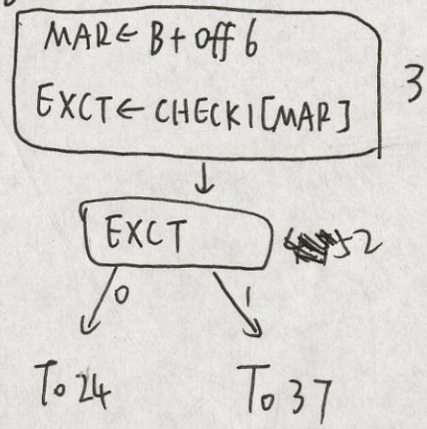
If "Unaligned Access Exception"

set EXCT=1;

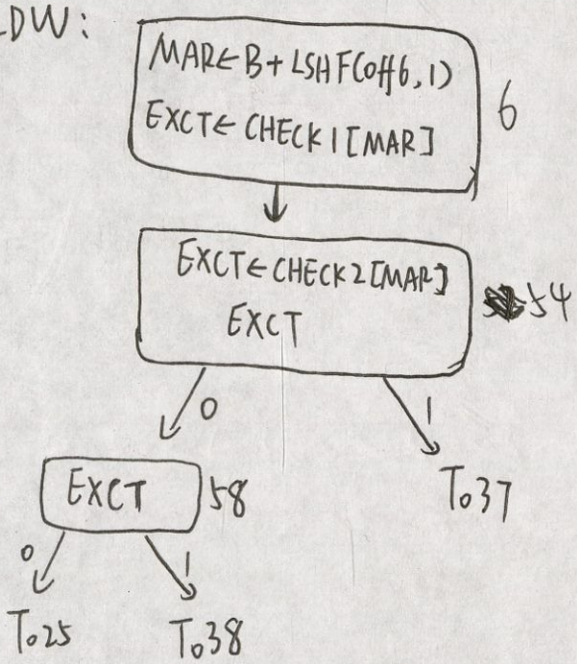
LDB:



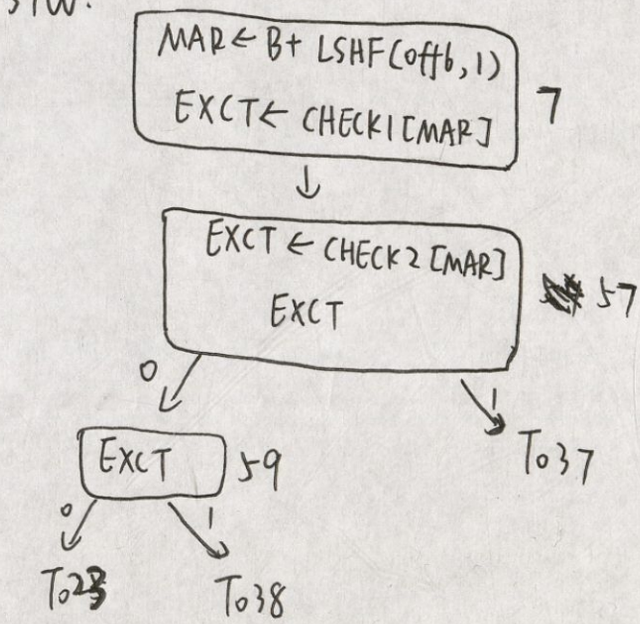
STB:



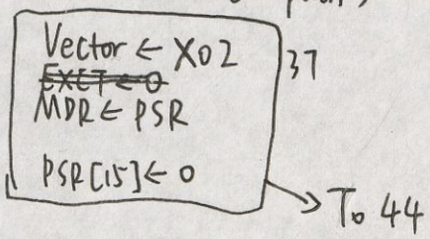
LDW:



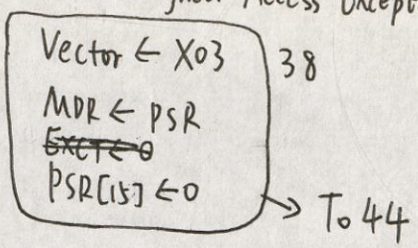
STW:



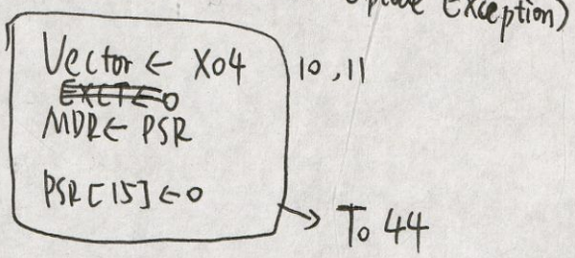
State 37 (Protection Exception)

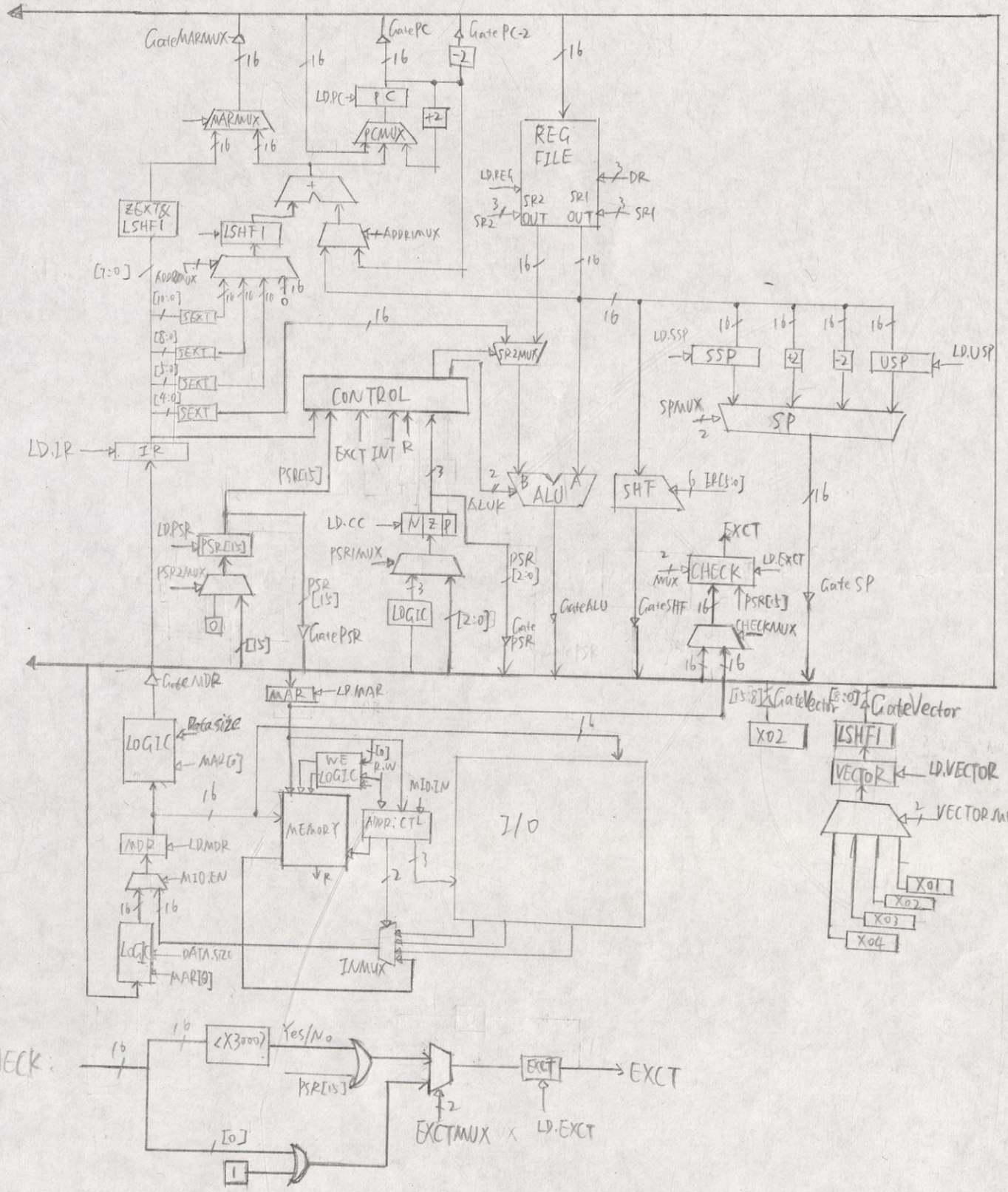


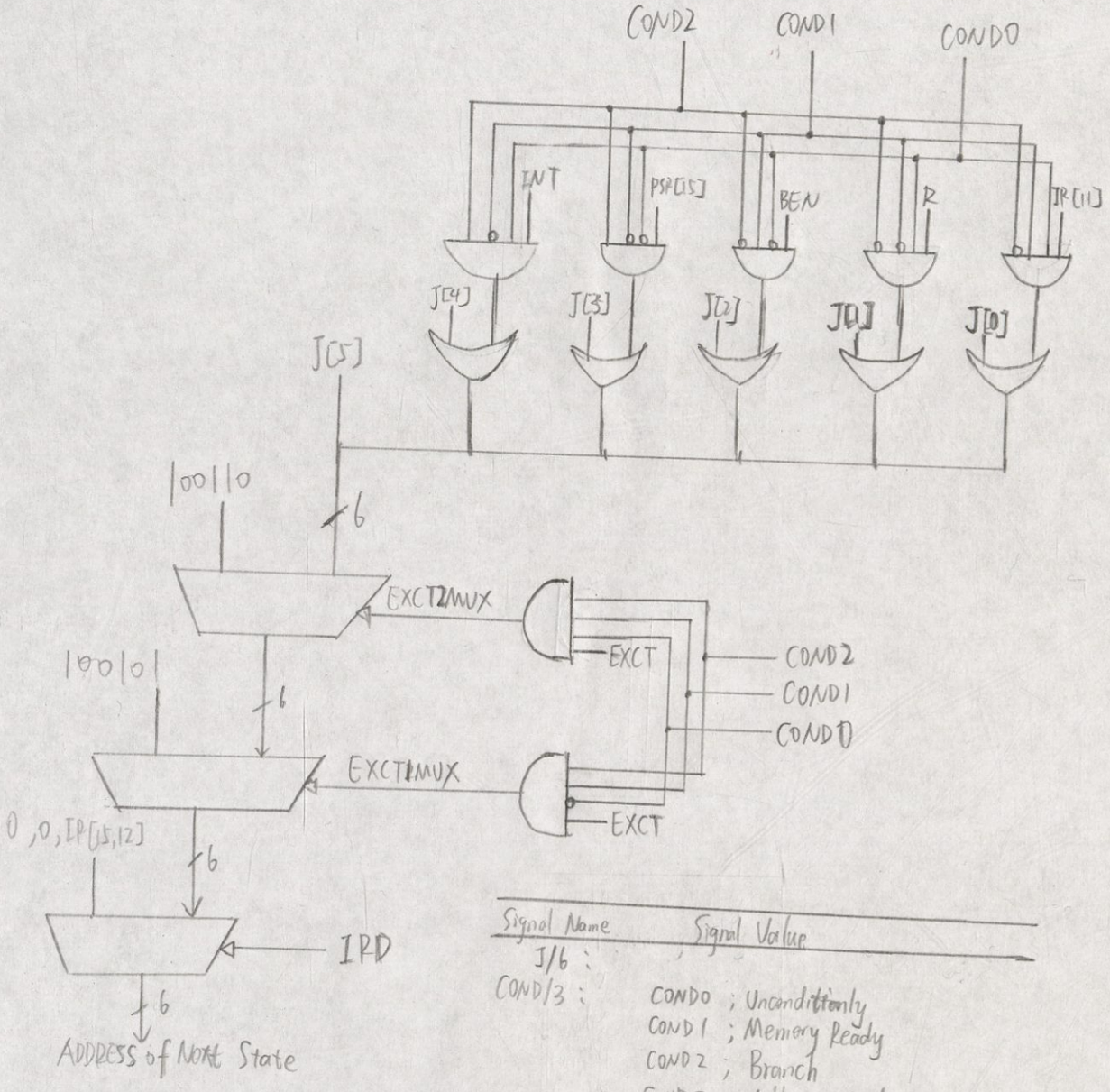
State 38 (Unaligned Access Exception)

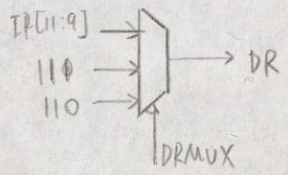


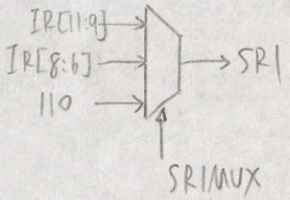
State 10 & State 11 (Unknown Opcode Exception)




GateMARMUX
GatePC
GatePC-2
LD.PC
PC
MARMUX
PCMUX
+2
-2
REG FILE
LD.REG
DR
SR2
SR1
OUT
ZEXT&LSHF1
LSHF1
ADDR1MUX
ADDR2MUX
SEXT
SR2MUX
CONTROL
LD.SSP
SSP
USP
LD.USP
SPMUX
SP
LD.IR
IR
EXCT
INT
R
PSR[15]
ALUK
ALU
SHF
IR[5:0]
LD.PSR
PSR[15]
PSR2MUX
LD.CC
N Z P
PSR1MUX
PSR [2:0]
PSR [15]
LOGIC
GatePSR
GateALU
GateSHF
CHECK
LD.EXCT
CHECKMUX
Gate SP
GateMDR
MAR
LD.MAR
Data.size
MAR[0]
WE
R.W
MIO.EN
MEMORY
ADDR.CTL
I/O
MDR
LD.MDR
INMUX
DATA.SIZE
GateVector
X02
VECTOR
LD.VECTOR
VECTOR1MUX
X01
X03
X04
CHECK:
<X3000>
Yes/No
PSR[15]
EXCT
EXCTMUX
LD.EXCT







Signal Name	Signal Value
J/6 :	
COND/3 :	COND0 ; Unconditionly
	COND1 ; Memory Ready
	COND2 ; Branch
	COND3 ; Addressing Mode
	COND4 ; PSR[15]
	COND5 ; INT
	COND6 ; Protection Exception
	COND7 ; Unaligned Exception
IRD/1 :	No , Yes
EXCT1MUX :	Choose Value from EXCT2MUX
	Choose Value of
EXCT2MUX :	Choose Value From Right Side
	Choose Value of

Added signals:

Signal Name	Signal Values
LD.EXCT/1 :	No, Load
LD.PSR[15]/1 :	No, Load
LD.Vector/1 :	No, Load
LD.SSP/1 :	No, Load
LD.USP/1 :	No, Load
Gate_PC-2/1 :	No, Yes
Gate_PSR/1 :	No, Yes
Gate_Vector/1 :	No, Yes
Gate_SP/1 :	No, Yes
PSR1MUX/1 :	LOGIC ; select logic
	PSR[2:0] ; select PSR[2:0]
PSR2MUX/1 :	0 ; select 0
	PSR[15] ; select PSR[15]
EXCTMUX/1 :	~~0 ; select 0~~
	CHECK1 ; select upper value
	CHECK2 ; select down value
CHECKMUX/1 :	CHECK1 ; select ~~bus~~ values from the bus
	CHECK2 ; select value from MAR
SPMUX/2 :	SSP ; select value from SSP
	+2 ; select "+2"
	-2 ; select "-2"
	USP ; select "USP"
VectorMUX/2 :	X01 ; select "X01"
	X02 ; select "X02"
	X03 ; select "X03"
	X04 ; select "X04"