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E344 Assignment 3

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Report submitted in partial fulfilment of the requirements of the module Design (E) 344 for the degree Baccalaureus in Engineering in the Department of Electrical and Electronic Engineering at Stellenbosch University.

August 27, 2021

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00024601 Studentenommer / *Student number*	 Handtekening / *Signature*
E. Stewdent Voorletters en van / *Initials and surname*	August 27, 2021 Datum / *Date*

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Nomenclature

Update this list to make it applicable to your project.

Variables and functions

$p(x)$	Probability density function with respect to variable x.
$P(A)$	Probability of event A occurring.
ε	The Bayes error.
ε_u	The Bhattacharyya bound.
B	The Bhattacharyya distance.
s	An HMM state. A subscript is used to refer to a particular state, e.g. s_i refers to the i^{th} state of an HMM.
$\mathbf{S}$	A set of HMM states.
$\mathbf{F}$	A set of frames.
$\mathbf{o}_f$	Observation (feature) vector associated with frame f.
$\gamma_s(\mathbf{o}_f)$	A posteriori probability of the observation vector $\mathbf{o}_f$ being generated by HMM state s.
μ	Statistical mean vector.
Σ	Statistical covariance matrix.
$L(\mathbf{S})$	Log likelihood of the set of HMM states $\mathbf{S}$ generating the training set observation vectors assigned to the states in that set.
$\mathcal{N}(\mathbf{x}\|\mu, \Sigma)$	Multivariate Gaussian PDF with mean μ and covariance matrix Σ.
a_{ij}	The probability of a transition from HMM state s_i to state s_j.
N	Total number of frames or number of tokens, depending on the context.
D	Number of deletion errors.
I	Number of insertion errors.
S	Number of substitution errors.

Acronyms and abbreviations

Update this list to make it applicable to your project.

AE	Afrikaans English
AID	accent identification
ASR	automatic speech recognition
AST	African Speech Technology
CE	Cape Flats English
DCD	dialect-context-dependent
DNN	deep neural network
G2P	grapheme-to-phoneme
GMM	Gaussian mixture model
HMM	hidden Markov model
HTK	Hidden Markov Model Toolkit
IE	Indian South African English
IPA	International Phonetic Alphabet
LM	language model
LMS	language model scaling factor
MFCC	Mel-frequency cepstral coefficient
MLLR	maximum likelihood linear regression
OOV	out-of-vocabulary
PD	pronunciation dictionary
PDF	probability density function
SAE	South African English
SAMPA	Speech Assessment Methods Phonetic Alphabet

Chapter 1

Fuse

1.1. Literature

Briefly summarise all the information you have gathered that was necessary to choose an appropriate fuse. This section is aimed at someone at your level of knowledge (the median E&E third year student).

1.2. Design

Put in calculations, assumptions, analysis, choice. This is an example of design by analysis, where we will not be testing it, since the test is destructive.

Chapter 2

Undervoltage battery protection

This chapter answers the question: "Did the student follow a systematic approach to design the sought solution?". You therefore need to follow a systematic/logic path, and did you clearly communicate it.

2.1. Literature

Here you can include stuff you learnt that you will use in the design - e.g. operational amplifiers as comparators, hysteresis, rail-to-rail comparators. If you feel there was nothing you had to learn to do this, feel free to leave this section out.

2.2. Overview

Explain your undervoltage circuit layout and functional-level choice of component types. Include the high-side switch configuration and opamp location in the circuit. You do not want to give any detail of the design, like resistors and capacitor values, just an overview of how your undervoltage circuit "hangs together" - similar to that part of the diagram in the Project Overview file provided. You will probably use a block diagram here (if you have space) and describe in in text.

2.3. 5V rail

Here explain your selection of 5V regulator.

2.4. High-side switch

Here you describe the design choices made for the switch (Note, this is not the switch we use to control the LM317 output, it is similar switch that controls the supply from the battery to the same regulated line, but it does not have a blocking diode).

2.5. Voltage monitoring with hysteresis design

Explain your design of the comparator with hysteresis, taking into account things like common mode voltages, differential voltages, input-to-rail voltages, hysteresis deadband, resistor values

and current consumption, etc.

2.6. Circuit diagram

Show your circuit diagram (the one you will submit). Ensure that it is of good quality, and preferably a vector (metadata) plot, not a raster (pixel) plot, so you can zoom in and select text from it.

2.7. Results

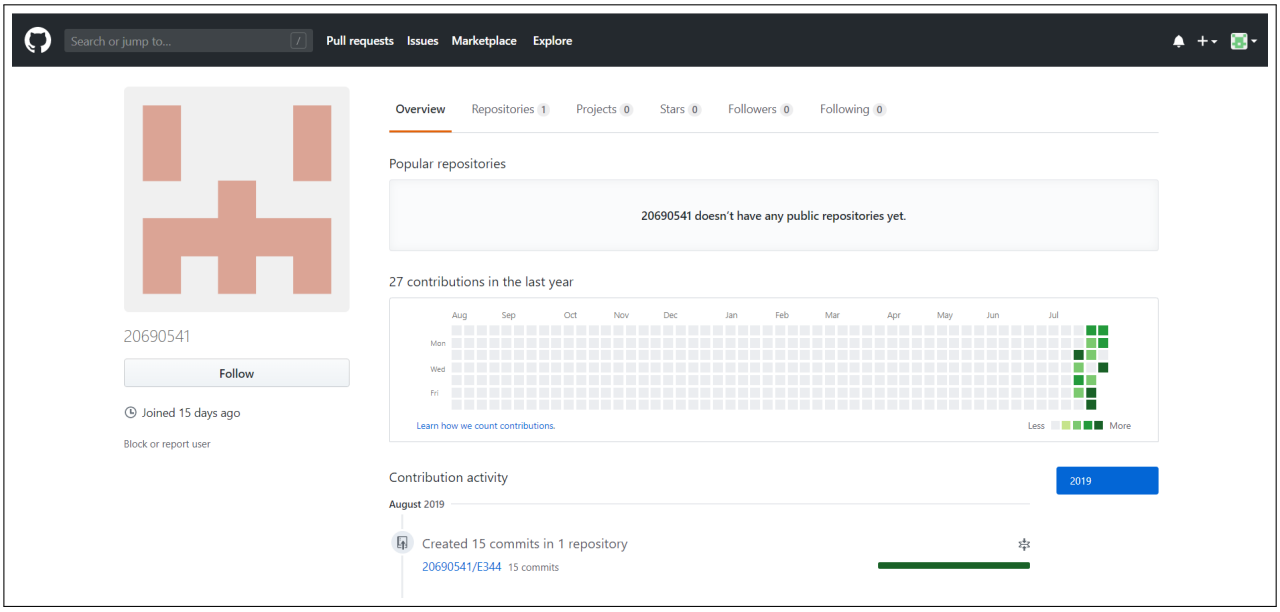
Here you include your simulation results and your measured results. For the measured results, it would be most beneficial to show on the same oscilloscope screen-grab (or CSV plot), how the switch went through the stages of the hysteresis loop (similar to what you had to do for the video). You are welcome to use subplots to save space.

Bibliography

Appendix A

GitHub Activity Heatmap

Take a screenshot of your github version control activity heatmap and insert here.



Appendix B

Stuff you want to include

remove this!!

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