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E344 Assignment 1

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Report submitted in partial fulfilment of the requirements of the module Design (E) 344 for the degree Baccalaureus in Engineering in the Department of Electrical and Electronic Engineering at Stellenbosch University.

August 14, 2021

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   Plagiarism is the use of ideas, material and other intellectual property of another's work and to present is as my own.

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5. Ek verklaar dat die werk in hierdie skryfstuk vervat, behalwe waar anders aangedui, my eie oorspronklike werk is en dat ek dit nie vantevore in die geheel of gedeeltelik ingehandig het vir bepunting in hierdie module/werkstuk of 'n ander module/werkstuk nie.
   I declare that the work contained in this assignment, except where otherwise stated, is my original work and that I have not previously (in its entirety or in part) submitted it for grading in this module/assignment or another module/assignment.

22546448	
Studentenommer / *Student number*	Handtekening / *Signature*
MC van der Berg	August 14, 2021
Voorletters en van / *Initials and surname*	Datum / *Date*

Contents

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Nomenclature

Update this list to make it applicable to your project.

Variables and functions

$p(x)$	Probability density function with respect to variable x.
$P(A)$	Probability of event A occurring.
ε	The Bayes error.
ε_u	The Bhattacharyya bound.
B	The Bhattacharyya distance.
s	An HMM state. A subscript is used to refer to a particular state, e.g. s_i refers to the i^{th} state of an HMM.
$\mathbf{S}$	A set of HMM states.
$\mathbf{F}$	A set of frames.
$\mathbf{o}_f$	Observation (feature) vector associated with frame f.
$\gamma_s(\mathbf{o}_f)$	A posteriori probability of the observation vector $\mathbf{o}_f$ being generated by HMM state s.
μ	Statistical mean vector.
Σ	Statistical covariance matrix.
$L(\mathbf{S})$	Log likelihood of the set of HMM states $\mathbf{S}$ generating the training set observation vectors assigned to the states in that set.
$\mathcal{N}(\mathbf{x}\|\mu, \Sigma)$	Multivariate Gaussian PDF with mean μ and covariance matrix Σ.
a_{ij}	The probability of a transition from HMM state s_i to state s_j.
N	Total number of frames or number of tokens, depending on the context.
D	Number of deletion errors.
I	Number of insertion errors.
S	Number of substitution errors.

Acronyms and abbreviations

Update this list to make it applicable to your project.

AE	Afrikaans English
AID	accent identification
ASR	automatic speech recognition
AST	African Speech Technology
CE	Cape Flats English
DCD	dialect-context-dependent
DNN	deep neural network
G2P	grapheme-to-phoneme
GMM	Gaussian mixture model
HMM	hidden Markov model
HTK	Hidden Markov Model Toolkit
IE	Indian South African English
IPA	International Phonetic Alphabet
LM	language model
LMS	language model scaling factor
MFCC	Mel-frequency cepstral coefficient
MLLR	maximum likelihood linear regression
OOV	out-of-vocabulary
PD	pronunciation dictionary
PDF	probability density function
SAE	South African English
SAMPA	Speech Assessment Methods Phonetic Alphabet

Chapter 1

Solar photovoltaic cells and solar modules

A PV cell is an energy harvesting technology, that converys solar energy into electricity through the photovoltaic effect (PV). The photovoltaic cell is a specially treated semi-conductor layer. This layer consist of two other layers: the p-type and n-type layer, forming a pn junction.This pn-junction is what actually converts the Sun's energy into useful electricity through a process called the photovoltaic effect. On either side of the semiconductor is a layer of conducting material. The conducting material is placed at the back of the cell and not on the front as to not to block the suns radiation from reaching the semiconductor. The last layer is the anit-reflection coating layer that is placed on the illuminated side of the cell. This is a requirement s all semiconductors are reflective and the sunlight that is reflective is energy that is not converted into electricity. [1]

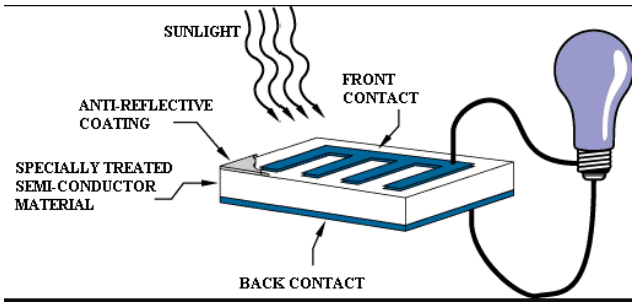


Figure 1.1: Basic operation of a photovoltaic cell [1].

A photovoltaic cell is however not ideal in practise is can only convert a percentage of the solar energy it receives into electrical energy, the average efficiency of a solar panel is between 17 to 19 percent efficient.This could be due to many reasons such the material of the panel component,Reflectance efficiency and Thermodynamic efficiency [2]
A photovoltaic cell has a IV curve that indicates the relationship between voltage and current of the cell under certain conditions.This relationship is almost parallel with the Voltage axis, but at the knee of this curve the current that is produced if more voltage introduced drops drastically until the curve is in a open-circuit state and the voltage is equal to the open-circuit voltage.
The open-circuit voltage VOC is the maximum voltage attainable by a photovoltaic cell. This voltage will be reached when the circuit is in a open circuit configuration, therefore the current will be zero as can also be seen on the **Figure 1.2** this value however is not fixed for a cell as it will decrease as temperature increases. The open circuit voltage of a single cell is typicaly 0.6V.

This is not very large but these cells are connected in series with one anothoer to produce a larger voltage value. These are called Solar Modules. [3]

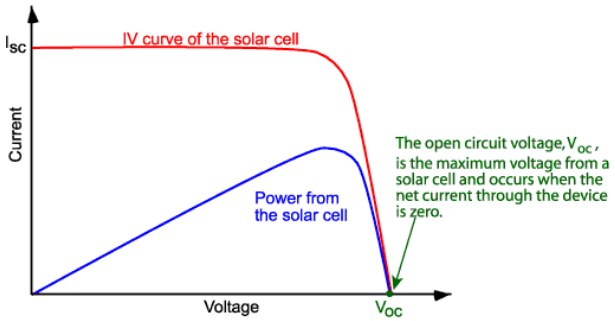


Figure 1.2: IV curve of a solar cell [1].

Table 1.1: Example of a simple table.

	V_{OC} [V]	I_{CC} [A]	V_{pmax} [V]
Theroretical per cell	1.0	1.0	1.0
Datasheet per module	1.0	1.0	1.0
Measured dark 1.0	1.0	1.0	
Measured upside-down 1.0	1.0	1.0	
Measured oblique 1.0	1.0	1.0	
Measured facing 1.0	1.0	1.0	

Chapter 2

Lead acid batteries

Chapter 3

High-side switching circuit

3.1. Intro

Introduce the reader to **what you want to present** in this chapter (i.e. what are you trying to achieve by initiating this communication?). Try to put yourself in the readers' shoes - what would you ~~like~~ need to see to be convinced that the author (1) knew what they were doing and understood what they had to do (2) properly designed for the requirements, (3) simulation-tested their design, and (4) correctly and critically assessed the outcome.

Include any references to literature you feel is needed. In this section, you put a very short summary of infrormation you gatherered from literature (papers, web sites, datasheets) that you used to do the design. Be sure to cite the references, which you can add in the `References.bib` file.

3.2. Design

In this section, you need to capture your design, which should include the following:

- Design rationale, i.e. what your thinking was behind the design.
- References to literature/sources as appropriate [**?**], but preferably in the intro above.
- You can assume the reader is in their third year of their E&E engineering degree, and that they will not need detailed explanations of trivial information (e.g. what a resistor is, or what Ohm's law is).
- Design calculations, for example to determine resistor values and capacitor values, or to check for allowed voltage and current ranges and levels. These calculations should also give expected outputs, which hopefully matches the simulated values.
- Analysis of given or expected input conditions.
- Expected values and ranges based on your design.
- Explain your choice of supply by referring to the advantages and disadvantages of each.

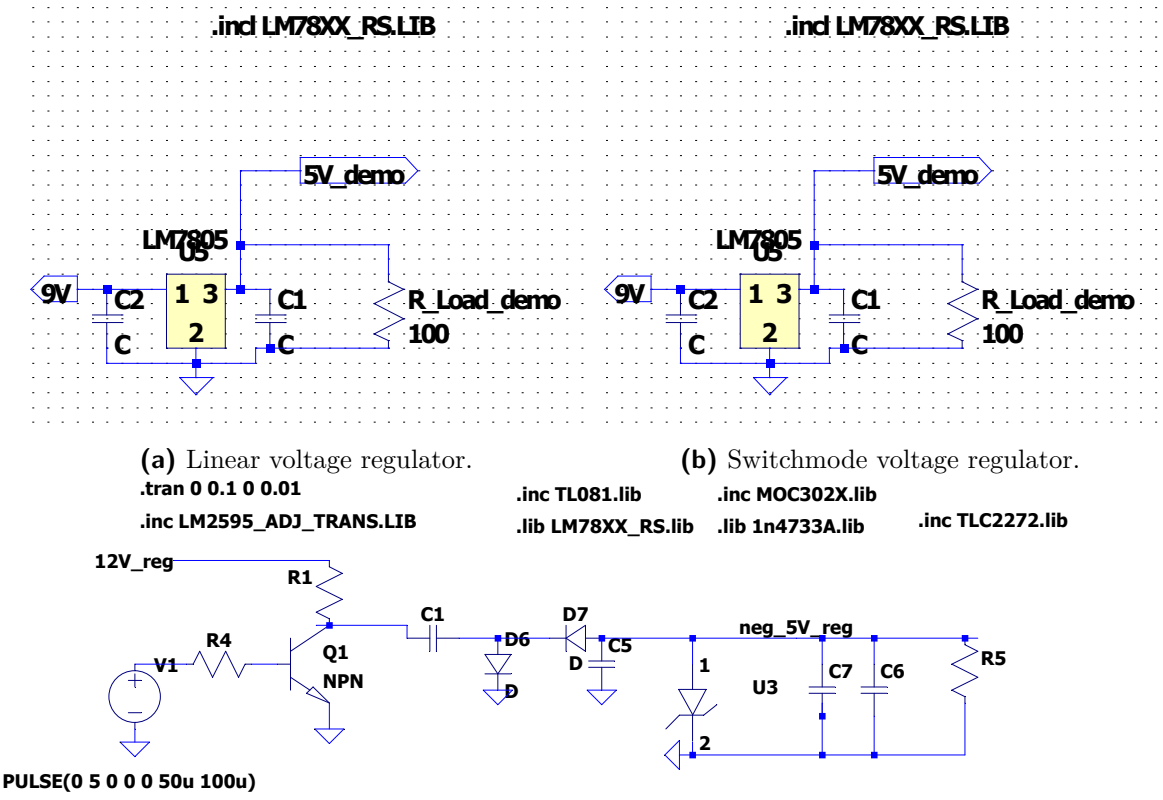
- Circuit diagram like the one in Figure 3.1. I used “print to PDF” from LTSpice, but feel free to use a cropped screengrab if you are PDF-challenged and do not have a PDF printer (there are some free PDF creators online). Also have a look at the demo video on SUNLearn.

For your benefit, here is how to write values with units: 150 mΩ or 199 myUnits, and this is how we write ranges: 2 to 5 kV.

Here is an inline equation $\frac{55}{45+3}$. Here is a numbered equation in Eq. 3.1.

$$a = \frac{55}{45+3}. \tag{3.1}$$

.



(a) Linear voltage regulator.

(b) Switchmode voltage regulator.

(c) Chargepump voltage regulator.

Figure 3.1: Circuit diagrams of the two voltage regulators, and another irrelevant one

.

3.3. Results

In this section, you want to demonstrate, by means of referring to simulation results, using the designed circuit, how your circuit behaves as you designed it in Section 3.2. Present and report on your simulated results in Figure 3.2. Be absolutely sure that the text and information in your report are readable.

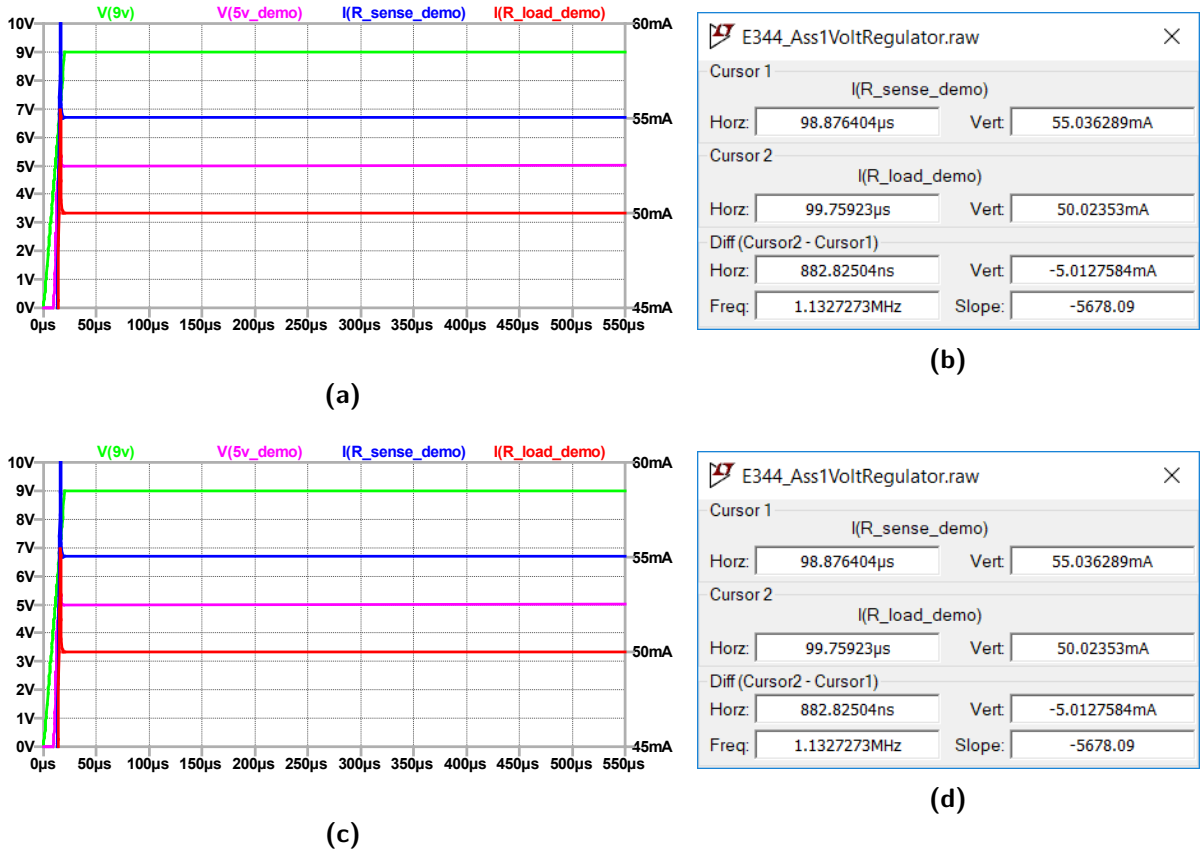


Figure 3.2: Voltage regulation, comparing the linear and switchmode regulators... (a) Blah blah. (b) Blah blah. (c) Blah blah. (d) Blah blah. As far as possible, please put input(s) and output(s) on the same plot rather than on separate plots. Based on the datasheet of XXXX in [**?**].

Table 3.1: Example of a simple table.

	2017	2018	Δ_{Abs}	Δ_{DiD}
A	9,868	10,399	+5	-11
B	10,191	10,590	+4	-12

Table 3.2: Example of another table.

Schools	Total energy used		Change	
	2017 [kWh]	2018 [kWh]	Δ_{Abs} [%]	Δ_{DiD} [%]
A	9,868	10,399	+5	-11
B	10,191	10,590	+4	-12

You can use screengrabs or photos of the oscilloscope, or download the CSVs and plot them as PDFs using Matlab, Excel or similar. You can also use tables, example of which are presented in Tables 3.1 and 3.2.

3.4. Summary

State whether your design performs as expected and what the limitations or things to keep in mind are.

Bibliography

[1] K. S. B. Y. J. D. Bethel Afework, Jordan Hanania. (2018) Photovoltaic cell. https://energyeducation.ca/encyclopedia/Photovoltaic_cell.

[2] solar.com. (2021, May) Solar panel efficiency - pick the most efficient solar panels. https://www.solar.com/learn/solar-panel-efficiency/.

[3] ——. (2021, May) Solar panel efficiency - pick the most efficient solar panels. https://www.pveducation.org/pvcdrom/solar-cell-operation/open-circuit-voltage.

Appendix A

Social contract

Download copy from SUNLearn, sign and include here (replace this one).

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E-design 344 Social Contract

2021

The purpose of this document is to establish commitment between the student and the organisers of E344. Beyond the commitment made here, it is not binding.

In the months preceeding the term, the lecturer (Thinus Booysen) and the Teaching Assistant (Kurt Coetzer) spent countless hours to prepare for E344 to ensure that you get your money's worth and that you are enabled to learn from the module and demonstrate and be assessed on your skills. We commit to prepare the assignments, to set the tests and assessments fairly, to be reasonably available, and to provide feedback and support as best and fast we can. We will work hard to give you the best opportunity to learn from and pass analogue electronic design E344.

I, ... have registered for E344 of my own volition with the intention to learn of and be assessed on the principals of analogue electronic design. Despite the potential publication online of supplementary videos on specific topics, I acknowledge that I am expected to attend the scheduled lectures to make the most of these appointments and learning opportunities. Moreover, I realise I am expected to spend the additional requisite number of hours on E344 as specified in the yearbook.

I acknowledge that E344 is an important part of my journey to becoming a professional engineer, and that my conduct should be reflective thereof. This includes doing and submitting my own work, working hard, starting on time, and assimilating as much information as possible. It also includes showing respect towards the University's equipment, staff, and their time.

Prof. MJ Booysen

Student number: .....

Signature: .....

Signature: .....

Date: 29 July 2021

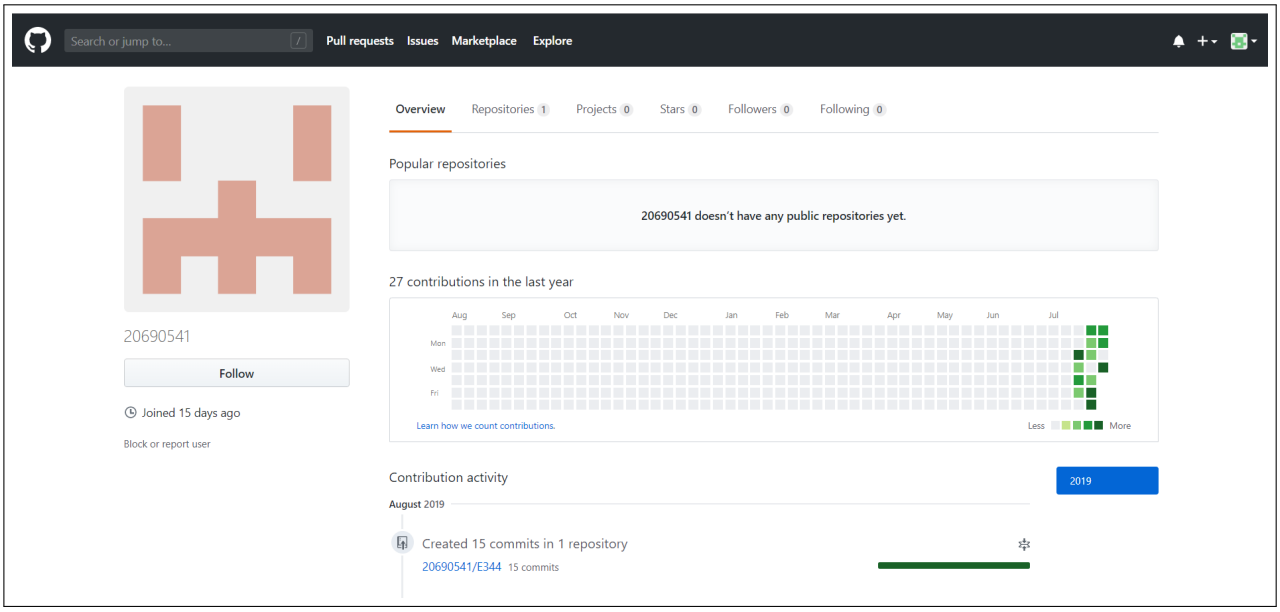
Date: .....

1

Appendix B

GitHub Activity Heatmap

Take a screenshot of your github version control activity heatmap and insert here.



Appendix C

Stuff you want to include

remove this!!

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