

ASSIGNMENT-1

1. Write a Verilog code for 2X4 decoder.
2. Write a Verilog code for Full subtractor.
3. Write a Verilog code for 2-bit comparator.
4. Write a Verilog code for 3 bit binary to gray convertor.
5. Write a Verilog code for BCD to excess 3 convertors.

Note: Use all design styles (EDA playground for simulation)

Submission Date: Before 25 August-2023