REV	Description	DATE	BY
A3	Production release.	10/21/2011	GC
A3A	Updated in conjunction with the release of the SRM. Typos and naming changes. No change in design.	11/5/2011	GC

PAGE NO.	SCHEMATIC PAGE
1	COVER PAGE
2	POWER MANAGEMENT
3	PROCESSOR 1 OF 3
4	PROCESSOR 2 OF 3
5	PROCESSOR 3 OF 3
6	LED, CONFIGURATION AND BUTTON
7	MEMORY
8	ETHERNET AND USB HOST
9	USB CONCENTRATOR
10	SERIAL AND JTAG
11	EXP CONN, uSD, AND LDO

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Title: BeagleBone Cover Page		
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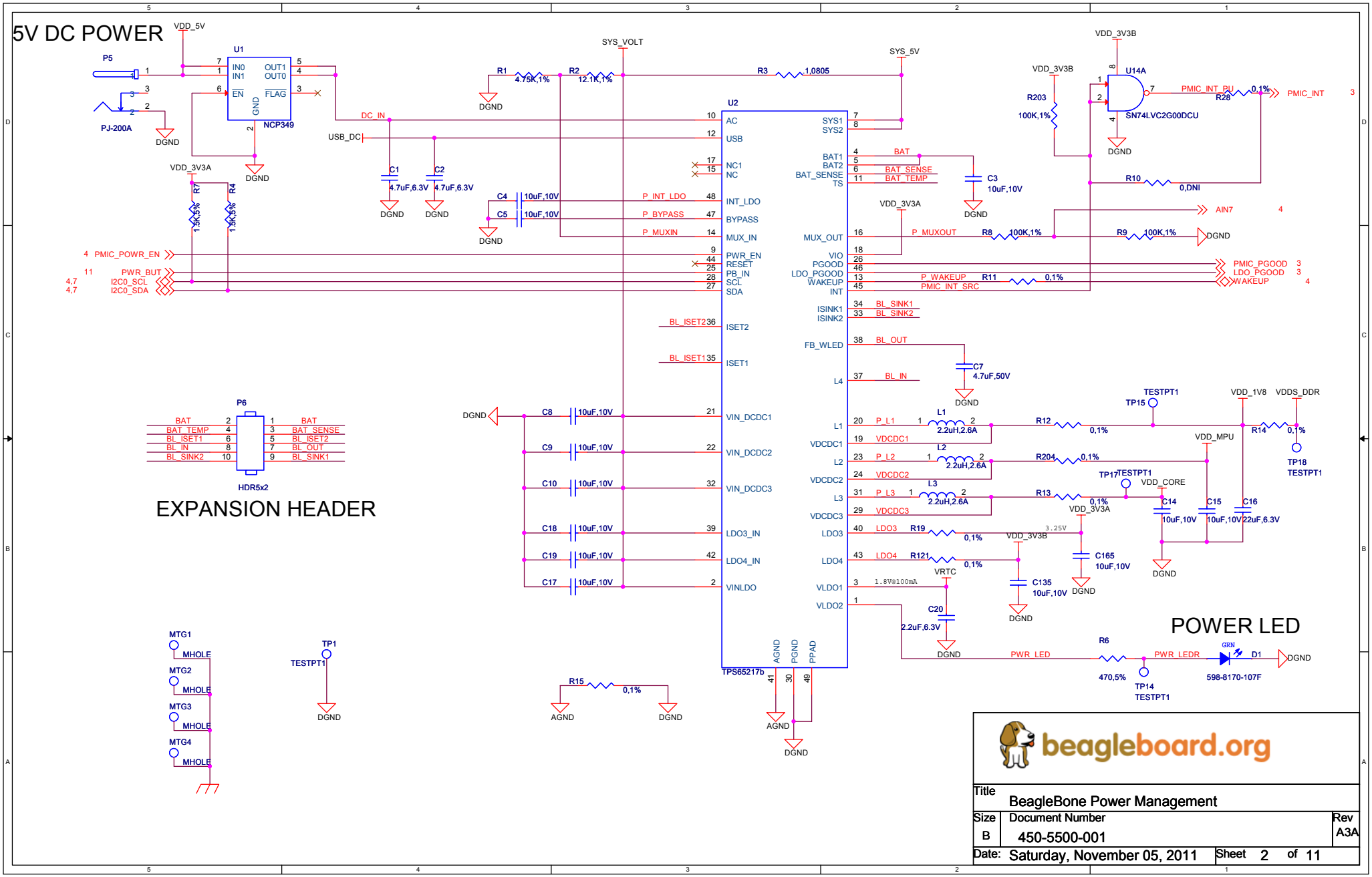
5V DC POWER
VDD_5V
P5
PJ-200A
U1
NCP349
IN0
IN1
OUT1
OUT0
EN
FLAG
GND
DGND
SYS_VOLT
R1
4.75K,1%
R2
12.1K,1%
R3
1,0805
SYS_5V
DC_IN
USB_DC
C1
4.7uF,6.3V
C2
4.7uF,6.3V
VDD_3V3A
R7
1.5K,5%
R4
1.5K,5%
C4
10uF,10V
C5
10uF,10V
U2
TPS65217b
AC
USB
NC1
NC
INT_LDO
BYPASS
MUX_IN
PWR_EN
RESET
PB_IN
SCL
SDA
ISET2
ISET1
VIN_DCDC1
VIN_DCDC2
VIN_DCDC3
LDO3_IN
LDO4_IN
VINLDO
SYS1
SYS2
BAT1
BAT2
BAT_SENSE
TS
MUX_OUT
VIO
PGOOD
LDO_PGOOD
WAKEUP
INT
ISINK1
ISINK2
FB_WLED
L4
L1
VDCDC1
L2
VDCDC2
L3
VDCDC3
LDO3
LDO4
VLDO1
VLDO2
AGND
PGND
PPAD
P_INT_LDO
P_BYPASS
P_MUXIN
PMIC_POWR_EN
PWR_BUT
I2C0_SCL
I2C0_SDA
BL_ISET2
BL_ISET1
BAT
BAT_SENSE
BAT_TEMP
P_MUXOUT
P_WAKEUP
PMIC_INT_SRC
BL_SINK1
BL_SINK2
BL_OUT
BL_IN
C3
10uF,10V
C7
4.7uF,50V
R8
100K,1%
R9
100K,1%
R11
0,1%
R10
0,DNI
R203
100K,1%
U14A
SN74LVC2G00DCU
PMIC_INT_PU
R28
0,1%
PMIC_INT
AIN7
PMIC_PGOOD
LDO_PGOOD
WAKEUP
P6
HDR5x2
BAT
BAT_TEMP
BAT_SENSE
BL_ISET1
BL_ISET2
BL_IN
BL_OUT
BL_SINK2
BL_SINK1
EXPANSION HEADER
C8
10uF,10V
C9
10uF,10V
C10
10uF,10V
C18
10uF,10V
C19
10uF,10V
C17
10uF,10V
L1
2.2uH,2.6A
L2
2.2uH,2.6A
L3
2.2uH,2.6A
R12
0,1%
R204
0,1%
R13
0,1%
R19
0,1%
R121
0,1%
VRTC
1.8V@100mA
3.25V
C20
2.2uF,6.3V
C135
10uF,10V
C165
10uF,10V
TESTPT1
TP15
TP17
TP18
VDD_1V8
VDDS_DDR
VDD_MPU
VDD_CORE
R14
0,1%
C14
10uF,10V
C15
10uF,10V
C16
22uF,6.3V
POWER LED
R6
470,5%
PWR_LED
PWR_LEDR
D1
GRN
598-8170-107F
TP14
R15
0,1%
MTG1
MTG2
MTG3
MTG4
MHOLE
TP1
TESTPT1
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BeagleBone Power Management
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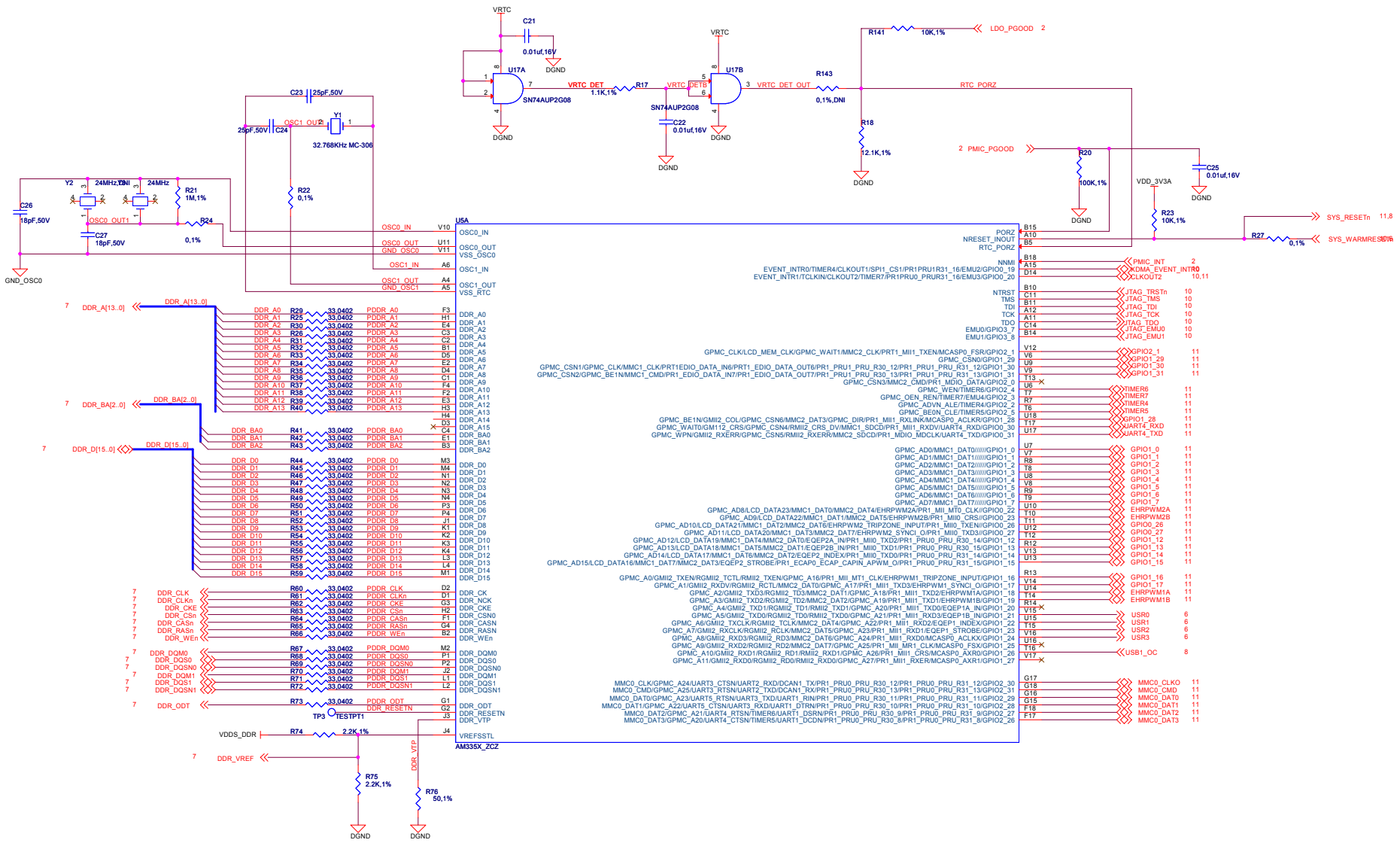

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Title	BeagleBone Processor 2 of 3	
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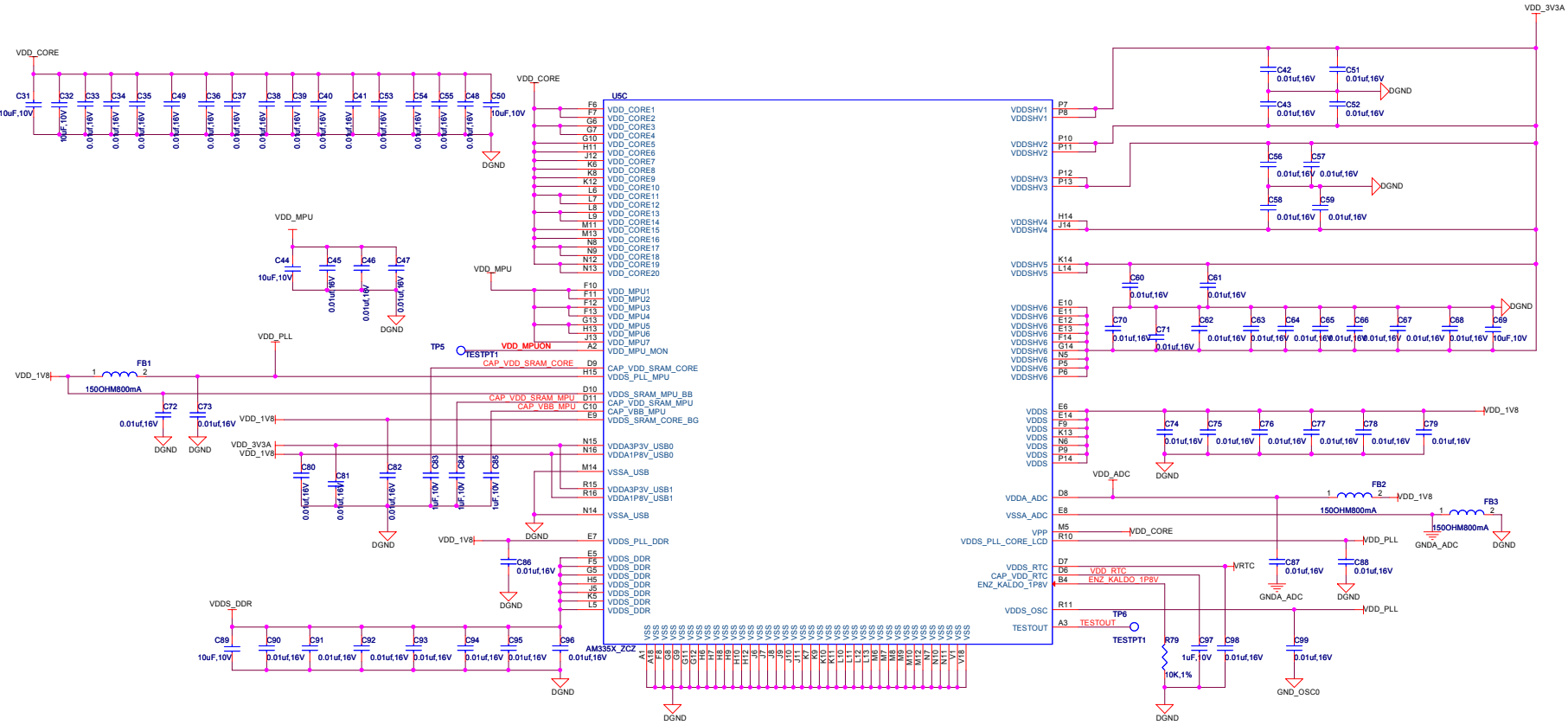

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Title	BeagleBone Processr 3 of 3
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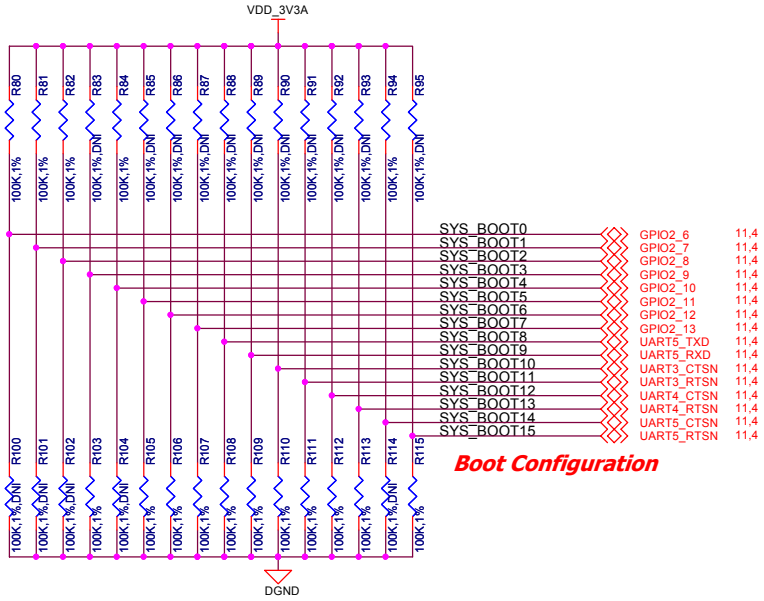
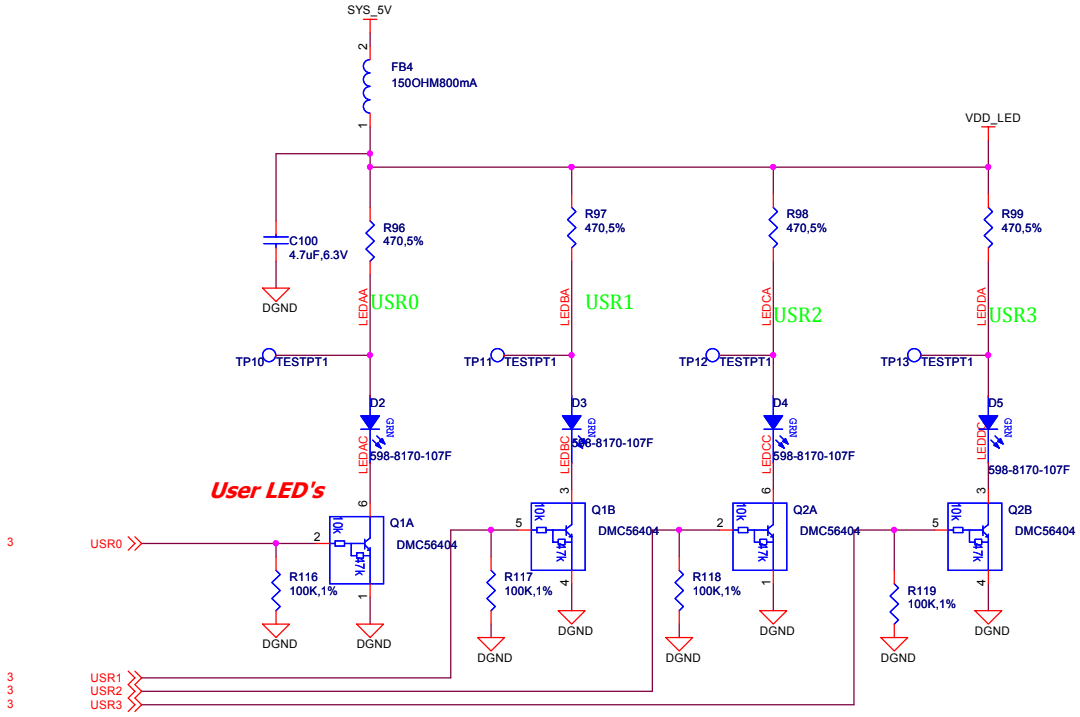
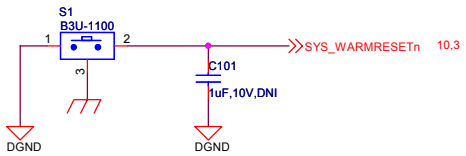


Boot Configuration

BOOT ORDER....MMC....SPI....UART....USB.....24MHZ



RESET BUTTON



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Title	BeagleBone LED, Configuration and Reset	
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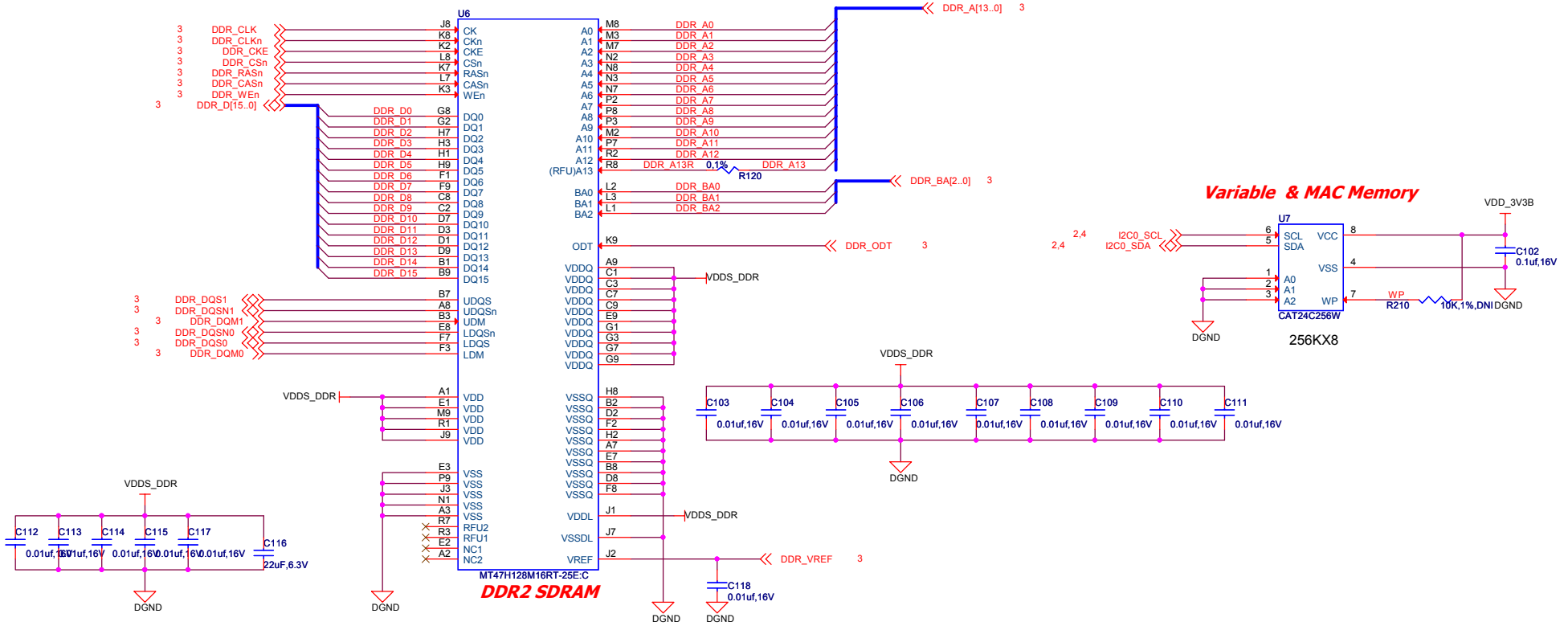

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Title	BeagleBone DDR2 Memory	
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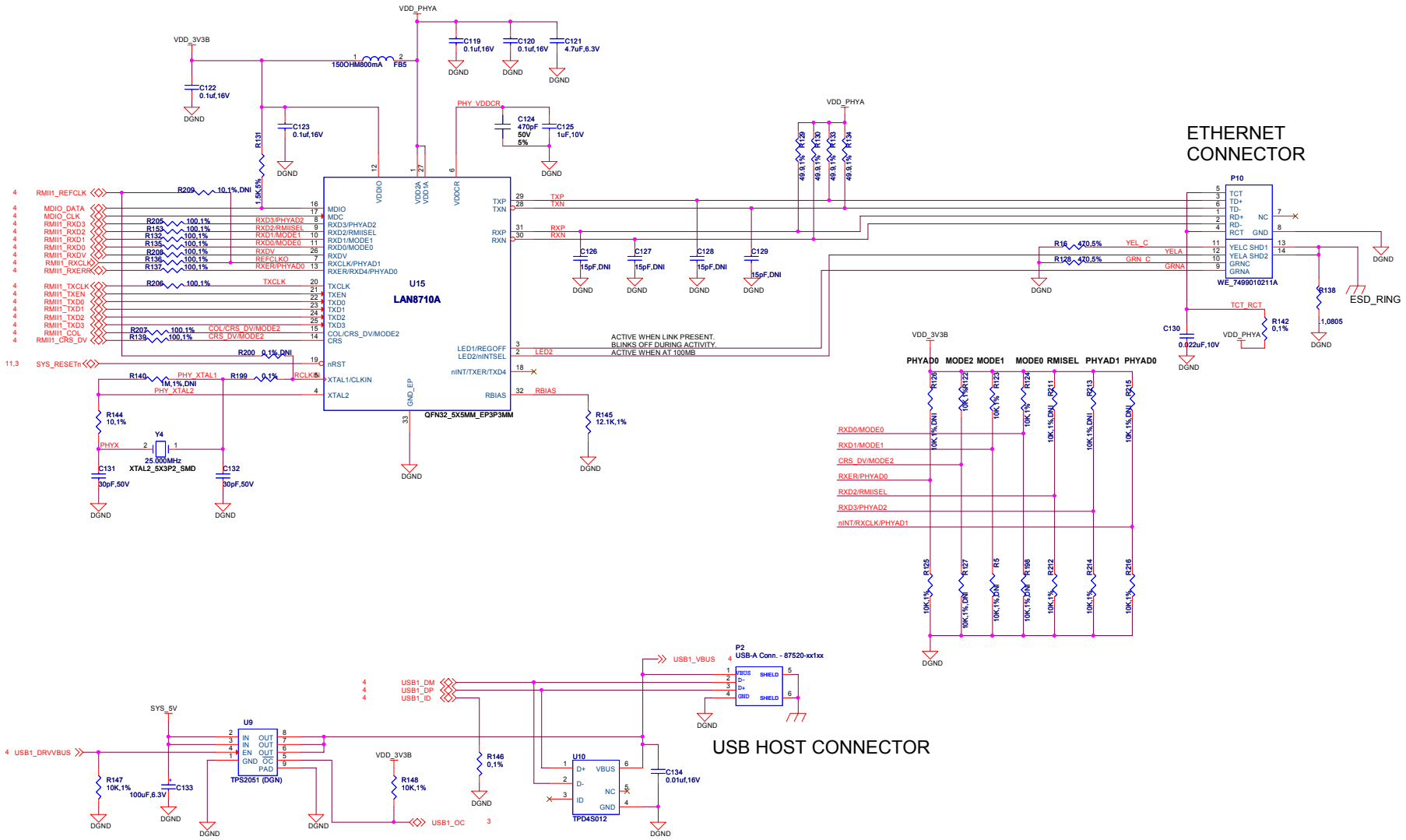

Title	BeagleBone Ethernet and USB Host
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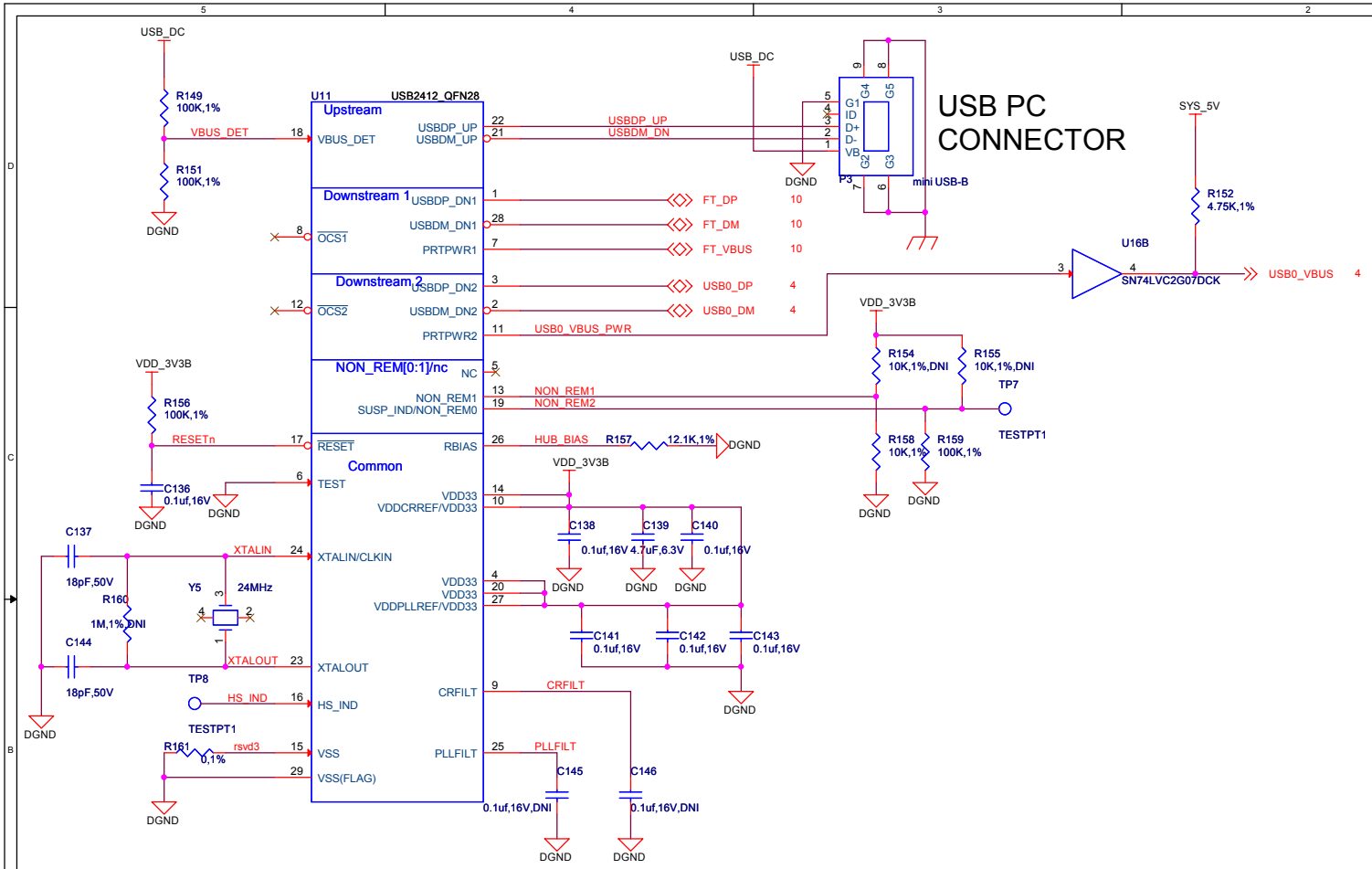

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Title	BeagleBone USB Concentrator	
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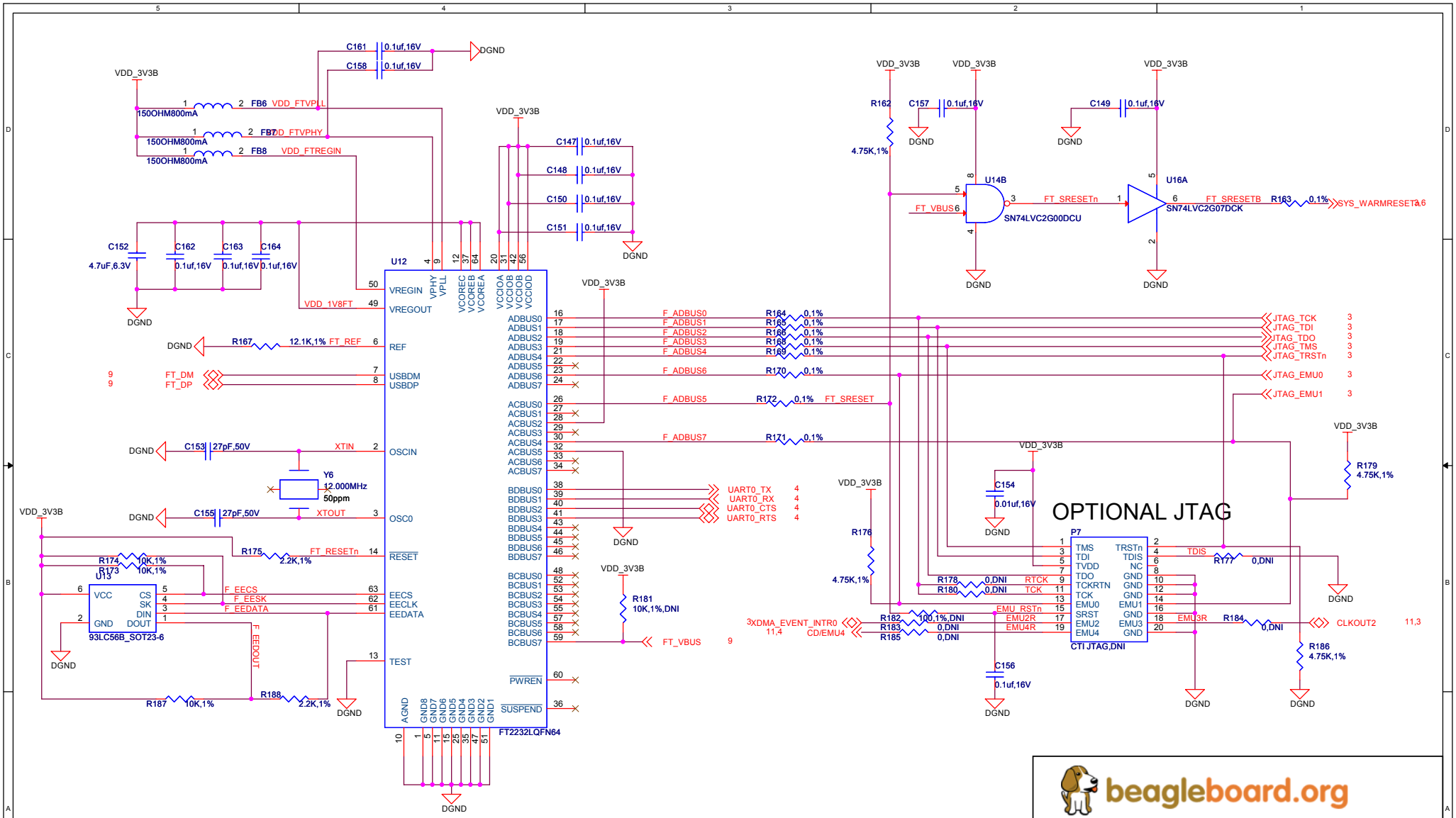
OPTIONAL JTAG
U12
FT2232LQFN64
U13
93LC56B_SOT23-6
U14B
SN74LVC2G00DCU
U16A
SN74LVC2G07DCK
P7
CTI JTAG,DNI
Y6
12.000MHz
50ppm
VDD_3V3B
DGND
VDD_1V8FT
VDD_FTVPLL
VDD_FTVPHY
VDD_FTREGIN
FT_DM
FT_DP
FT_RESETn
FT_SRESETn
FT_SRESETB
FT_VBUS
F_EECS
F_EESK
F_EEDATA
EEDOUT
XTIN
XTOUT
UART0_TX
UART0_RX
UART0_CTS
UART0_RTS
JTAG_TCK
JTAG_TDI
JTAG_TDO
JTAG_TMS
JTAG_TRSTn
JTAG_EMU0
JTAG_EMU1
SYS_WARMRESETn
CLKOUT2
XDMA_EVENT_INTR0
CD/EMU4
TDIS
EMU3R
EMU_RSTn
C161 0.1uf,16V
C158 0.1uf,16V
C147 0.1uf,16V
C148 0.1uf,16V
C150 0.1uf,16V
C151 0.1uf,16V
C152 4.7uF,6.3V
C162 0.1uf,16V
C163 0.1uf,16V
C164 0.1uf,16V
C153 27pF,50V
C155 27pF,50V
C157 0.1uf,16V
C149 0.1uf,16V
C154 0.01uf,16V
C156 0.1uf,16V
FB6 150OHM800mA
FB7 150OHM800mA
FB8 150OHM800mA
R162 4.75K,1%
R163 0,1%
R164 0,1%
R165 0,1%
R166 0,1%
R167 12.1K,1%
R169 0,1%
R170 0,1%
R171 0,1%
R172 0,1%
R173 10K,1%
R174 10K,1%
R175 2.2K,1%
R176 4.75K,1%
R177 0,DNI
R178 0,DNI
R179 4.75K,1%
R180 0,DNI
R181 10K,1%,DNI
R182 100,1%,DNI
R183 0,DNI
R184 0,DNI
R185 0,DNI
R186 4.75K,1%
R187 10K,1%
R188 2.2K,1%
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Title
BeagleBone Serial and JTAG
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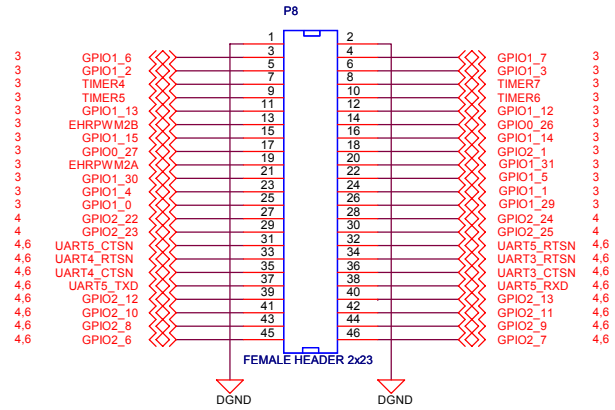
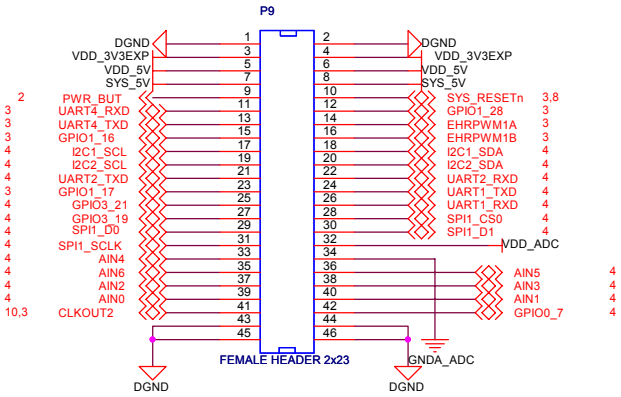


EXPANSION HEADER



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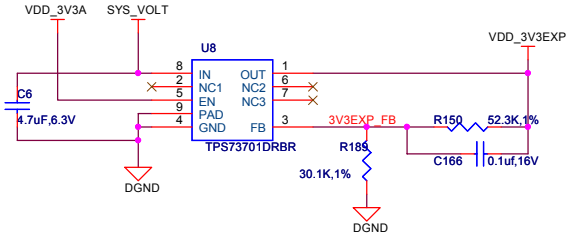

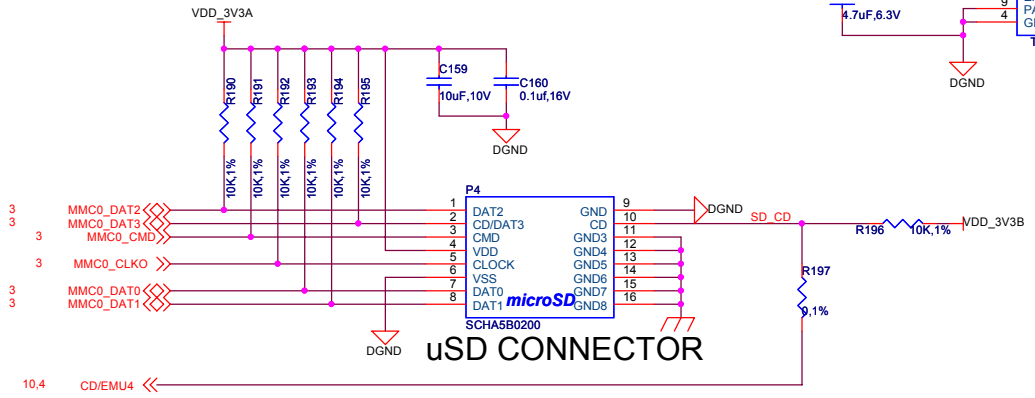

uSD CONNECTOR

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