

BeagleBone LCD3 Cape Rev A2 System Reference Manual

Revision A2
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BEAGLEBONE LCD3 CAPE DESIGN

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NOTES



1.0 Introduction

This document is the System Reference Manual for the BeagleBone LCD3 Cape, an add-on board for the BeagleBone.

This document is intended as a guide to assist anyone purchasing or who are considering purchasing the board to understand the overall design and usage of the BeagleBone LCD3 Cape from the system level perspective.

The design is subject to change without notice as we will work to keep improving the design as the product matures.

The key sections in this document are:

[Section 2.0 – Change History](#)

Provides tracking for the changes made to the System Reference Manual.

[Section 3.0 – Overview](#)

This is a high level overview of the BeagleBone LCD3 Cape.

[Section 4.0 – Features and Specification](#)

Provided here are the features and electrical specifications of the board.

[Section 5.0 – System Architecture and Design](#)

This section provides information on the overall architecture and design of the BeagleBone LCD3 Cape. This is a very detailed section that goes into the design of each circuit on the board.

[Section 6.0 – Mechanical](#)

Information is provided here on the dimensions of the BeagleBone LCD3 Cape.

[Section 7.0 – Design Materials](#)

This section provides information on where to get the design files.

2.0 Change History

2.1 Change History

Table 1 tracks the changes made for each revision of this document.

Table 1. Change History

Rev	Changes	Date	By
A1	Initial release.	06/20/2012	BBT
A2	1. Add A1 vs. A2 (section 2.2)	10/17/2012	BBT

2.2 A1 vs. A2

Stand-alone backlight circuit for LCD panel is added. EHRPWM1A signal (J2.14) is now used to control the backlight circuit. User LED control signal is changed to GPIO3_19 (J2.27). The Enter button is mapped to GPIO0_3 (J2.21).

3.0 BeagleBone LCD3 Cape Overview

3.1 Descriptions

The BeagleBone LCD3 Cape provides a portable LCD solution with touchscreen capability for BeagleBone boards. The 3.5" TFT LCD screen, attached to the topside of the board, can display up to a resolution of 320x240 and is a 4-wire resistive touchscreen. The board is equipped with seven switches located at finger-friendly positions. These switches include power, reset, and five user switches, which are mapped to different GPIO signals. The backside of the Cape is where a BeagleBone can be mounted.

Figure 1 below is a picture of the board.

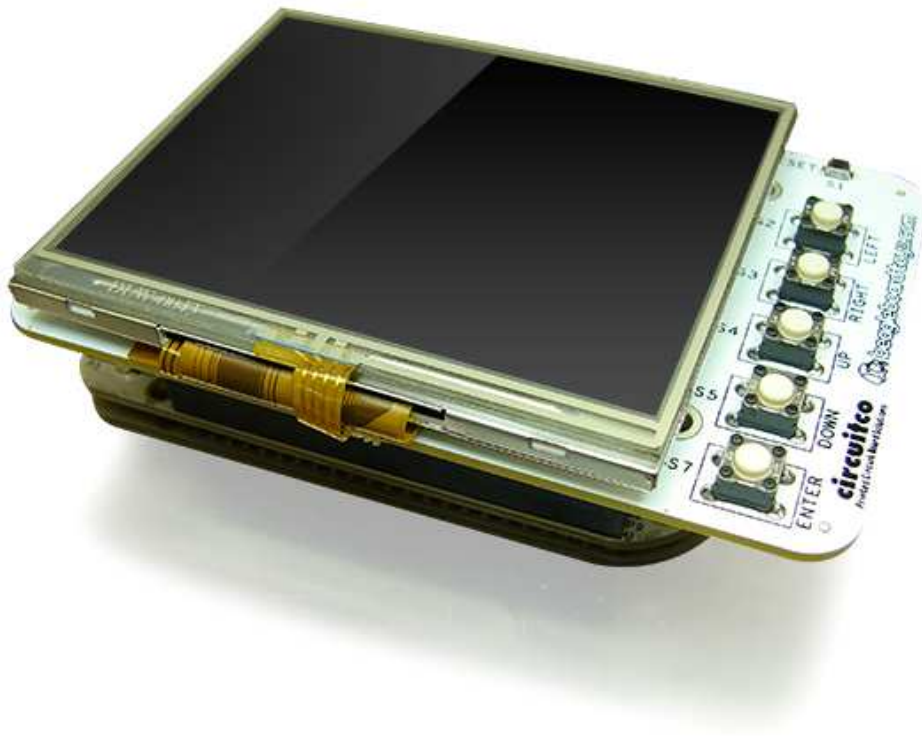


Figure 1. The BeagleBone LCD3 Cape

3.2 In The Box

The final packaged BeagleBone LCD3 Cape will contain the following items:

- 1 BeagleBone LCD3 Cape

3.3 Getting Started

Following the instructions below to start using your BeagleBone LCD3 Cape:

1. Mount the BeagleBone to the connectors on the bottom side of the LCD3 Cape.
2. Make sure the SD card using with BeagleBone has the latest Angstrom image.
3. Plug in a 5V DC power supply to the DC connector on the BeagleBone.
4. Following screens will be displayed on the LCD screen:
 - Angstrom log-in prompt
 - Auto Calibration utility (displays only on the first time)
5. Follow the instructions given by the Auto Calibration utility to calibrate the LCD3 touchscreen.
6. After calibrated, the Angstrom desktop will be displayed on the LCD screen.

You can start using the board.

3.4 Repairs

If you feel the board is in need of repair, follow the RMA Request process found at <http://www.beagleboardtoys.com/support/rma>

Do not send the board in for repair until an RMA authorization has been provided.

Do not return the board to the distributor unless you want to get a refund. You must get authorization from the distributor before returning the board.

4.0 Features and Specifications

This section covers the specifications of the BeagleBone LCD3 Cape and provides a high level description of the major components and interfaces that make up the board.

Table 2 provides a list of the BeagleBone LCD3 Cape's features.

Table 2. BeagleBone LCD3 Cape Features

	Feature	
Display	Elpa Displays S035Q01	
LCD Size	3.5"	
Panel Type	a-Si TFT active matrix	
Resolution	320x240	
Colors	16.7M	
Interface	RGB 16-bit	
Touch Panel	Resistive	
Color Depth	RGB 16-bit	
Power Supply	5V DC via BeagleBone expansion	3.3V via BeagleBone expansion
PCB	4.00" x 2.50"	4 layers
Indicators	Power LED	
	User LED	
EEPROM	Board ID EEPROM	
Connectors	46-position male header	
	20-position male header	



4.1 Key Component Locations

Figure 2 below shows the top side locations of key components on the PCB layout of the BeagleBone LCD3 Cape:

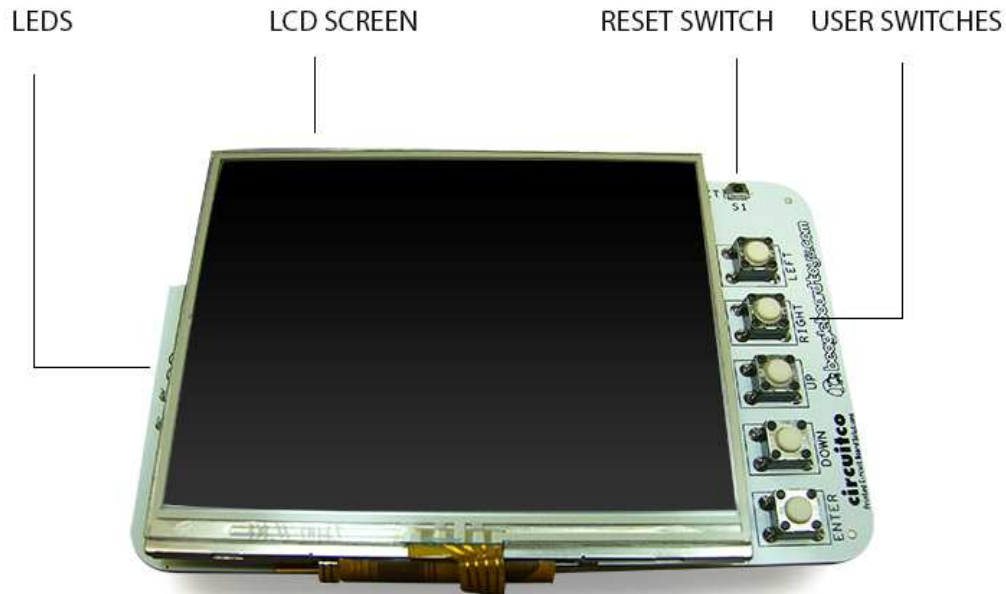


Figure 2. Key Components on top side

Figure 3 below shows key components on the bottom side of the board:

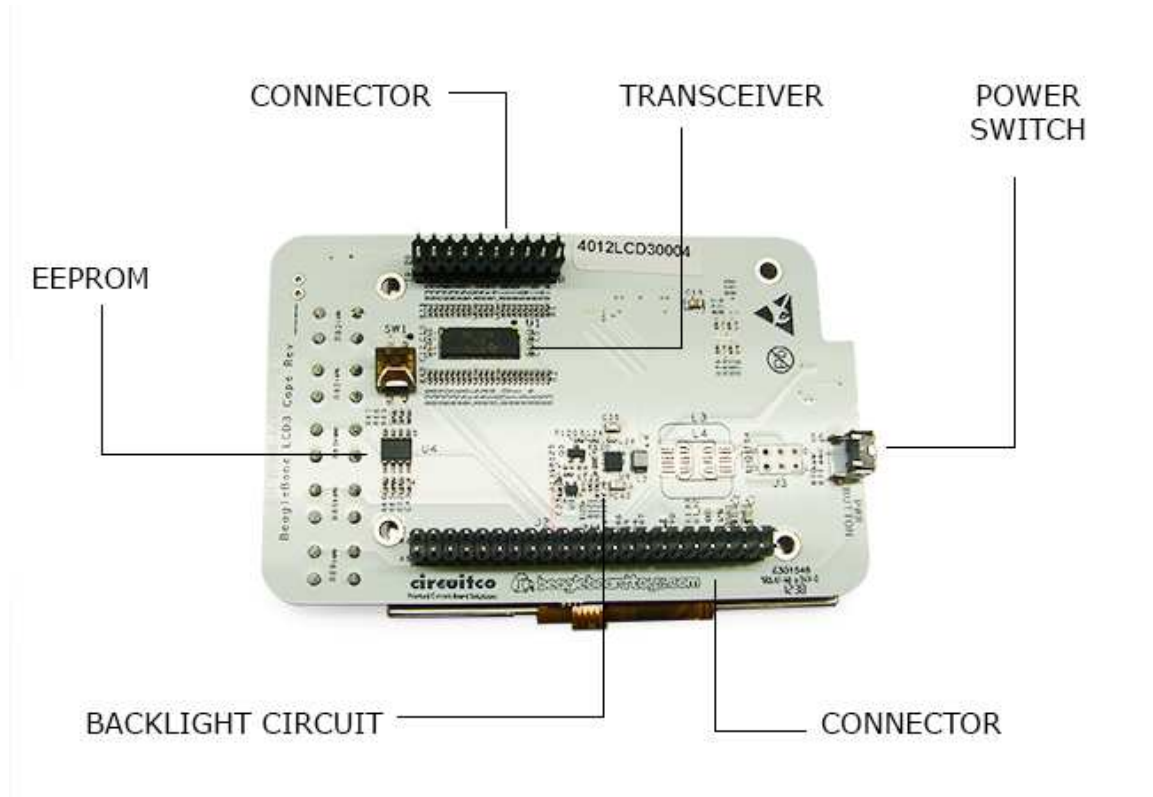


Figure 3. Key Components on bottom side



4.2 LCD Panel

The LCD Panel is supplied by Elpa. The Model number is S035Q01. This display uses amorphous silicon (a-Si) as a switching device. The resolution is 320x240 and it can display up to 16.7M colors.

4.3 Indicators

There is one Power LED and one User LED located on board. Power LED D3 indicates that power is applied to the board. User LED D5 is controlled by GPIO3_19. This signal is exposed as pin 27 on expansion header P9 of BeagleBone. These two LEDs are green when lit and located on top of the Ethernet connector when mounted on a BeagleBone.

4.4 Expansion Header

Since revision A2 there are two male connectors located on the bottom side of the board. The 46-position and 20-position connectors will stack on top of the expansion connectors of BeagleBone. The 6-position connector, which was available in revision A1, is now removed since the backlight of the LCD3 is provided by a stand-alone circuit.

4.5 Switches

The power switch will sit between the Ethernet connector and Power connector when mounted on a BeagleBone. This switch is a through-hole part and is populated at 90 degree to the PCB. The reset switch, on the other hand, is a surface mount part and located on the topside and on the opposite end. The power switch is used to power on or off the board. When the board is powered on, pressing and holding the power switch for 10 seconds will power off the LCD3 Cape. Pressing the power switch again will power on the board. The reset switch is used to reset the board.

Located next to the reset switch on the topside of LCD3 Cape are five user switches. These switches are labeled as Left, Right, Up, Down, and Enter. They are mapped to different GPIO signals, which can be accessed via connector. The reset switch and power switch are connected to the RESET and PWR_BUT signals respectively which can also be accessed at connector J2.

4.6 Mechanical Specifications

Size:	4.00" x 2.50"
Layers:	4
PCB thickness:	.062"
RoHS Compliant:	Yes

4.7 Electrical Specifications

Table 3 is the electrical specification of the external interfaces to the BeagleBone LCD3 panel.

Table 3. BeagleBone LCD3 Electrical Specifications

Specification	Min	Typ	Max	Unit
Power				
Input Voltage DC		3.3		V
Backlight Supply Voltage		5.0		V
Backlight Current Supply		100		mA
Environmental				
Temperature range	0		+85	C

5.0 System Architecture and Design

This section provides a high level description of the design of the BeagleBone LCD3 Cape and its overall architecture.

5.1 System Block Diagram

Figure 4 is the high level block diagram of the BeagleBone LCD3 Cape.

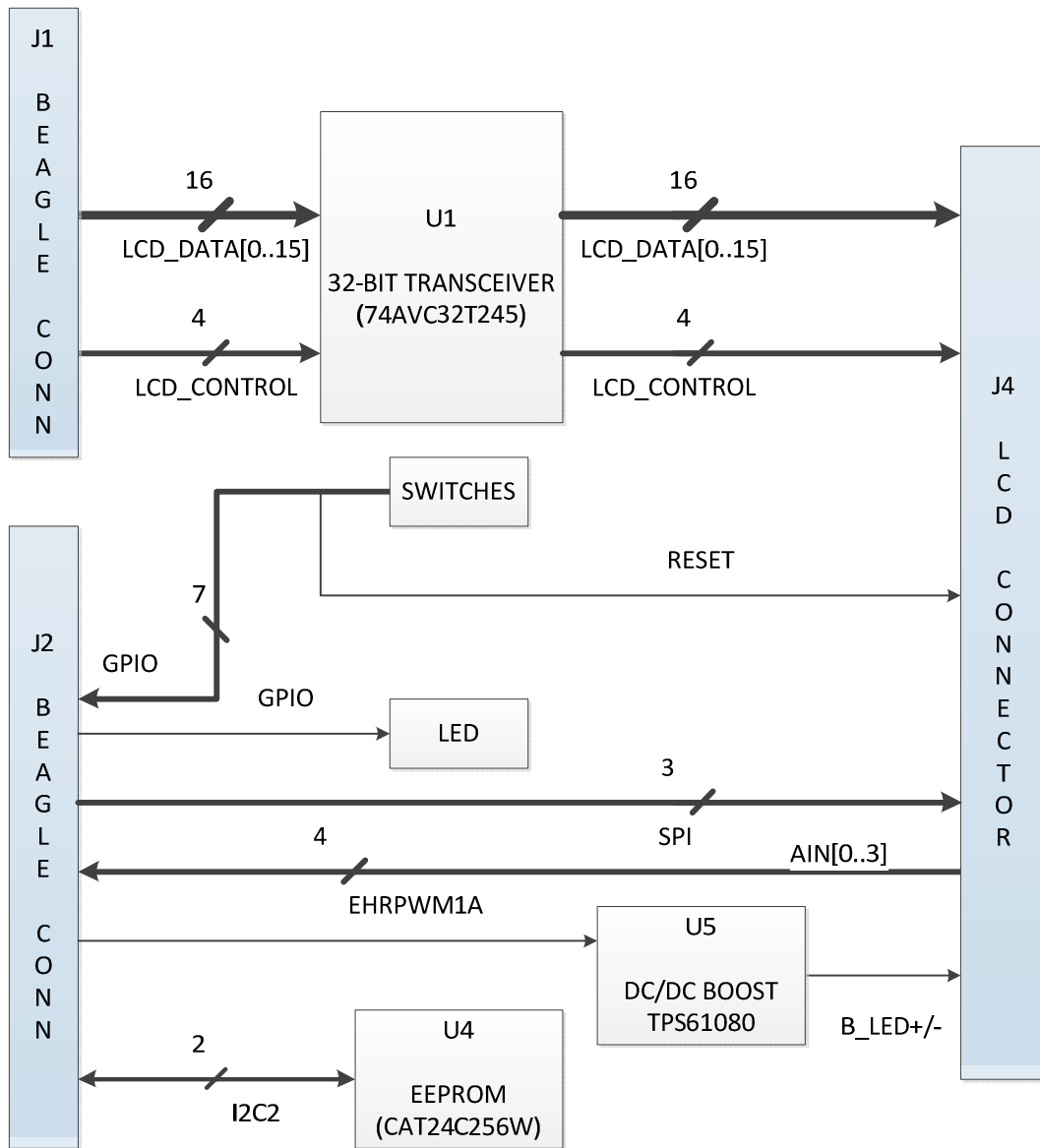


Figure 4. BeagleBone LCD3 Cape High Level Block Diagram

5.2 LCD Interface

5.2.1 Expansion connectors

The LCD interface of the BeagleBone LCD3 Cape uses 16 data and 4 control signals from the BeagleBone's expansion connectors. By using only 16 signals for LCD_DATA, more pins can be available at the expansion header for other capes. The quality of 16-bit LCD output, nonetheless, is very similar to 24-bit. The 4 control signals are horizontal sync (LCD_HSYNC), vertical sync (LCD_VSYNC), enable input (LCD_EN), and the pixel clock (LCD_PCLK). **Figure 5** shows these LCD signals at expansion connector J1.

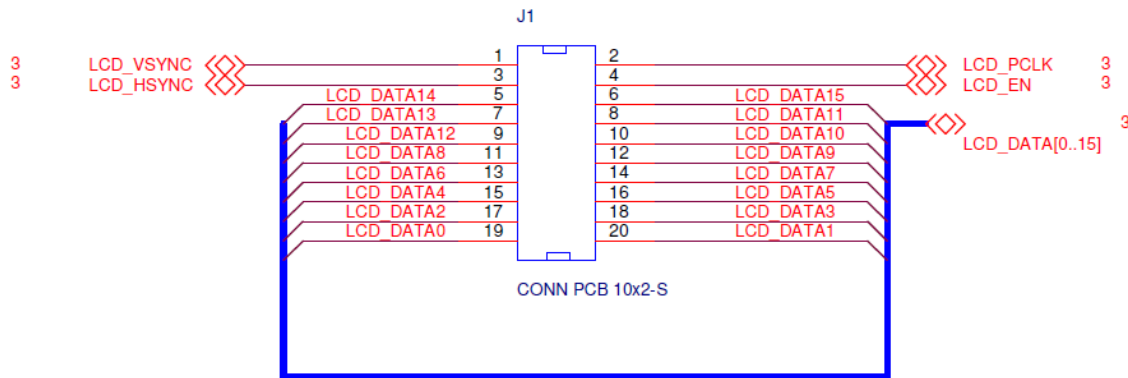


Figure 5. LCD Signals at expansion connector J1

5.2.2 Non-Inverting Bus Transceiver

The LCD signals are buffered through U1 (74AVC32T245), which is a 32-bit dual-supply bus transceiver. 74AVC32T245 features two ports, A and B. Each port tracks a separate power-supply rail. This allows for universal low-voltage translation. The output-enable (OE) input specifies whether the buses are isolated. The direction-control input, on the other hand, determines the data transmission direction. On the BeagleBone LCD3 Cape, no voltage translation required since its two power rails are both 3.3V. Both the direction-control and output-enable inputs are set low, so the data are transmitted from B to A before going to LCD connector J4. **Figure 6** shows the LCD signals are buffered through U1.

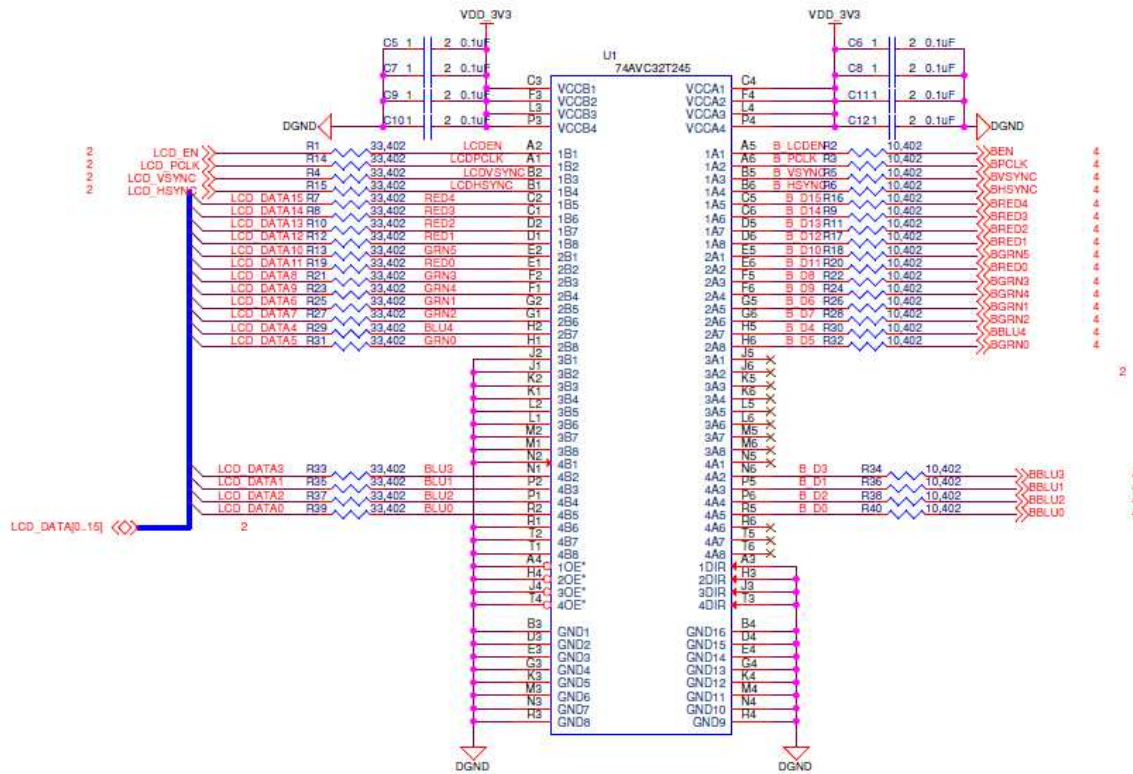


Figure 6. LCD Signals buffered through U1

5.2.3 Touchscreen Interface

The S035Q01 touchscreen is resistive and composed of 4 different X or Y signals. These signals are connected to the analog inputs AIN0 to AIN4, which can be accessed via expansion connector J2. Both touchscreen and LCD flex cables share the same connector J4.

5.3 LCD Power Supply

The Elpa S035Q01 is powered by 3.3V from the expansion connector of BeagleBone. This power is required for logic operation of the LCD module. The backlight of S035Q01 is composed of 6 LED's that are in series. The forward current for each LED is 20mA and can go up to 25mA. The backlight circuit of LCD3 Cape has been changed since revision A2. LCD3 Cape now has its own stand-alone backlight circuit instead of using the BeagleBone PMIC backlight expansion connector J3. As a result, J3 is no longer populated.

The backlight of the LCD panel is powered by a High Voltage DC/DC Boost Converter TPS61080. This boost converter can operate from input supply of 5V and provide output voltage up to 27V. This IC features an input/output isolation which prevents any output leakage under shutdown, avoids inrush current during start up or unlimited short current. The enable (EN) pin is pulled high and the backlight current is controlled by the PWM signal EHRPWM1A which is exposed at pin 14 of expansion connector J2. TPS61080 uses a current mode control with constant PWM. The switching frequency is configured to be 1.2MHz by connecting FSW pin to a logic high. TPS61080 utilizes an external inductor to store and transfer energy to the output capacitor which is connected to the Anode terminal of the LCD backlight. The Cathode terminal is connected to the feedback (FB) pin of the device. A current sense resistor is connected to the FB pin for a constant current output.

Figure 7 shows the backlight circuit of the BeagleBone LCD3 Cape:

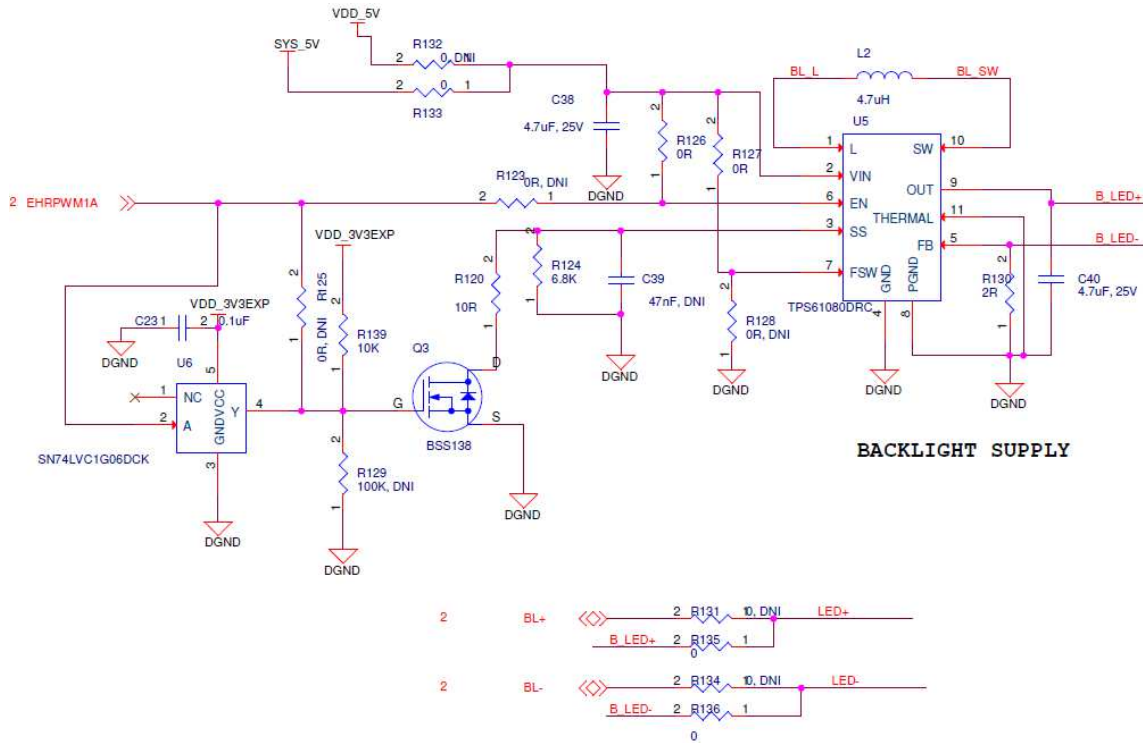


Figure 7. BeagleBone LCD3 Backlight Circuit

5.4 Serial Port Interface

The LCD panel of the BeagleBone LCD3 Cape features NT39016D as the driver IC. This IC uses the 3-wire serial port as communication interface for all function and parameter setting. The serial port interface is not populated in this revision since no parameter changes are necessary for the Elpa S035Q01 panel.

5.5 EEPROM

The BeagleBone LCD3 Cape has an EEPROM containing information that will allow the SW to identify the board and to configure the expansion headers pins as needed. EEPROMs are required for all Capes sold in order for them to operate correctly when plugged in the BeagleBone.

The EEPROM used on this cape is the same one as is used on the BeagleBone, a CAT24C256. The CAT24C256 is a 256 kb Serial CMOS EEPROM, internally organized

as 32,768 words of 8 bits each. It features a 64-byte page write buffer and supports the Standard (100 kHz), Fast (400 kHz) and Fast-Plus (1 MHz) I2C protocol. **Figure 8** is the design of the EEPROM circuit.

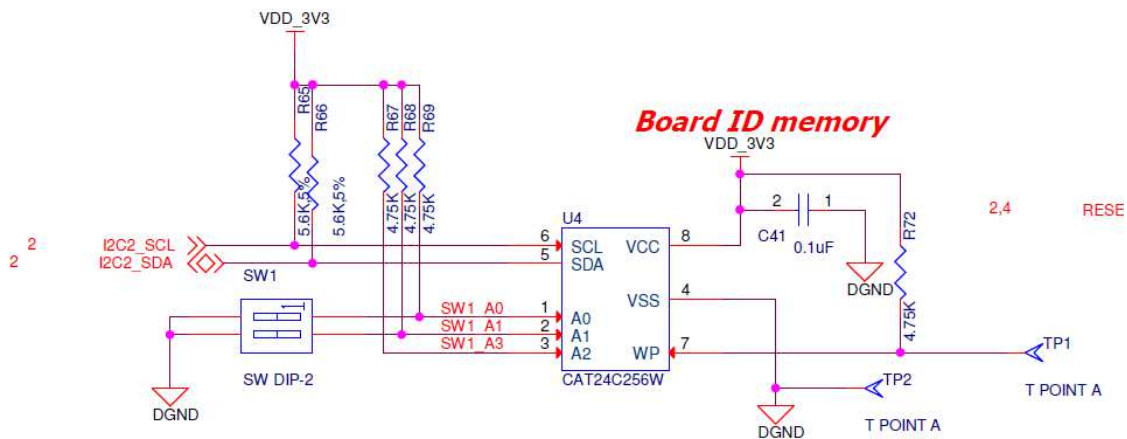


Figure 8. BeagleBone LCD3 Cape EEPROM

5.5.1 EEPROM Address

In order for each Cape to have a unique address, a board ID scheme is used that sets the address to be different depending on the order in which it is stacked onto the main board. A two position dipswitch or jumpers is used to set the address pins of the EEPROM. It is the responsibility of user to set the proper address for each board. Address line A2 is always tied high. This sets the allowable address range for the expansion cards to 0x54 to 0x57. All other I2C addresses can be used by the user in the design of their Capes. But, these addresses must not be used other than for the board EEPROM information.

5.5.2 I2C Bus

The EEPROMs on each expansion board is connected to I2C2. For this reason I2C2 must always be left connected and should not be changed by SW to remove it from the expansion header pin mux. The I2C signals require pull-up resistors. Each board must have a 5.6K resistor on these signals. With four resistors this will be an effective resistance of 1.4K if all Capes were installed.

6.0 Mechanical Information

This section provides information on the mechanical aspect of the BeagleBone LCD3 Cape. **Figure 9** is the dimensions of the BeagleBone LCD3 Cape.

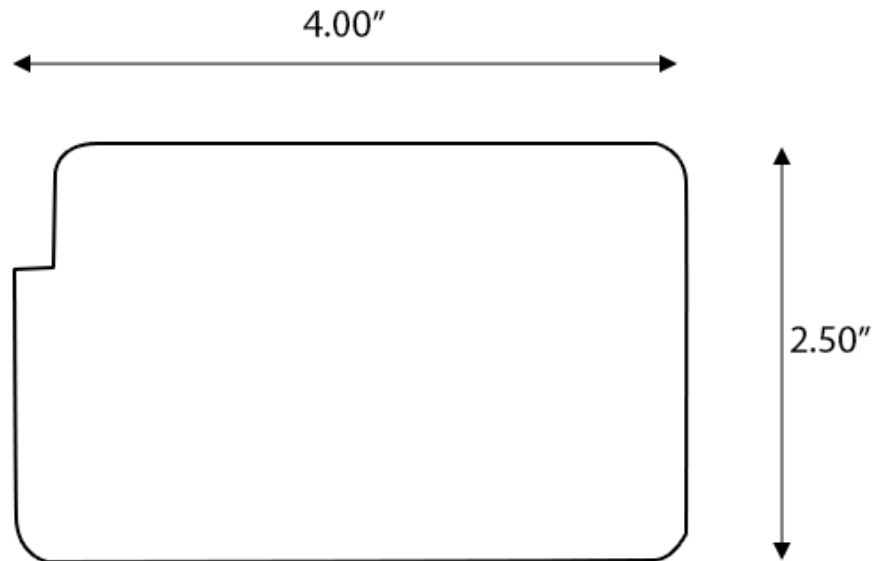


Figure 9. BeagleBone LCD3 Cape Dimensions Drawing



7.0 Design Materials

Design information can be found at BeagleBoardToys wiki:

http://beagleboardtoys.com/wiki/index.php?title=BeagleBone_LCD3

Provided there is:

- Schematic in PDF
- Schematic in OrCAD
- Manufacturing files
 - o PCB Gerber
 - o PCB Layout (Allegro)
- Bill of Materials
- System Reference Manual (This document)

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