Training Notification Form, IIT Delhi

Company Overview

Name: ANALOG DEVICES

Website: www.analog.com

Company Type: Core (Technical)

Description: Analog Devices is a global leader in the design and manufacturing of integrated circuits to help solve the toughest engineering challenges. Analog Devices enables our customers to interpret the world around us by intelligently bridging the physical and digital with unmatched technologies that sense, measure and connect. We create innovative solutions to solve design challenges in instrumentation, automation, communications, healthcare, automotive and numerous other industries

Project Details

Designation: Digital Design Intern

Type: Core (Technical)

Location: Bengaluru

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Analog Devices Inc (ADI) is looking for Digital IC Design and Verification Engineers for its Chip Design and development team in Bangalore, India.
The selected students will work on chip development based on ultra-deep submicron semiconductor process technologies. They will be guided and trained by ADI's experienced design and verification engineers.
This will include
• Development of key digital blocks and SoCs.
• Development of optimal micro-architecture and design by analyzing power, performance and area tradeoffs.
• Verification of key digital blocks and SoCs in High Speed signal conversion/communication products.
• Definition of testplan, tests and verification methodology for block and chip-level verification.
• They will use industry leading CAD tools.
Essential:
1. Knowledge of Verilog
2. Knowledge of digital circuit design – combination and sequential
3. Hands on any scripting languages such as perl/phyton
4. Good Analytical skills
5. Should be able to work independently and proactive
Additional Skills being looked for:

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1. System Verilog
2. Any projects on FPGA/matlab would benefit
3. Understanding of signal processing/filtering background
4. Good understanding of ADCs/DACs
5. Basic understanding of pipe lining, 8085/any-other-assembly language

Stipend Details

Stipend:	50,000 INR Per Month
Accommodation:	No
Travel Expenses:	Yes
Perks / Bonus:	Food allowance 1700

Selection Process

Resume Shortlist:	Yes
Written Test:	No
Online Test:	Yes
Group Discussion:	No
Personal Interview:	Yes
No. of Offers:	4

Eligibility

Diversity Recruiting:	No
Eligible Years:	Graduating in 2025 (Pre-Final Year Students) - B.Tech / Dual / Master's
Eligible Departments:	B.Tech in Electrical Engineering, B.Tech in Electrical Engineering (Power and Automation)