Training Notification Form, IIT Delhi

Company Overview

Name:	Graviton Research Capital
Website:	https://www.gravitontrading.com/
Company Type:	Other (High frequency Trading)
Description:	Graviton is a high frequency trading firm with a presence in international markets. We operate across all asset classes through the use of in-house trade execution strategies. We have a team with an enviable track record managing high volumes across various exchanges

Project Details

Designation:	Intern - Software Engineer, Ultra Low Latency
Type:	Core (Technical)
Location:	Gurgaon
Project Details:	Description: Graviton, a privately funded quantitative trading firm, is striving for brilliance in building cutting-edge trading systems and excellence in financial markets research. Team Facula at Graviton comprises some of the finest programmers in India, dedicated to working on cutting-edge technologies, primarily focused on designing and managing ultra-low latency trading systems capable of competing at a global scale. These systems operate at nanoseconds scale and handle millions of dollars' worth of trading transactions on a daily basis with unwavering robustness. After years of intentionally keeping a low profile, the team is now offering an opportunity to self-driven, innovative, and fearless minds at your campus

As a Software Engineer - Ultra Low Latency with Graviton your responsibilities will include:
Designing and implementing ultra low latency high-frequency trading systems
Building live reporting and administration tools for the trading systems
Scaling to new markets, optimizing and improving the overall latency of systems, through algorithm research and using cutting edge tools and techniques
End-to-end ownership of modules, including design, development, deployment and support

Requirements : The ideal requirements for our candidates are:
Pursuing Bachelor's degree in Electronics/Computer Science Engineering from top IIT's
Skilled with Python, C/C++ and object-oriented programming
Basic Knowledge of Digital Logic Design
Introductory level skills in VHDL/Verilog would be a plus
Good knowledge of algorithms and data structures
Excellent problem solving skills
Knowledge of Linux systems
Strong communication skills and collaborative nature

Stipend Details

Stipend: 450,000 INR Per Month

Accommodation: Yes

Travel Expenses: Yes

Selection Process

Resume Shortlist: No

Written Test: No

Online Test: Yes

Group Discussion: No

Personal Interview: Yes

No. of Offers: 1

Eligibility

Diversity Recruiting: No

Eligible Years: Graduating in 2025 (Pre-Final Year Students) - B.Tech / Dual / Master's

Eligible Departments: B.Tech in Biochemical Engineering & Biotechnology, B.Tech in Chemical Engineering, B.Tech in Civil Engineering, B.Tech in Computer Science & Engineering, B.Tech in Electrical Engineering, B.Tech in Electrical Engineering (Power and Automation), B.Tech in Energy Engineering, B.Tech in Engineering Physics, B.Tech in Engineering and Computational Mechanics, B.Tech in Materials Engineering, B.Tech in Mathematics & Computing, B.Tech in Mechanical Engineering, B.Tech in Production & Industrial Engineering, B.Tech in Textile Engineering, B.Tech and M.Tech in Chemical Engineering, B.Tech and M.Tech in Computer Science & Engineering, B.Tech and M.Tech in Mathematics & Computing