

Training Notification Form, IIT Delhi

Company Overview

Name:	KLA
Website:	www.kla.com
Company Type:	Information Technology
Description:	KLA Corporation, KLAC (NASDAQ) - a leading provider of process control and yield management solutions, partners with customers around the world to develop state-of-the-art inspection and metrology technologies. These technologies serve the semiconductor, data storage, LED, and other related nano electronics industries. With a portfolio of industry-standard products and a team of world-class engineers and scientists, the company has created superior solutions for its customers for over 40 years. Headquartered in Milpitas, California, KLA has dedicated customer operations and service centres around the world. Additional information may be found at www.kla-tencor.com

Project Details

Designation:	AI/ML Intern
Type:	Information Technology
Location:	Chennai
Project Details:	<p>Internship Opportunity @ KLA Tencor - Machine Learning Group</p> <p>KLA-Tencor Corporation, KLAC (NASDAQ) - a leading provider of process control and yield management solutions, partners with customers around the world to develop state-of-the-art inspection and metrology technologies. These technologies serve the semiconductor, data storage, LED, and other related nano electronics industries. With a portfolio of industry-standard products and a team of world-class engineers and scientists, the company has created superior solutions for its customers for over 40 years. Headquartered in Milpitas, California, KLA-Tencor has dedicated customer operations and service centres around the world. Additional information may be found at www.kla-tencor.com</p> <p>Who are we? We research, develop, and manufacture the world's most advanced inspection and measurement equipment for the Semiconductor and Nano electronics Industries. We enable the digital age by pushing the boundaries of technology, creating tools capable of finding defects smaller than a wavelength of visible light. We create smarter processes so that technology leaders can manufacture high-performance chips—the kind in that phone in your pocket, the tablet on your desk and nearly every electronic device you own—faster and better. We're passionate</p>

and making every electronic device you own faster and better. We're passionate about creating solutions that drive progress and help people do what wouldn't be possible without us. The future is calling. Will you answer?

Group/Division

The Wafer Inspection Division (WIN) is the world leader in the design and manufacture of advanced optical inspection tools for inline monitoring of process defects in advanced Semiconductor factories

Intern Roles and Responsibilities

You will be part of a machine learning group that develops algorithms and software solutions that enhance the performance of our inspection tools.

Your responsibilities will include:

- Data/problem analysis
- Research the solution space
- Develop concepts
- Prototype and validate concepts
- Specify software requirements
- Software/algorithm development (productization/programming)

Preferred Qualifications

- Research/scientific background is desirable
- Knowledge and understanding of modern machine learning approaches is highly desirable
- Solid background in statistics and mathematics is desirable
- Experience with image processing / signal processing is a plus
- Experience with heterogenous parallel computing (including GPU) is a plus
- Good demonstrated programming skills are desirable
- Analytical skills, problem-solving skills (quantitative & algorithmic)
- Interest in algorithm development & programming
- Ability to work in a team, and contribute to the team effort
- Proficiency in C++, Python and DL frameworks

Minimum Qualifications

Bachelor's or Master's level degree in Computer Science, AI, EE, Data Science, Engineering Science

Intern Stipend: INR 2,00,000 per month

Work Location: Chennai

Stipend Details

Stipend:	200,000 INR Per Month
Accommodation:	Yes
Travel Expenses:	Yes

Selection Process

Resume Shortlist:	No
Written Test:	No
Online Test:	Yes
Group	No

Discussion:

Personal Interview: Yes

No. of Offers: 5

Eligibility

Diversity Recruiting: No

Eligible Years: Graduating in 2025 (Pre-Final Year Students) - B.Tech / Dual / Master's

Eligible Departments: B.Tech in Computer Science & Engineering, B.Tech in Electrical Engineering, B.Tech in Electrical Engineering (Power and Automation), B.Tech in Mathematics & Computing, B.Tech and M.Tech in Computer Science & Engineering, B.Tech and M.Tech in Mathematics & Computing