

Started on Friday, 1 October 2021, 10:05 AM

State Finished

Completed on Friday, 1 October 2021, 10:09 AM

Time taken 4 mins 55 secs

Marks 1.00/1.00

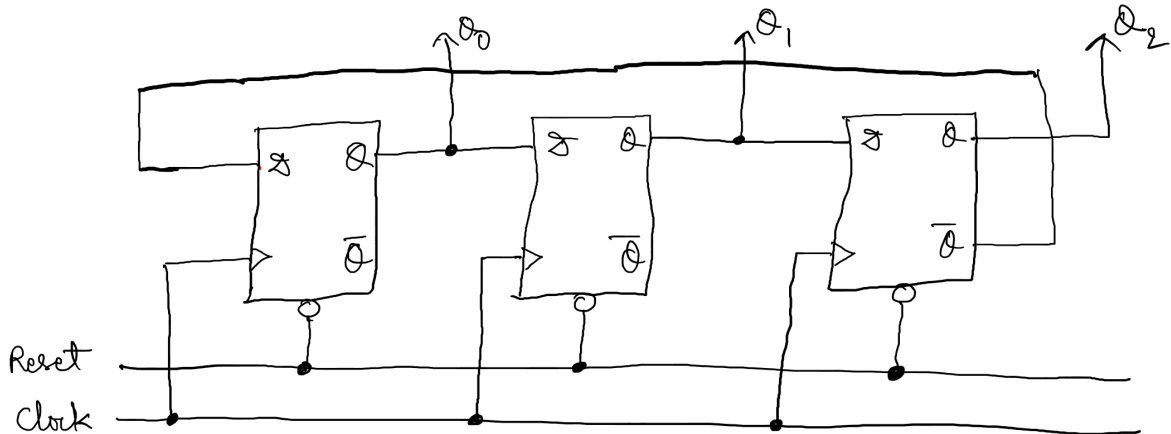
Grade 10.00 out of 10.00 (100%)

Question 1

Correct

Mark 1.00 out of 1.00

In the following circuit, three positive edge-triggered D flip-flops are used to implement a code given by $Q_0Q_1Q_2$. At the rising edge of the first clock cycle, the inputs to all flip-flops are reset to 0. Which of the following represents the correct code sequence during the third, fourth, fifth, and sixth cycles.



Select one:

- ☐ None of the given options
- ☐ 100, 101, 111, 011
- ☒ 110, 111, 011, 001 ✓
- ☐ 011, 111, 101, 100
- ☐ 000, 001, 010, 100

Your answer is correct.

The correct answer is: 110, 111, 011, 001