

Training Notification Form, IIT Delhi

Company Overview

Name: Intel

Website: <https://www.intel.in/content/www/in/en/homepage.html>

Company Type: Core (Technical)

Description: Semicon Industry

Project Details

Designation: Hardware/Software Engineering Intern

Type: Core (Technical)

Location: Bangalore/Hyderabad

Project Details: Job Description

We have roles in the digital and analog domains wherein you will be:
Working towards definition, design, verification, and documentation for SoC (System on a Chip) development
Determining architecture design, logic design, and system simulation
Performing all aspects of SoC design flow—from high-level design to synthesis, place and route, timing, and power—to create a design database that is ready for manufacturing
Translating SoC requirements to analog circuit specifications, architecting and designing analog IPs: ADC/DAC, LDOs, PLLs, temperature sensors, etc.
Designing innovative analog and mixed signal circuits and contributing to delivering analog IPs in advanced Intel process nodes
Collaborating with the SoC team for IP integration, silicon tests, etc.
Developing and supporting OFT strategy and implementation
Collaborating with the bench validation team for silicon validation, debugging, etc.

Hardware Engineering Intern

Qualifications

Any candidate for a temporary full-time internship position must be:

Currently enrolled in a relevant Bachelor's/Master's degree from a reputed academic institute

Available for a 1-year internship (course completion by June/July 2024)

Willing to ramp-up quickly on required areas like Microelectronics, RTL, verification/validation, structural design/physical design, analog, and so on

Job Description

We have roles in Embedded, Wireless, AI/ML, Computer Science, Graphics and Firmware wherein you will be:

Conducting or participating in multidisciplinary research, and collaborating with design, layout and/or hardware engineers in the design, development, and utilization of productivity enhancement layout tools and design rule checkers, as well as electronic data processing systems software

Determining computer user needs, advising hardware designers on machine characteristics that affect software systems such as storage capacity, processing

characteristics that affect software systems such as storage capacity, processing speed, and input/output requirements, designing and developing compilers and assemblers, utility programs, and operating systems
Responding to customer/client requests as they occur.
Developing solutions to problems utilizing requisite software-specific skillsets

Software Engineer Intern

Required Skillsets and Qualities

Strong programming knowledge in either Assembly/C/C++/Embedded/Firmware Scripting: Preferably Python

Sound logical reasoning and good aptitude

Interest in hardware setups and debugging

Basics of wireless communication systems

Understanding of Physical, MAC and Network layer in any one of the wireless standards: Wi-Fi/Bluetooth/GNSS or 3GPP - 2G/3G/4G/5G

Basic knowledge of embedded and real-time systems: interrupts, interrupt handling, timers, interfacing with hardware and so on

Qualifications

Any candidate for this temporary full-time internship position must be:

Currently enrolled in a relevant Bachelor's/Master's degree from a reputed academic institute

Available for a 1-year internship (course completion by June/July 2024) Willing to ramp-up quickly in areas like computer science, embedded, firmware

Capable of developing expertise in required programming languages such as Assembly, C, C++ and so on

Stipend Details

Stipend:	45,000 INR Per Month
Accommodation:	No
Travel Expenses:	No
Perks / Bonus:	One-time Payment and relocation as per policy

Selection Process

Resume Shortlist:	Yes
Written Test:	No
Online Test:	No
Group Discussion:	No
Personal Interview:	Yes
No. of Offers:	10

Eligibility

Diversity Recruiting:	No
Eligible Years:	Graduating in 2025 (Pre-Final Year Students) - B.Tech / Dual / Master's
Eligible Departments:	B.Tech and M.Tech in Computer Science & Engineering, B.Tech and M.Tech in Mathematics & Computing, Master of Design in Industrial Design