Training Notification Form, IIT Delhi

Company Overview

Name:	Sony Japan
Website:	https://www.sony.com/en/
Company Type:	Information Technology

Description: Sony was established by founders who shared a dream and a strong will to enrich people's lives through the power of technology. From our founders' dreams and aspirations, Sony has grown over the years connecting more people with their dreams.
Sony runs a diverse portfolio of businesses from Electronic Business to Music, Movies, and Entertainment, all of which center around "People".
Talents from all over the world have been playing critical roles to make this happen, and will continue to in light of our journey ahead.
We are excited to continue trailblazing with a globally oriented mindset, and we ask that you consider joining us on our journey ahead!

Project Details

Designation:	TNF 1: Visual Recognition Computation Systems and Architecture (Research Engineer)
Type:	Information Technology
Location:	Tokyo, Japan

Project Details:
[Technology Field]
System Architecture / Processor
Computer Vision

[Position Summary]
Contemporary products and services must be "intelligent" and to accomplish this, they must include devices that are able to perceive surroundings at an increasingly accurate rate. As such, development of these products and services involves the incorporation of various sensors, such as visual image sensors, to understand the surrounding environment, people, obstacles, and so forth. Sony thrives in this area of development, as we have a very strong position in image sensors, and have created vision-processing SoCs.

Our team is working with production teams for these future products, as well as core research engineer teams in the machine vision domain, focusing on areas such as computational systems and system architecture.

[Responsibilities]
- Develop low latency/high bandwidth/high performance per watt computation systems on edge computers.
- Collaboratively work with related teams responsible for wireless communications and cloud-side computations.
- Port Sony's in-house algorithms to build assets on Sony's internal hardware.
- Design and propose software systems for future products and services with Sony's technical assets, and help realize them as PoCs or business units.

[Required qualifications]
- Bachelor's Degree (graduated or currently pursuing) in computer science, a related technical field, OR equivalent practical experience.
- 5+ years of experience with one or more general purpose programming languages.
- Familiarity working with, debugging, and troubleshooting software systems.
- A proactive mindset, both in terms of working on current projects and proposing new feature ideas for actual implementation.
- Communication and Visualization: Excellent written and verbal communication skills, with the ability to articulate optimization proposals clearly and concisely.
- Continuous Improvement Mindset: A commitment to continuous learning and keeping up-to-date with industry trends, emerging technologies, and best practices in system optimization.

[Preferred qualifications]
- Master's Degree (graduated or currently pursuing) in computer science.
- Hands-on experience with performance optimization (CPU, GPU, DSP, memory).
- Hands-on experience with implementing and fixing low-layer bugs (low layer = operating system Kernel/its driver/compiler).
- Solid experience with 5+ programming languages, including scripting languages (it is best to also have experience with assembly language).
- Experience working with at least two of the following: operating systems, programming language implementation, robotics SW (either algorithmic or operating systems), visual algorithms, and machine learning.

[Product, Service]
Robotics, games (e.g. PlayStation), entertainment, cameras, and other cloud-based services that include Sony's new business domains.

[Development Environment]
Linux PC and Embedded Linux.

Stipend Details

Stipend:	10,000 JPY per Working Day
Accommodation:	Yes
Travel Expenses:	Yes
Perks / Bonus:	

[Stipend]
Bachelor: JPY 10,000/ working day (Net)
Master: JPY 11,000/ working day (Net)

Your stipend will be calculated based on the working day.

Example: If there are 22 working days in a month, net stipend in a month is
Bachelor JPY 220,000 / month (Net)
Master JPY 242,000 / month (Net)

[Other Benefits/Support]
Single rental apartment/hotel with Wifi, Visa, flight, commuting fee, international travel insurance all provided by Sony.

Selection Process

Resume Shortlist:	Yes
Written Test:	No
Online Test:	Yes
Group Discussion:	No
Personal Interview:	Yes
No. of Offers:	2
Selection Process:	* To submit CV& Essay and take Coding Test(Python) is compulsory for this position. Essay format will be distributed through placement office. Please follow the instruction given by the placement office.

Eligibility

Diversity Recruiting:	No
Eligible Years:	Graduating in 2025 (Pre-Final Year Students) - B.Tech / Dual / Master's
Eligible Departments:	B.Tech in Biochemical Engineering & Biotechnology, B.Tech in Chemical Engineering, B.Tech in Civil Engineering, B.Tech in Computer Science & Engineering, B.Tech in Electrical Engineering, B.Tech in Electrical Engineering (Power and Automation), B.Tech in Energy Engineering, B.Tech in Engineering Physics, B.Tech in Engineering and Computational Mechanics, B.Tech in Materials Engineering, B.Tech in Mathematics & Computing, B.Tech in Mechanical Engineering, B.Tech in Production & Industrial Engineering, B.Tech in Textile Engineering, B.Tech and M.Tech in Chemical Engineering, B.Tech and M.Tech in Computer Science & Engineering, B.Tech and M.Tech in Mathematics & Computing, M.Sc in Chemistry, M.Sc in Cognitive Science, M.Sc in Economics, M.Sc in Mathematics, M.Sc in Physics, Master of Design in Industrial Design