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Marks	0.00/1.00
Grade	0.00 out of 10.00 (0%)

Question 1

Incorrect
Mark 0.00 out of
1.00

Which of the following statements are true?

Select one or more:

- ☐ The size of the configuration bit-file is independent of the size of the design.
- ☐ Typical FPGA implementations are faster than equivalent ASIC implementations.
- ☒ Typically, in an FPGA, the configuration defined by bit-file cannot be changed at run-time by the design that is being executed. ✓
- ☒ A typical FPGA device provides only one type of interconnect to provide maximum flexibility in mapping of interconnections. ✗

Your answer is incorrect.

The correct answers are:

The size of the configuration bit-file is independent of the size of the design.,

Typically, in an FPGA, the configuration defined by bit-file cannot be changed at run-time by the design that is being executed.