

Lecture 6: The RAM Model

*Harvard SEAS - Fall 2022**Sept. 20, 2022*

1 Announcements

Recommended Reading:

- CLRS Sec 2.2
- **Thursday class 9-10:15am in Cambridge, Emerson 105**
- Adam OH today after class.
- PS2 due tomorrow, Participation Highlights 1 due Sat, PS0 revisions due Sun.
- Fill out reflection survey after SRE.
- Remember to bring/use name placards

PS0+PS1 Survey feedback:

- Time spent on psets is too long!
 1. PS0 median time 10hrs (cf. 14hrs in 2021), 25% spent 15+hrs (cf. 18+hrs in 2021).
 2. PS1 median time 12hrs (cf. 14hrs in 2021), 25% spent 20+hrs (cf. 19+hrs in 2021).
 3. Stop if you are spending so long! Remember revision policy, and up to 4 R- grades on psets is still in A-range.
 4. It will get easier! Last year Q eval: median time 10hrs/week, 25% spent 14+hrs/week.
 5. Look through entire pset before beginning
 6. Google sheet for finding pset buddies
 7. Pset due dates: can't change at this point, Fri-Fri or overlapping cycles bring other complications (weekend sections, same due date as other courses, scheduling of section and OH).
- Office hours: not enough and too crowded
 1. We were understaffed after the class tripled in size.
 2. We have hired a bunch of new part-time CAs for grading and OH!
- Lectures
 1. Mixed calls for more proof details, more examples, more big picture. We will try to strike a balance! One of the most important roles for lecture in cs120 is to introduce you to the mathematical abstractions. Section is a good place for more examples and going over proof details.

2. Pacing uneven and a bit fast. Will try to smooth it out, need to balance against requests for lecture to do more.
3. Writing. Legibility better in person. Still bad on Zoom/Panopto; can't do much about that.

Benji: maybe also add Zoom/Panopto are backups for the in-person instruction mode

4. Lecture Notes. We can post *drafts* of the detailed lecture notes on the course schedule in advance, but (a) strongly encourage you to work from the un-detailed ones during class in order to be actively engaged, and (b) note that the detailed lecture notes will usually be updated with corrections and more details after class.

5. In-class activities (e.g. edstem quizzes) appreciated.

- Sections: very positive feedback. Attendance encouraged!

2 Goals

So far, our conception of an algorithm has been informal: “a well-defined procedure for transforming inputs to outputs” whose runtime is measured as “basic operations” performed on a given input. This is unsatisfactory: how can we identify the fastest algorithm to solve a given problem if we don't have agreement on what counts as an algorithm or a basic operation? To address this, we need to specify a *computational model* for describing algorithms.

What do we want from a computational model?

- Unambiguity. Precisely defines what is and isn't an algorithm, and what counts or doesn't count as a basic operation.
- Expressivity. Everything we intuitively consider to be an algorithm should be expressible, or we should have a good reason for excluding it.
- Mathematical simplicity. We want something that is manageable to reason about. In contrast, the specification of most modern programming languages is too long and cumbersome to prove anything general by hand.¹ For example, the Python Language Reference (Release 3.4.3), which “describes the syntax and ‘core semantics’ of the language” (not including libraries) is 135 pages long.
- Robustness. Small changes to the model should not fundamentally change what can or cannot be computed, and should have only a small effect on efficiency.
- Technological relevance. The model should capture reasonably well the implementability and efficiency of algorithms on actual computing hardware, even as technology evolves with time.

¹As we'll discuss at the end of the course, there are software tools that assist in proving properties of programs. However, to make effective use of these tools you need to have experience in proving things about programs yourself!

3 The RAM Model

Our first attempt at a precise model of computation is the *RAM model*, which models memory as an infinite array M of *natural numbers*.

Definition 3.1 (RAM Programs: syntax). A *RAM Program* $P = (V, C_0, \dots, C_{\ell-1})$ consists of a finite set V of *variables* (or *registers*), and a sequence $C_0, C_1, \dots, C_{\ell-1}$ of *commands* (or *lines of code*), chosen from the following:

- (assignment to a constant) $\text{var} = c$, for a variable $\text{var} \in V$ and a constant $c \in \mathbb{N}$.
- (arithmetic) $\text{var}_0 = \text{var}_1 \text{ op } \text{var}_2$, for variables $\text{var}_0, \text{var}_1, \text{var}_2 \in V$, and an operation op chosen from $+, -, \times, /$.
- (read from memory) $\text{var}_0 = M[\text{var}_1]$ for variables $\text{var}_0, \text{var}_1 \in V$.
- (write to memory) $M[\text{var}_0] = \text{var}_1$ for variables $\text{var}_0, \text{var}_1 \in V$.

In addition, we require that every RAM Program has three special variables: `input_len`, `output_ptr`, and `output_len`.

Definition 3.2 (Computation of a RAM Program: semantics). A RAM Program $P = (V, (C_0, \dots, C_{\ell-1}))$ *computes* on an input x as follows:

1. Initialization: The input x is encoded (in some predefined manner) as a sequence of natural numbers placed into memory locations $(M[0], \dots, M[n-1])$, and all of the remaining memory locations are set to 0. The variable `input_len` is initialized to n , the length of x 's encoding. All other variables are initialized to 0.
2. Execution: The sequence of commands C_0, C_1, C_2, \dots are executed in order (except when jumps are done due to GOTO commands), updating the values of variable and memory locations according to the usual interpretations of the operations. Since we are working with natural numbers, if the result of subtraction would be negative, it is replaced with 0. Similarly, the results of division are rounded down, and divide by 0 results in 0.
3. Output: If line ℓ is reached (possibly due to a GOTO ℓ), the output $P(x)$ is defined to be the subarray of M of length `output_len` starting at location `output_ptr`. That is,

$$P(x) = (M[\text{output_ptr}], M[\text{output_ptr} + 1], \dots, M[\text{output_ptr} + \text{output_len} - 1]).$$

The *running time* of P on input x , denoted $\text{Time}_P(x)$, is defined to be: the number of commands executed during the computation (possibly ∞).

The definition of the RAM Model above is mathematically precise, so achieves our unambiguity desideratum (unless we've forgotten to specify something!).

The RAM Model also does quite well on the mathematical simplicity front. We described it in one page of lecture notes, compared to 100+ pages for most modern programming languages. That said, there are even simpler models of computation, such as the Turing Machine and the Lambda Calculus. However, those are harder to describe algorithms in and less accurately describe

computing technology. We will briefly discuss those later in the course when we cover the Church–Turing Thesis, and they (along with other models of computation) are studied in depth in CS121.

Our focus for the rest of today’s class will be to get convinced of the *expressivity* of the RAM model. We will do this by seeing how to implement algorithms we have seen in the RAM model. We will turn to robustness and technological relevance next time.

4 Iterative Algorithms

Let’s see an example RAM program for Insertion Sort, when the keys and values are both given as natural numbers.

<p>Input : An array $x = (K_0, V_0, K_1, V_1, \dots, K_{n-1}, V_{n-1})$, occupying memory locations $M[0], \dots, M[2n - 1]$</p> <p>Output : A valid sorting of x. in the same memory locations as the input</p> <p>Variables: input_len, output_len, zero, one, two, output_ptr, outer_key_ptr, outer_rem, outer_key, inner_key_ptr, inner_rem, inner_key, key_diff, insert_key, insert_value, temp_ptr, temp_key, temp_value</p>	<pre> 1 zero = 0 ; /* useful constants */ 2 one = 1; 3 two = 2; 4 output_ptr = 0 ; /* output will overwrite input */ 5 output_len = input_len + zero; 6 outer_key_ptr = 0 ; /* pointer to the key we want to insert */ 7 outer_rem = input_len/two ; /* # outer-loop iterations remaining */ 8 outer_key_ptr = outer_key_ptr + two ; /* start of outer loop */ 9 outer_rem = outer_rem - one; 10 IF outer_rem == 0 GOTO 35; 11 outer_key = M[outer_key_ptr] ; /* key to be inserted */ 12 inner_key_ptr = 0 ; /* pointer to potential insertion point */ 13 inner_rem = outer_key_ptr/two ; /* # inner-loop iterations remaining */ 14 inner_key = M[inner_key_ptr] ; /* start 1st inner loop */ 15 key_diff = inner_key - outer_key ; /* if inner_key ≤ outer_key, then */ 16 IF key_diff == 0 GOTO 31 ; /* proceed to next inner iteration */ 17 insert_key = outer_key + zero ; /* key to be inserted */ 18 temp_ptr = outer_key_ptr + one; 19 insert_value = M[temp_ptr] ; /* value to be inserted */ 20 temp_key = M[inner_key_ptr] ; /* start of 2nd inner loop */ 21 temp_ptr = inner_key_ptr + one; 22 temp_value = M[temp_ptr]; 23 M[inner_key_ptr] = insert_key; 24 M[temp_ptr] = insert_value; 25 insert_key = temp_key + zero; 26 insert_value = temp_value + zero; 27 inner_key_ptr = inner_key_ptr + two; 28 IF inner_rem == 0 GOTO 8; 29 inner_rem = inner_rem - one; 30 IF zero == 0 GOTO 20; 31 inner_key_ptr = inner_key_ptr + two; 32 inner_rem = inner_rem - one; 33 IF inner_rem == 0 GOTO 8; 34 IF zero == 0 GOTO 14; 35 HALT ; /* not an actual command */ </pre>
--	---

Algorithm 1: RAM implementation of Insertion Sort

5 Data

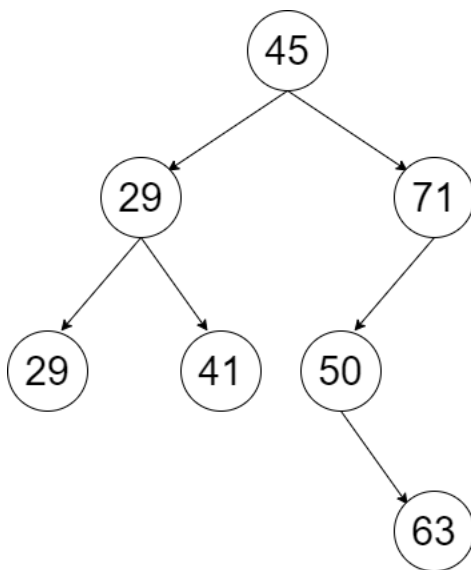
Implicit in the expressivity requirement is that we can describe the inputs and outputs of algorithms in the model. In the RAM model, all inputs and outputs are arrays of natural numbers. How can we represent other types of data?

- (Signed) integers: natural numbers with an additional sign bit
- Rational numbers: pair of integers (numerator and denominator)
- Real numbers: impossible! (For those of you who have taken CS20, this is because the set of real numbers is uncountable, but the set of finite sequences of natural numbers is countable.) Although this is a constraint on expressivity, the inability to represent and manipulate infinite-precision real numbers is generally accepted as part of both the intuitive and physically realizable concepts of computation.²

To approximate real numbers, Python and other programming languages often use *floating-point numbers*, in the form $x \cdot 2^y$ for signed integers x and y of bounded bit-length. Almost every operation on floating-point numbers introduces error through rounding, and it can be very tricky to reason about how these errors propagate. Thus, *in the rest of the course, we will generally stick to problems involving integer or rational arithmetic*. Dealing with floating-point errors is a topic sometimes covered in courses on numerical analysis, scientific computation, numerical linear algebra, and/or optimization, which you might find in the Applied Math or Applied Computation parts of the catalogue.

- Strings: an array of ASCII values (where each character is represented as a number in $\{0, 1, \dots, 127\}$)

What about a fancy data structure like a binary search tree? We can represent a BST as an array of 4-tuples (K_0, V_0, P_L, P_R) where P_L and P_R are pointers to the left and right children. Let's consider the example from last class:



²That said, one can define real-number models of computation, and these are mathematically interesting to study. See the book *Complexity and Real Computation* by Blum, Shub, and Smale.

Assuming all of the associated values are 0, this would be represented as the following array of length 28:

[45, 0, 4, 8, 29, 0, 12, 16, 71, 0, 20, 0, 20, 0, 0, 0, 41, 0, 0, 0, 50, 0, 0, 24, 63, 0, 0, 0]

For nodes that do not have a left or right child, we assign the value of 0 to P_L or P_R . Assigning the pointer value to 0 does not refer to the value at the memory location of 0, as we assume that the root of the tree cannot be a child for any of the nodes in the rest of the tree. Note that there are many ways to construct a binary tree using this array representation.

6 Recursive Algorithms

We will not cover this in lecture, but include it here in case you are interested and/or want more convincing about the expressivity of the RAM Model.

It is not entirely obvious that the RAM Model can implement recursion, since there are no function calls in its description. The way this is done (both in theory and in practice) via the use of a *stack* data structure. We simulate a function call $f(x)$ through the following steps:

1. Push local variables (in scope of the calling code), the input x , and an indicator of which line number to return to after the f is done executing.
2. GOTO the line number that starts the implementation of f .
3. The implementation of f pops its input x off the top of the stack, computes $y = f(x)$, and pushes y onto the top of the stack, and then GOTO to the line number after the function call (which it also read off the top of the stack).
4. After the return, $y = f(x)$ is read off the top of the stack, along with the local variables needed to continue the computation where it left off before calling f .

Below we present an example for a recursive computation of the height of a binary tree. Since our RAM model doesn't allow negative numbers, our recursive functions will compute height plus one (so that an empty tree has height 0), and we will subtract one from the height at the end. Also, because this algorithm does not use any memory other than the stack and the arrays, we implement the stack as a contiguous segment of memory starting after the input. However, in general, one may need to implement it as a linked list in order to be able to skip over portions of memory that

are being used for global state.

Input	: A Binary Tree of Key-Value Pairs, given as an array of 4-tuples (K, V, P_L, P_R)
Output	: The height of the input tree
Variables:	
1	zero = 0 ; /* useful constants */
2	one = 1;
3	two = 2;
4	stack_ptr = input_len + zero;
5	$M[\text{stack_ptr}] = \text{zero}$; /* push pointer to root of tree to top of stack */
6	stack_ptr = stack_ptr + one;
7	$M[\text{stack_ptr}] = \text{zero}$; /* branch-indicator for root call */
8	branch = $M[\text{stack_ptr}]$; /* pop branch indicator */
9	stack_ptr = stack_ptr - one;
10	node_ptr = $M[\text{stack_ptr}]$; /* pop pointer to current node */
11	stack_ptr = stack_ptr + two ; /* and repush both back onto stack */
12	temp_ptr = node_ptr + two;
13	child_ptr = $M[\text{temp_ptr}]$; /* pointer to left child */
14	IF child_ptr == 0 GOTO 23;
15	$M[\text{stack_ptr}] = \text{child_ptr}$; /* push pointer to left child */
16	stack_ptr = stack_ptr + one;
17	$M[\text{stack_ptr}] = \text{one}$; /* left branch indicator */
18	IF zero == 0 GOTO 8 ; /* recurse */

Algorithm 2: RAM implementation of Calculate Height


```

19  left_height = M[stack_ptr] ;                /* pop height of left child */
20  stack_ptr = stack_ptr - one;
21  node_ptr = M[stack_ptr] ;                    /* pop pointer to current node */
22  IF zero == 0 GOTO 24
23  left_height = 0 ;                            /* left child is empty */
24  temp_ptr = node_ptr + three;
25  child_ptr = M[temp_ptr] ;                    /* pointer to right child */
26  IF child_ptr == 0 GOTO 38;
27  M[stack_ptr] = left_height ;                /* push height of left child */
28  stack_ptr = stack_ptr + one;
29  M[stack_ptr] = child_ptr ;                  /* push pointer to right child */
30  stack_ptr = stack_ptr + one;
31  M[stack_ptr] = two ;                        /* right branch indicator */
32  IF zero == 0 GOTO 8 ;                       /* recurse */
33  right_height = M[stack_ptr] ;               /* pop height of right child */
34  stack_ptr = stack_ptr - one;
35  left_height = M[stack_ptr] ;               /* pop height of left child */
36  stack_ptr = stack_ptr - one;
37  IF zero == 0 GOTO 39;
38  right_height = 0
39  branch = M[stack_ptr] ;                     /* pop branch indicator */
40  diff_heights = left_height - right_height;
41  IF diff_heights == 0 GOTO 44 ;              /* right-child taller */
42  height = left_height + one ;               /* left-child taller */
43  IF zero == 0 GOTO 45;
44  height = right_height + one;
45  IF branch == zero GOTO 51;
46  M[stack_ptr] = height ;                    /* push return value */
47  branch = branch - one;
48  IF branch == zero GOTO 19;
49  branch = branch - one;
50  IF branch == zero GOTO 33;
51 height = height - one ;                     /* subtract one for output height */
52 M[stack_ptr] = height;
53 output_ptr = stack_ptr;
54 output_len = 1;
55 HALT

```

Algorithm 3: RAM implementation of Calculate Height (cont.)

7 Reductions

We may not have time to cover this in class, but is again included for your interest.

We can also formalize *reductions* using the following extension of the RAM model.

Definition 7.1. An *oracle-aided RAM Program* is like an ordinary RAM program, except it can also have commands of the form

ORACLE($\text{var}_0, \text{var}_1, \text{var}_2$),

which means call the oracle on the array $(M[\text{var}_0], M[\text{var}_0 + 1], \dots, M[\text{var}_0 + \text{var}_1 - 1])$ and write the oracle's answer in the locations $(M[\text{var}_2], M[\text{var}_2 + 1], \dots)$.

For example, our reduction from IntervalScheduling-Decision to Sorting is given by the following oracle-aided RAM program:

<p>Input : An array $x = (a_0, b_0, a_1, b_1, \dots, a_{n-1}, b_{n-1})$, occupying memory locations $M[0], \dots, M[2n - 1]$, with $a_i \leq b_i$ for all i</p> <p>Output : 1 (YES) if all of the intervals $[a_i, b_i]$ are disjoint, 0 (NO) otherwise</p> <pre> 1 zero = 0; 2 one = 1; 3 two = 2; 4 ORACLE(zero, input_len, zero) ; 5 output_ptr = 0; 6 output_len = 1; 7 M[zero] = one ; 8 temp_ptr = 1; 9 remaining = input_len - two ; /* how many adjacent pairs left to check, times two */ 10 IF remaining == 0 GOTO 20; 11 end_curr = M[temp_ptr] ; 12 temp_ptr = temp_ptr + one; 13 start_next = M[temp_ptr] ; 14 temp = start_next - end_curr; 15 IF temp == 0 GOTO 19 ; 16 temp_ptr = temp_ptr + one; 17 remaining = remaining - two; 18 IF zero == 0 GOTO 10; 19 M[zero] = zero ; 20 HALT </pre>	<pre> /* sort input by start time */ /* default output is 1 = YES */ /* conflict found */ /* change output to 0 = NO */ </pre>
---	--

Algorithm 4: Oracle-RAM implementation of $\text{IntervalScheduling-Decision} \leq_{O(n), n} \text{Sorting}$