TRION 20 DEVELOPMENT BOARD (Rev. 3)

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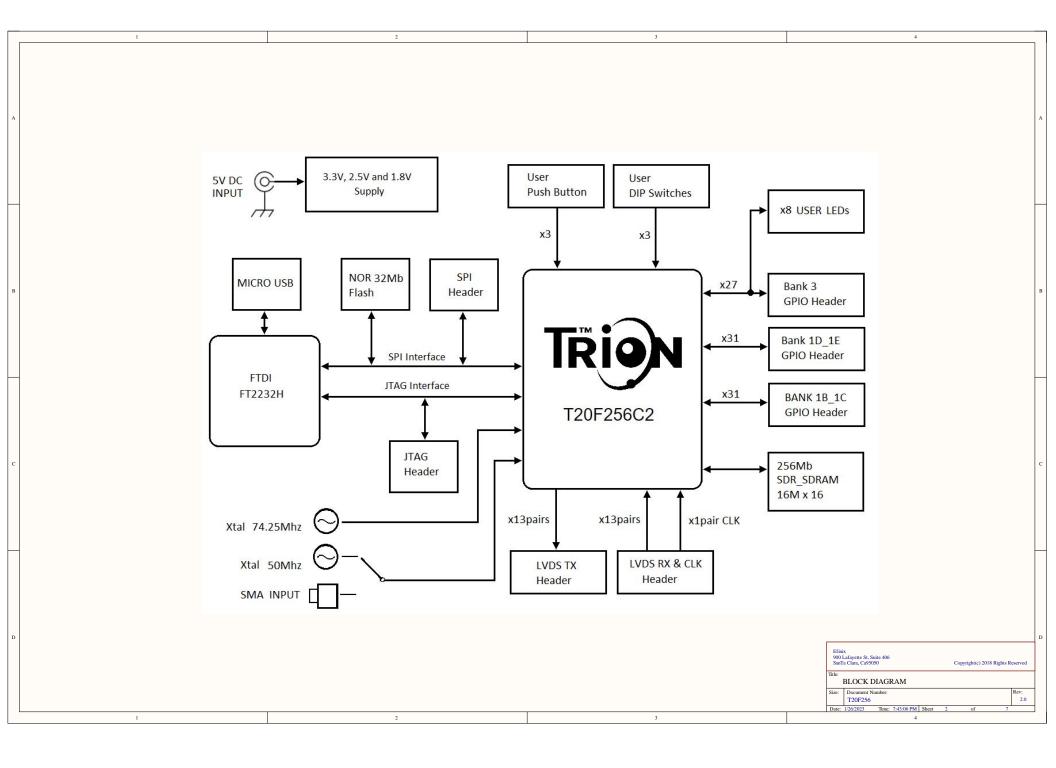
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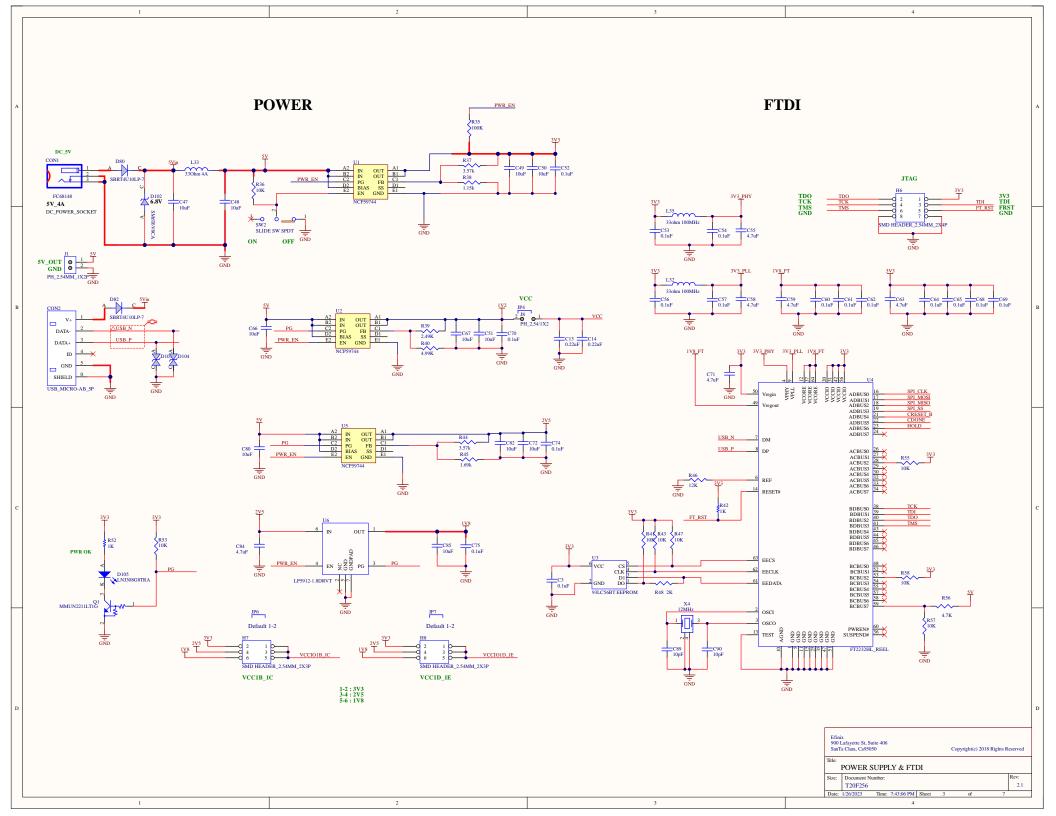
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REVISION HISTORY

1.0	INITIAL CREATION	22 NOV 2018
2.0	SECOND RELEASE	09 JAN 2019
3.0	THIRD RELEASE	29 MAR 2022
4.0	FOURTH RELEASE	03 OCT 2023







TRION T20 CONTROL SIGNALS SDR_SDRAM TACT_SW 2 GPIOL_04_CSI 33ohm 100MHz SMD DIP_SWITCH_3WAY TACT_SW 2 GPIOL_05_CSO USER_PUSHBUTTON HOLE U8 256Mb SDRAM NDS36PT5-20ET CLK W25Q32JVSSIQ 25 Al 26 A3 30 A5 31 A5 32 A6 33 A8 34 A9 22 A9 410/AP A12 GPIOL_00_SS GPIOL_01_CCK GPIOL_08_CDI0 GPIOL_09_CDI1 CRESET_N CONDONE GPIOL_71_NSTATUS GPIOL_02 GPIOL_04_CSI GPIOL_05_CSO GPIOR_149_CBUS2 GPIOR_150_CBUS1 GPIOR_151_CBUS0 GPIOR_158_TESTN GPIOR_128_CTRL11 GPIOR_129_CTRL10 GPIOR_130_CTRL19 H14 GPIOR_130_CTRL10 H15 GPIOR_130_CTRL10 H16 GPIOR_130_CTRL10 17 CAS# 18 RAS# 19 CS# OSW1 TACT_SW CDONE UDQM 39 UDQM CRST T20F256C LNJ308G8TRA R15 10K Q2 MMUN2211LT1G VCCIOID_IE CLK MOSI MISO SS FRST NSTATUS D2 LNJ308G8TRA 를 GND Q3 MMUN2211LT1G For power-up recommendation, please follow Trion power-up sequence in Trion datasheet and voltage supervisory IC (Reset IC) is recommended at CRESET N which provide reliable solution 900 Lafayette St, Suite 406 SanTa Clara, Ca95050 Copyright(c) 2018 Rights Reserved CONTROL SIGNAL ize: Document Numb T20F256 2.1 Date: 10/31/2023 Time: 7:43:07 PM Sheet

