

# 手搓 SIMD

人肉 compiler 之路

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# 命名來源：B站

- 手搓大模型
- 手搓cpu
- ...



# 在我之前介紹的 SIMD

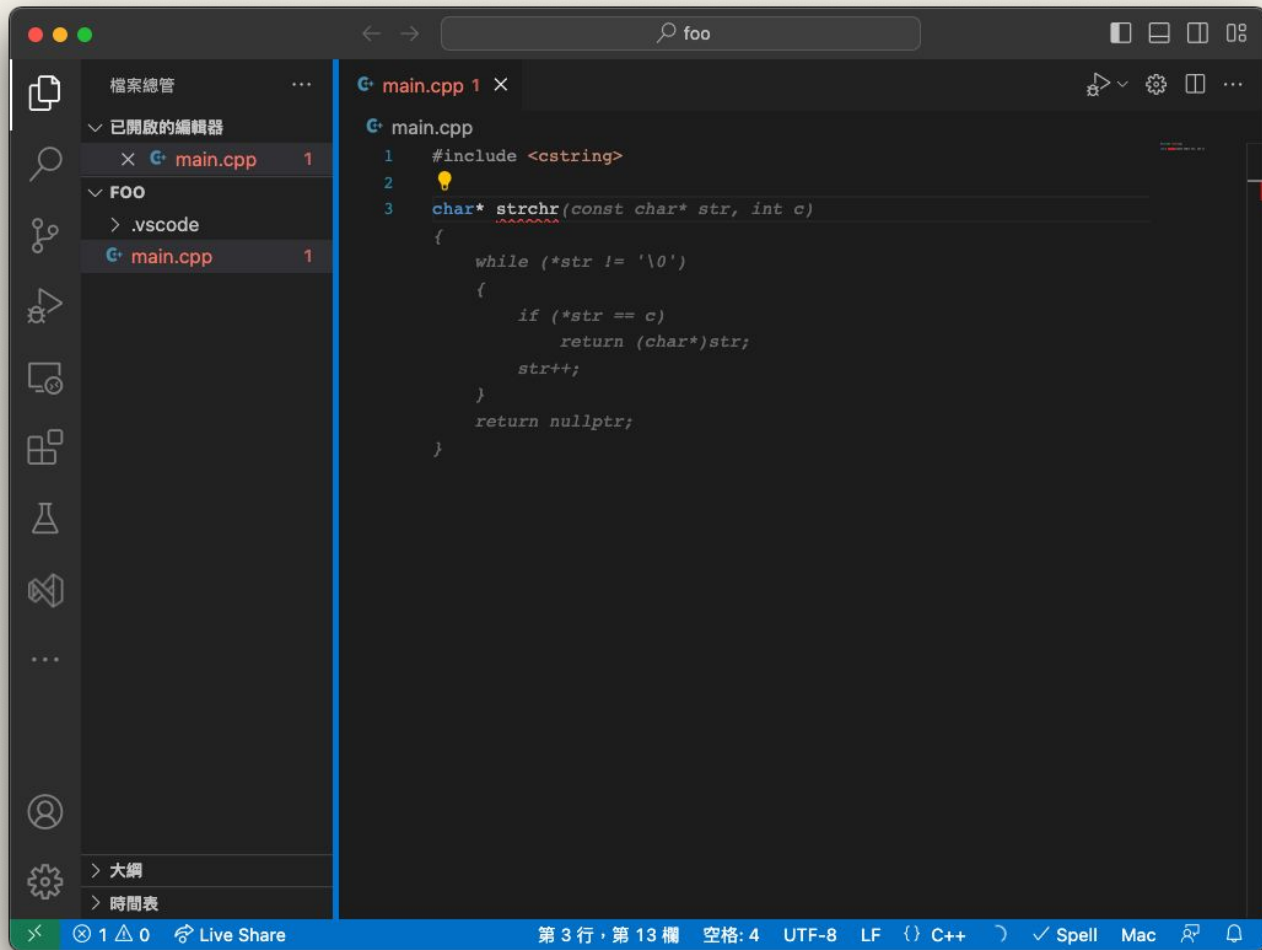
```
char *strchr(const char*, int);
```

# 在我之前介紹的 SIMD

```
char *strchr(const char* s, int c);
```

The `strchr` function locates the **first occurrence** of `c` (converted to a `char`) in the string pointed to by `s`. The terminating null character is considered to be part of the string. [§7.24.5.2](#)

# CS 101



```
1 #include <cstring>
2
3 char* strchr(const char* str, int c)
{
    while (*str != '\0')
    {
        if (*str == c)
            return (char*)str;
        str++;
    }
    return nullptr;
}
```

# CS 101

```
char* strchr(const char* str, int c) {  
    while (*str != '\0') {  
        if (*str == c)  
            return (char*)str;  
        str++;  
    }  
    return nullptr;  
}
```

肉眼 compiler 上場



x86 應該怎麼寫？

# CS 101

```
char* strchr(const char* str, int c) {  
    while (*str != '\0') {  
        if (*str == c)  
            return (char*)str;  
        str++;  
    }  
    return nullptr;  
}
```

```
strchr(char const*, int):  
    push rbp  
    mov rbp, rsp  
    mov QWORD PTR [rbp-8], rdi  
    mov DWORD PTR [rbp-12], esi  
    jmp .while_loop  
.if_match:  
    mov rax, QWORD PTR [rbp-8]  
    movzx eax, BYTE PTR [rax]  
    movsx eax, al  
    cmp DWORD PTR [rbp-12], eax  
    jne .str_pp  
    mov rax, QWORD PTR [rbp-8]  
    jmp .ret  
.str_pp:  
    inc QWORD PTR [rbp-8]  
.while_loop:  
    mov rax, QWORD PTR [rbp-8]  
    movzx eax, BYTE PTR [rax]  
    test al, al  
    jne .if_match  
    mov eax, 0  
.ret:  
    pop rbp  
    ret
```



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# CS 101

char\* str

```
char* strchr(const char* str, int c) {  
    while (*str != '\0') {  
        if (*str == c)  
            return (char*)str;  
        str++;  
    }  
    return nullptr;  
}
```

```
strchr(char const*, int):  
    push rbp  
    mov rbp, rsp  
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    movzx eax, BYTE PTR [rax]  
    movsx eax, al  
    cmp DWORD PTR [rbp-12], int c  
    jne .str_pp  
    mov rax, QWORD PTR [rbp-8]  
    jmp .ret  
.str_pp:  
    inc QWORD PTR [rbp-8]  
.while_loop:  
    mov rax, QWORD PTR [rbp-8]  
    movzx eax, BYTE PTR [rax]  
    test al, al  
    jne .if_match  
    mov eax, 0  
.ret:  
    pop rbp  
    ret
```

# CS 101

```
char* strchr(const char* str, int c) {  
    while (*str != '\0') {  
        if (*str == c)  
            return (char*)str;  
        str++;  
    }  
    return nullptr;  
}
```

```
strchr(char const*, int):  
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    movsx eax, al  
    cmp DWORD PTR [rbp-12], eax  
    jne .str_pp  
    mov rax, QWORD PTR [rbp-8]  
    jmp .ret  
.str_pp:  
    inc QWORD PTR [rbp-8]  
.while_loop:  
    mov rax, QWORD PTR [rbp-8]  
    movzx eax, BYTE PTR [rax]  
    test al, al  
    jne .if_match  
    mov eax, 0  
.ret:  
    pop rbp  
    ret
```



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# CS 101

```
char* strchr(const char* str, int c) {  
    while (*str != '\0') {  
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        str++;  
    }  
    return nullptr;  
}
```

```
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    push rbp  
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    movsx eax, al  
    cmp DWORD PTR [rbp-12], eax  
    jne .str_pp  
    mov rax, QWORD PTR [rbp-8]  
    jmp .ret  
.str_pp:  
    inc QWORD PTR [rbp-8]  
.while_loop:  
    mov rax, QWORD PTR [rbp-8]  
    movzx eax, BYTE PTR [rax]  
    test al, al  
    jne .if_match  
    mov eax, 0  
.ret:  
    pop rbp  
    ret
```



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# CS 101

```
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    while (*str != '\0') {  
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            return (char*)str;  
        str++;  
    }  
    return nullptr;  
}
```

```
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    push rbp  
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    movzx eax, BYTE PTR [rax]  
    movsx eax, al  
    cmp DWORD PTR [rbp-12], eax  
    jne .str_pp  
    mov rax, QWORD PTR [rbp-8]  
    jmp .ret  
.str_pp:  
    inc QWORD PTR [rbp-8]  
.while_loop:  
    mov rax, QWORD PTR [rbp-8]  
    movzx eax, BYTE PTR [rax]  
    test al, al  
    jne .if_match  
    mov eax, 0  
.ret:  
    pop rbp  
    ret
```



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# CS 101

```
char* strchr(const char* str, int c) {  
    while (*str != '\0') {  
        if (*str == c)  
            return (char*)str;  
        str++;  
    }  
    return nullptr;  
}
```

```
strchr(char const*, int):  
    push rbp  
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    mov rax, QWORD PTR [rbp-8]  
    movzx eax, BYTE PTR [rax]  
    movsx eax, al  
    cmp DWORD PTR [rbp-12], eax  
    jne .str_pp  
    mov rax, QWORD PTR [rbp-8]  
    jmp .ret  
.str_pp:  
    inc QWORD PTR [rbp-8]  
.while_loop:  
    mov rax, QWORD PTR [rbp-8]  
    movzx eax, BYTE PTR [rax]  
    test al, al  
    jne .if_match  
    mov eax, 0  
.ret:  
    pop rbp  
    ret
```



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# CS 101

```
char* strchr(const char* str, int c) {  
    while (*str != '\0') {  
        if (*str == c)  
            return (char*)str;  
        str++;  
    }  
    return nullptr;  
}
```

```
strchr(char const*, int):  
    push rbp  
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    jmp .while_loop  
.if_match:  
    mov rax, QWORD PTR [rbp-8]  
    movzx eax, BYTE PTR [rax]  
    movsx eax, al  
    cmp DWORD PTR [rbp-12], eax  
    jne .str_pp  
    mov rax, QWORD PTR [rbp-8]  
    jmp .ret  
.str_pp:  
    inc QWORD PTR [rbp-8]  
.while_loop:  
    mov rax, QWORD PTR [rbp-8]  
    movzx eax, BYTE PTR [rax]  
    test al, al  
    jne .if_match  
    mov eax, 0  
.ret:  
    pop rbp  
    ret
```



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# CS 101



```
char* strchr(const char* str, int c) {  
    while (*str != '\0') {  
        if (*str == c)  
            return (char*)str;  
        str++;  
    }  
    return nullptr;  
}
```

```
strchr(char const*, int):
```

```
.while_loop:
```

```
    movsx edx, BYTE PTR [rdi]
```

```
    test dl, dl
```

```
    jne .if_match
```

```
    xor eax, eax
```

```
    ret
```

```
.if_match:
```

```
    cmp edx, esi
```

```
    je .found
```

```
    inc rdi
```

```
    jmp .while_loop
```

```
.found:
```

```
    mov rax, rdi
```

```
    ret
```

---

# What's the problem

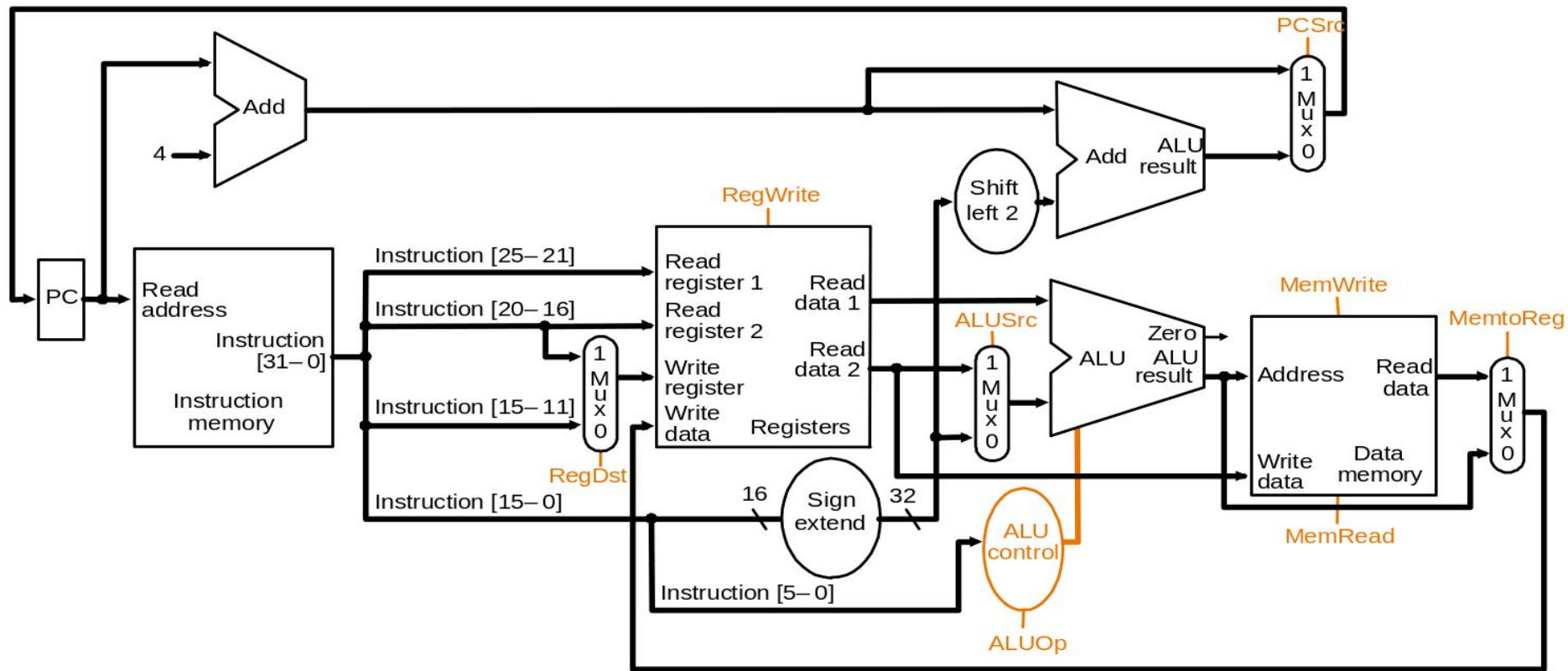




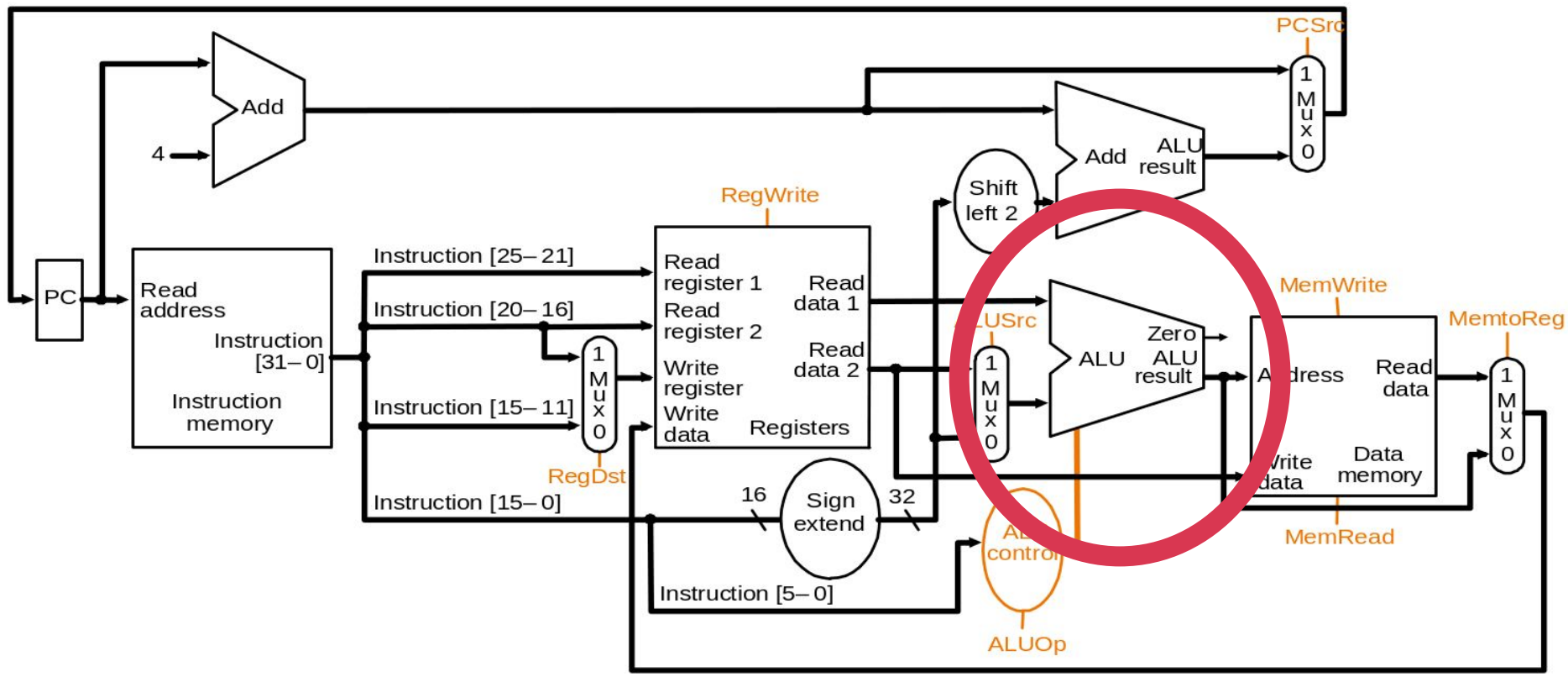
---

# You're in 32 or 64 bit system

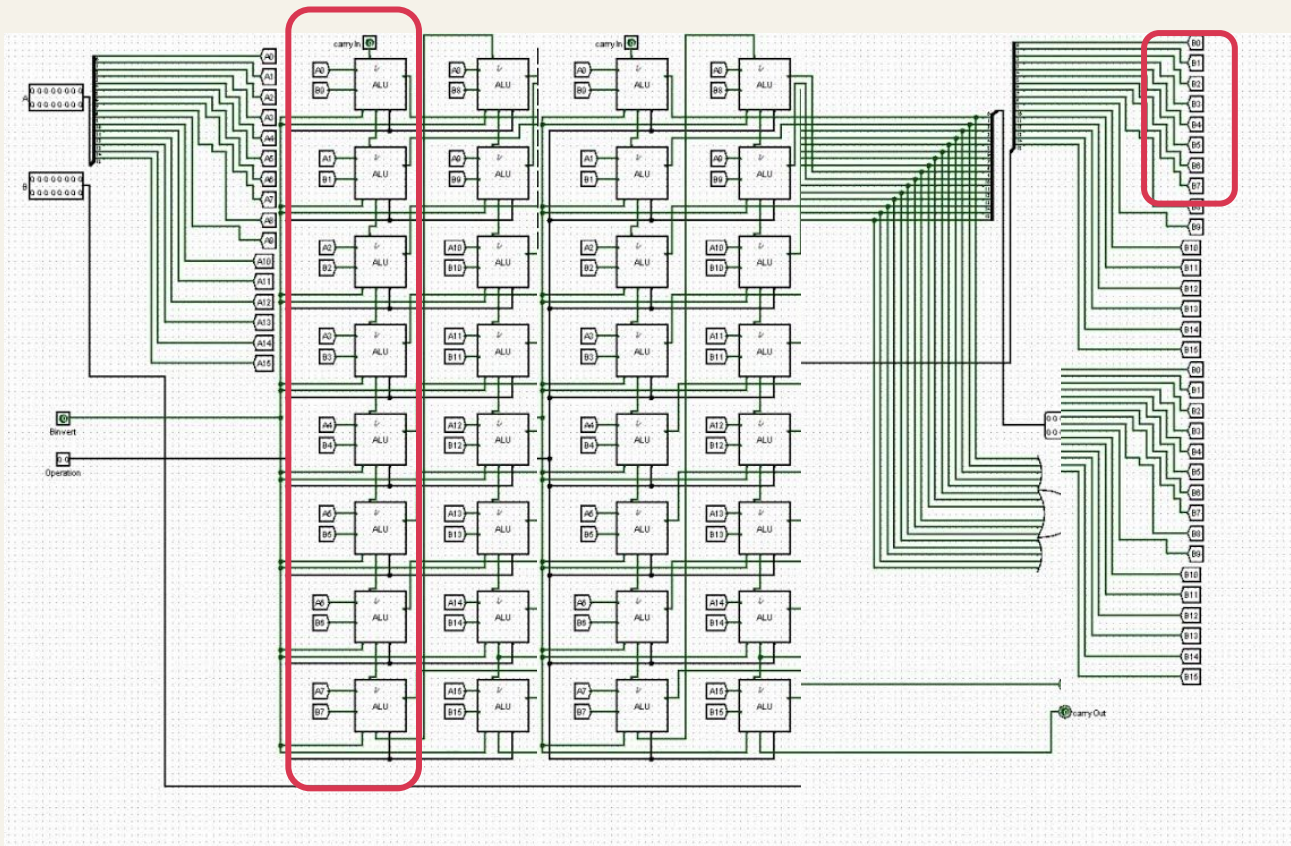
# Single core CPU



# Single core CPU



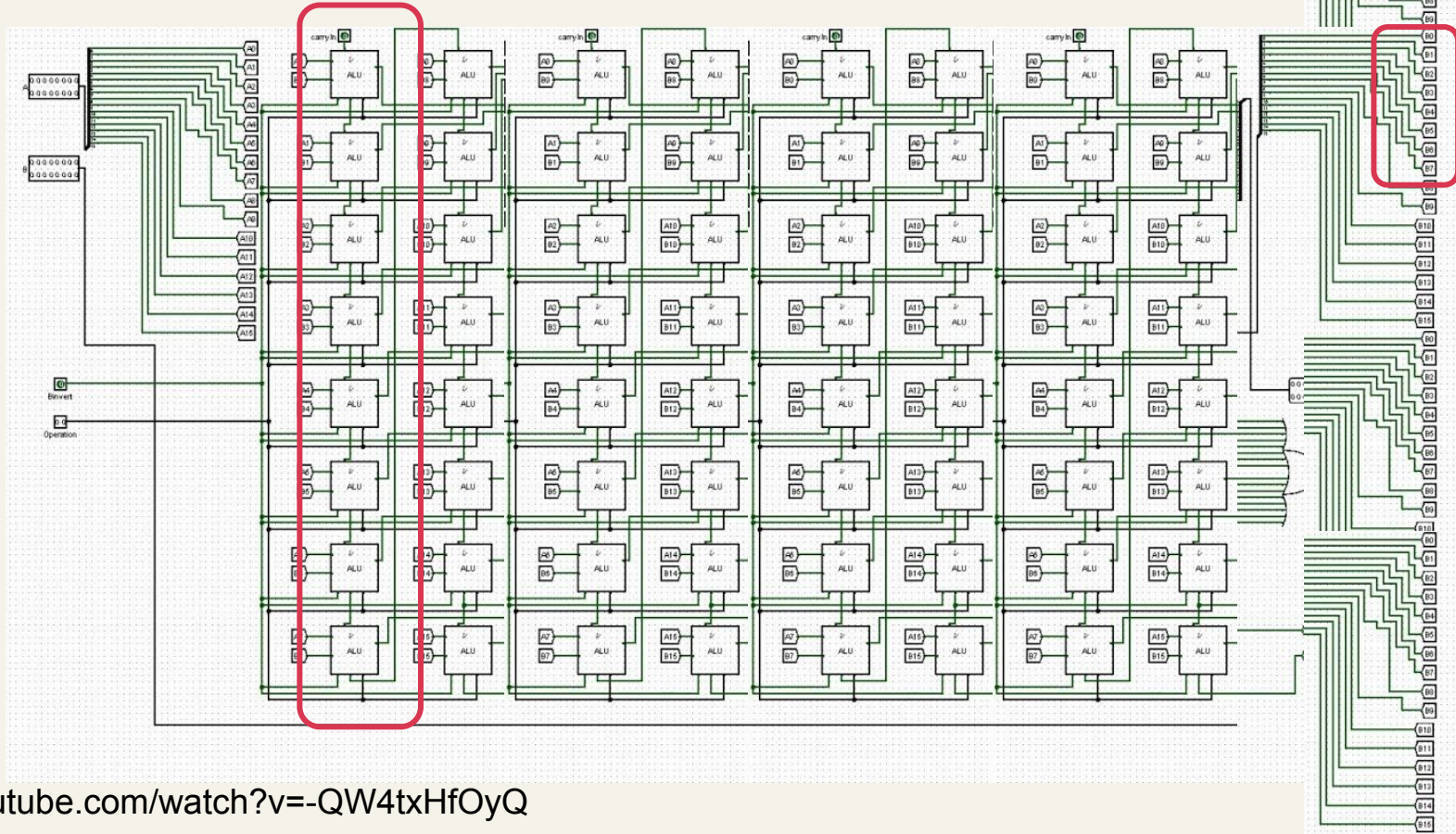
# Single core CPU



# Single core CPU



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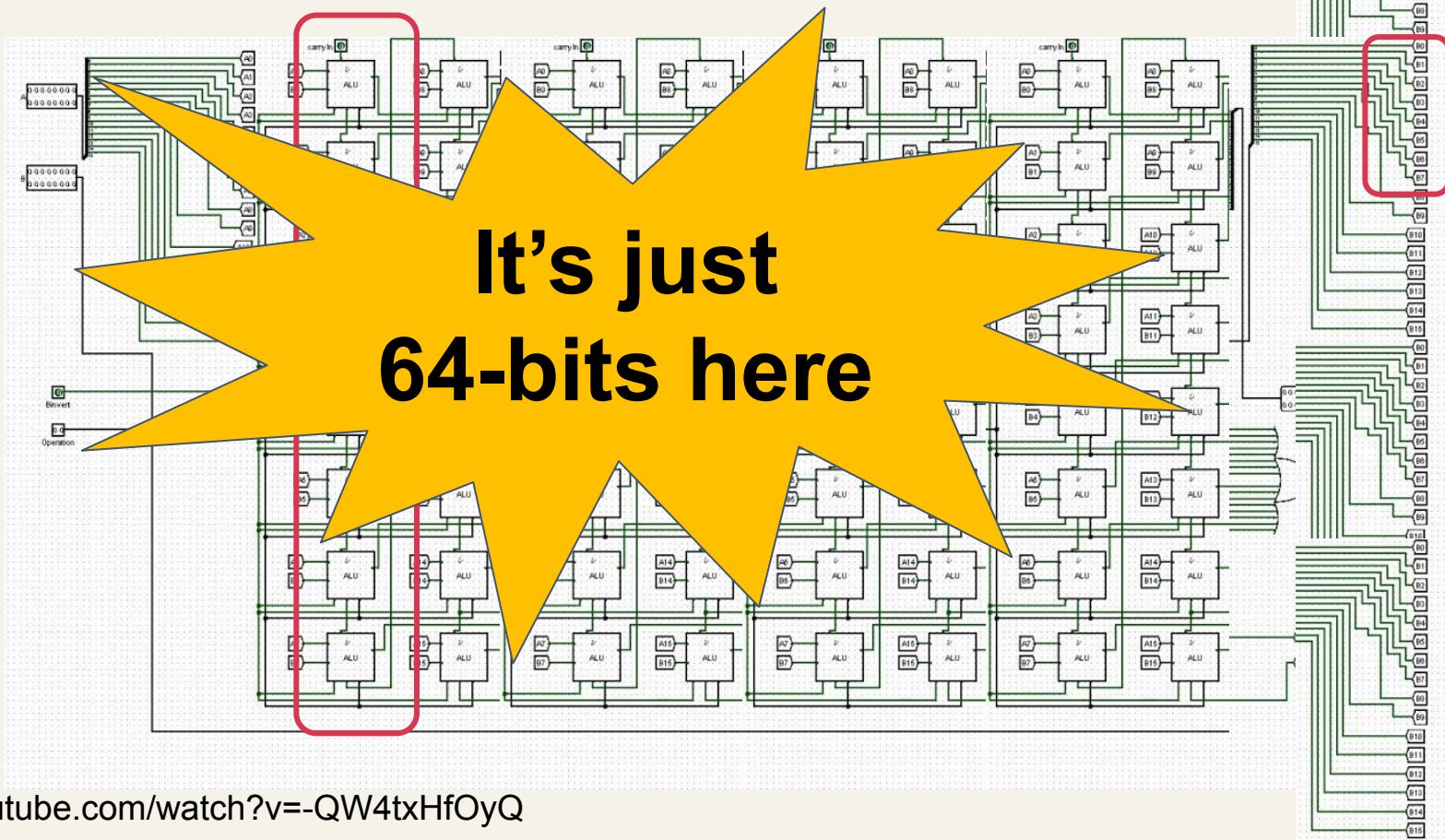




# Single core CPU



大学  
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# Single core CPU



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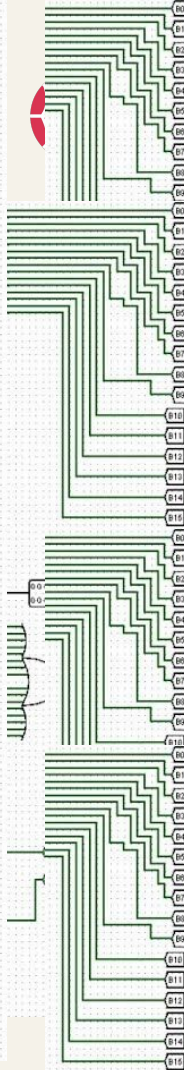
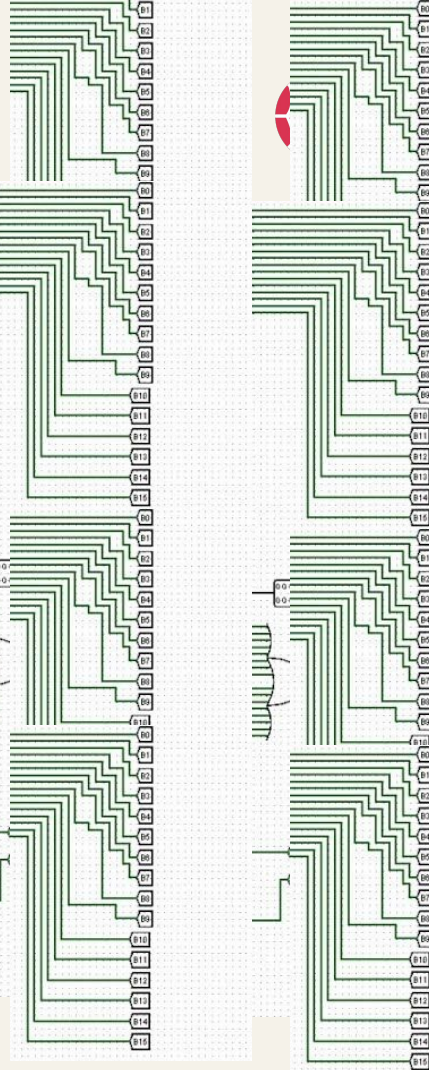
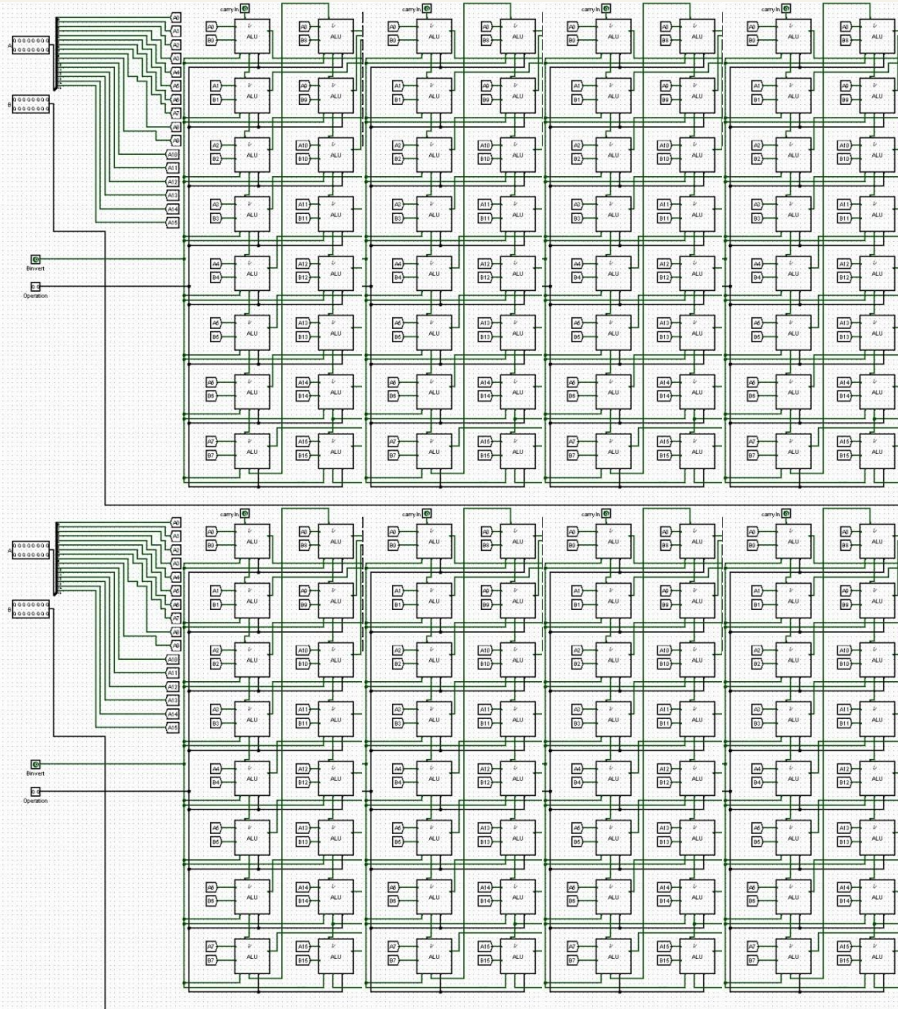
**Actually, it can offer  
128-bits or more.**

# Intel SSE

```
while (len >= 16) {  
    __m128i inp = _mm_loadu_si128((__m128i *)cstr);  
    __m128i mask = _mm_cmpeq_epi8(inp, _mm_set1_epi8(c));  
    int mask_int = _mm_movemask_epi8(mask);  
    if (unlikely(mask_int != 0))  
        return (char *) (cstr + __builtin_ctz(mask_int));  
    len -= 16;  
    cstr += 16;  
}
```



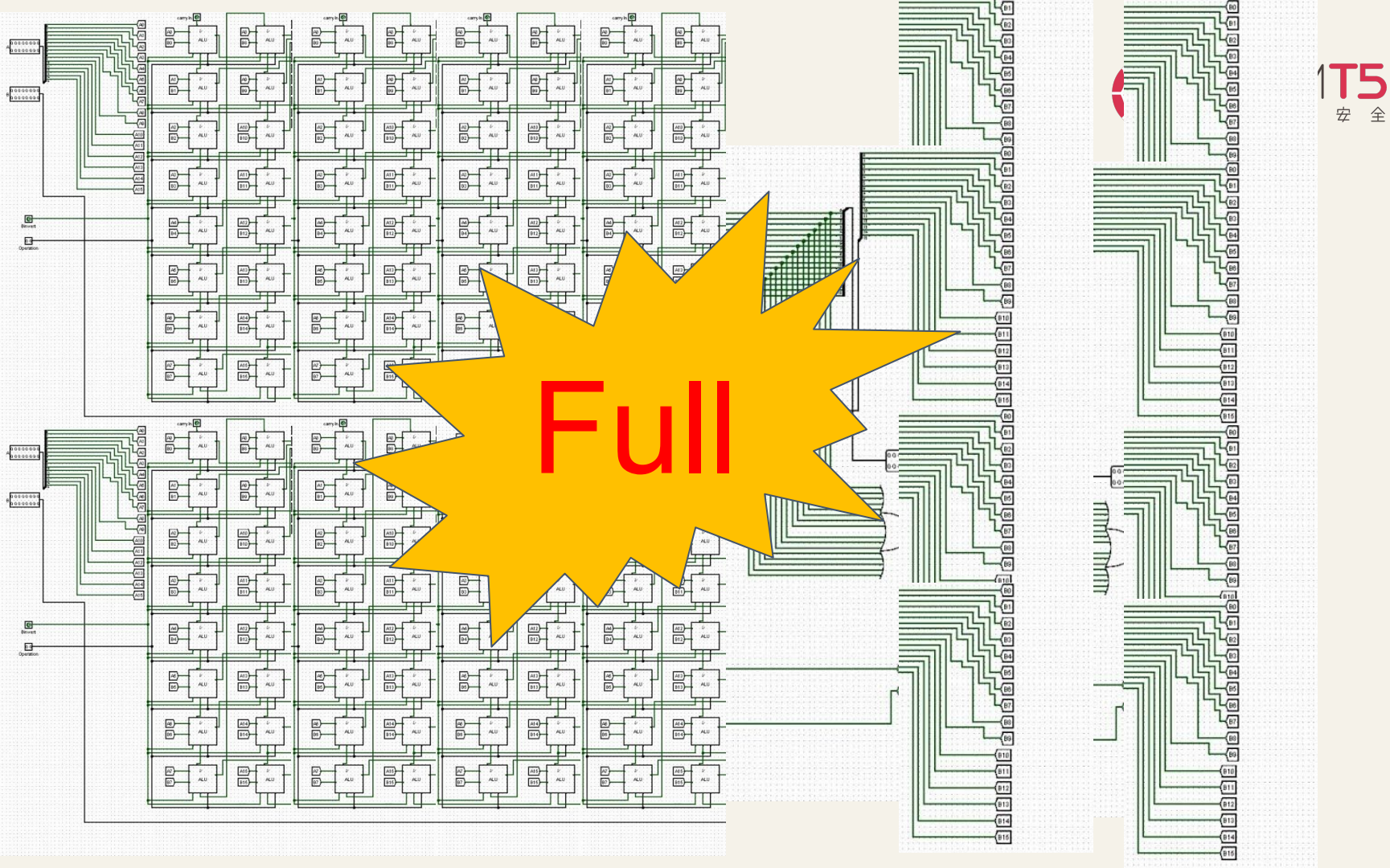
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---

# What's the problem



---

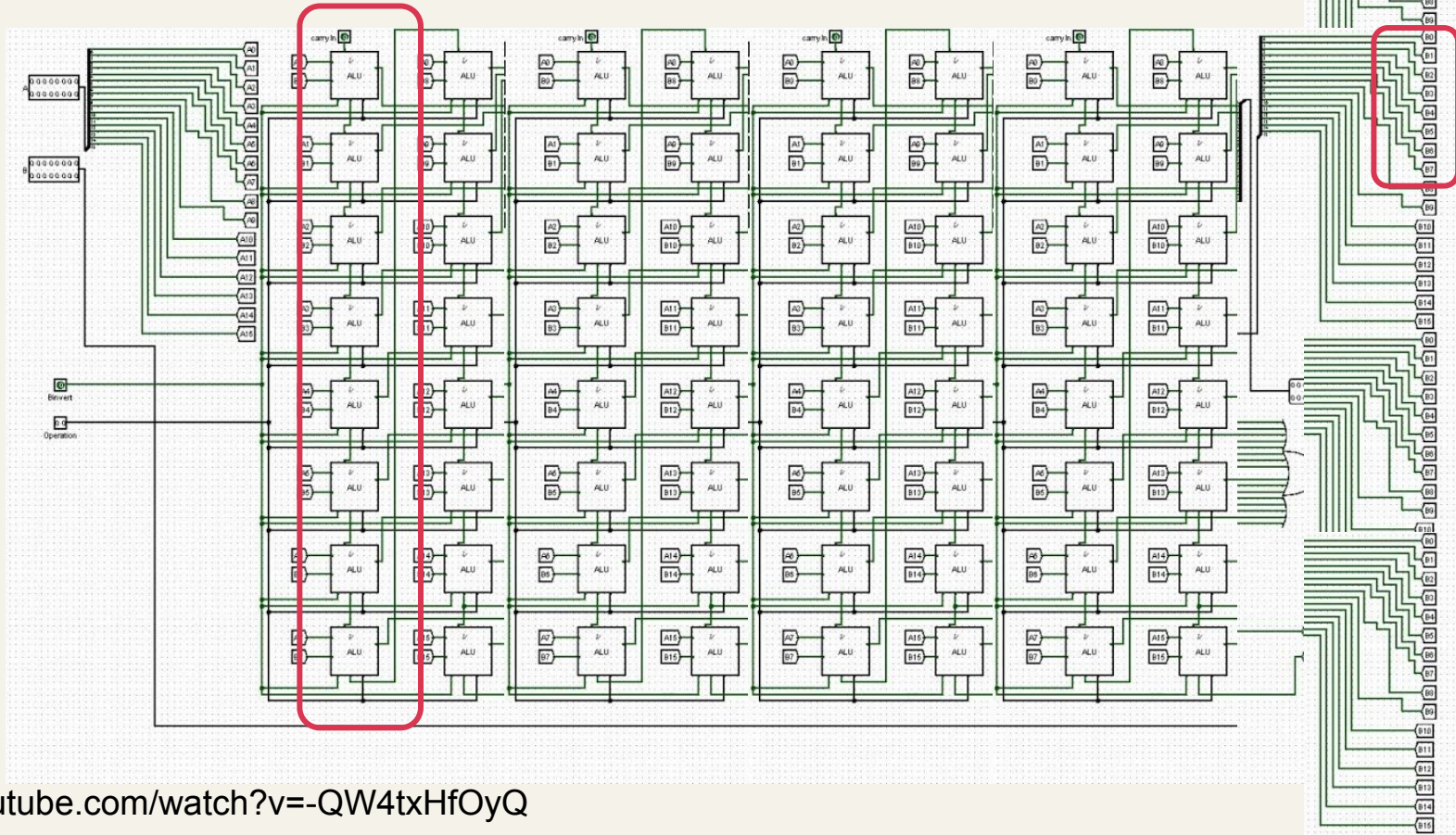
# What if $< 16$ ?



# Single core CPU



下  
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---

int also can SIMD?!

`sizeof(char) * 4`  
`sizeof(int)`



```
while (len >= 4) {  
    uint32_t inp = *(uint32_t *)cstr;  
    uint32_t mask = inp ^ (c * 0x01010101);
```



```
    const uint8_t &mask1_is_match = (!! (mask & 0x000000ff));  
    const uint8_t &mask2_is_match = (!! (mask & 0x0000ff00));  
    const uint8_t &mask3_is_match = (!! (mask & 0x00ff0000));  
    const uint8_t &mask4_is_match = (!! (mask & 0xff000000));  
    const auto &is_all_not_match = mask1_is_match & mask2_is_match &  
    mask3_is_match & mask4_is_match;  
    if (is_all_not_match) {  
        len -= 4;  
        cstr += 4;  
        continue;  
    }  
  
    if (mask1_is_match)  
        return (char *)cstr;  
    else if (mask2_is_match)  
        return (char *) (cstr + 1);  
    else if (mask3_is_match)  
        return (char *) (cstr + 2);  
    return (char *) (cstr + 3);
```

```
}
```



```
while (len >= 4) {  
    uint32_t inp = *(uint32_t *)cstr;  
    uint32_t mask = inp ^ (c * 0x01010101);  
  
    const uint8_t &mask1_is_match = (!(mask & 0x000000ff));  
    const uint8_t &mask2_is_match = (!(mask & 0x0000ff00));  
    const uint8_t &mask3_is_match = (!(mask & 0x00ff0000));  
    const uint8_t &mask4_is_match = (!(mask & 0xff000000));  
    const auto &is_all_not_match = mask1_is_match & mask2_is_match & mask3_is_match & mask4_is_match;  
    if (is_all_not_match) {  
        len -= 4;  
        cstr += 4;  
        continue;  
    }  
  
    if (mask1_is_match)  
        return (char *)cstr;  
    else if (mask2_is_match)  
        return (char *) (cstr + 1);  
    else if (mask3_is_match)  
        return (char *) (cstr + 2);  
    return (char *) (cstr + 3);  
}
```

```
while (len >= 4) {  
    uint32_t inp = *(uint32_t *)cstr;  
    uint32_t mask = inp ^ (c * 0x01010101);
```

```
    const uint8_t &mask1_is_match = (!(mask & 0x000000ff));  
    const uint8_t &mask2_is_match = (!(mask & 0x0000ff00));  
    const uint8_t &mask3_is_match = (!(mask & 0x00ff0000));  
    const uint8_t &mask4_is_match = (!(mask & 0xff000000));  
    const auto &is_all_not_match = mask1_is_match & mask2_is_match &  
    if (is_all_not_match) {  
        len -= 4;  
        cstr += 4;  
        continue;  
    }  
  
    if (mask1_is_match)  
        return (char *)cstr;  
    else if (mask2_is_match)  
        return (char *) (cstr + 1);  
    else if (mask3_is_match)  
        return (char *) (cstr + 2);  
    return (char *) (cstr + 3);  
}
```

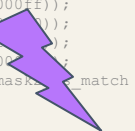


```
while (len >= 4) {
    uint32_t inp = *(uint32_t *)cstr;
    uint32_t mask = inp ^ (c * 0x01010101);
```

```
const uint8_t &mask1_is_match = (!(mask & 0x000000ff));
const uint8_t &mask2_is_match = (!(mask & 0x0000ff00));
const uint8_t &mask3_is_match = (!(mask & 0x00ff0000));
const uint8_t &mask4_is_match = (!(mask & 0xff000000));
const auto &not_mask1_is_match = !mask1_is_match &
if (is_all_match) {
    len -= 4;
    cstr += 4;
    continue;
}
```

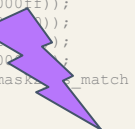
```
if (mask1_is_match)
    return (char *)cstr;
else if (mask2_is_match)
    return (char *) (cstr + 1);
else if (mask3_is_match)
    return (char *) (cstr + 2);
return (char *) (cstr + 3);
```

```
}
```



```
while (len >= 4) {
    uint32_t inp = *(uint32_t *)cstr;
    uint32_t mask = inp ^ (c * 0x01010101);
```

```
const uint8_t &mask1_is_match = (!! (mask & 0x000000ff));
const uint8_t &mask2_is_match = (!! (mask & 0x0000ff00));
const uint8_t &mask3_is_match = (!! (mask & 0x00ff0000));
const uint8_t &mask4_is_match = (!! (mask & 0xff000000));
const auto &not_mask1_is_match = !mask1_is_match;
const auto &not_mask2_is_match = !mask2_is_match;
const auto &not_mask3_is_match = !mask3_is_match;
const auto &not_mask4_is_match = !mask4_is_match;
if (is_all_match) {
    len -= 4;
    cstr += 4;
    continue;
}
if (mask1_is_match & not_mask2_is_match & not_mask3_is_match & not_mask4_is_match) {
    return (char *) (cstr + 1);
}
else if (mask2_is_match & not_mask1_is_match & not_mask3_is_match & not_mask4_is_match) {
    return (char *) (cstr + 2);
}
else if (mask3_is_match & not_mask1_is_match & not_mask2_is_match & not_mask4_is_match) {
    return (char *) (cstr + 3);
}
return (char *) (cstr + 4);
}
```



}

```
while (len >= 4) {  
    uint32_t inp = *(uint32_t *)cstr;  
    uint32_t mask = inp ^ (c * 0x01010101);  
  
    const uint8_t &mask1_is_match = (!! (mask & 0x000000ff));  
    const uint8_t &mask2_is_match = (!! (mask & 0x0000ff00));  
    const uint8_t &mask3_is_match = (!! (mask & 0x00ff0000));  
    const uint8_t &mask4_is_match = (!! (mask & 0xff000000));  
    const auto &is_all_not_match = mask1_is_match & mask2_is_match &  
    mask3_is_match & mask4_is_match;  
    if (is_all_not_match) {  
        len -= 4;  
        cstr += 4;  
        continue;  
    }  
  
    if (mask1_is_match)  
        return (char *)cstr;  
    else if (mask2_is_match)  
        return (char *) (cstr + 1);  
    else if (mask3_is_match)  
        return (char *) (cstr + 2);  
    return (char *) (cstr + 3);  
}
```

0 if that pos is 0

```
while (len >= 4) {
```

```
    uint32_t inp = *(uint32_t *)cstr;  
    uint32_t mask = inp ^ (c * 0x01010101);
```

```
    const uint8_t &mask1_is_match = (!! (mask & 0x000000ff));
```

```
    const uint8_t &mask2_is_match = (!! (mask & 0x0000ff00));
```

```
    const uint8_t &mask3_is_match = (!! (mask & 0x00ff0000));
```

```
    const uint8_t &mask4_is_match = (!! (mask & 0xff000000));
```

```
    const auto &is_all_not_match = mask1_is_match & mask2_is_match &
```

```
    mask3_is_match & mask4_is_match;
```

```
    if (is_all_not_match) {
```

```
        len -= 4;
```

```
        cstr += 4;
```

```
    } else {
```

```
        if (mask1_is_match)
```

```
            return (char *)cstr;
```

```
        else if (mask2_is_match)
```

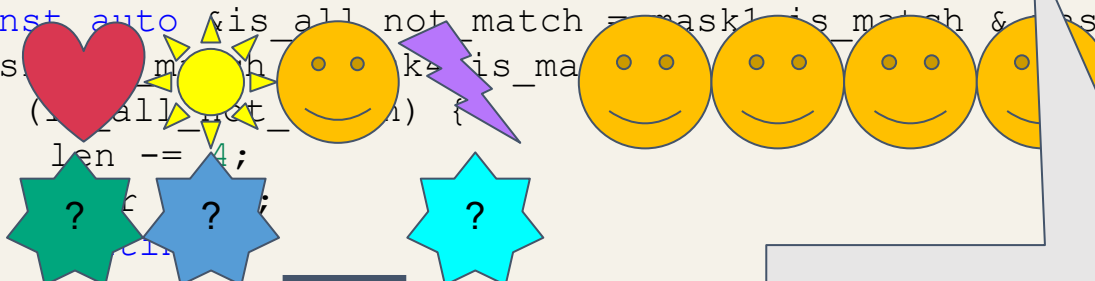
```
            return (char *) (cstr + 1);
```

```
        else if (mask3_is_match)
```

```
            return (char *) (cstr + 2);
```

```
        return (char *) (cstr + 3);
```

```
}
```



3 is False

```
while (len >= 4) {  
    uint32_t inp = *(uint32_t *)cstr;  
    uint32_t mask = inp ^ (c * 0x01010101);  
  
    const uint8_t &mask1_is_match = (!! (mask & 0x000000ff));  
    const uint8_t &mask2_is_match = (!! (mask & 0x0000ff00));  
    const uint8_t &mask3_is_match = (!! (mask & 0x00ff0000));  
    const uint8_t &mask4_is_match = (!! (mask & 0xff000000));  
    const auto &is_all_not_match = mask1_is_match & mask2_is_match &  
    mask3_is_match & mask4_is_match;  
    if (is_all_not_match) {  
        len -= 4;  
        cstr += 4;  
        continue;  
    }  
  
    if (mask1_is_match)  
        return (char *)cstr;  
    else if (mask2_is_match)  
        return (char *) (cstr + 1);  
    else if (mask3_is_match)  
        return (char *) (cstr + 2);  
    return (char *) (cstr + 3);  
}
```

True & True & False & True

```
while (len >= 4) {  
    uint32_t inp = *(uint32_t *)cstr;  
    uint32_t mask = inp ^ (c * 0x01010101);  
  
    const uint8_t &mask1_is_match = (!! (mask & 0x000000ff));  
    const uint8_t &mask2_is_match = (!! (mask & 0x0000ff00));  
    const uint8_t &mask3_is_match = (!! (mask & 0x00ff0000));  
    const uint8_t &mask4_is_match = (!! (mask & 0xff000000));  
    const auto &is_all_not_match = mask1_is_match & mask2_is_match &  
    mask3_is_match & mask4_is_match;  
    if (is_all_not_match) {  
        len -= 4;  
        cstr += 4;  
        continue;  
    }  
  
    if (mask1_is_match)  
        return (char *)cstr;  
    else if (mask2_is_match)  
        return (char *) (cstr + 1);  
    else if (mask3_is_match)  
        return (char *) (cstr + 2);  
    return (char *) (cstr + 3);  
}
```



False



```
while (len >= 4) {
    uint32_t inp = *(uint32_t *)cstr;
    uint32_t mask = inp ^ (c * 0x01010101);

    const uint8_t &mask1_is_match = (!! (mask & 0x000000ff));
    const uint8_t &mask2_is_match = (!! (mask & 0x0000ff00));
    const uint8_t &mask3_is_match = (!! (mask & 0x00ff0000));
    const uint8_t &mask4_is_match = (!! (mask & 0xff000000));
    const auto &is_all_not_match = mask1_is_match & mask2_is_match &
    mask3_is_match & mask4_is_match;
    if (is_all_not_match) {
        len -= 4;
        cstr += 4;
    }
    return (char *) (cstr + 2);
    return (char *) (cstr + 3);
}
```

Why 4 variable?  
Collapsing them into an int is better?

```
while (len >= 4) {
```

```
    uint32_t inp = *(uint32_t *)cstr;  
    uint32_t mask = inp ^ (c * 0x01010101);
```

```
    const uint8_t &mask1_is_match = (!! (mask & 0x000000ff));
```

```
    const uint8_t &mask2_is_match = (!! (mask & 0x0000ff00));
```

```
    const uint8_t &mask3_is_match = (!! (mask & 0x00ff0000));
```

```
    const uint8_t &mask4_is_match = (!! (mask & 0xff000000));
```

```
    const auto &is_all_not_match = mask1_is_match & mask2_is_match &
```

```
    mask3_is_match & mask4_is_match;
```

```
    if (is_all_not_match) {
```

```
        len -
```

```
        cstr;
```

```
    }  
}
```

Why 4 variable?

Collapsing them into an int is better?

```
    return (char *) (cstr + 2);  
    return (char *) (cstr + 3);
```

.L2:

```
    cmp rdx, 3
```

```
    jbe .L6
```

```
    mov ecx, DWORD PTR [rax]
```

```
    xor ecx, 707406378
```

```
    mov esi, ecx
```

```
    movzx edi, cl
```

```
    and esi, 65280
```

```
    cmp ecx, 16777215
```

```
    seta r9b
```

```
    and ecx, 16711680
```

```
    setne r8b
```

```
    test r9b, r8b
```

```
    je .L7
```

```
    test esi, esi
```

```
    setne r9b
```

```
    test edi, edi
```

```
    setne r8b
```

```
    test r9b, r8b
```

```
    jne .L39
```

OoOE  
Friendly

## Flags Affected

The OF and CF flags are cleared; the SF, ZF, and PF flags are set according to the result. The state of the AF flag is undefined.

## Protected Mode Exceptions

#GP(0)

If the destination operand points to a non-writable segment.

If the destination operand points to a non-writable segment.

```
test r9b, r9b  
jne .L39
```

[rax]

```
while
```

```
uint32_t  
uint32_t
```

```
const
```

```
const
```

```
const
```

```
const
```

```
const
```

```
max
```

```
if
```

```
C
```

```
return
```

```
}
```

```
while (len >= 4) {
    uint32_t inp = *(uint32_t *)cstr;
    uint32_t mask = inp ^ (c * 0x01010101);

    const uint8_t &mask1_is_match = (!(mask & 0x000000ff));
    const uint8_t &mask2_is_match = (!(mask & 0x0000ff00));
    const uint8_t &mask3_is_match = (!(mask & 0x00ff0000));
    const uint8_t &mask4_is_match = (!(mask & 0xff000000));
    const auto &is_all_not_match = mask1_is_match & mask2_is_match & mask3_is_match & mask4_is_match;
    if (is_all_not_match) {
        len -= 4;
        cstr += 4;
        continue;
    }

    if (mask1_is_match)
        return (char *)cstr;
    else if (mask2_is_match)
        return (char *) (cstr + 1);
    else if (mask3_is_match)
        return (char *) (cstr + 2);
    return (char *) (cstr + 3);
}
```

```
while (len >= 4) {
    uint32_t inp = *(uint32_t *)cstr;
    uint32_t mask = inp ^ (c * 0x01010101);

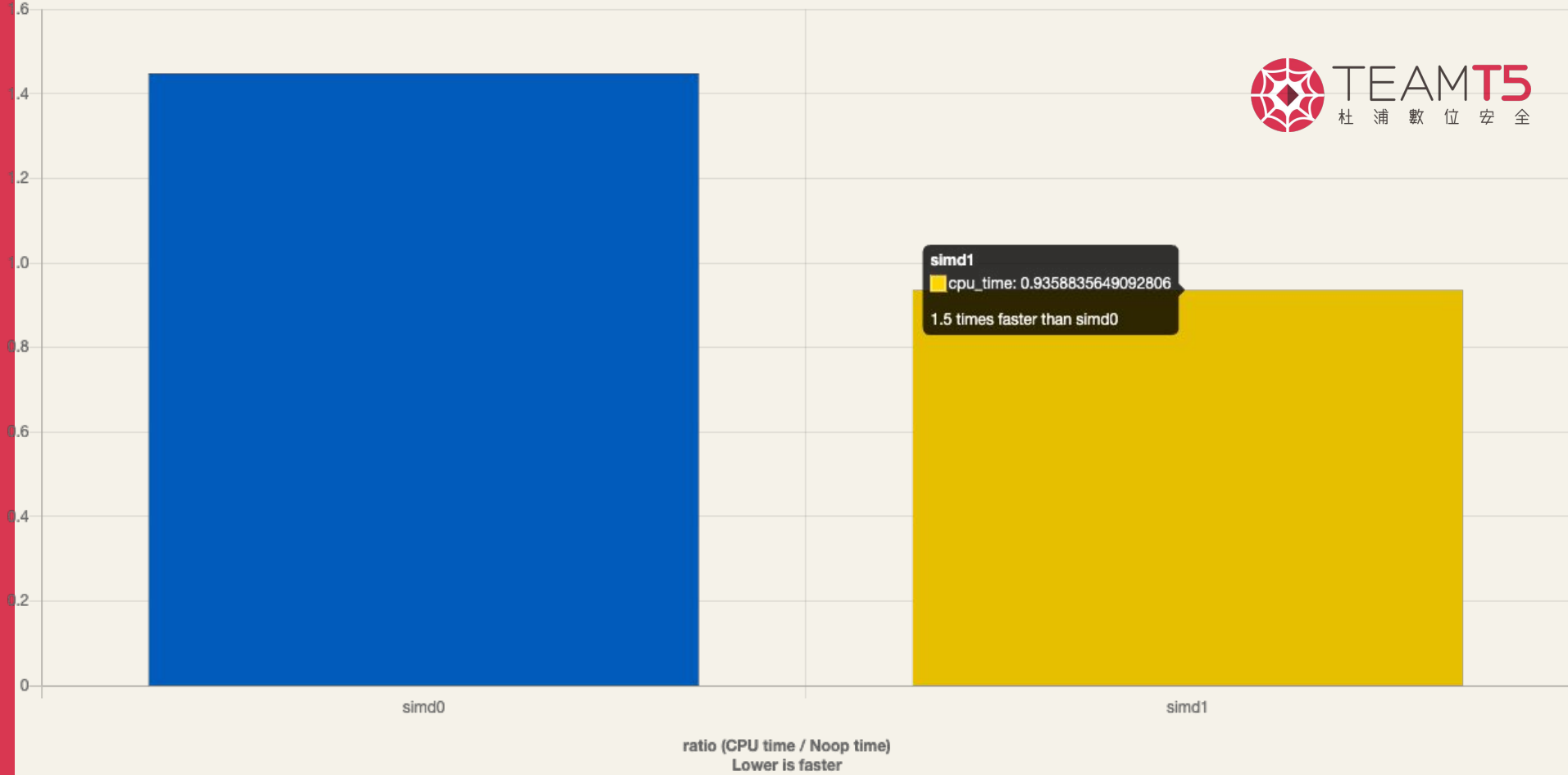
    const uint8_t &mask1_is_match = (!!(mask & 0x000000ff));
    const uint8_t &mask2_is_match = (!!(mask & 0x0000ff00));
    const uint8_t &mask3_is_match = (!!(mask & 0x00ff0000));
    const uint8_t &mask4_is_match = (!!(mask & 0xff000000));
    const auto &is_all_not_match = mask1_is_match & mask2_is_match & mask3_is_match & mask4_is_match;
    if (is_all_not_match) {
        len -= 4;
        cstr += 4;
        continue;
    }

    if (mask1_is_match)
        return (char *)cstr;
    else if (mask2_is_match)
        return (char *) (cstr + 1);
    else if (mask3_is_match)
        return (char *) (cstr + 2);
    return (char *) (cstr + 3);
}
```

### Why if-else here?

I have no idea currently, it ran faster on my laptop and PC.

Maybe it can be calculated parallely.



# Thank you for your listening.

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