

New Features of IEEE Std 1666-2011
SystemC

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Introduction



This presentation briefly describes all of the significant new features introduced in IEEE Std 1666-2011, the SystemC Language Reference Manual, and implemented in the Accellera Systems Initiative proof-of-concept simulator version 2.3.x

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John Aynsley, Doulos, 9-May-2012



Contents





- Process Control
- Stepping and Pausing the Scheduler
- sc_vector
- Odds and Ends
- TLM-2.0
- SystemC and O/S Threads



Process Control



- suspend
- o resume
- disable
- o enable
- sync_reset_on
- sync_reset_off
- o reset
- o kill
- o throw_it

- reset_event
- sc_unwind_exception
- sc_is_unwinding

- reset_signal_is
- async_reset_signal_is



Framework for Examples



```
struct M: sc module
 M(sc module name n)
    SC THREAD(calling);
    SC THREAD(target);
 void calling()
 void target()
  SC HAS PROCESS (M);
```

```
int sc_main(int argc, char* argv[])
{
    M m("m");
    sc_start(500, SC_NS);
    return 0;
}
```



Events



```
M(sc_module_name n)
{
   SC_THREAD(calling);
   SC_THREAD(target);
}
```

```
sc_event ev;
```

```
void calling()
{
   ev.notify(5, SC_NS);
}
```

```
void target()
{
    while (1)
    {
        wait(ev);
        cout << sc_time_stamp();
    }
}</pre>
```



Process Handles



```
M(sc_module_name n)
{
   SC_THREAD(calling);
   SC_THREAD(target);
   t = sc_get_current_process_handle();
}
```

```
sc_process_handle t;
```

```
void calling()
{
   assert( t.valid() );
   cout << t.name();
   cout << t.proc_kind();
}
m.target 2</pre>
```

```
void target()
{
   while (1)
   {
     wait(100, SC_NS);
     cout << sc_time_stamp();
   }
}</pre>
```



suspend & resume



```
void calling()
  wait(20, SC_NS);
                     at 20
  t.suspend();
  wait(20, SC NS);
                     at 40
  t.resume();
  wait(110, SC NS);
  t.suspend();
                     at 150
  wait(200, SC NS);
  t.resume();
                     at 350
```

```
void target()
{
   while (1)
   {
     wait(100, SC_NS);
     cout << sc_time_stamp();
   }
}</pre>
```



suspend & resume



```
void calling()
  wait(20, SC_NS);
                     at 20
  t.suspend();
  wait(20, SC NS);
                     at 40
  t.resume();
  wait(110, SC NS);
  t.suspend();
                     at 150
  wait(200, SC_NS);
  t.resume();
                     at 350
```

```
void tick() {
   while (1) {
     wait(100, SC_NS);
     ev.notify();
   }
}
```

```
void target()
{
   while (1)
   {
     wait(ev);
     cout << sc_time_stamp();
   }
}</pre>
```



disable & enable



```
void calling()
  wait(20, SC_NS);
                     at 20
  t.disable();
  wait(20, SC NS);
  t.enable();
                     at 40
  wait(110, SC NS);
  t.disable();
                     at 150
  wait(200, SC NS);
  t.enable();
                     at 350
```

```
SC_THREAD(target);
sensitive << clock.pos();</pre>
```

```
void target()
{
    while (1)
    {
        wait();
        cout << sc_time_stamp();
    }
}</pre>
```



suspend versus disable



```
void calling()
{
    ...
    t.suspend();
    ...
    t.resume();
    ...
}
```

- Clamps down process until resumed
- Still sees incoming events & time-outs
- Unsuitable for clocked target processes
- Building abstract schedulers

```
void calling()
{
    ...
    t.disable();
    ...
    t.enable();
    ...
}
```

- Disconnects sensitivity
- Runnable process remains runnable
- Suitable for clocked targets
- Abstract clock gating



An Abstract Scheduler



```
M(sc_module_name n)
{
   SC_THREAD(scheduler);
   for (int i = 0; i < n; i++)
     task_handle[i] = sc_spawn(sc_bind(&M::task, this , i));
}</pre>
```

```
sc_process_handle task_handle[n];
```

```
void scheduler() {
  for (int i = 0; i < n; i++)
    task_handle[i].suspend();
  while (1)
  for (int i = 0; i < n; i++) {
    task_handle[i].resume();
    wait(timeslot);
    task_handle[i].suspend();
  }
}</pre>
```

```
void task(int number)
{
  while (1)
  {
    ...
    sc_time busy_for;
    wait(busy_for);
    ...
  }
}
```

Abstract Clock Gating



```
M(sc_module_name n)
{
   SC_CTHREAD(calling, clk.pos());
   SC_CTHREAD(target, clk.pos());
   t = sc_get_current_process_handle();
}
```

```
void calling()
{
    while (1)
    {
        wait();
        t.disable();

    wait();
        t.enable();

    wait();
        q = 1
        t.enable();
}
```

```
int q;
```

```
void target()
{
   int q = 0;
   while (1)
   {
      wait();
     ++q;
   }
}
```



Scheduling



```
void calling1()
{
   t.suspend();
}
```

Target suspended immediately

```
void calling2()
{
   t.resume();
}
```

Target runnable immediately, may run in current eval phase

```
void target()
{
  while (1)
  {
    wait(ev);
    ...
}
```

```
void calling3()
{
   t.disable();
}
```

Sensitivity disconnected immediately, target may run in current eval phase

```
void calling4()
{
   t.enable();
}
```

Sensitivity reconnected immediately, never itself causes target to run



Self-control



```
M(sc module name n)
  SC THREAD(thread proc);
    t = sc get current process handle();
  SC METHOD (method proc);
    m = sc_get_current_process_handle();
```

```
void method proc()
void thread proc()
                                                     Non-blocking
                   Blocking
                                    m.suspend();
  t.suspend();
  t.disable();
                                    m.disable();
                                                     Non-blocking
                  Non-blocking
  wait(...);
```

sync_reset_on/off



```
SC_THREAD(calling);
SC_THREAD(target);
t = sc_get_current_process_handle();
```

```
void calling() {
  wait(10, SC NS);
  ev.notify();
                      ++q
  wait(10, SC NS);
  t.sync reset on();
  wait(10, SC NS);
                      q = 0
  ev.notify();
  wait(10, SC NS);
  t.sync reset off();
  wait(10, SC NS);
  ev.notify();
                       ++q
```

```
void target()
{
    q = 0;
    while (1)
    {
        wait(ev);
        ++q;
    }
}
```

Wakes at 10 30 50



Interactions



```
void calling()
  t.suspend();
  t.sync reset on();
  t.suspend();
  t.disable();
  t.sync reset off();
  t.resume();
  t.enable();
  t.resume();
```

3 independent flags

- disable / enable (highest priority)
- suspend / resume
- sync_reset_on / off (lowest priority)

```
void target()
{
    q = 0;
    while (1)
    {
        wait(ev);
        ++q;
    }
}
```



Forbidden Interactions



- Suspend does not play with disable
- Suspend does not play with sync_reset_on
- Suspend does not play with clocked threads
- Disable does not play with time-outs
- All implementation-defined
- Disable and sync_reset_on play together



Process Control



- o suspend
- o resume
- o disable
- o enable
- o sync_reset_on
- o sync_reset_off
- o reset
- o kill
- o throw_it

- o reset_event
- sc_unwind_exception
- sc_is_unwinding

- o reset_signal_is
- o async_reset_signal_is



reset and kill



```
SC_THREAD(calling);
SC_THREAD(target);
t = sc_get_current_process_handle();
```

```
void calling()
  wait(10, SC NS);
  ev.notify();
  wait(10, SC NS);
  t.reset();
  wait(10, SC_NS);
  ev.notify();
  wait(10, SC NS);
  t.kill();
```

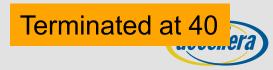
```
++q
```

```
q = 0
```

```
++q
```

```
void target()
{
    q = 0;
    while (1)
    {
        wait(ev);
        ++q;
    }
}
```

Wakes at 10 20 30



reset and kill are Immediate



```
void calling()
 wait(10, SC NS);
  ev.notify();
  assert(q == 0);
                           ++q
 wait(10, SC NS);
  assert( q == 1 );
                          q = 0
  t.reset();
  assert(q == 0);
 wait(10, SC NS);
  t.kill();
  assert( t.terminated() );
             Forever
```

```
int q;
```

```
void target()
{
    q = 0;
    while (1)
    {
        wait(ev);
        ++q;
    }
}
```

Cut through suspend, disable

Disallowed during elaboration



Unwinding the Call Stack



SYSTEMS INITIATIVE

```
void target()
  q = 0;
  while (1)
    try {
                                                            kill()
                                                 reset()
      wait(ev);
      ++q;
    catch (const sc_unwind_exception& e)
```

Unwinding the Call Stack



```
void target()
  q = 0;
  while (1)
    try {
                                                            kill()
      wait(ev);
                                                 reset()
      ++q;
    catch (const sc unwind exception& e)
      sc assert( sc is unwinding() );
      if (e.is reset()) cout << "target was reset";</pre>
      else
                           cout << "target was killed";</pre>
```

Unwinding the Call Stack



```
void target()
  q = 0;
  while (1)
    try {
                                                 reset()
                                                             kill()
      wait(ev);
      ++q;
    catch (const sc_unwind_exception& e)
       sc assert( sc is unwinding() );
       if (e.is reset()) cout << "target was reset";</pre>
      else
                           cout << "target was killed";</pre>
      proc handle.reset();
                                Resets some other process
       throw e;
                  Must be re-thrown
```

reset_event



```
SC_THREAD(calling);
SC_THREAD(target);
t = sc_get_current_process_handle();

SC_METHOD(reset_handler);
dont_initialize();
sensitive << t.reset_event();

SC_METHOD(kill_handler);
dont_initialize();
sensitive << t.terminated_event();</pre>
```

```
void calling()
{
   wait(10, SC_NS);
   t.reset();
   wait(10, SC_NS);
   t.kill();
   ...
```

```
void target()
{
    ...
    while (1)
    {
        wait(ev);
        ...
    }
}
```

Suicide



```
void target()
  q = 0;
  while (1)
    wait(ev);
    ++q;
    if (q == 5)
      handle = sc_get_current_process_handle();
      handle.kill();
                                  Never executes this line
      assert( false );
```



throw_it



std::exception recommended

```
std::exception ex;
```

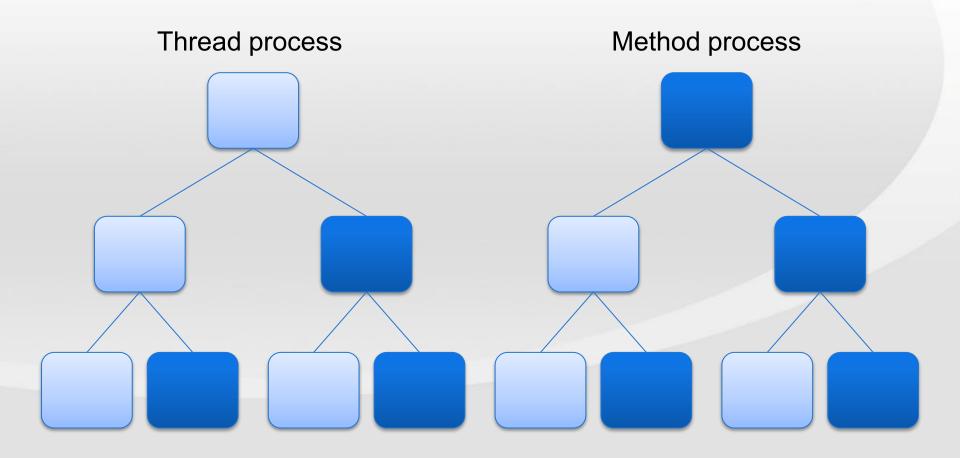
```
void calling()
{
    ...
    t.throw_it(ex);
    ...
}
```

Immediate - 2 context switches

```
void target()
  q = 0;
  while (1) {
    try {
      wait(ev);
                        Must catch exception
      ++q;
    catch (const std::exception& e)
      if (...)
         ; // wait(ev);
      else
         return;
                   May continue or terminate
```

Include Descendants







Include Descendants



```
M(sc_module_name n)
{
   SC_THREAD(calling);
   t = sc_spawn(sc_bind(&M::child_thread, 3));
   m = sc_spawn(sc_bind(&M::child_method, 3), "m", &opt);
}
```

```
void child_thread(int level)
{
  if (level > 0) {
    sc_spawn(sc_bind(&M::child_thread, level - 1));
    sc_spawn(sc_bind(&M::child_method, level - 1), "m", &opt);
  }
  while (1)
  {
    wait(ev);
    ...
  }
}
```

Include Descendants



```
void calling()
{
  wait(10, SC_NS);
  t.suspend();

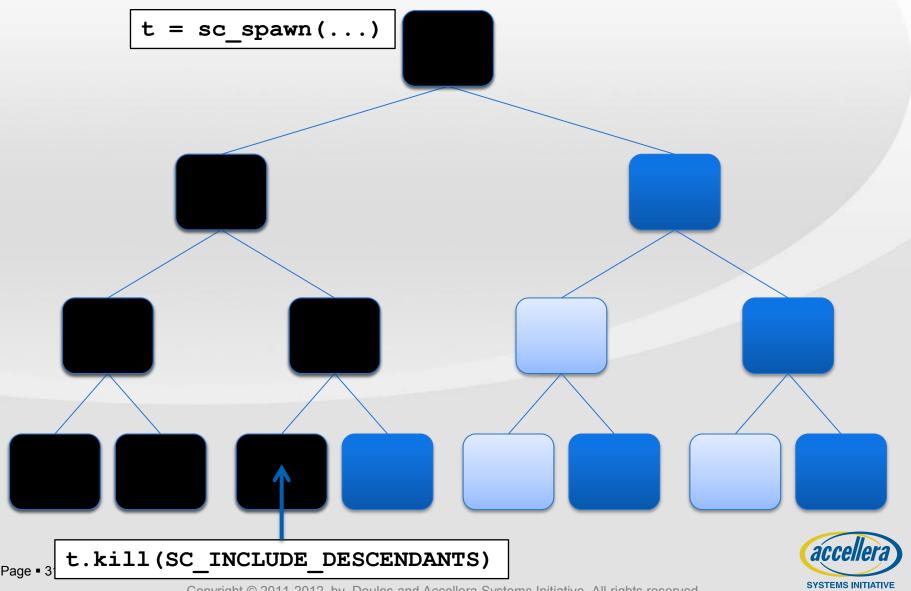
wait(10, SC_NS);
  t.suspend(SC_INCLUDE_DESCENDANTS);
  Null action on t itself
}
```

```
void child_thread(int level)
{
    ...
    if (...)
        t.kill(SC_INCLUDE_DESCENDANTS); Kills itself!
    ...
}
```



Attempted Genocide





Process Control



- o suspend
- o resume
- o disable
- o enable
- o sync_reset_on
- o sync_reset_off
- o reset
- o kill
- o throw_it

- o reset_event
- sc_unwind_exception
- o sc_is_unwinding

- reset_signal_is
- async_reset_signal_is



Styles of Reset

```
handle.reset();
handle.sync_reset_on();
handle.sync reset off();
SC THREAD(target);
reset signal is(reset, active level);
async reset signal is(reset, active level);
sc spawn options opt;
opt.reset_signal_is(reset, active_level);
opt.async reset signal is(reset, true);
```

Styles of Reset



```
SC_THREAD(target);
  sensitive << ev;
  reset_signal_is(sync_reset, true);
  async_reset_signal_is(async_reset, true);</pre>
```

```
t.reset();
t.sync reset on();
ev.notify();
t.sync reset off();
sync reset = true;
ev.notify();
sync reset = false;
async_reset = true;
ev.notify();
```

Effectively

```
t.reset();
t.reset();
t.reset();
t.reset();
t.reset();
```



Processes Unified!



```
SC_METHOD(M);
sensitive << clk.pos();
reset_signal_is(r, true);
async_reset_signal_is(ar, true);</pre>
```

```
SC_THREAD(T);
sensitive << clk.pos();
reset_signal_is(r, true);
async_reset_signal_is(ar, true);</pre>
```

```
SC_CTHREAD(T, clk.pos());
  reset_signal_is(r, true);
  async_reset_signal_is(ar, true);
```

```
void M() {
  if (r|ar)
    q = 0;
  else
    ++q
}
```

```
void T() {
   if (r|ar)
      q = 0;
   while (1)
   {
      wait();
      ++q;
   }
}
```



Reset Technicalities



- Can have any number of sync and async resets
- Reset clears dynamic sensitivity and restores static sensitivity
- Reset wipes the slate clean for resume
- Method process called when reset
 - Synchronous reset resets sensitivity
 - else can only mean clock

- Clocked threads not called during initialization
- Clocked threads sensitive to one clock

```
void M() {
   if (reset)
      q = 0;
   else
     ++q
}
```

```
void T() {
   if (reset)
      q = 0;
   while (1)
      ...
}
```

Processes in Containers



```
#include <map>
typedef std::map<sc_process_handle, int> proc_map_t;
proc_map_t all_procs;
```

```
SC_THREAD(proc);
handle = sc_get_current_process_handle();
all_procs[handle] = ++num;
```



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Process Control



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Stepping Simulation



```
int sc_main(...)
  Top top("top");
                          Simulation time = 10ns?
  sc start(10, NS);
  sc_start(0, SC_NS); Did anything happen?
                          Simulation time = max time?
  sc start();
                          Nothing left to do?
  sc start();
```



Event Starvation



```
int sc_main(...)
 Top top("top");
  sc time period(10, SC NS);
  sc start(period);
  sc_start(period, SC_RUN_TO TIME);
  sc start(period, SC EXIT ON STARVATION)
  sc start();
  sc start();
```

Time = end time

Don't run processes at end time

Time = latest event



sc_start(0)



```
int sc_main(...)
 Top top("top");
  sc_start(0, SC_NS);
  sc_start(0, SC_NS);
```

Initialization phase

Evaluation phase

Update phase

Delta notification phase

Evaluation phase

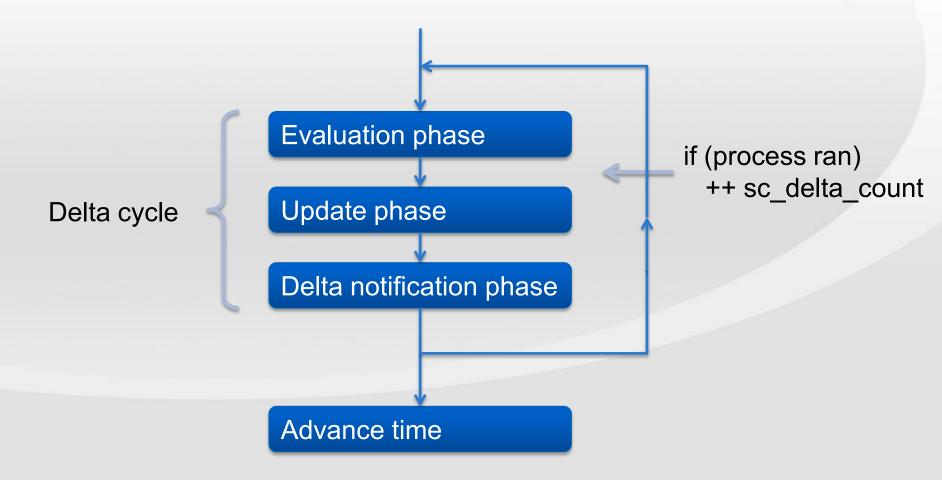
Update phase

Delta notification phase



The Delta Cycle







Pending Activity



Pseudo-code

```
sc_time_sc_time_to_pending_activity()
{
  if ( sc pending activity at current time() )
    return SC ZERO TIME;
 else if ( sc_pending_activity_at_future_time() )
    return (time of earliest event) - sc time stamp();
 else
    return sc_max_time() - sc_time_stamp();
```



Single Stepping the Scheduler SYSTEMC

```
int sc_main(...) {
  Top top("top");

... Create some activity

while (sc_pending_activity())
  sc_start(sc_time_to_pending_activity());
}
```

- Either run one delta cycle at current time
- or advance simulation time but don't run any processes



Pausing Simulation



```
int sc_main(...)
                                          void thread process()
        Top top("top");
        sc start();
                            End of delta
                                                             Non-blocking
                                             sc_pause();
sc_spawn()
                                            wait(...);
request_update()
notify()
                                             sc pause();
suspend()
                           End of delta
                                            wait(...);
        sc start();
```



Simulation Status

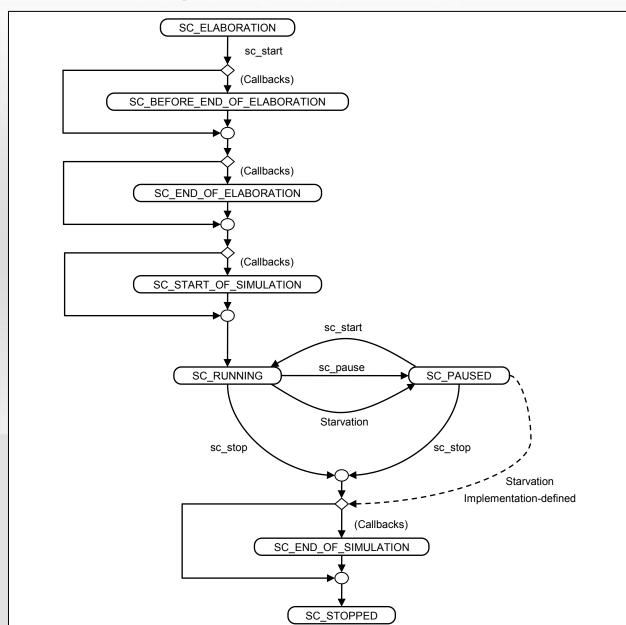


```
int sc main(...)
  Top top("top");
  assert( sc_get_status() == SC_ELABORATION );
  sc start();
  assert( sc_get_status() == SC_PAUSED );
  sc start();
  sc start();
  assert( sc_get_status() == SC_STOPPED );
```



Simulation Status







Immediate Notification



```
SC_THREAD(target);
sensitive << ev;</pre>
```

```
void target()
  assert( sc delta count() == 0 );
  wait(SC ZERO TIME);
  assert( sc_delta_count() == 1 );
  ev.notify(5, SC NS);
  assert( sc time to pending activity()
                                           Assuming!
                == sc time(5, SC NS));
  wait(ev);
  ev.notify();
                           Process does not awake
  wait(ev);
  sc assert( false );
```



Contents



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Array of Ports or Signals



```
struct Child: sc_module
{
   sc_in<int> p[4];
   ...
```

Ports cannot be named

```
struct Top: sc module
  sc signal<int> sig[4];
 Child* c;
  Top(sc module name n)
    c = new Child("c");
    c->p[0].bind(sig[0]);
    c->p[1].bind(sig[1]);
    c->p[2].bind(sig[2]);
    c->p[3].bind(sig[3]);
```

Signals cannot be named



Array or Vector of Modules



```
struct Child: sc module
  sc in<int> p;
                  struct Top: sc module
                    sc signal<int> sig[4];
                    std::vector<Child*> vec;
                    Top(sc module name n) {
                      vec.resize(4);
                      for (int i = 0; i < 4; i++)
                        std::stringstream n;
                        n << "vec " << i;
                        vec[i] = new Child(n.str().c str(), i);
                        vec[i]->p.bind(sig[i]);
                              Modules not default constructible
```

sc_vector of Ports or Signals (SYSTEM CT

```
struct Child: sc module
  sc vector< sc in<int> > port vec;
 Child(sc module name n)
  : port vec("port vec", 4)
                                 Elements are named
```

```
struct Top: sc module
  sc vector< sc signal<int> > sig vec;
  Child* c;
  Top(sc module name n)
                                  Size passed to ctor
  : sig vec("sig vec", 4)
    c = new Child("c");
    c->port_vec.bind(sig vec); | Vector-to-vector bind
```

sc vector of Modules



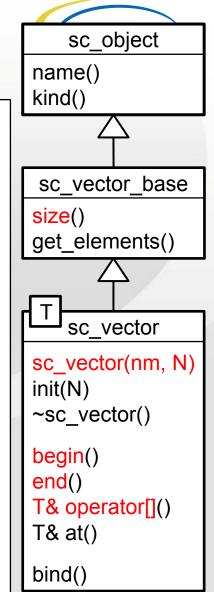
```
struct Child: sc_module
{
   sc_in<int> p;
   ...
```

```
struct Top: sc module
  sc vector< sc signal<int> > sig vec;
  sc vector< Child > mod vec;
  Top(sc module name n)
  : sig vec("sig vec")
                                   Elements are named
   mod vec("mod vec")
    sig vec.init(4);
                                   Size deferred
    mod vec.init(4);
    for (int i = 0; i < 4; i++)
      mod vec[i]->p.bind(sig_vec[i]);
```



sc_vector methods

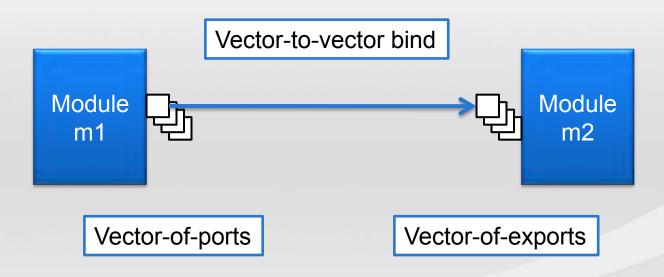
```
struct M: sc module
  sc vector< sc signal<int> > vec;
 M(sc module name n)
  : vec("vec", 4) {
    SC THREAD (proc)
  void proc() {
    for (unsigned int i = 0; i < vec.size(); i++)</pre>
      vec[i].write(i);
    wait(SC ZERO TIME);
    sc vector< sc signal<int> >::iterator it;
    for (it = vec.begin(); it != vec.end(); it++)
      cout << it->read() << endl;</pre>
```





Binding Vectors



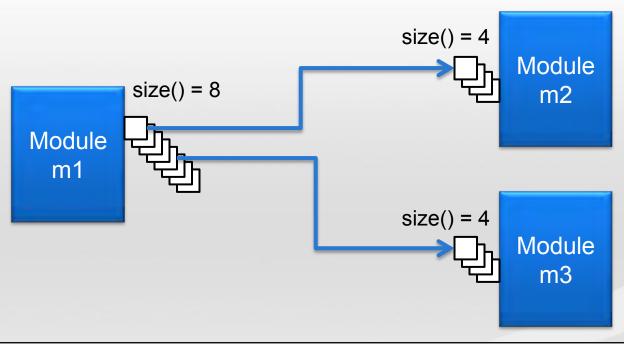


m1->port_vec.bind(m2->export_vec);



Partial Binding

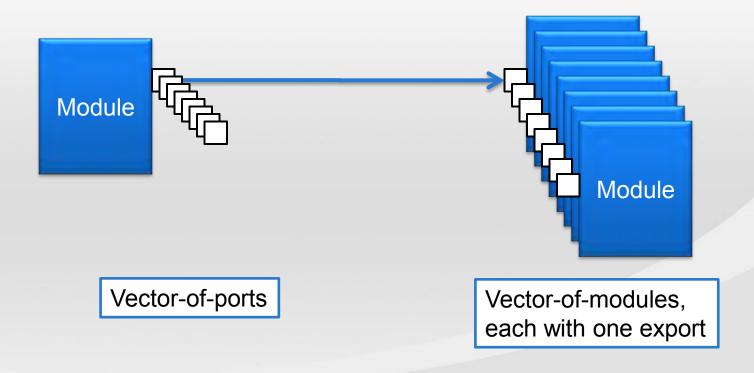




accentra

sc_assemble_vector





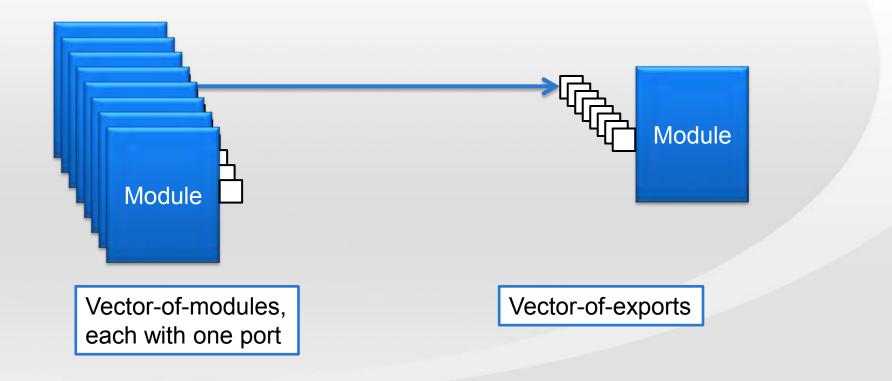
```
init->port_vec.bind(
    sc_assemble_vector(targ_vec, &Target::export) );
```

Substitute for a regular vector



sc_assemble_vector







Constructor Arguments



```
struct M: sc_module
{
   M(sc_module_name n, int a, bool b);
   ...
```

```
sc_vector<M> mod_vec;
```

```
static M* creator_func( const char* name, size_t s )
{
  return new M(name, 3, true);
}
Pass args to constructor
```

```
mod_vec.init(4, creator_func);
```



Fancy Variant 1



```
struct M: sc_module
{
   M(sc_module_name n, int a, bool b);
   ...
```

```
sc_vector<M> mod_vec;
```

```
M* creator_func( const char* name, size_t s )
{
   return new M(name, 3, true);
}
Pass args to constructor
```



Fancy Variant 2



```
struct M: sc_module
{
   M(sc_module_name n, int a, bool b);
   ...
```

```
sc_vector<M> mod_vec;
```

```
struct creator {
   creator(int a, bool b): m_a(a), m_b(b) {}
   int m_a;
   bool m_b;
   M* operator() (const char* name, size_t) {
     return new M(name, m_a, m_b);
   }
};
Pass args to constructor
```

```
mod_vec.init(4, creator(3, true));
```



sc_vector Restrictions



- Restricted to sc_vector<derived_from_sc_object>
- Elements become children of vector's parent
- Cannot be resized
- Cannot be copied or assigned



Contents



- Process Control
- Stepping and Pausing the Scheduler
- sc_vector



- Odds and Ends
- TLM-2.0
- SystemC and O/S Threads



Odds and Ends



- Event List Objects
- Named Events
- sc_writer_policy
- Verbosity
- Virtual Bind
- Other Enhancements



Waiting on a List of Events



```
sc_port<sc_signal_in_if<int>, 0> port;
...

void thread_process()
{
   wait(port[0] | port[1] | port[2] | ...);
   ...
}
Not expressible in SystemC
```



Event List Objects



```
Multiport
sc port<sc signal in if<int>, 0> port;
void thread process()
  sc_event_or_list or_list;
  for (int i = 0; i < port.size(); i++)
    or list |= port[i]->default event();
  wait(or_list);
```



Event List Technicalities



```
sc_event ev1, ev2, ev3, ev4;
```

```
sc event or list or list;
                                     Can't mix them up
sc event and list and list = ev1;
assert( or list.size() == 0 );
assert( and list.size() == 1 );
or list = ev1;
or_list = or_list | ev2 | ev3;
or list |= ev4;
assert( or list.size() == 4 );
and list &= ev2 & ev2 & ev2;
                                    Duplicates don't count
assert( and_list.size() == 2 );
wait(or list);
                     List must be valid when process resumes
wait(and list);
```

Named Events



```
struct M: sc module
 sc event my event;
 M(sc module name n)
  : my event("my event")
    assert( my event.in hierarchy() );
    assert( my event.get parent object() == this );
    assert( sc find event("top.my event") == &my event );
    std::vector<sc event*> vec = this->get child events();
    assert( vec.size() == 1 );
```

Events created during elab are named

Events are not sc_objects

Run-Time Events



```
struct M: sc module
 M(sc_module_name n) { SC_THREAD(proc); }
  void proc()
    sc event ev1("ev1");
    assert( ev1.in hierarchy() );
    sc event ev2;
    assert( !ev2.in_hierarchy() );
                                      Implementation-defined
                                       for performance
    cout << ev2.name();</pre>
```



Kernel Events



```
struct M: sc module
  sc event
                my event;
  sc signal<bool> my sig;
  M(sc module name n)
  : my event("my event")
  , my sig("my sig")
                                  Kernel events not hierarchically named
    cout << my sig.default event().name();</pre>
                    m.$$$kernel event$$$$ value changed event
    assert( sc hierarchical name exists("m.my event") );
    assert( sc hierarchical name exists("m.my sig") );
            sc object and sc event share the same namespace
```

(accellera)

sc_writer_policy



```
void proc1()
{
    sig1.write(1);
    wait(1, SC_NS);
    sig_many.write(3); OK
    wait(1, SC_NS);
    sig_many.write(4);
}
```

```
void proc2()
{

sig_many.write(2);

wait(1, SC_NS);

sig1.write(4);

wait(1, SC_NS);

sig_many.write(6);

Error

Py Doulos and Accellera Systems Initiative
```

sc_writer_policy/b_transport (SYSTEM CT



```
sc signal<int, SC MANY WRITERS> interrupt;
```

```
void b transport( tlm::tlm generic_payload& trans,
                  sc time& delay )
  tlm::tlm command cmd = trans.get command();
  sc dt::uint64 adr = trans.get address();
  if ( cmd == tlm::TLM WRITE COMMAND && adr == 0xFFFF)
    interrupt.write(level);
                                       Called from several initiators
  trans.set response status( tlm::TLM OK RESPONSE );
```



Verbosity Filter for Reports



```
enum sc_verbosity {
   SC_NONE = 0,
   SC_LOW = 100,
   SC_MEDIUM = 200,
   SC_HIGH = 300,
   SC_FULL = 400,
   SC_DEBUG = 500
};
```

Sets a global maximum

```
sc_report_handler::set_verbosity_level( SC_LOW );
```

```
SC_REPORT_INFO("msg_type", "msg");

Default is SC_MEDIUM

SC_REPORT_INFO_VERB("msg_type", "msg", SC_LOW);
```

Ignored if argument > global maximum



virtual bind



```
Relevant to all specialized ports
template<typename IF>
struct my port: sc core::sc port<IF> {
  typedef sc core::sc port<IF> base port;
  virtual void bind( IF& iface ) {
                                      Do something special
    base port::bind( iface );
                                   Don't override operator()
  using base_port::bind;
};
     struct M: sc module
       my port< sc fifo in if<int> > my fifo in;
     sc fifo<int> my fifo;
     M m("m");
                                 Call sc port<IF>::operator()
     m.my fifo in(my fifo);
```

Other Enhancements



- Certain fixed-point constructors made explicit
- Preprocessor macros to return SystemC version
- sc_mutex and sc_semaphore no longer primitive channels
- Asynchronous update requests for primitive channels



Contents



- Process Control
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- **TLM-2.0**
- SystemC and O/S Threads



```
#define SC DISABLE VIRTUAL BIND
                                        To run SystemC 2.3 with TLM-2.0.1
#include <systemc>
using namespace sc core;
                                          1666-2011 allows #include <tlm>
#include <tlm.h>
```

```
int sc main(int argc, char* argv[])
  #ifdef IEEE 1666 SYSTEMC
                                       2.3.0 pub rev 20111121-OSCI
    cout << SC VERSION << endl;</pre>
                                       20111121
    cout << SC VERSION RELEASE DATE
  #endif
                                       2.0.1 -TLMWG
  cout << TLM VERSION << endl;</pre>
  cout << TLM VERSION RELEASE DATE <- 20090715
  sc start();
  return 0;
```



TLM-2.0 Compliance



- TLM-2.0-compliant-implementation
- TLM-2.0-base-protocol-compliant
- TLM-2.0-custom-protocol-compliant



Generic Payload Option



Attribute	Transport	DMI	Debug		
Command	Yes	Yes	Yes		
Address	Yes	Yes	Yes		
Data pointer	Yes	No	Yes		
Data length	Yes	No	Yes		
Byte enable pointer	Yes	No	No		
Byte enable length	Yes	No	No		la la al a i a a
Streaming width	Yes	No	No		abled using option
DMI hint	Yes	No	No	3F_	
Response status	Yes	No	No		
Extensions	Yes	Yes	Yes		

Backward compatible with pre-IEEE version



set/get_gp_option



Initiator

```
trans->set_gp_option(TLM_FULL_PAYLOAD);
trans->set_streaming_width(4);
socket->transport_dbg( *trans );
```

Target

```
if (trans.get_gp_option() == TLM_FULL_PAYLOAD)
{
   trans.set_gp_option(TLM_FULL_PAYLOAD_ACCEPTED);
   trans.set_response_status( TLM_OK_RESPONSE );
}
```

Initiator

```
if (trans->get_gp_option() == TLM_FULL_PAYLOAD_ACCEPTED )
  if (trans->is_response_error())
   ...
```



gp_option Technicalities



- TLM_MIN_PAYLOAD
 - Default, backward compatible
 - All components ignore optional attributes
- TLM_FULL_PAYLOAD
 - Set by initiator for DMI and Debug only
 - Set all attributes to proper values
- TLM_FULL_PAYLOAD_ACCEPTED
 - Set by target
 - DMI & Debug response status used
 - Debug byte enables, streaming, and DMI hint used



Other Changes



- TLM_IGNORE_COMMAND used for custom commands
- Generic payload data array pointer may now be null
- Target may now return any value from transport_dbg

- Macro DECLARE_EXTENDED_PHASE is deprecated
- Renamed to TLM_DECLARE_EXTENDED_PHASE



Contents



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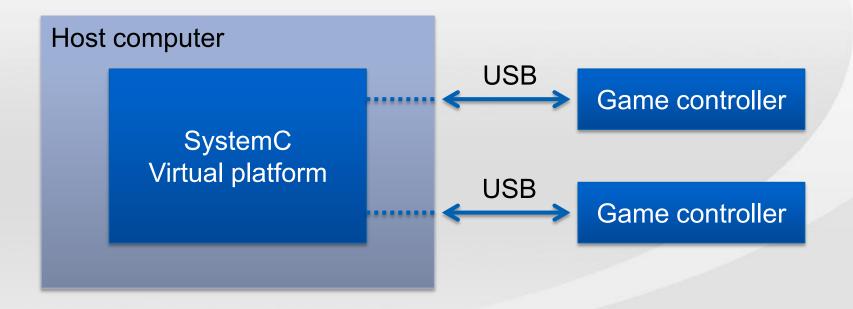


SystemC and O/S Threads



One Motivation





Expect near-real-time responsiveness



Co-operative Multitasking



```
SC_THREAD(thread1);
SC_THREAD(thread2);
```

```
void thread1()
 wait(0, SC_NS);
```

```
void thread2()
  while (1) {
    wait(ev1);
```



Co-operative Multitasking



```
SC_THREAD(thread1);
SC_THREAD(thread2);
```

```
void thread1()
  wait(0, SC_NS);
  while (1) {
    a = b + 1;
    ev1.notify();
    p = q + 1;
    wait(ev2);
```

```
void thread2()
  while (1) {
    wait(ev1);
```



Co-operative Multitasking



```
SC_THREAD(thread1);
SC_THREAD(thread2);
```

```
void thread1()
  wait(0, SC_NS);
  while (1) {
    a = b + 1;
    ev1.notify();
    p = q + 1;
    wait(ev2);
```

```
void thread2()
  while (1) {
    wait(ev1);
    ev2.notify();
    b = a + p;
    q = a - p;
```



Pre-emption



```
status = pthread_create(&p1, NULL, pthread1, NULL);
status = pthread_create(&p2, NULL, pthread2, NULL);
```

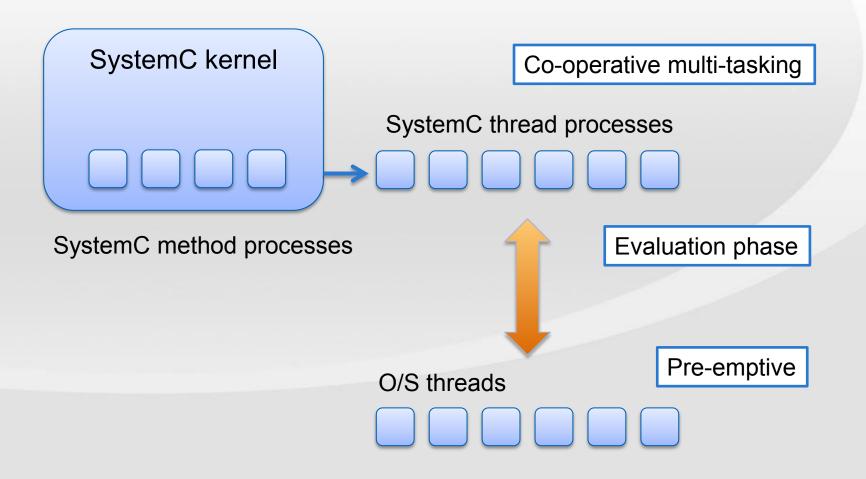
```
void* pthread1(void* v)
{
    while (1) {
        a = b + 1;
        sem_post(&sem1);
        p = q + 1;
        sem_wait(&sem2);
    }
}
```

```
void* pthread2(void* v)
{
    while (1) {
        sem_wait(&sem1);
        sem_post(&sem2);
        b = a + p;
        q = a - p;
    }
}
```



SystemC and O/S Threads







Creating a pthread



```
#include <pthread.h>
struct M: sc module
 pthread t pthread;
 M(sc module name n)
    int status;
    status = pthread create(&pthread, NULL, pth, this);
    SC THREAD (scth);
    sem_init(&empty, 0, 1);
    sem init(&full, 0, 0);
  ~M() { pthread_join( pthread, NULL ); }
```

pthread and SC_THREAD



```
void* pth(void* ptr)
                                      pthread - producer
  for (int i = 0; i < 8; i++)
    rendezvous put(i);
  return NULL;
void scth()
                                      SC_THREAD - consumer
  for (int i = 0 i < 8; i++)
    cout << rendezvous get() << endl;</pre>
    wait(1, SC NS);
```



Synchronization



```
#include <semaphore.h>
sem_t empty;
cannot use sc_semaphore
sem_t full;
int data;
```

```
sem_init(&empty, 0, 1);
sem_init(&full, 0, 0);
```

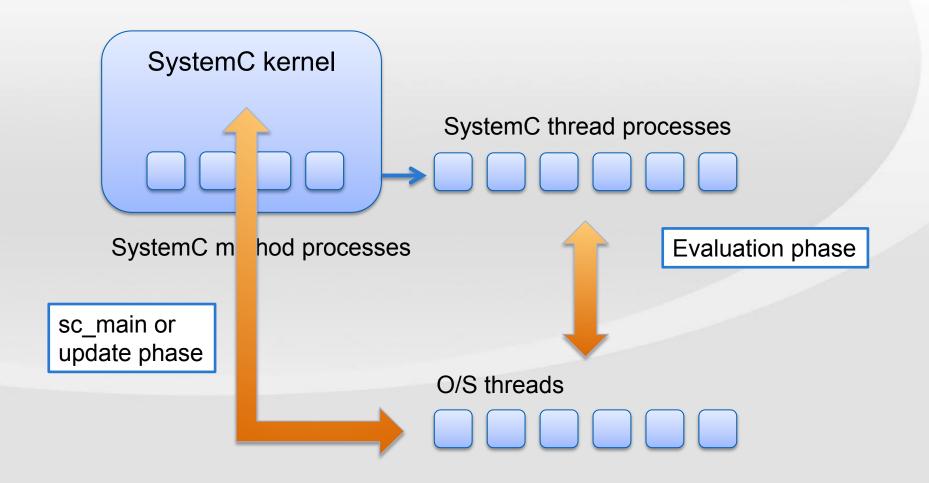
```
void rendezvous_put(int _data)
{
   sem_wait(&empty);
   data = _data;
   sem_post(&full);
}
```

```
int rendezvous_get()
{
  int result;
  sem_wait(&full);
  result = data;
  sem_post(&empty);
  return result;
}
```



Sync with Kernel







Thread-Safe Primitive Channel SYSTEM CT

```
struct thread safe channel: sc prim channel, IF
  thread safe channel(const char* name);
  virtual void write(int value);
                                      Callable from external threads
  virtual int read();
  virtual const sc event& default event() const;
protected:
  virtual void update();
private:
  int m current value;
  int m next value;
  sc event m value changed event;
};
```



async_request_update



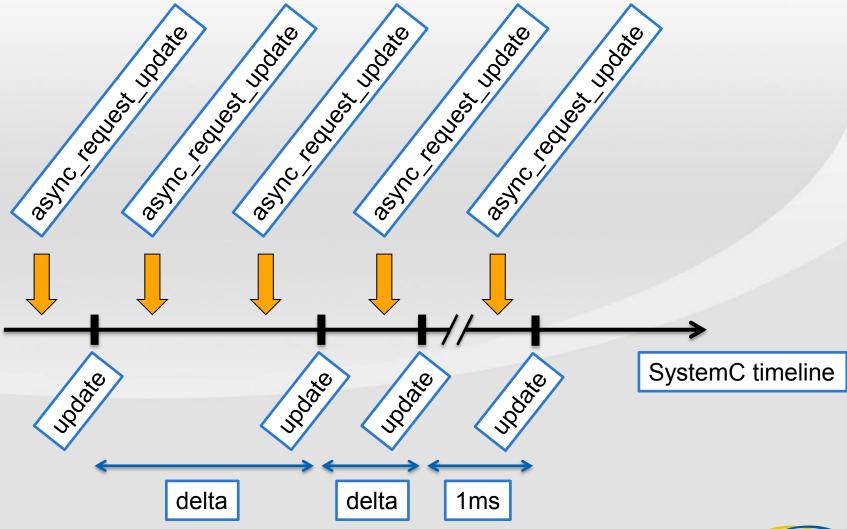
```
virtual void write(int value)
{
    ...
    m_next_value = value;
    async_request_update();
    ...
}
```

```
virtual void update()
{
    ...
    if (m_next_value != m_current_value)
    {
        m_current_value = m_next_value;
        m_value_changed_event.notify(SC_ZERO_TIME);
    }
    ...
}
```



async_request_update







Shared Memory



```
pthread_mutex_t mutex = PTHREAD_MUTEX_INITIALIZER;
```

```
virtual void write(int value) {
  pthread_mutex_lock(&mutex);
    m_next_value = value;
    async_request_update();
  pthread_mutex_unlock(&mutex);
}
Cannot use sc_mutex
```

```
virtual void update() {
   pthread_mutex_lock(&mutex);
   if (m_next_value != m_current_value) {
        m_current_value = m_next_value;
        m_value_changed_event.notify(SC_ZERO_TIME);
   }
   pthread_mutex_unlock(&mutex);
}
```





THE END

