

STM32 CubeMX

1. Description

1.1. Project

Project Name	ARD
Board Name	custom
Generated with:	STM32CubeMX 6.15.0
Date	12/18/2025

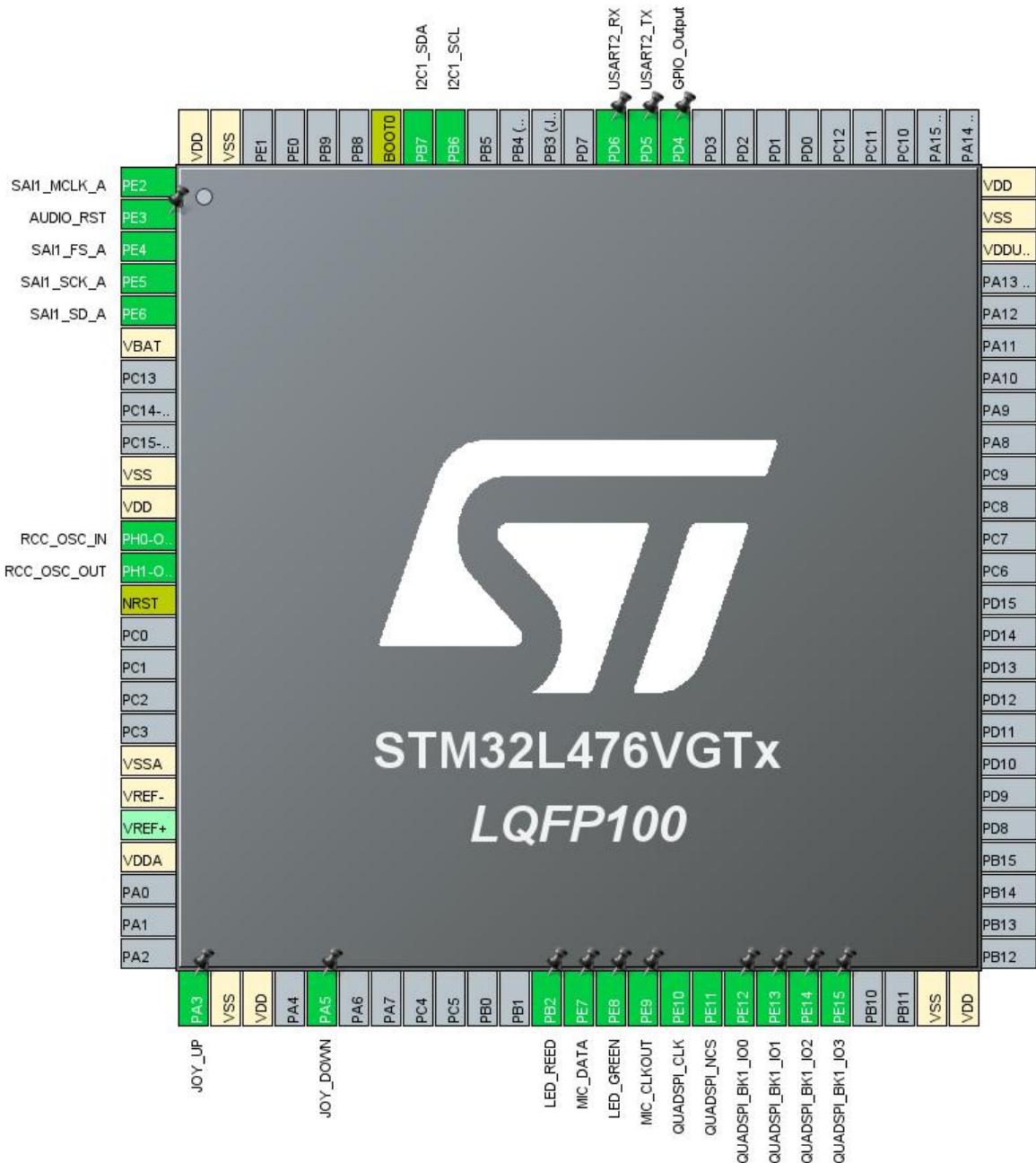
1.2. MCU

MCU Series	STM32L4
MCU Line	STM32L4x6
MCU name	STM32L476VGTx
MCU Package	LQFP100
MCU Pin number	100

1.3. Core(s) information

Core(s)	Arm Cortex-M4
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2. Pinout Configuration



3. Pins Configuration

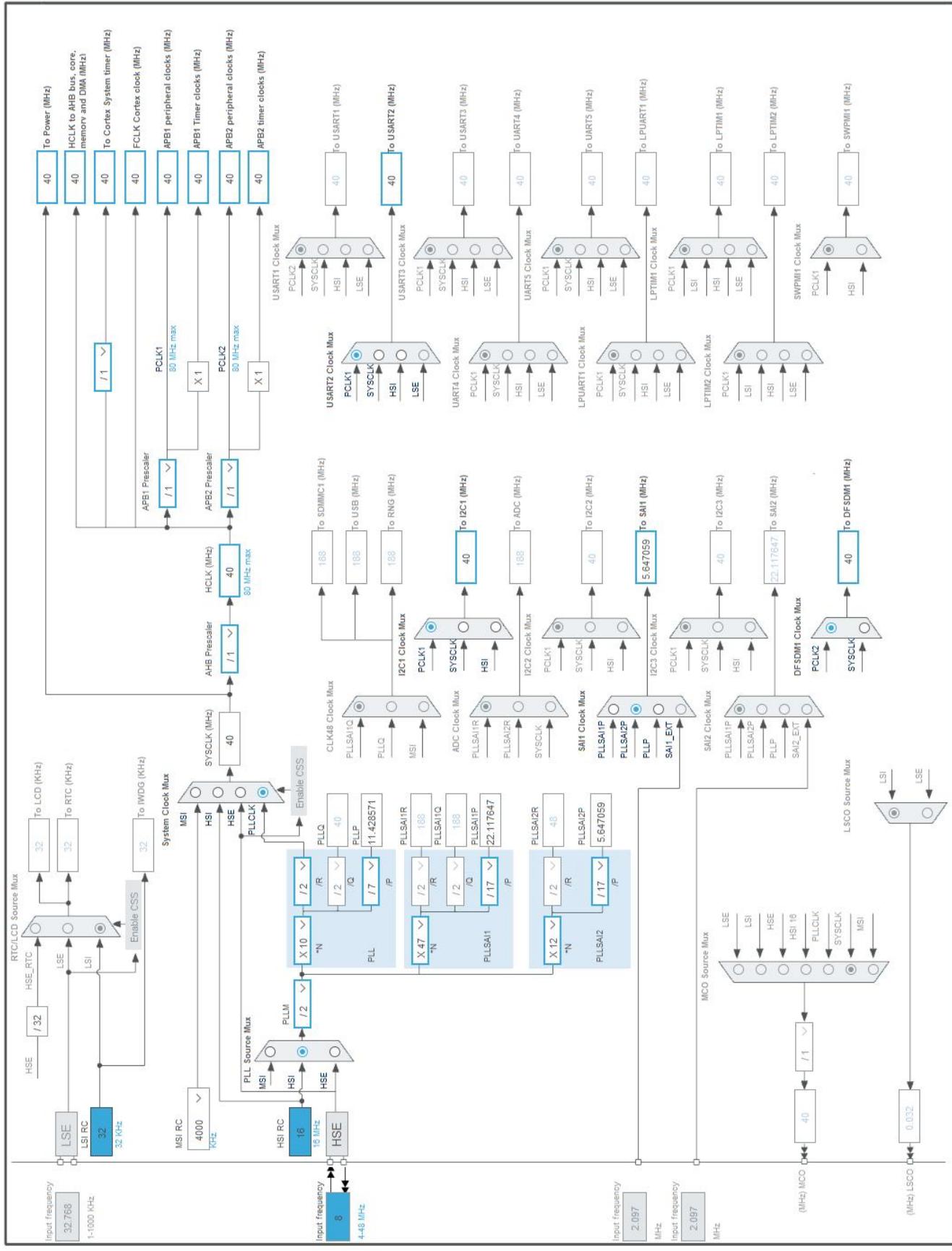
Pin Number LQFP100	Pin Name (function after reset)	Pin Type	Alternate Function(s)	Label
1	PE2	I/O	SAI1_MCLK_A	
2	PE3 *	I/O	GPIO_Output	AUDIO_RST
3	PE4	I/O	SAI1_FS_A	
4	PE5	I/O	SAI1_SCK_A	
5	PE6	I/O	SAI1_SD_A	
6	VBAT	Power		
10	VSS	Power		
11	VDD	Power		
12	PH0-OSC_IN (PH0)	I/O	RCC_OSC_IN	
13	PH1-OSC_OUT (PH1)	I/O	RCC_OSC_OUT	
14	NRST	Reset		
19	VSSA	Power		
20	VREF-	Power		
22	VDDA	Power		
26	PA3 *	I/O	GPIO_Input	JOY_UP
27	VSS	Power		
28	VDD	Power		
30	PA5 *	I/O	GPIO_Input	JOY_DOWN
37	PB2 *	I/O	GPIO_Output	LED_RED
38	PE7	I/O	DFSDM1_DATIN2	MIC_DATA
39	PE8 *	I/O	GPIO_Output	LED_GREEN
40	PE9	I/O	DFSDM1_CLKOUT	MIC_CLKOUT
41	PE10	I/O	QUADSPI_CLK	
42	PE11	I/O	QUADSPI_NCS	
43	PE12	I/O	QUADSPI_BK1_IO0	
44	PE13	I/O	QUADSPI_BK1_IO1	
45	PE14	I/O	QUADSPI_BK1_IO2	
46	PE15	I/O	QUADSPI_BK1_IO3	
49	VSS	Power		
50	VDD	Power		
73	VDDUSB	Power		
74	VSS	Power		
75	VDD	Power		
85	PD4 *	I/O	GPIO_Output	GPIO_Output
86	PD5	I/O	USART2_TX	
87	PD6	I/O	USART2_RX	

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Pin Number LQFP100	Pin Name (function after reset)	Pin Type	Alternate Function(s)	Label
92	PB6	I/O	I2C1_SCL	
93	PB7	I/O	I2C1_SDA	
94	BOOT0	Boot		
99	VSS	Power		
100	VDD	Power		

* The pin is affected with an I/O function

4. Clock Tree Configuration



1. Power Consumption Calculator report

1.1. Microcontroller Selection

Series	STM32L4
Line	STM32L4x6
MCU	STM32L476VGTx
Datasheet	DS10198_Rev4

1.2. Parameter Selection

Temperature	25
Vdd	3.0

1.3. Battery Selection

Battery	Li-SOCL2(A3400)
Capacity	3400.0 mAh
Self Discharge	0.08 %/month
Nominal Voltage	3.6 V
Max Cont Current	100.0 mA
Max Pulse Current	200.0 mA
Cells in series	1
Cells in parallel	1

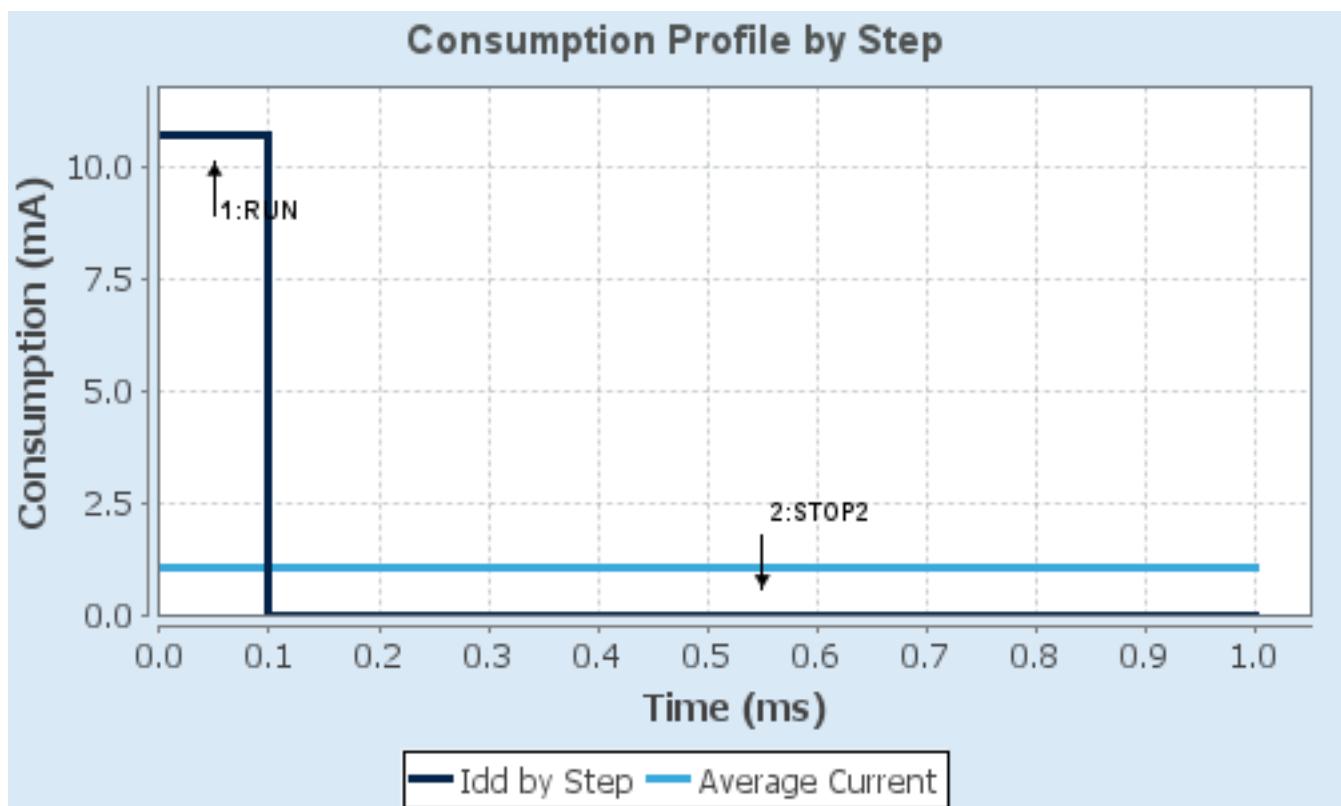
1.4. Sequence

Step	Step1	Step2
Mode	RUN	STOP2
Vdd	3.0	3.0
Voltage Source	Battery	Battery
Range	Range1-High	NoRange
Fetch Type	SRAM2	n/a
CPU Frequency	80 MHz	0 Hz
Clock Configuration	HSE PLL	ALL CLOCKS OFF
Clock Source Frequency	4 MHz	0 Hz
Peripherals		
Additional Cons.	0 mA	0 mA
Average Current	10.7 mA	1.18 µA
Duration	0.1 ms	0.9 ms
DMIPS	100.0	0.0
T_a Max	103.65	105
Category	In DS Table	In DS Table

1.5. Results

Sequence Time	1 ms	Average Current	1.07 mA
Battery Life	4 months, 10 days, 3 hours	Average DMIPS	100.0 DMIPS

1.6. Chart



2. Software Project

2.1. Project Settings

Name	Value
Project Name	ARD
Project Folder	C:\Users\xxkry\STM32CubeIDE\workspace_1.19.0\ARDrepo\Kod
Toolchain / IDE	STM32CubeIDE
Firmware Package Name and Version	STM32Cube FW_L4 V1.18.1
Application Structure	Advanced
Generate Under Root	Yes
Do not generate the main()	No
Minimum Heap Size	0x200
Minimum Stack Size	0x400

2.2. Code Generation Settings

Name	Value
STM32Cube MCU packages and embedded software	Copy only the necessary library files
Generate peripheral initialization as a pair of '.c.h' files	No
Backup previously generated files when re-generating	No
Keep User Code when re-generating	Yes
Delete previously generated files when not re-generated	Yes
Set all free pins as analog (to optimize the power consumption)	No
Enable Full Assert	No

2.3. Advanced Settings - Generated Function Calls

Rank	Function Name	Peripheral Instance Name
1	SystemClock_Config	RCC
2	MX_GPIO_Init	GPIO
3	MX_DMA_Init	DMA
4	MX_DFSDM1_Init	DFSDM1
5	MX_USART2_UART_Init	USART2
6	MX_I2C1_Init	I2C1
7	MX_SAI1_Init	SAI1
8	MX_QUADSPI_Init	QUADSPI

3. Peripherals and Middlewares Configuration

3.1. DFSDM1

mode: PDM/SPI input from ch2 and internal clock

mode: CKOUT

3.1.1. Filter 0:

regular channel selection:

regular channel selection	Channel 2 *
Continuous Mode	Continuous Mode
Trigger to start regular conversion	Software trigger
Fast Mode	Enable *
Dma Mode	Enable *

injected channel selection:

Channel0 as injected channel	Disable
Channel1 as injected channel	Disable
Channel2 as injected channel	Disable
Channel3 as injected channel	Disable
Channel4 as injected channel	Disable
Channel5 as injected channel	Disable
Channel6 as injected channel	Disable
Channel7 as injected channel	Disable

Filter parameters:

Sinc Order	Sinc 3 filter type *
Fosr	90 *
Iosr	1

3.1.2. Filter 1:

regular channel selection:

regular channel selection	- None -
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injected channel selection:

Channel0 as injected channel	Disable
Channel1 as injected channel	Disable
Channel2 as injected channel	Disable
Channel3 as injected channel	Disable
Channel4 as injected channel	Disable
Channel5 as injected channel	Disable
Channel6 as injected channel	Disable
Channel7 as injected channel	Disable

3.1.3. Filter 2:

regular channel selection:

regular channel selection - None -

injected channel selection:

Channel0 as injected channel	Disable
Channel1 as injected channel	Disable
Channel2 as injected channel	Disable
Channel3 as injected channel	Disable
Channel4 as injected channel	Disable
Channel5 as injected channel	Disable
Channel6 as injected channel	Disable
Channel7 as injected channel	Disable

3.1.4. Filter 3:

regular channel selection:

regular channel selection - None -

injected channel selection:

Channel0 as injected channel	Disable
Channel1 as injected channel	Disable
Channel2 as injected channel	Disable
Channel3 as injected channel	Disable
Channel4 as injected channel	Disable
Channel5 as injected channel	Disable
Channel6 as injected channel	Disable
Channel7 as injected channel	Disable

3.1.5. Output Clock:

Output Clock parameters:

Selection	Source for ouput clock is system clock
Divider	20 *

3.1.6. Channel 2:

Channel 2 parameters:

Type	SPI with rising edge
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Spi Clock	Internal SPI clock
Offset	0
Right Bit Shift	0x08 *

Analog watchdog parameters:

Filter Order	FastSinc filter type
Oversampling	1

3.2. I2C1

I2C: I2C

3.2.1. Parameter Settings:

Timing configuration:

Custom Timing	Disabled
I2C Speed Mode	Standard Mode
I2C Speed Frequency (KHz)	100
Rise Time (ns)	100
Fall Time (ns)	100
Coefficient of Digital Filter	0
Analog Filter	Enabled
Timing	0x00B07CB4 *

Slave Features:

Clock No Stretch Mode	Disabled
General Call Address Detection	Disabled
Primary Address Length selection	7-bit
Dual Address Acknowledged	Disabled
Primary slave address	0

3.3. QUADSPI

Single Bank: Quad SPI Line

3.3.1. Parameter Settings:

General Parameters:

Clock Prescaler	0 *
Fifo Threshold	4 *
Sample Shifting	No Sample Shifting
Flash Size	23 *
Chip Select High Time	1 Cycle

Clock Mode Low

3.4. RCC

High Speed Clock (HSE): Crystal/Ceramic Resonator

3.4.1. Parameter Settings:

System Parameters:

VDD voltage (V)	3.3
Instruction Cache	Enabled
Prefetch Buffer	Disabled
Data Cache	Enabled
Flash Latency(WS)	2 WS (3 CPU cycle)

RCC Parameters:

HSI Calibration Value	16
MSI Calibration Value	0
MSI Auto Calibration	Disabled
HSE Startup Timeout Value (ms)	100
LSE Startup Timeout Value (ms)	5000

Power Parameters:

Power Regulator Voltage Scale	Power Regulator Voltage Scale 1
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3.5. SAI1

Mode: Master with Master Clock Out

mode: I2S/PCM Protocol

3.5.1. Parameter Settings:

SAI A:

Synchronization Inputs	Asynchronous
Basic Parameters	
Audio Mode	Master Transmit
Output Mode	Stereo
Companding Mode	No companding mode
SAI SD Line Output Mode	Driven
Protocol Parameters	
Protocol	I2S Standard
Data Size	16 Bits
Number of Slots (only Even Values)	2
Clock Parameters	

Master Clock Divider	Enabled
Audio Frequency	22.05 KHz *
Real Audio Frequency	22.058 KHz *
Error between Selected	0.03 % *
Advanced Parameters	
Fifo Threshold	Empty
Output Drive	Disabled

3.6. SYS

Timebase Source: SysTick

3.7. USART2

Mode: Asynchronous

3.7.1. Parameter Settings:

Basic Parameters:

Baud Rate	921600 *
Word Length	8 Bits (including Parity)
Parity	None
Stop Bits	1

Advanced Parameters:

Data Direction	Receive and Transmit
Over Sampling	16 Samples
Single Sample	Disable

Advanced Features:

Auto Baudrate	Disable
TX Pin Active Level Inversion	Disable
RX Pin Active Level Inversion	Disable
Data Inversion	Disable
TX and RX Pins Swapping	Disable
Overrun	Enable
DMA on RX Error	Enable
MSB First	Disable

* User modified value

4. System Configuration

4.1. GPIO configuration

IP	Pin	Signal	GPIO mode	GPIO pull/up pull down	Max Speed	User Label
DFSDM1	PE7	DFSDM1_DATIN2	Alternate Function Push Pull	No pull-up and no pull-down	Low	MIC_DATA
	PE9	DFSDM1_CLKOUTT	Alternate Function Push Pull	No pull-up and no pull-down	Low	MIC_CLKOUT
I2C1	PB6	I2C1_SCL	Alternate Function Open Drain	No pull-up and no pull-down	Very High*	
	PB7	I2C1_SDA	Alternate Function Open Drain	No pull-up and no pull-down	Very High*	
QUADSPI	PE10	QUADSPI_CLK	Alternate Function Push Pull	No pull-up and no pull-down	Very High*	
	PE11	QUADSPI_NCS	Alternate Function Push Pull	No pull-up and no pull-down	Very High*	
	PE12	QUADSPI_BK1_I_O0	Alternate Function Push Pull	No pull-up and no pull-down	Very High*	
	PE13	QUADSPI_BK1_I_O1	Alternate Function Push Pull	No pull-up and no pull-down	Very High*	
	PE14	QUADSPI_BK1_I_O2	Alternate Function Push Pull	No pull-up and no pull-down	Very High*	
	PE15	QUADSPI_BK1_I_O3	Alternate Function Push Pull	No pull-up and no pull-down	Very High*	
RCC	PH0-OSC_IN(PH0)	RCC_OSC_IN	n/a	n/a	n/a	
	PH1-OSC_OUT(PH1)	RCC_OSC_OUT	n/a	n/a	n/a	
SAI1	PE2	SAI1_MCLK_A	Alternate Function Push Pull	No pull-up and no pull-down	Low	
	PE4	SAI1_FS_A	Alternate Function Push Pull	No pull-up and no pull-down	Low	
	PE5	SAI1_SCK_A	Alternate Function Push Pull	No pull-up and no pull-down	Low	
	PE6	SAI1_SD_A	Alternate Function Push Pull	No pull-up and no pull-down	Low	
USART2	PD5	USART2_TX	Alternate Function Push Pull	No pull-up and no pull-down	Very High*	
	PD6	USART2_RX	Alternate Function Push Pull	No pull-up and no pull-down	Very High*	
GPIO	PE3	GPIO_Output	Output Push Pull	No pull-up and no pull-down	Low	AUDIO_RST

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IP	Pin	Signal	GPIO mode	GPIO pull/up pull down	Max Speed	User Label
	PA3	GPIO_Input	Input mode	Pull-down *	n/a	JOY_UP
	PA5	GPIO_Input	Input mode	Pull-down *	n/a	JOY_DOWN
	PB2	GPIO_Output	Output Push Pull	No pull-up and no pull-down	Low	LED_REED
	PE8	GPIO_Output	Output Push Pull	No pull-up and no pull-down	Low	LED_GREEN
	PD4	GPIO_Output	Output Push Pull	No pull-up and no pull-down	Low	GPIO_Output

4.2. DMA configuration

DMA request	Stream	Direction	Priority
DFSDM1_FLT0	DMA1_Channel4	Peripheral To Memory	Low
SAI1_A	DMA2_Channel1	Memory To Peripheral	Low
QUADSPI	DMA1_Channel5	Peripheral To Memory	High *

DFSDM1_FLT0: DMA1_Channel4 DMA request Settings:

Mode: **Circular ***
Peripheral Increment: Disable
Memory Increment: **Enable ***
Peripheral Data Width: Word
Memory Data Width: Word

SAI1_A: DMA2_Channel1 DMA request Settings:

Mode: **Circular ***
Peripheral Increment: Disable
Memory Increment: **Enable ***
Peripheral Data Width: **Half Word ***
Memory Data Width: **Half Word ***

QUADSPI: DMA1_Channel5 DMA request Settings:

Mode: Normal
Peripheral Increment: Disable
Memory Increment: **Enable ***
Peripheral Data Width: **Half Word ***
Memory Data Width: **Half Word ***

4.3. NVIC configuration

4.3.1. NVIC

Interrupt Table	Enable	Preenemption Priority	SubPriority
Non maskable interrupt	true	0	0
Hard fault interrupt	true	0	0
Memory management fault	true	0	0
Prefetch fault, memory access fault	true	0	0
Undefined instruction or illegal state	true	0	0
System service call via SWI instruction	true	0	0
Debug monitor	true	0	0
Pendable request for system service	true	0	0
System tick timer	true	15	0
DMA1 channel4 global interrupt	true	0	0
DMA1 channel5 global interrupt	true	0	0
DMA2 channel1 global interrupt	true	0	0
PVD/PVM1/PVM2/PVM3/PVM4 interrupts through EXTI lines 16/35/36/37/38		unused	
Flash global interrupt		unused	
RCC global interrupt		unused	
I2C1 event interrupt		unused	
I2C1 error interrupt		unused	
USART2 global interrupt		unused	
DFSDM1 filter3 global interrupt		unused	
DFSDM1 filter0 global interrupt		unused	
DFSDM1 filter1 global interrupt		unused	
DFSDM1 filter2 global interrupt		unused	
QUADSPI global interrupt		unused	
SAI1 global interrupt		unused	
FPU global interrupt		unused	

4.3.2. NVIC Code generation

Enabled interrupt Table	Select for init sequence ordering	Generate IRQ handler	Call HAL handler
Non maskable interrupt	false	true	false
Hard fault interrupt	false	true	false
Memory management fault	false	true	false
Prefetch fault, memory access fault	false	true	false
Undefined instruction or illegal state	false	true	false
System service call via SWI instruction	false	true	false
Debug monitor	false	true	false

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Enabled interrupt Table	Select for init sequence ordering	Generate IRQ handler	Call HAL handler
Pendable request for system service	false	true	false
System tick timer	false	true	true
DMA1 channel4 global interrupt	false	true	true
DMA1 channel5 global interrupt	false	true	true
DMA2 channel1 global interrupt	false	true	true

* User modified value

5. System Views

5.1. Category view

5.1.1. Current

6. Docs & Resources

Type	Link
BSDL files	https://www.st.com/resource/en/bsdl_model/stm32I4_bsdl.zip
IBIS models	https://www.st.com/resource/en/ibis_model/stm32I4_ibis.zip
System View	https://www.st.com/resource/en/svd/stm32I4_svd.zip
Description	
Presentations	https://www.st.com/resource/en/product_presentation/stm32-stm8_embedded_software_solutions.pdf
Presentations	https://www.st.com/resource/en/product_presentation/stm32_eval-tools_portfolio.pdf
Presentations	https://www.st.com/resource/en/product_presentation/stm32_stm8_functional-safety-packages.pdf
Presentations	https://www.st.com/resource/en/product_presentation/stm32-stm8_software_development_tools.pdf
Presentations	https://www.st.com/resource/en/product_presentation/microcontrollers-stm32-family-overview.pdf
Presentations	https://www.st.com/resource/en/product_presentation/microcontrollers-stm32I4-series-product-overview.pdf
Brochures	https://www.st.com/resource/en/brochure/brstm32ulp.pdf
Flyers	https://www.st.com/resource/en/flyer/flstm32I4.pdf
Flyers	https://www.st.com/resource/en/flyer/flstm32nucleo.pdf
Flyers	https://www.st.com/resource/en/flyer/flstm32trust.pdf
Flyers	https://www.st.com/resource/en/flyer/flstm32gui.pdf
Magazine Articles	https://www.st.com/resource/en/magazine/design-elektronik_august2017.pdf
Magazine Articles	https://www.st.com/resource/en/magazine/design-elektronik_october2016.pdf
Product Certifications	https://www.st.com/resource/en/certification_document/sesip-2000002-01-cert.pdf
Product Certifications	https://www.st.com/resource/en/certification_document/sesip-2000002-01-st2.pdf

Product Certifications	https://www.st.com/resource/en/certification_document/psa-certificate_stm32l4.pdf
Security Bulletin	https://www.st.com/resource/en/technical_note/tn1489-security-bulletin-tn1489stpsirt-physical-attacks-on-stm32-and-stm32cube-firmware-stmicroelectronics.pdf
Application Notes	https://www.st.com/resource/en/application_note/an1709-emc-design-guide-for-stm8-stm32-and-legacy-mcus-stmicroelectronics.pdf
Application Notes	https://www.st.com/resource/en/application_note/an2606-stm32-microcontroller-system-memory-boot-mode-stmicroelectronics.pdf
Application Notes	https://www.st.com/resource/en/application_note/an3126-audio-and-waveform-generation-using-the-dac-in-stm32-products-stmicroelectronics.pdf
Application Notes	https://www.st.com/resource/en/application_note/an3155-usart-protocol-used-in-the-stm32-bootloader-stmicroelectronics.pdf
Application Notes	https://www.st.com/resource/en/application_note/an3156-usb-dfu-protocol-used-in-the-stm32-bootloader-stmicroelectronics.pdf
Application Notes	https://www.st.com/resource/en/application_note/an4221-i2c-protocol-used-in-the-stm32-bootloader-stmicroelectronics.pdf
Application Notes	https://www.st.com/resource/en/application_note/an4286-spi-protocol-used-in-the-stm32-bootloader-stmicroelectronics.pdf
Application Notes	https://www.st.com/resource/en/application_note/an4555-getting-started-with-stm32l4-series-and-stm32l4-series-hardware-development-stmicroelectronics.pdf
Application Notes	https://www.st.com/resource/en/application_note/an4612-migrating-from-stm32l1-series-to-stm32l4-series-and-stm32l4-series-microcontrollers-stmicroelectronics.pdf
Application Notes	https://www.st.com/resource/en/application_note/an4616-migrating-from-stm32f401-and-stm32f411-lines-to-stm32l4-series-and-stm32l4-series-microcontrollers-stmicroelectronics.pdf
Application Notes	https://www.st.com/resource/en/application_note/an4621-stm32l4-and-stm32l4-ultralowpower-features-overview-stmicroelectronics.pdf
Application Notes	https://www.st.com/resource/en/application_note/an4629-adc-hardware-oversampling-for-microcontrollers-of-the-stm32-l0-and-l4-series

stmicroelectronics.pdf

Application Notes https://www.st.com/resource/en/application_note/an4649-migrating-from-stm32f1-series-to-stm32l4-series--stm32l4-series-microcontrollers-stmicroelectronics.pdf

Application Notes https://www.st.com/resource/en/application_note/an4655-virtually-increasing-the-number-of-serial-communication-peripherals-in-stm32-applications-stmicroelectronics.pdf

Application Notes https://www.st.com/resource/en/application_note/an4726-stm32cube-firmware-examples-for-stm32l4-series-and-stm32l4-series-stmicroelectronics.pdf

Application Notes https://www.st.com/resource/en/application_note/an4729-stm32l0l4-firewall-overview-stmicroelectronics.pdf

Application Notes https://www.st.com/resource/en/application_note/an4730-using-the-firewall-embedded-in-stm32l0l4l4-series-mcus-for-secure-access-to-sensitive-parts-of-code-and-data-stmicroelectronics.pdf

Application Notes https://www.st.com/resource/en/application_note/an4746-optimizing-power-and-performance-with-stm32l4-and-stm32l4-series-microcontrollers-stmicroelectronics.pdf

Application Notes https://www.st.com/resource/en/application_note/an4750-handling-of-soft-errors-in-stm32-applications-stmicroelectronics.pdf

Application Notes https://www.st.com/resource/en/application_note/an4754-migrating-between-stm32l486xx476xx-and-stm32l443xx433xx-microcontrollers-stmicroelectronics.pdf

Application Notes https://www.st.com/resource/en/application_note/an4761-using-stm32l476486-fsmc-peripheral-to-drive-external-memories--stmicroelectronics.pdf

Application Notes https://www.st.com/resource/en/application_note/an4776-generalpurpose-timer-cookbook-for-stm32-microcontrollers-stmicroelectronics.pdf

Application Notes https://www.st.com/resource/en/application_note/an4803-highspeed-si-simulations-using-ibis-and-boardlevel-simulations-using-hyperlynx-si-on-stm32-mcus-and-mpus-stmicroelectronics.pdf

Application Notes https://www.st.com/resource/en/application_note/an4809-migrating-between-stm32l0-series-and-stm32l4-series--stm32l4-series-microcontrollers-stmicroelectronics.pdf

- Application Notes https://www.st.com/resource/en/application_note/an4821-migrating-from-stm32f405415-line-and-stm32f407417-line-to-stm32l4-series-and-stm32l4-series-microcontrollers-stmicroelectronics.pdf
- Application Notes https://www.st.com/resource/en/application_note/an4822-migrating-between-stm32l476xx486xx-and-stm32l496xx4a6xx-microcontrollers-stmicroelectronics.pdf
- Application Notes https://www.st.com/resource/en/application_note/an4831-migrating-from-stm32f2x5-line-to-stm32l4-series-and-stm32l4-series-microcontrollers-stmicroelectronics.pdf
- Application Notes https://www.st.com/resource/en/application_note/an4832-migrating-from-stm32f303-line-to-stm32l4-series-and-stm32l4-series-microcontrollers-stmicroelectronics.pdf
- Application Notes https://www.st.com/resource/en/application_note/an4989-stm32-microcontroller-debug-toolbox-stmicroelectronics.pdf
- Application Notes https://www.st.com/resource/en/application_note/an4990-getting-started-with-sigmadelta-digital-interface-on-applicable-stm32-microcontrollers-stmicroelectronics.pdf
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- Application Notes https://www.st.com/resource/en/application_note/an5012-analogtodigital-audio-conversion-example-using-stm32l4-series-microcontroller-peripherals-stmicroelectronics.pdf
- Application Notes https://www.st.com/resource/en/application_note/an5017-migrating-between-stm32l476xx486xx-and-stm32l4-series-microcontrollers-stmicroelectronics.pdf
- Application Notes https://www.st.com/resource/en/application_note/an5027-interfacing-pdm-digital-microphones-using-stm32-mcus-and-mpus-stmicroelectronics.pdf
- Application Notes https://www.st.com/resource/en/application_note/an5138-migrating-from-stm32l4-and-stm32l4-to-stm32l5-series-microcontrollers-stmicroelectronics.pdf
- Application Notes https://www.st.com/resource/en/application_note/an4760-quadspi-interface-on-stm32-microcontrollers-and-microprocessors-stmicroelectronics.pdf

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