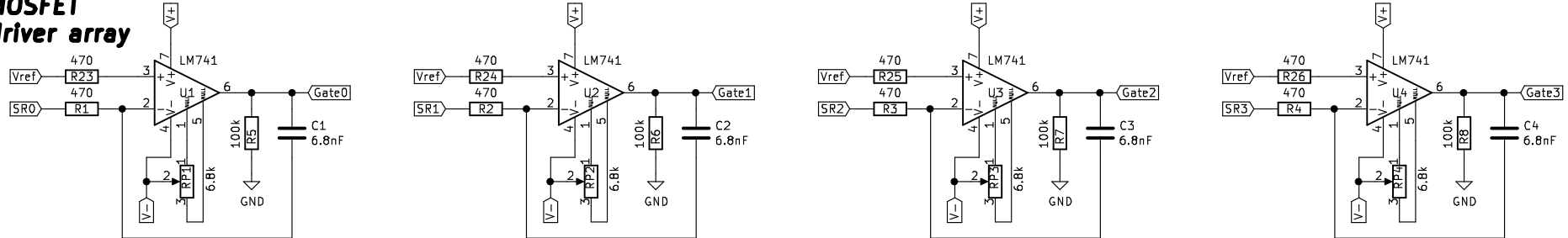
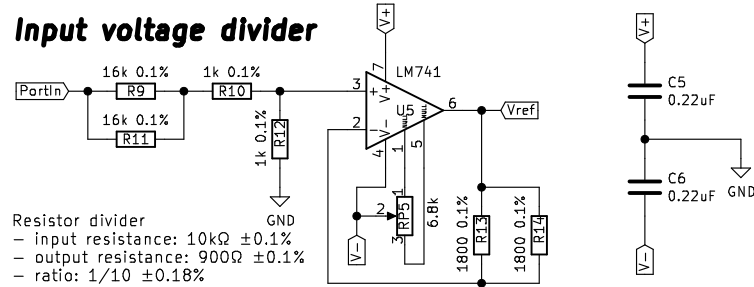


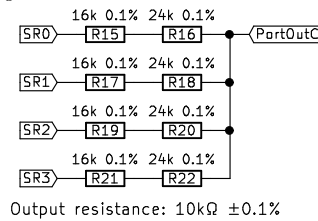
MOSFET driver array



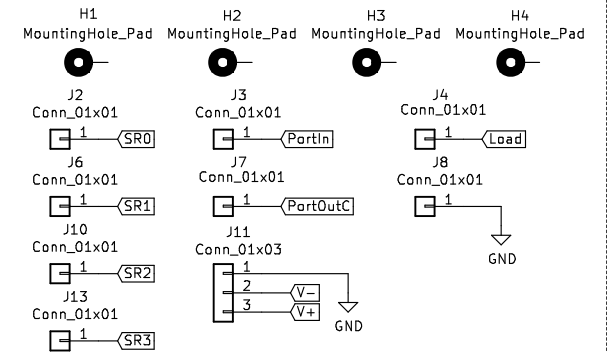
Input voltage divider



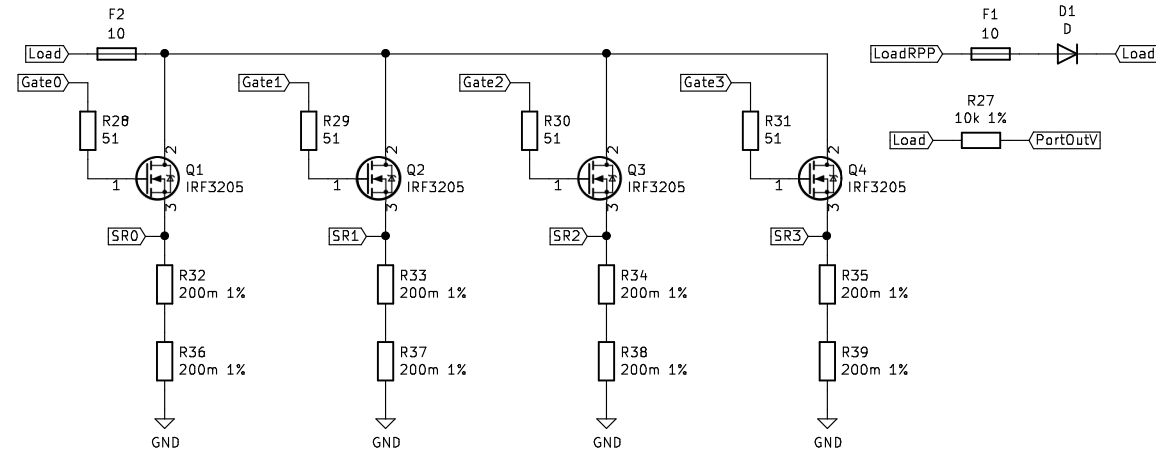
Current reference generator



Interface



Point-to-point construction



Sheet: /
File: vcdcl_main_board.sch
Title: VCDCL Main Board

Size: A4 Date:
KiCad E.D.A. kicad 5.1.10-88a1d61d5890ubuntu20.04.1

Rev: 1.7
Id: 1/1