

PCB ASSEMBLY SPECIFICATIONS

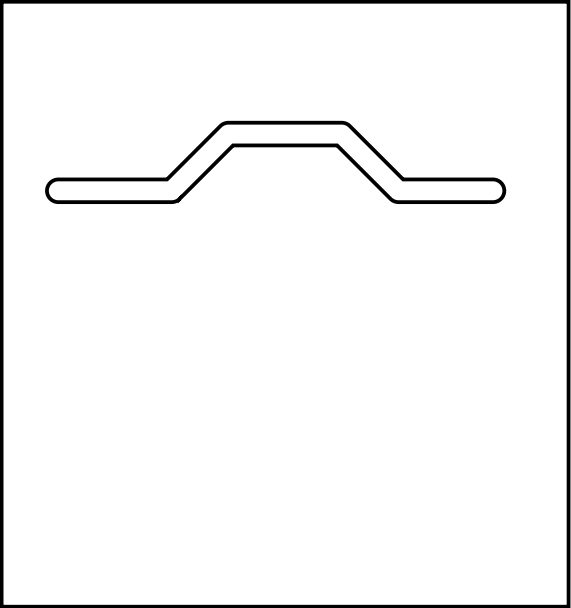
"Altium Templates"

Variant: "[No Variations]"

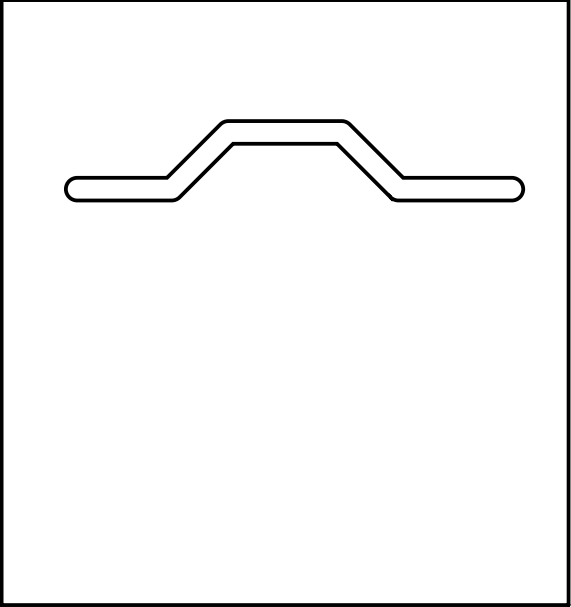
SPECIFICATIONS

NOTE #	NOTE
1	ALL SPECIFICATIONS REFERENCED SHALL BE OF THE LATEST REVISION UNLESS OTHERWISE NOTED
2	SUPPLIER SHALL NOT MODIFY THE DESIGN WITHOUT WRITTEN PERMISSION
3	REFER TO EXCEL BOM FOR UP-TO-DATE INFORMATION
4	THE BOM IN THIS DOCUMENT IS PURELY AN AID TO ASSEMBLY OPERATIONS AND MAY NOT HAVE THE MOST UP-TO-DATE DATA OR ALL APPROVED COMPONENT ALTERNATIVE.

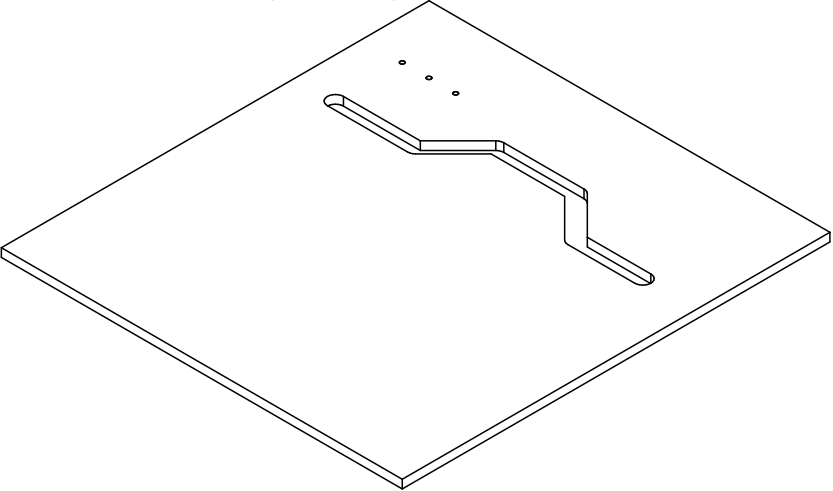
View from Top side (Scale 1:1)



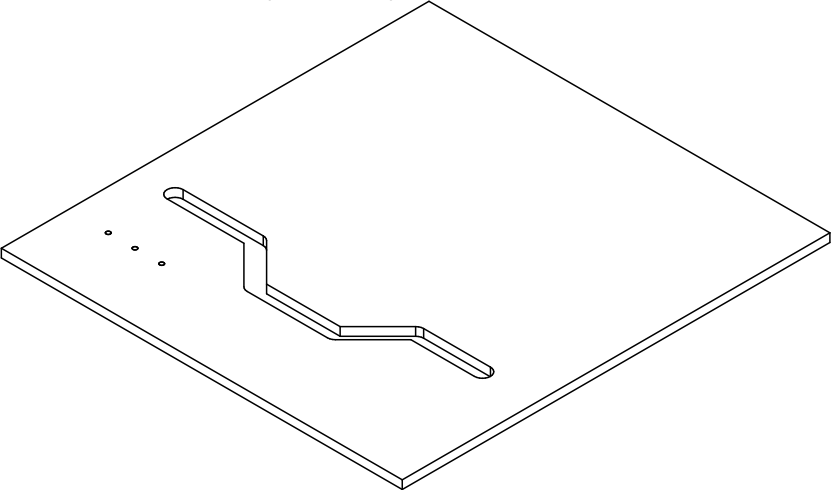
View from Bottom side (Scale 1:1)



View from Front side (Scale 1:1)



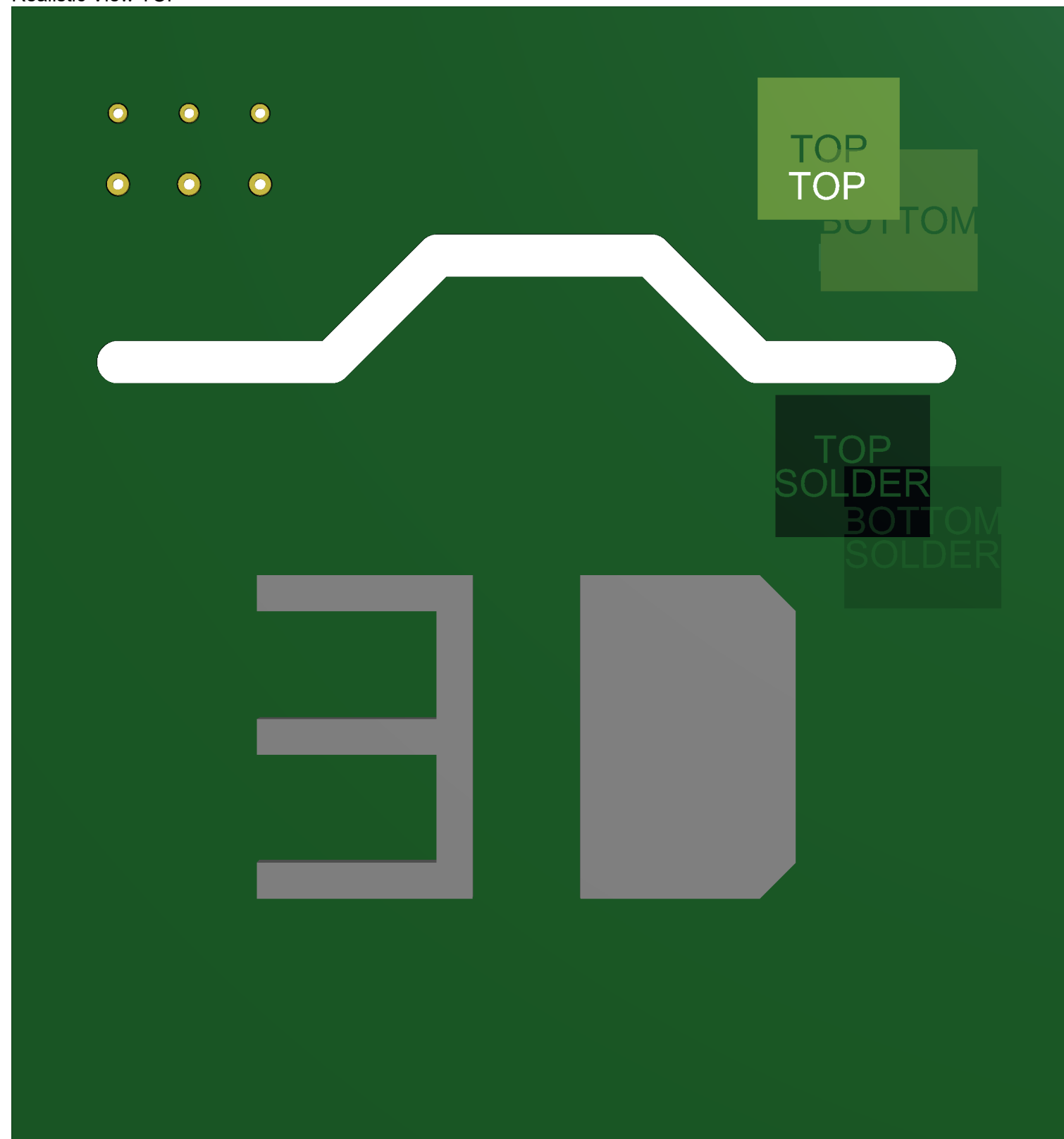
View from Back side (Scale 1:1)



Title: Altium Templates			Author:		CONFIDENTIAL
Size: A3			Approved:		
Unit: mm			Prj: Altium Templates		
Date: 25/08/2020 22:57			Sheet 1 of 9		
Git Hash: 679fb11c51c933bf01012419407c88c01740e91d			[Modified]		
File: C:\Users\d\Documents\Fermium\AltiumTemplates\Draftsman\PCB ASSEMBLY.DwfDot					

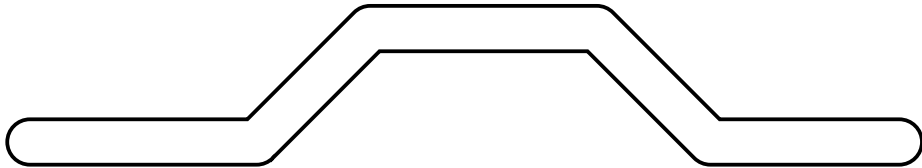
[YOUR LOGO HERE]

Realistic View TOP



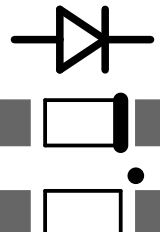
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			Approved:			
Size: A3	Prj: Altium Templates		Edited: 25/08/2020			My Company
Unit: mm			Variant: [No Variations]			Address Line 1
Date: 25/08/2020 22:57	Sheet 2 of 9	SW version: 20.1.14.287	Address Line 2			
Git Hash: 679fb11c51c933bf01012419407c88c01740e91d [Modified]			Address Line 3			
File: C:\Users\d\Documents\Fermium\AltiumTemplates\Draftsman\PCB ASSEMBLY.DwfDot				Address Line 4		

View from Top side (Scale 2:1)



DIODE ORIENTATION

Name	ANODE
Short name	A
Pin number	2
Silkscreen	thin line
assembly view	no dot ()

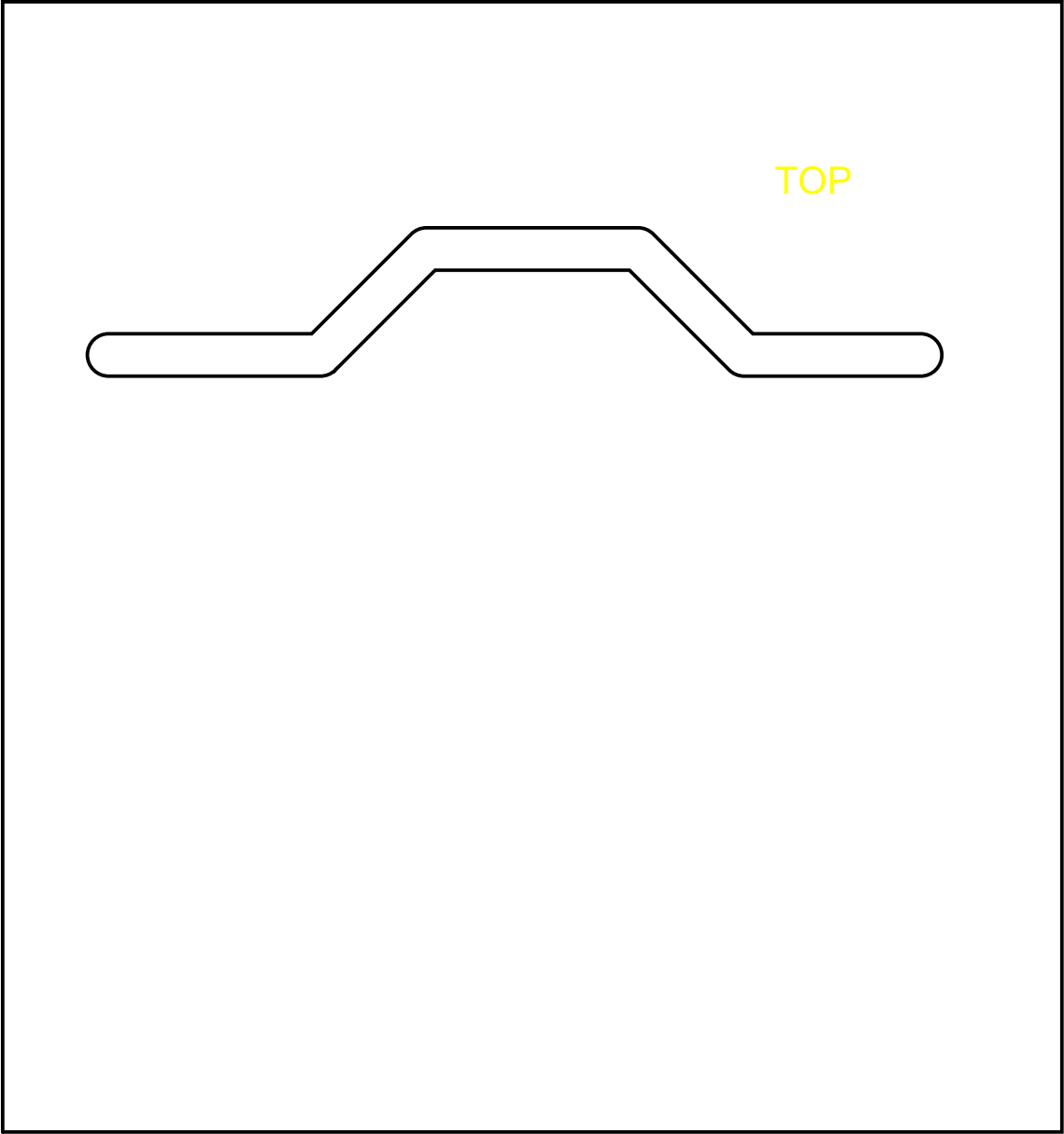


Name	CATHODE
Short name	K
Pin number	1
Silkscreen	thick line / dot
assembly view	dot (•)

Title: Altium Templates		Author:	CONFIDENTIAL
Size: A3		Approved:	
Unit: mm		Edited: 25/08/2020	
Prj: Altium Templates		Variant: [No Variations]	
Date: 25/08/2020 22:57		Sheet 3 of 9	My Company
Git Hash: 679fb11c51c933bf01012419407c88c01740e91d [Modified]		SW version: 20.1.14.287	
File: C:\Users\d\Documents\Fermium\AltiumTemplates\Draftsman\PCB_ASSEMBLY.DwfDot			

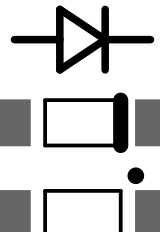
[YOUR LOGO HERE]

View from Top side (Scale 2:1)



DIODE ORIENTATION

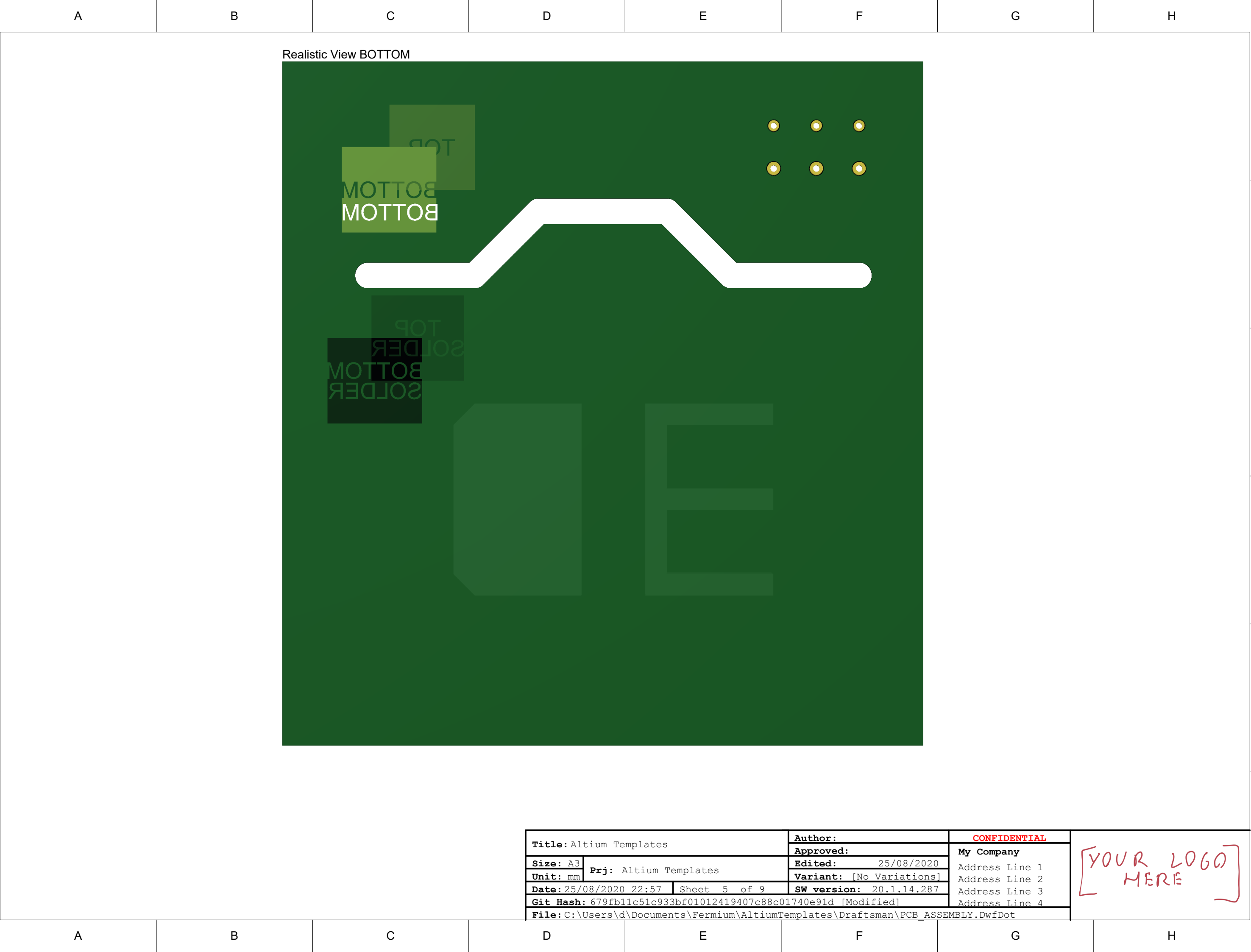
Name	ANODE
Short name	A
Pin number	2
Silkscreen	thin line
assembly view	no dot ()



Name	CATHODE
Short name	K
Pin number	1
Silkscreen	thick line / dot
assembly view	dot (•)

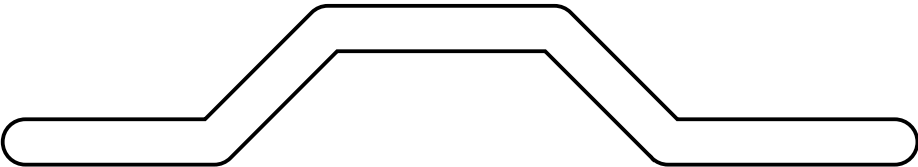
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Unit: mm			Edited: 25/08/2020		
Date: 25/08/2020 22:57			Variant: [No Variations]		
Sheet 4 of 9		SW version: 20.1.14.287	Address Line 1		
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[YOUR LOGO HERE]



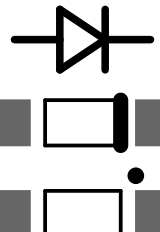
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Prj: Altium Templates			Approved:		
Size: A3			Edited: 25/08/2020	Address Line 1	
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Date: 25/08/2020 22:57	Sheet 5 of 9		SW version: 20.1.14.287	Address Line 3	
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File: C:\Users\d\Documents\Fermium\AltiumTemplates\Draftsman\PCB ASSEMBLY.DwfDot					

View from Bottom side (Scale 2:1)



DIODE ORIENTATION

Name	ANODE
Short name	A
Pin number	2
Silkscreen	thin line
assembly view	no dot ()

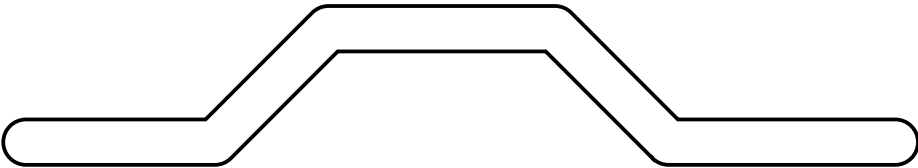


Name	CATHODE
Short name	K
Pin number	1
Silkscreen	thick line / dot
assembly view	dot (•)

Title: Altium Templates			Author:		CONFIDENTIAL		
Size: A3 Prj: Altium Templates			Approved:			My Company	
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Date: 25/08/2020 22:57			SW version: 20.1.14.287				Address Line 2
Sheet 6 of 9			Git Hash: 679fb11c51c933bf01012419407c88c01740e91d [Modified]				Address Line 3
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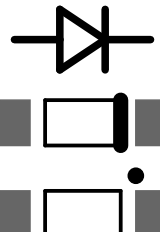
[YOUR LOGO HERE]

View from Bottom side (Scale 2:1)



DIODE ORIENTATION

Name	ANODE
Short name	A
Pin number	2
Silkscreen	thin line
assembly view	no dot ()



Name	CATHODE
Short name	K
Pin number	1
Silkscreen	thick line / dot
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Title: Altium Templates			Author:		CONFIDENTIAL		
Size: A3			Approved:			My Company	
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Date: 25/08/2020 22:57			Sheet 7 of 9				
Git Hash: 679fb11c51c933bf01012419407c88c01740e91d [Modified]			SW version: 20.1.14.287		Address Line 3		
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[YOUR LOGO HERE]

A

B

C

D

E

F

G

H

1

2

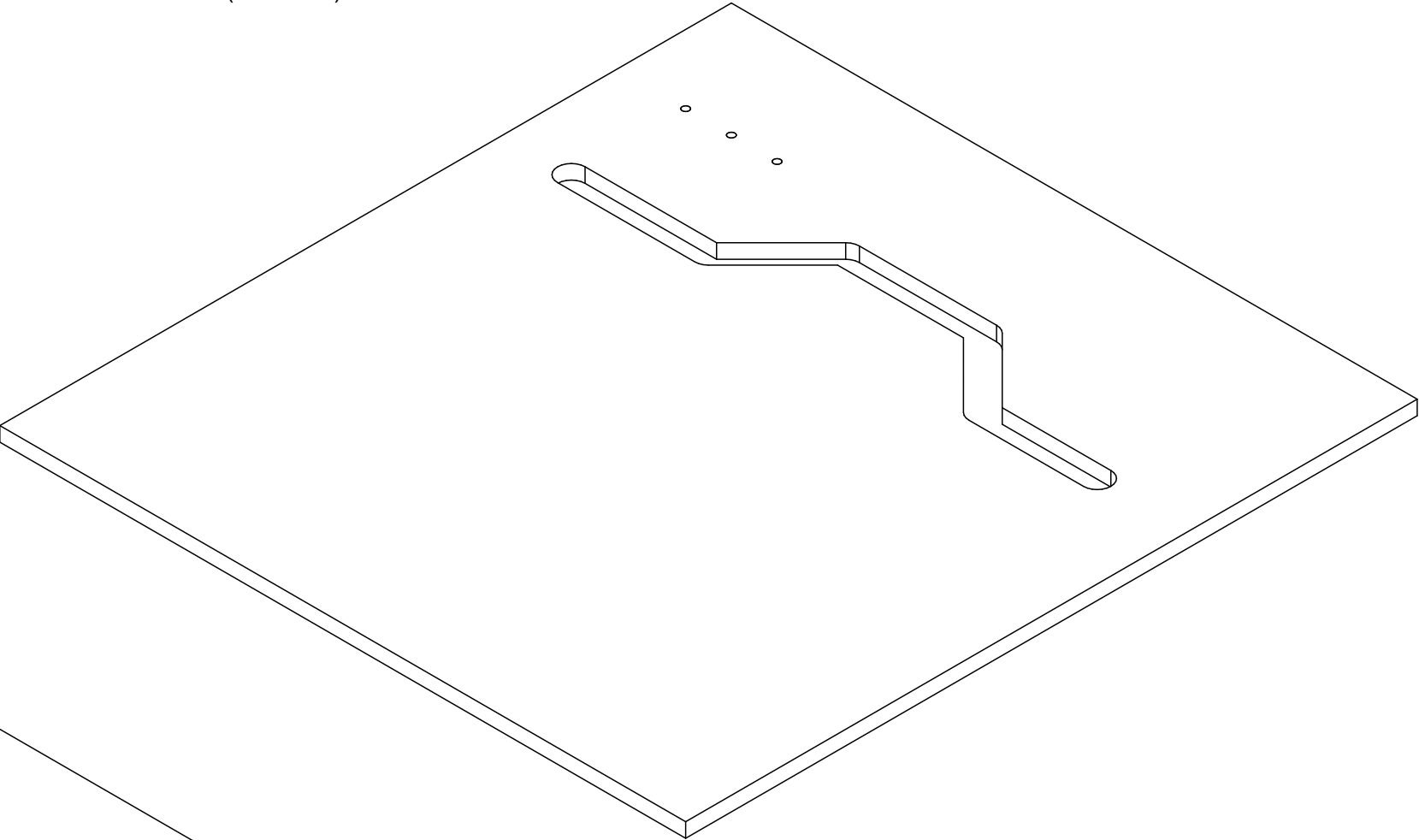
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4

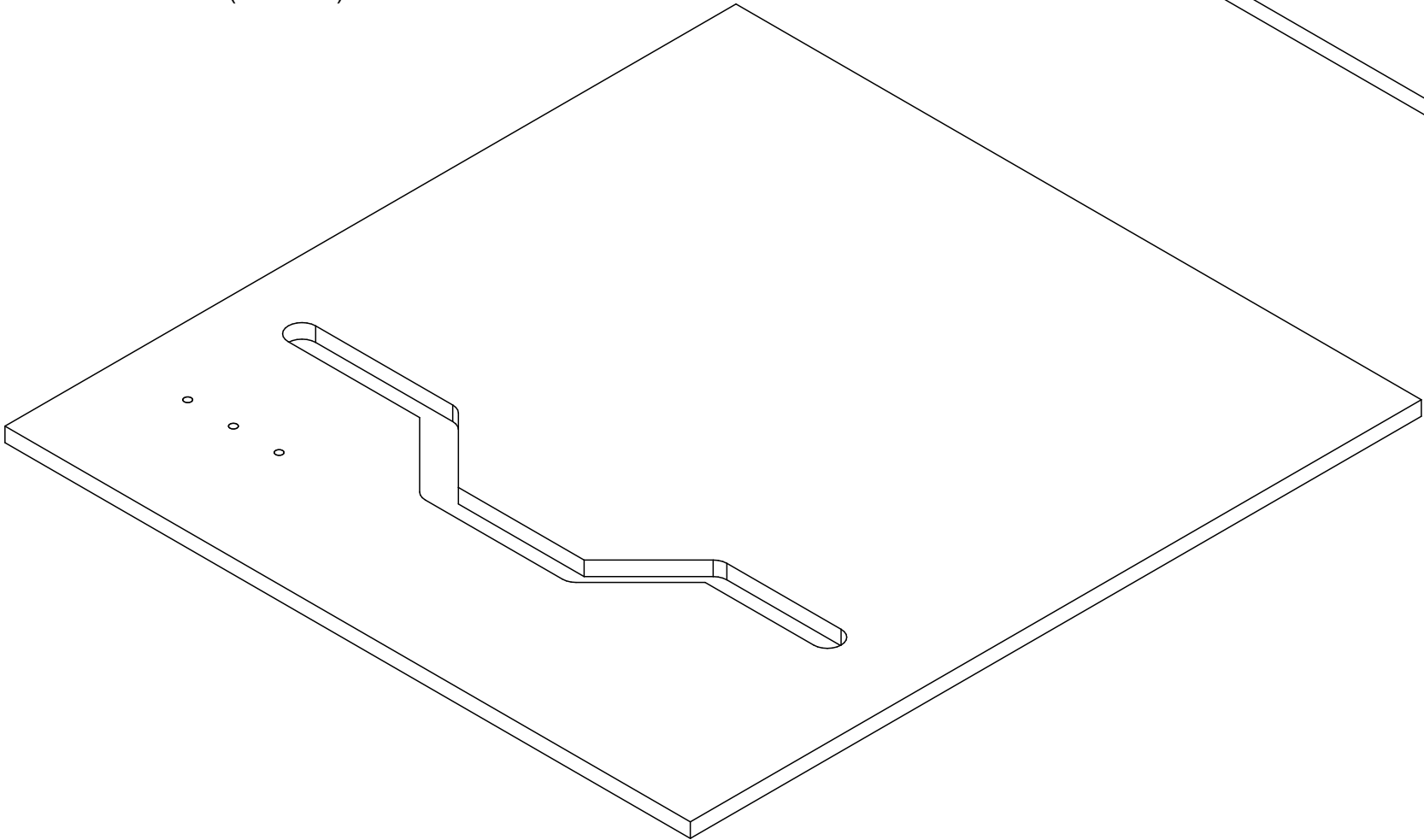
5

6

View from Front side (Scale 2:1)



View from Back side (Scale 2:1)



Title: Altium Templates			Author:		CONFIDENTIAL	
Size: A3			Approved:			My Company
Unit: mm			Edited: 25/08/2020			
Prj: Altium Templates			Variant: [No Variations]			Address Line 1
Date: 25/08/2020 22:57			SW version: 20.1.14.287			Address Line 2
Sheet 8 of 9			Git Hash: 679fb11c51c933bf01012419407c88c01740e91d [Modified]		Address Line 3	
File: C:\Users\d\Documents\Fermium\AltiumTemplates\Draftsman\PCB ASSEMBLY.DwfDot					Address Line 4	

[YOUR LOGO HERE]

	A	B	C	D	E	F	G	H																																																						
1	<div>Bill Of Materials</div> <table><tr><th>Line #</th><th>Description</th><th>Designator</th><th>Quantity</th><th>Manufacturer Part Number 1</th><th>Part Number</th><th>Layer</th></tr></table>								Line #	Description	Designator	Quantity	Manufacturer Part Number 1	Part Number	Layer	1																																														
Line #	Description	Designator	Quantity	Manufacturer Part Number 1	Part Number	Layer																																																								
2									2																																																					
3	<div>Please consider LCSC (□□□□) as our first supplier</div>								3																																																					
4									4																																																					
5									5																																																					
6	<div>BOM FOR REFERENCE ONLY ALWAYS REFER TO THE LATEST EXCEL BOM PROVIDED</div> <table><tr><td colspan="3">Title: Altium Templates</td><td colspan="2">Author:</td><td colspan="3">CONFIDENTIAL</td></tr><tr><td>Size: A3</td><td colspan="2" rowspan="6">Prj: Altium Templates</td><td colspan="2">Approved:</td><td colspan="3">My Company</td></tr><tr><td>Unit: mm</td><td colspan="2">Edited: 25/08/2020</td><td colspan="3">Address Line 1</td></tr><tr><td colspan="3">Date: 25/08/2020 22:57</td><td colspan="2">Variant: [No Variations]</td><td colspan="3">Address Line 2</td></tr><tr><td colspan="3">Sheet 9 of 9</td><td colspan="2" rowspan="3">SW version: 20.1.14.287</td><td colspan="3">Address Line 3</td></tr><tr><td colspan="5">Git Hash: 679fb11c51c933bf01012419407c88c01740e91d [Modified]</td><td colspan="3">Address Line 4</td></tr><tr><td colspan="7">File: C:\Users\d\Documents\Fermium\AltiumTemplates\Draftsman\PCB_ASSEMBLY.DwfDot</td></tr></table> <div>[YOUR LOGO HERE]</div>								Title: Altium Templates			Author:		CONFIDENTIAL			Size: A3	Prj: Altium Templates		Approved:		My Company			Unit: mm	Edited: 25/08/2020		Address Line 1			Date: 25/08/2020 22:57			Variant: [No Variations]		Address Line 2			Sheet 9 of 9			SW version: 20.1.14.287		Address Line 3			Git Hash: 679fb11c51c933bf01012419407c88c01740e91d [Modified]					Address Line 4			File: C:\Users\d\Documents\Fermium\AltiumTemplates\Draftsman\PCB_ASSEMBLY.DwfDot							6
Title: Altium Templates			Author:		CONFIDENTIAL																																																									
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