Ming Kuan Pang

#	Before		Instruction	After		Opcode	Number of Bytes	Mod	Reg	R/M
1.	EAX	00 00 01 A2	mov eax, 100	EAX	00 00 00 64	B8	5			
2.	EAX	00 12 34 AB	mov AH, 0	EAX	00 12 00 AB	B4	2			
3.	EDX	FF 75 4C 2E	mov edx, dval	EDX	FF FF FF FF	8B	5	00	010	101
	dval	DWORD -1		dval	FF FF FF FF					
4.	ECX	00 00 3A 4C	mov dval, ecx	ECX	00 00 3A 4C	89	5	00	001	101
	dval	DWORD -100		dval	00 00 3A 4C					
5.	EBX	00 00 00 00	mov EBX, 128	EBX	00 00 00 80	BB	5			
6.	ECX	1A 2B 3C 4D	mov CL, bval	ECX	1A 2B 3C F6	8A	5	00	001	101
	bval	BYTE -10		bval	FF FF FF F6					
7.	EAX	1A 2B 3C 4D	xchg tval, eax	EAX	FF FF FF FF	87	5	00	000	101
	tval	DWORD -1		tval	1A 2B 3C 4D					
8.	DX	FF 75	xchg dl, dh	DX	75 FF	86	4	11	010	110
9.	EAX	12 BC 9A 78	xchg eax, edx	EAX	56 DE 34 F0	92	5			
	EDX	56 DE 34 F0		EDX	12 BC 9A 78					
10.	EBX	12 34 56 78	xchg bx, dx	EBX	12 34 DE F0	66 87	5	11	011	010
	EDX	9A BC DE F0		EDX	9A BC 56 78					