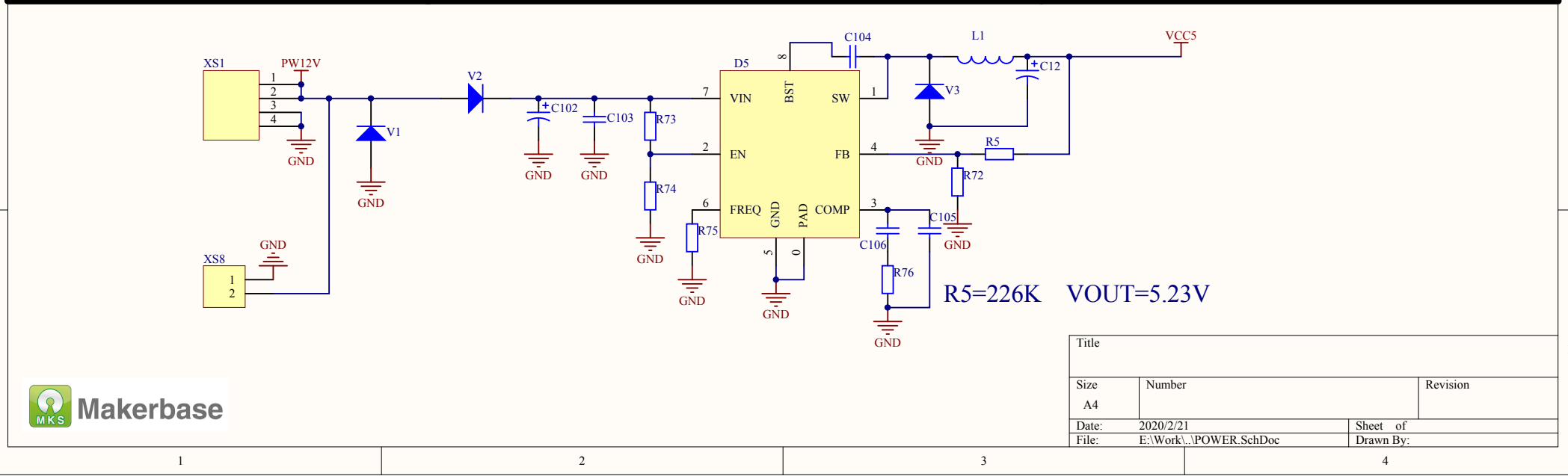
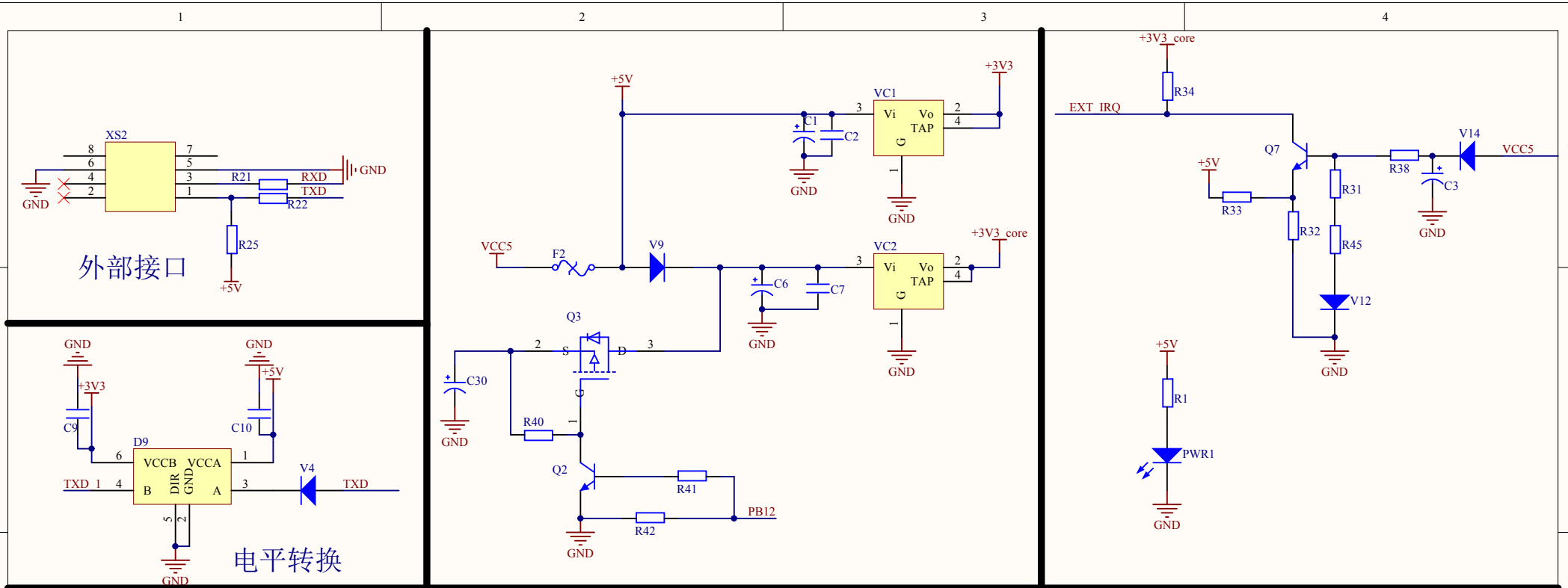
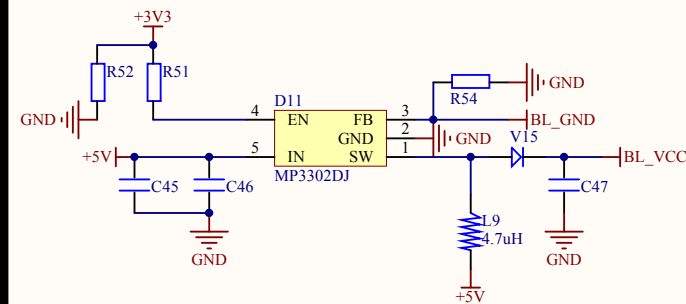
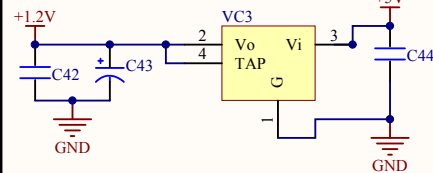
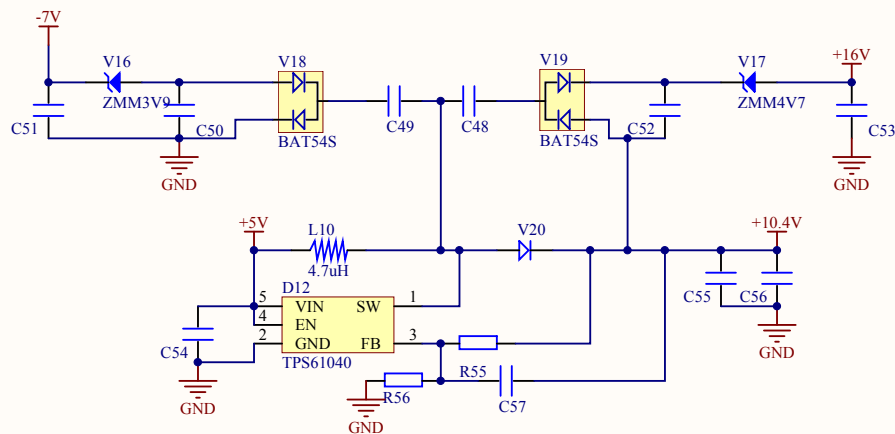
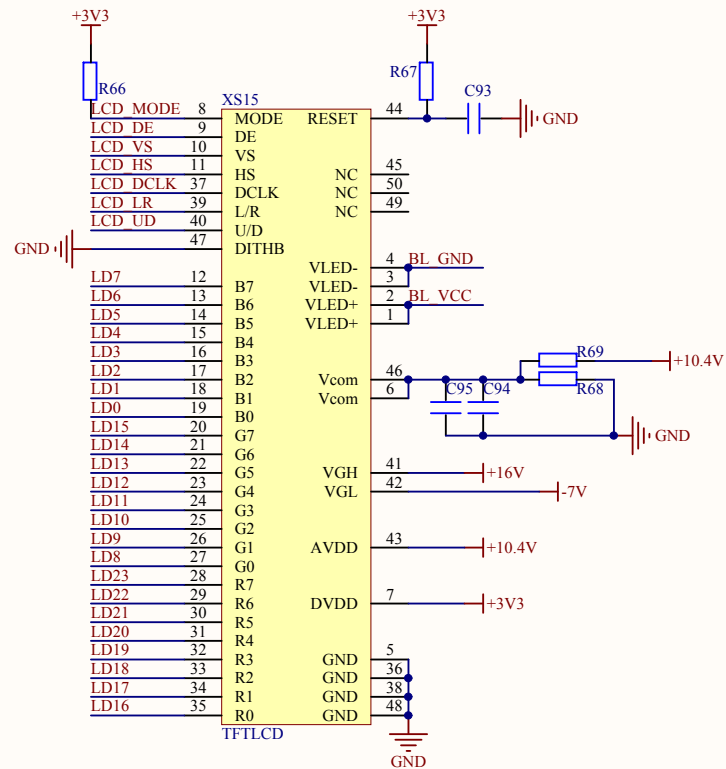
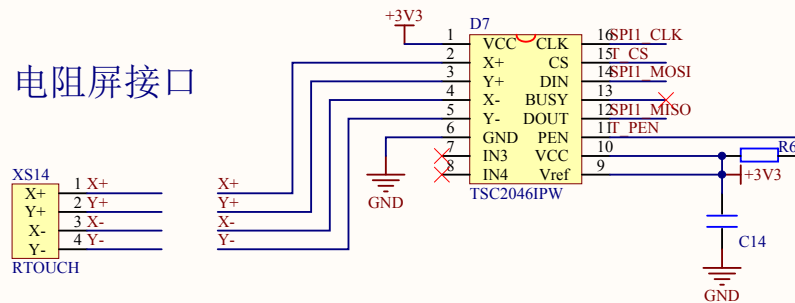


Title		
Size	Number	Revision
A4		
Date:	2020/2/21	Sheet of
File:	E:\Work\STM32VET6.SchDoc	Drawn By:



Title		
Size	Number	Revision
A4		
Date:	2020/2/21	Sheet of
File:	E:\Work\POWER.SchDoc	Drawn By:

电阻屏接口



Makerbase

Title		
Size	Number	Revision
A4		
Date:	2020/2/21	Sheet of
File:	E:\Work\TFT_LCD.SchDoc	Drawn By:

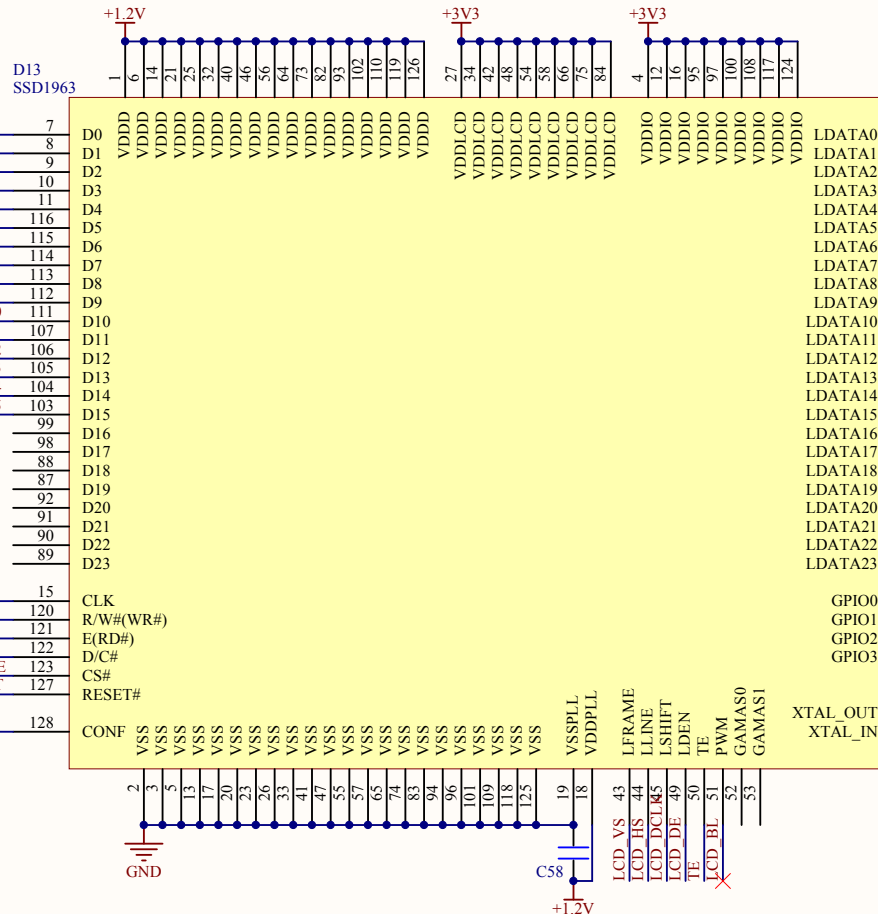
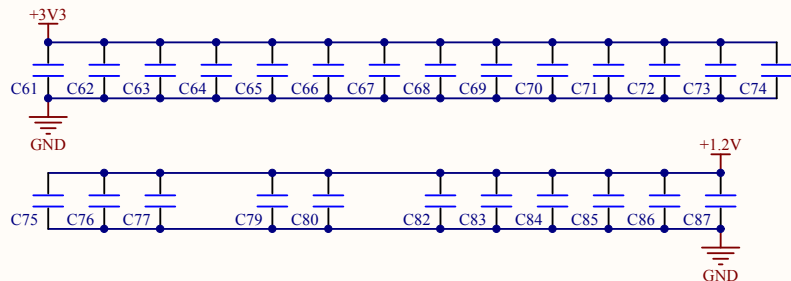
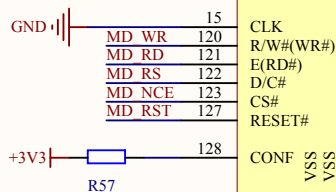
FSMC D15 RP1 MD D15
FSMC D14 MD D14
FSMC D13 MD D13
FSMC D12 MD D12

FSMC D11 RP2 MD D11
FSMC D10 MD D10
FSMC D9 MD D9
FSMC D8 MD D8

FSMC D7 RP3 MD D7
FSMC D6 MD D6
FSMC D5 MD D5
FSMC D4 MD D4

FSMC D3 RP4 MD D3
FSMC D2 MD D2
FSMC D1 MD D1
FSMC D0 MD D0

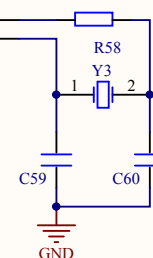
FSMC NWE RP5 MD WR
FSMC NOE MD RD
FSMC A17 MD RS
FSMC NE1 MD NCE



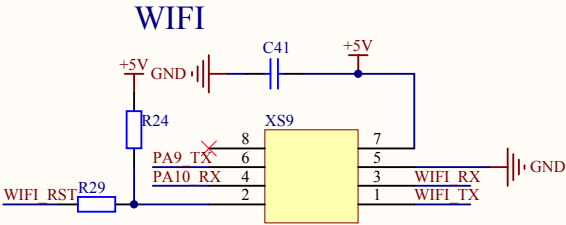
LDATA0 81 LD0
LDATA1 80 LD1
LDATA2 79 LD2
LDATA3 78 LD3
LDATA4 77 LD4
LDATA5 76 LD5
LDATA6 72 LD6
LDATA7 71 LD7
LDATA8 70 LD8
LDATA9 69 LD9
LDATA10 68 LD10
LDATA11 67 LD11
LDATA12 63 LD12
LDATA13 61 LD13
LDATA14 60 LD14
LDATA15 59 LD15
LDATA16 35 LD16
LDATA17 86 LD17
LDATA18 85 LD18
LDATA19 31 LD19
LDATA20 30 LD20
LDATA21 29 LD21
LDATA22 28 LD22
LDATA23 28 LD23

GPIO0 36 LCD LR
GPIO1 37 LCD UD
GPIO2 38
GPIO3 39

XTAL_OUT 24
XTAL_IN 22



Title		
Size	Number	Revision
A4		
Date:	2020/2/21	Sheet of
File:	E:\Work\SSD1963.SchDoc	Drawn By:



Makerbase

Title		
Size A4	Number	Revision
Date: 2020/2/21	Sheet of	
File: E:\Work\...\WIFI.SchDoc	Drawn By:	

