

Effect of DC Bias on Effective Capacitance of MLCCs

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Rough Draft

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1. Introduction

Opening up any modern computer will reveal hundreds of electrical components, ranging from the miniscule resistors to large chips with hundreds of electrical contacts. Among these one shows up on nearly every circuit board. A multi-layer ceramic capacitor or MLCC is used in just about every circuit produced today. Both in precise high speed applications, such as on a CPU, and high power designs, like a DC-DC converter, they have a wide range of uses. Through their combination of ease of use in assembly, low cost, and small size, MLCCs have become an indispensable tool in circuit design.

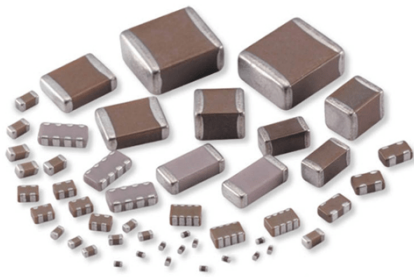


Figure 1: MLCCs come in many shapes and sizes, with some as small as a grain of sand

Despite being mainly used in mass production of consumer products, MLCCs are simultaneously used in hobbyist designs. While nearly always coming in Surface Mount packages, though hole packages exist, making them easy to solder by hand. And while other types of capacitors exist, they do not offer the same characteristics. .

1.1. Voltage, Current, and Resistance

To understand their function and why they are universally used, it is important to first understand the basic theory of how electricity flows. Voltage, a potential difference, causes current, the flow of electrons, through a conductor.

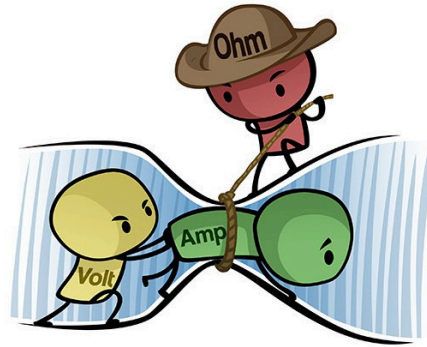


Figure 2: A popular cartoon representing voltage and amperage

When a voltage difference, such as from a battery, is applied across a conductor, current is allowed to flow unrestricted through that conductor. This is known as a short circuit. The amount of current flowing can be limited by using a resistor. A resistor is a component where the current flowing is directly proportional to the amount of voltage across the resistor. This relationship is shown in the following equation where V = voltage (measured in volts), I = current (measured in Amps), and R = Resistance (measured in ohms).

$$V = I \cdot R$$

This relationship is known as Ohm's Law. As an example, if a 5V battery is connected to a 100Ω resistor, the current can be found.

$$V = I \cdot R$$

$$5V = I \cdot 100\Omega$$

$$\frac{5V}{100\Omega} = I$$

$$0.02A = I$$

As seen above knowing any two variables of this equation allows solving for the third. The equation can be manipulated to solve for Resistance, $R = \frac{I}{V}$, or current, $I = \frac{V}{R}$.

1.2. Model of Capacitor

A capacitor is a device where the current flowing through is proportional to the rate of

change of voltage. That is, $I = C \frac{dv}{dt}$ where C is the capacitance in Farads. When a capacitor is connected to a constant current source the voltage across the capacitor will increase at a constant rate. Functionally, it is very similar to a battery in that it can be charged and then discharged.

This models an ideal capacitor, but real capacitors and especially MLCCs face non-idealities which change this model. One of the most important, yet often overlooked, ones in MLCCs is DC-bias. DC-bias causes the capacitance, C , of a capacitor to be reduced as a voltage is applied across the capacitor. This introduces a new curve, which is the capacitance versus voltage curve. Manufacturers of capacitors will provide this graph. An example of the DC Bias graph of standard capacitor is shown in Figure 3.

These curves have a domain of $0 < V < v_{\max}$ due to maximum ratings and capacitors not having directionality.

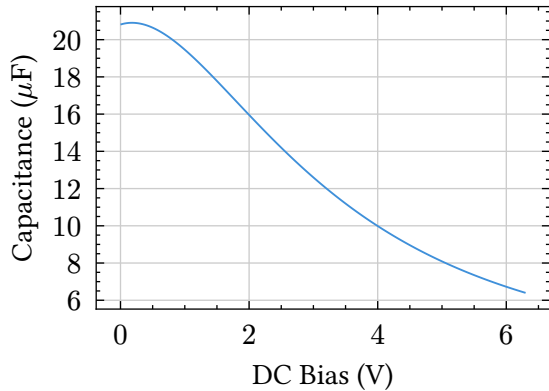


Figure 3: DC Bias of 22µF capacitor from Murata¹

Because Capacitance is in fact a function of voltage, $C = C(V)$, a more accurate model of a capacitor is $I = C(V) \frac{dv}{dt}$. This is important when there is large change in voltage across the capacitor.

1.3. Research Question

The effect of DC Bias is mainly used in AC analysis applications, such as for a bypass capacitor. In these applications, because the voltage does not make any large changes, it is

sufficient to put C as a constant, functionally making the capacitor a derated version of itself. This can not be done when a device is first turned on, and a MLCC goes from 0V to V_{on} , a large voltage change.

Often components will have an enable pin that will only allow the device to turn on when the voltage reaches a certain threshold. Meeting rigid timing requirements necessitates knowing how quickly the voltage across a capacitor will rise. This this paper will attempt to find **the effect DC Bias has on the effective capacitance of a MLCC during device turn on.**

2. RC Formula

2.1. Applications

Creating the constant current source in Section 1.2 for a capacitor can be difficult, so instead a resistor, as mentioned in Section 1.1, can be used to limit voltage increasing across a capacitor. Resistors, similar to capacitors, are relatively cheap and come in small package sizes. Combining these two components creates a circuit often referred to as an RC Circuit. The diagram of this is shown in Figure 4.

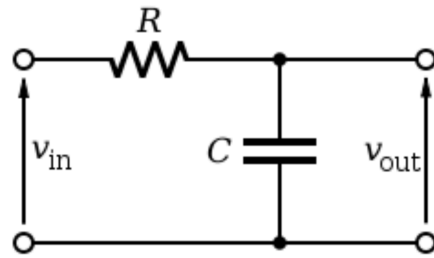


Figure 4: Schematic of RC Circuit

This circuit is often used to limit noise in a system in order to prevent unexpected behavior. Here it will be used to slow down the rise time of voltage across a capacitor.

By mathematically modelling this circuit, the below equations are generated.

$$V_R + V_C = V_{\text{on}} \rightarrow V_C = V_{\text{on}} - V_R$$

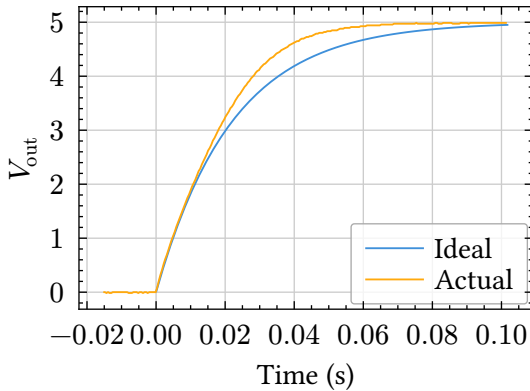
$$V_{\text{out}} = V_C = V_{\text{on}} - V_R$$

¹Part number GRM21BR60J226ME39L

contact with the resistor side of the PCB and a waveform was obtained. This set up is depicted in. More details on the PCB and oscilloscope used can be found in Appendix A.

3.3. Results

As expected, the voltage rises faster than an ideal $22\mu\text{F}$ capacitor. A comparison is shown in the figure between an ideal $22\mu\text{F}$ capacitor and the real capacitor.

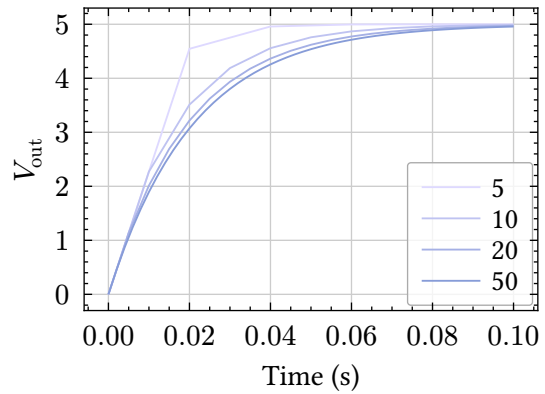


We can use this graph to approximate a RC value for the circuit. Because R is known we can find what the equivalent capacitance would be.

4. Simulation

4.1. Overview

The speed of modern computing has made simulation in a variety of situations useful. Due to the efficiency of processing repeated operations, computer software is perfect for circuit simulation. In this section, python, a modern programming language will be used to simulate the effect of DC Bias on the RC circuit. In the python code in Appendix B, small time steps can be used. In each time step the change in voltage across the capacitor is based on the current through the resistor for that amount of time. Making these time steps very small can lead to a very accurate result. The graph in figure shows five to fifty time steps.



Using this script to generate the curves we are able to also change the capacitance with reference to voltage. Using the DC Bias curve from the manufacturer, the same from Section 1.2, we can use a linear function to approximate the capacitance based on voltage. The formula chosen was $C(V) = 22\mu\text{F}$. This approximation is shown in Figure 5.

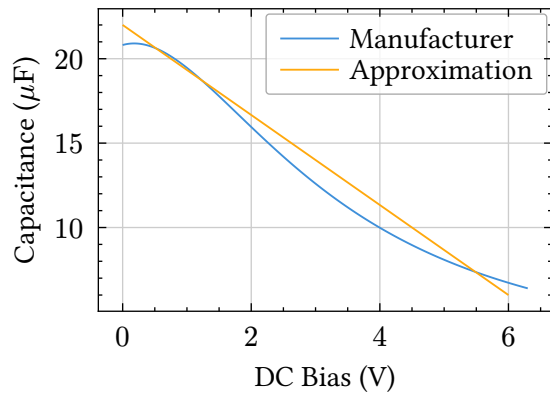
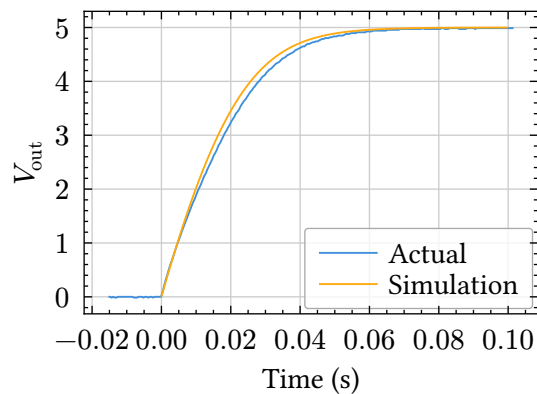


Figure 5: DC Bias of $22\mu\text{F}$ capacitor from Murata

4.2. Results

Using this approximation, a simulation is run using 200 time steps across a 100ms period. The curve obtained is compared with the actual, acquired from oscilloscope, in figure.



5. Deriving New Formula

Using the same estimation we can substitute in $\hat{C}(V)$ for $C(V)$. Using a similar process as for the original RC formula we can find a new formula.

[illegible]

5.2. Results

6. Comparison of Methods

7. Generalization

Compare capacitance vs. voltage of different types of capacitors (x5r, x7r, 0805, 0603)

Try to find a general formula that can fit this curve

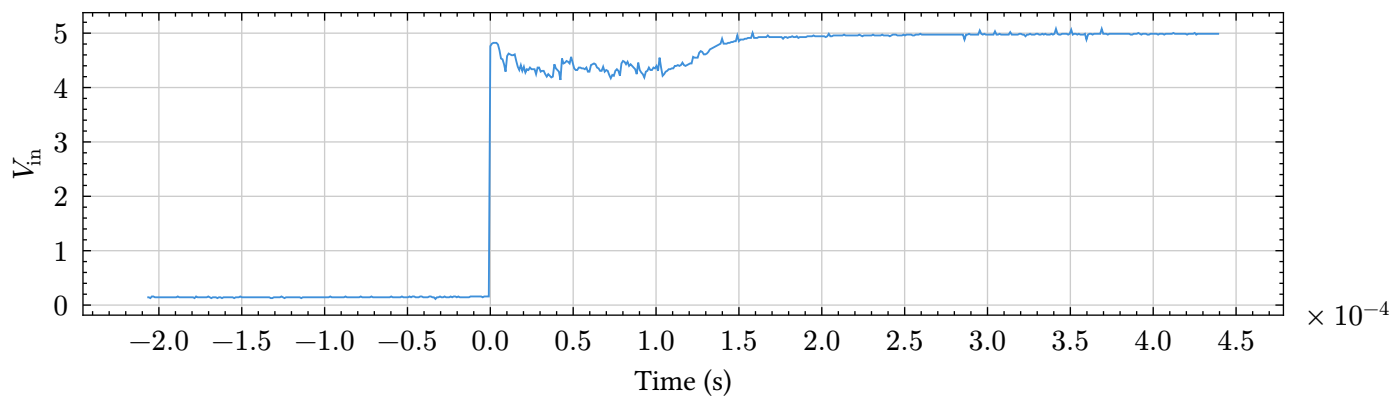
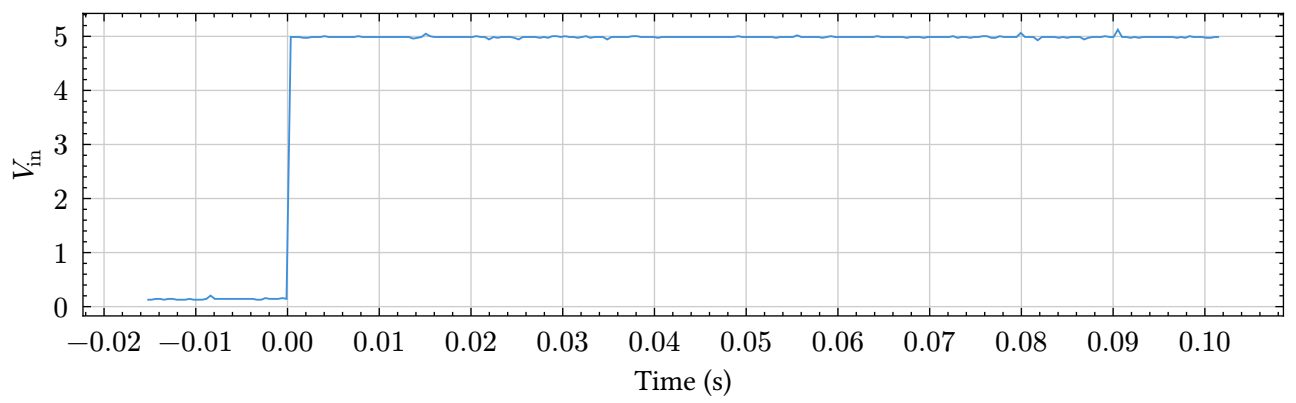
Do five again but with a generalized formula

8. Conclusion

How this could be useful in my life and elsewhere

Why it might not be accurate (This focused on bulk capacitors which might not be as common for enable pins due to size, Model might not be accurate because capacitance might not immediately change with dc applied across the capacitor (maybe experiment with this with different resistor values and see if C from RC stays the same))

A Waveforms



B Code

```
1 import csv
2
3 file = open("data/sim-50.csv", mode= "w", newline = "")
4 writer = csv.writer(file)
5
6 #1k ohm
7 R = 1e3
8 #5V
9 V = 5
10 #22uf
11 C = 22e-6
12
13 #100ms
14 end = 0.1
15 steps = 50
16 dt = end/steps
17 t = 0
18
19 #voltage across capacitor
20 out = 0
21
22 writer.writerow([0, 0])
23
24 #DC Bias capacitor
25 for i in range(steps):
26     #Voltage and thus current across resistor during this step
27     Vr = V-out
28     I = Vr/R
29
30     #Linear approximation of capacitance due to DC bias
31     C_bias = 22e-6 - (2.33e-6 * out)
32
33     #Based on current and amount of time, increment voltage
34     dv = (I*dt)/C
35     out = out + dv
36
37     #record result
38     t = t + dt
39     writer.writerow([t, out])
40
41
42
43 file.close()
```