

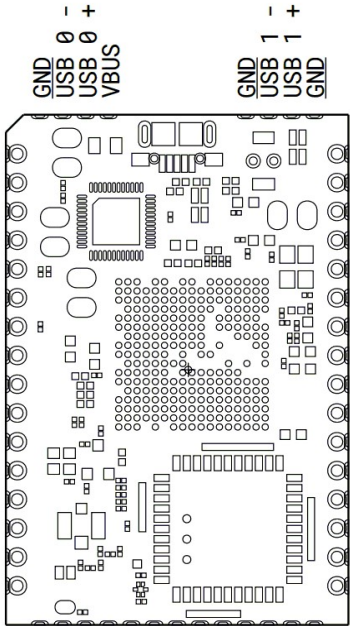
REVISION HISTORY

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P05: MISC  
P06: SDIO WIFI  
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P08: BESIDE CPU  
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PB2 SPDIF-D0  
PG13 EINT13  
PB16 GPIO  
PB15 GPIO  
PD2 LCD-D2  
PD3 LCD-D3  
PD4 LCD-D4  
PD5 LCD-D5

GND  
VCC-3V3  
IPSOUT  
CHG-IN  
PWRON  
GND  
TS  
BAT  
PWM0  
PWM1  
TWI1-SDA  
TWI1-SCK  
UART2-TX  
UART2-RX  
UART2-CTS  
UART2-RTS



GND  
HPL  
HPCOM  
HPR  
I2S-MCLK  
I2S-BCLK  
I2S-LCLK  
I2S-D0  
I2S-D1  
AGND  
MICIN1  
MICIN2  
VMIC  
PB5  
PB6  
PB7  
PB8  
PB9

GND  
UART1-TX  
UART1-RX  
LRADC0  
CSIPCK  
CSIMCLK  
CSIHSYNC  
CSIVSYNC  
CSID0  
CSID1  
CSID2  
CSID3  
CSID4  
CSID5  
CSID6  
CSID7  
EINT3  
EINT4  
SPI2-CS0  
SPI2-CLK  
SPI2-MOSI  
SPI2-MISO  
SDC2-D0  
SDC2-D1  
SDC2-D2  
SDC2-D3  
SDC2-CMD  
SDC2-CLK  
UART1-TX  
UART1-RX  
PG3  
PG4  
PE0  
PE1  
PE2  
PE3  
PE4  
PE5  
PE6  
PE7  
PE8  
PE9  
PE10  
PE11

SOFTWARE-DEFINED  
ALT. MUX FUNCTION  
GR8 PIN PORT NO.

| Revision History | Description | Date       | Drawn | Checked | Approved |
|------------------|-------------|------------|-------|---------|----------|
| CRUMB            | version 0.1 | 2016-08-05 |       |         |          |
|                  |             |            |       |         |          |
|                  |             |            |       |         |          |
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NEXT/THING  
COMPANY

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Design Name  
C.H.I.P. Pro

Size  
A3

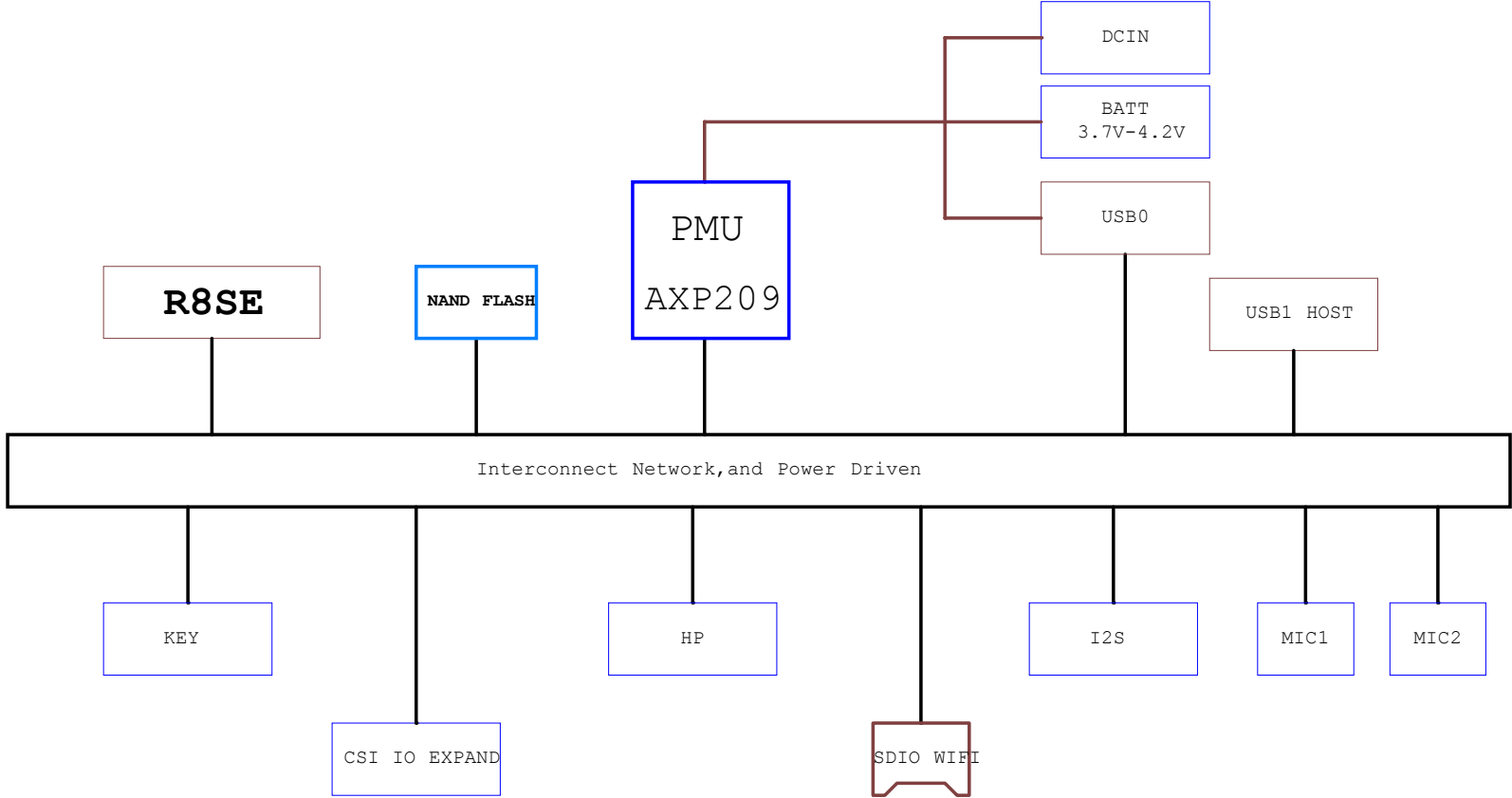
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BLOCK



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Design Name  
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Size  
A3

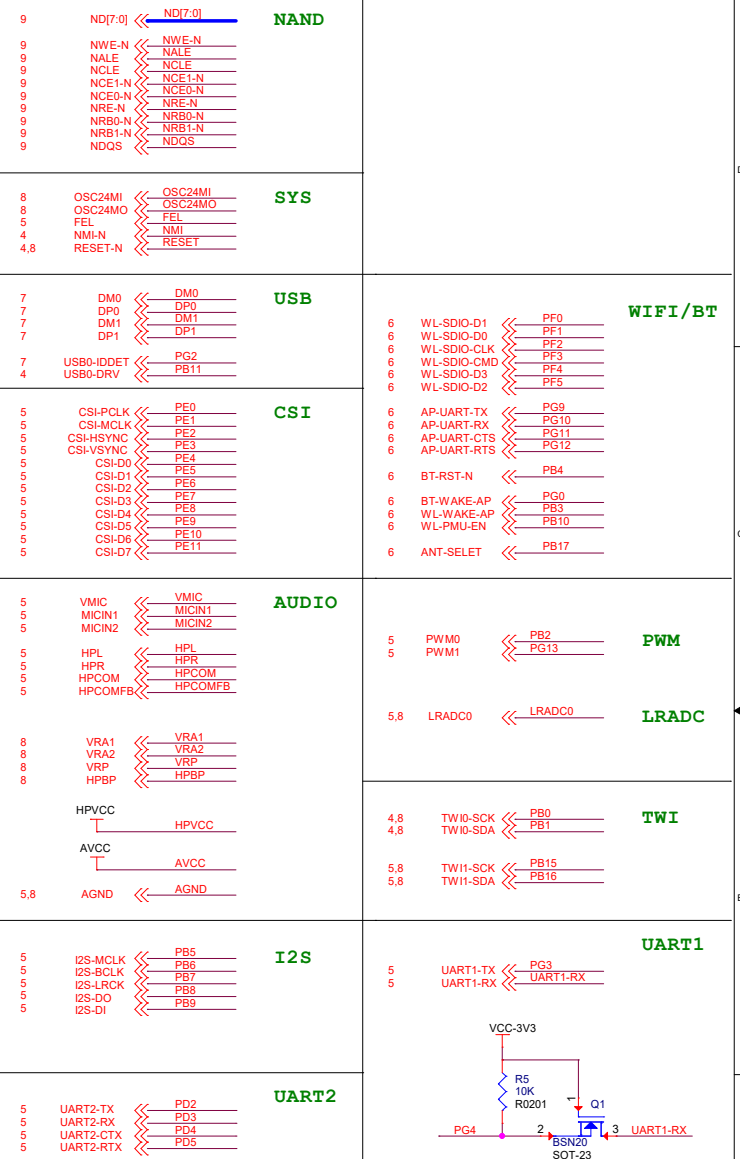
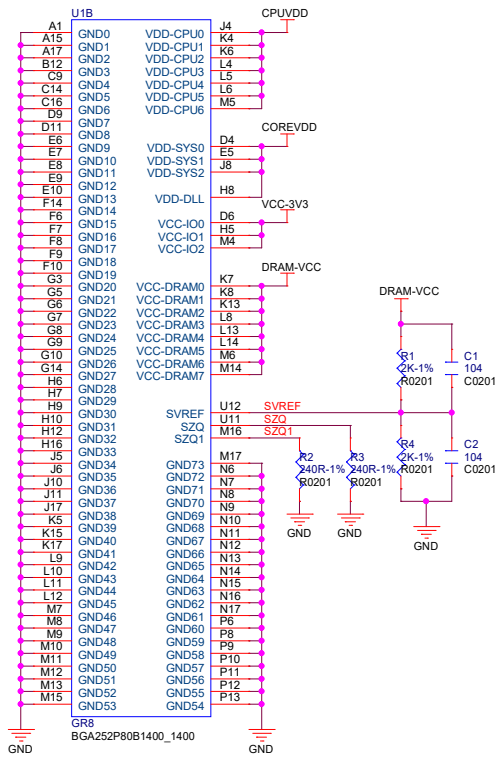
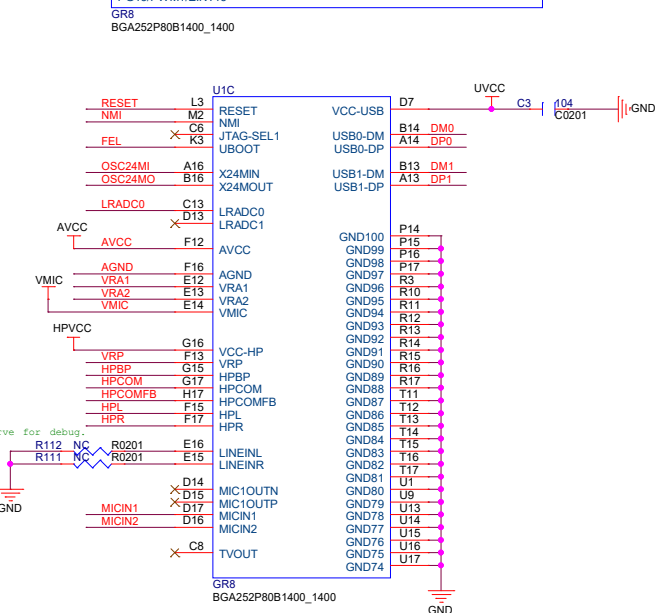
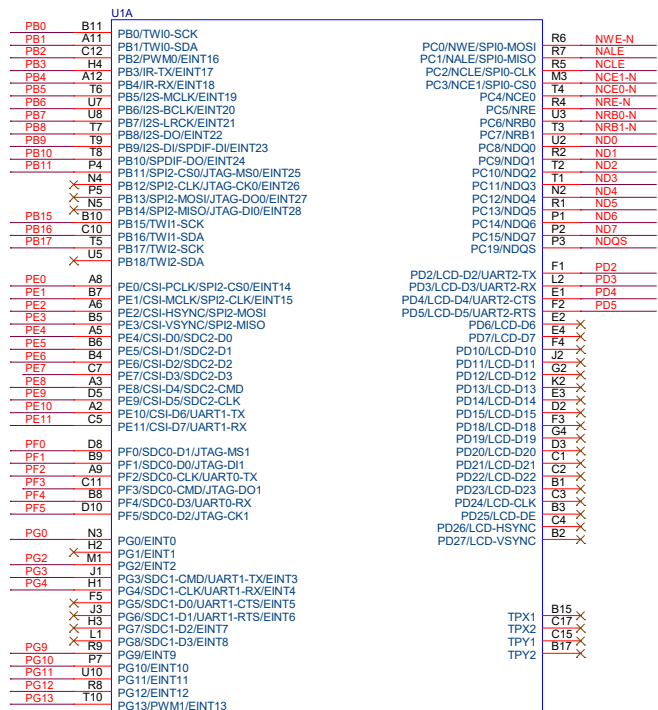
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## CPU



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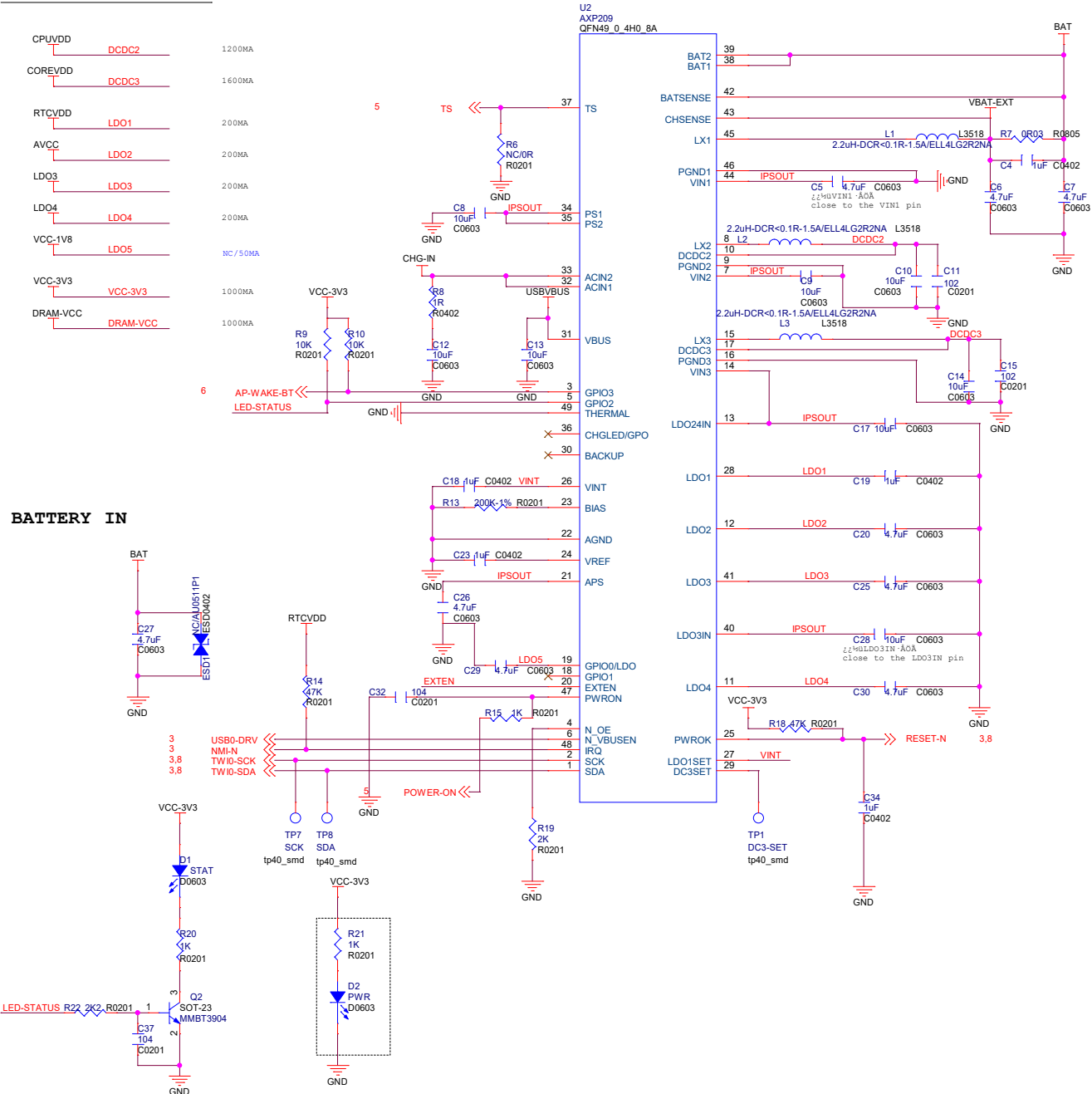
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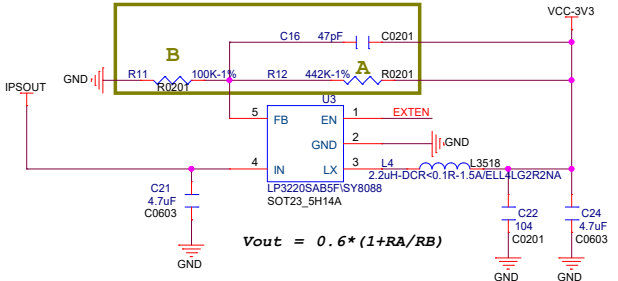
Size: A3 | Page Name: **CPU** | Rev: v0.1

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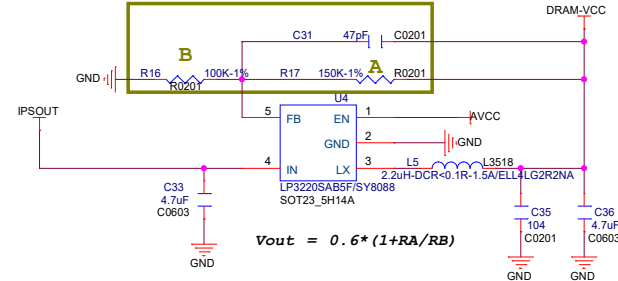
POWER/PMU



IPSOUT TO 3V3



IPSOUT TO 1V6



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POWER

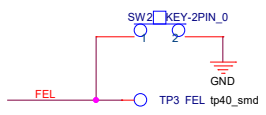
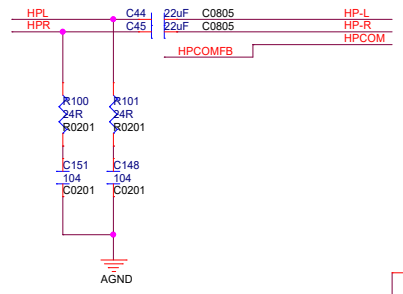
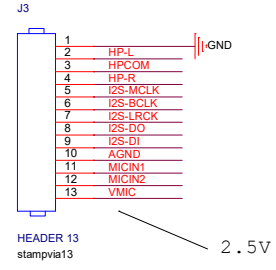
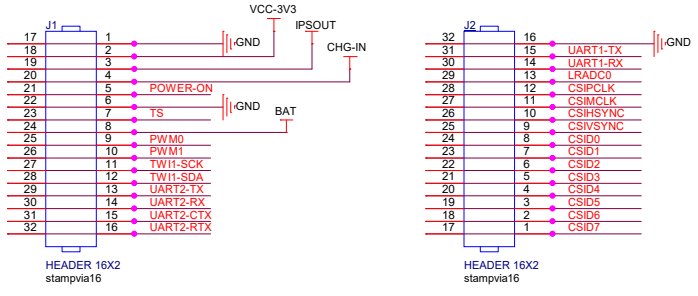
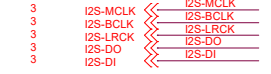
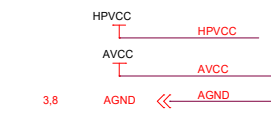
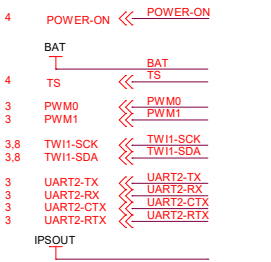
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MISC



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Size  
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Page Name  
MISC

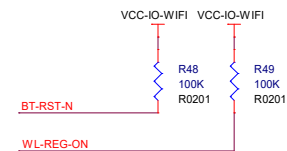
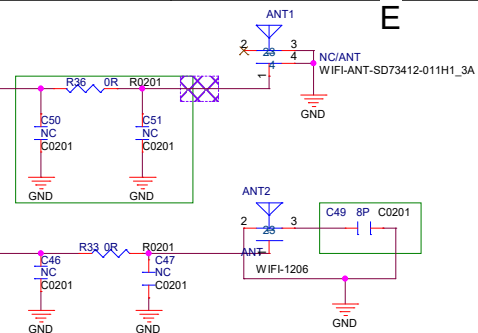
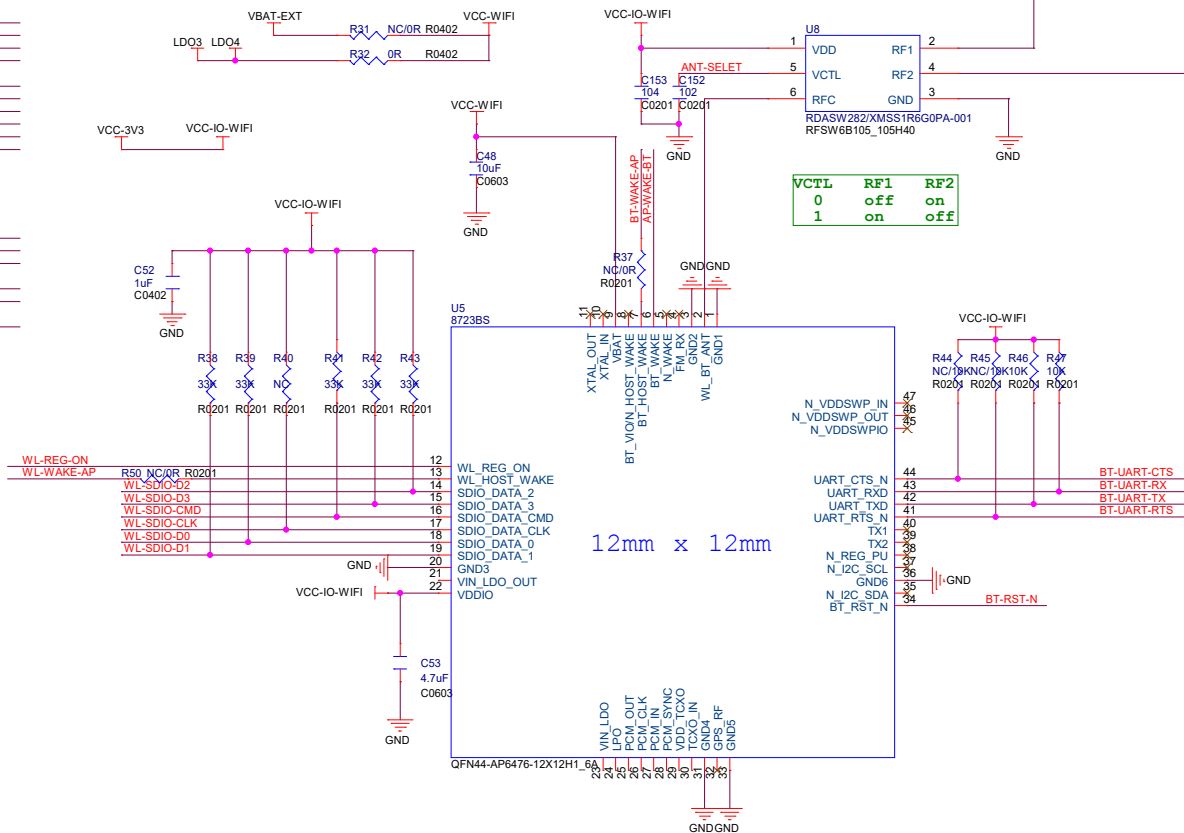
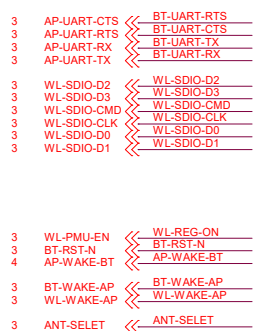
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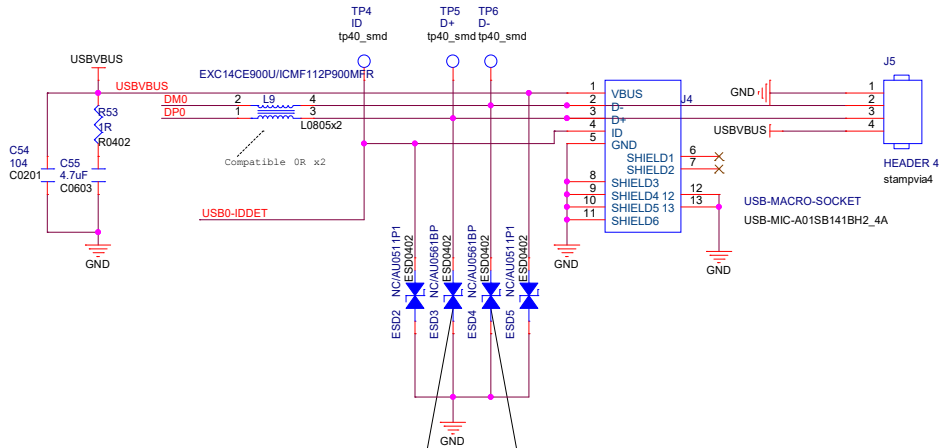
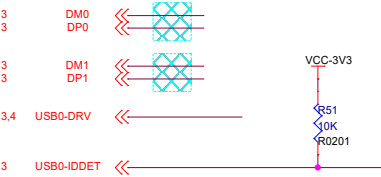
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# SDIO WIFI&BT



USB



D+/D- ESD protection: NC/AU0511P1 ESD0402  
The parasitic capacitance of esd on D+/D- < 4pf



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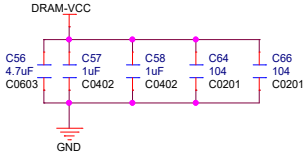
Size A3  
Page 7  
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USB  
Rev v0.1

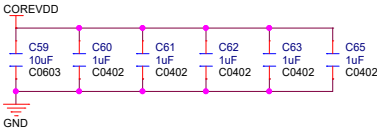
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# BESIDE CPU

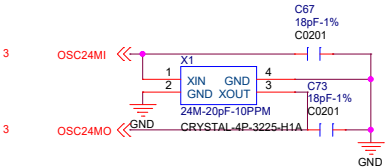
## DRAM



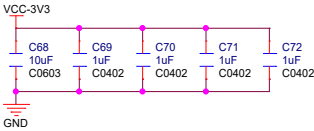
## CORE



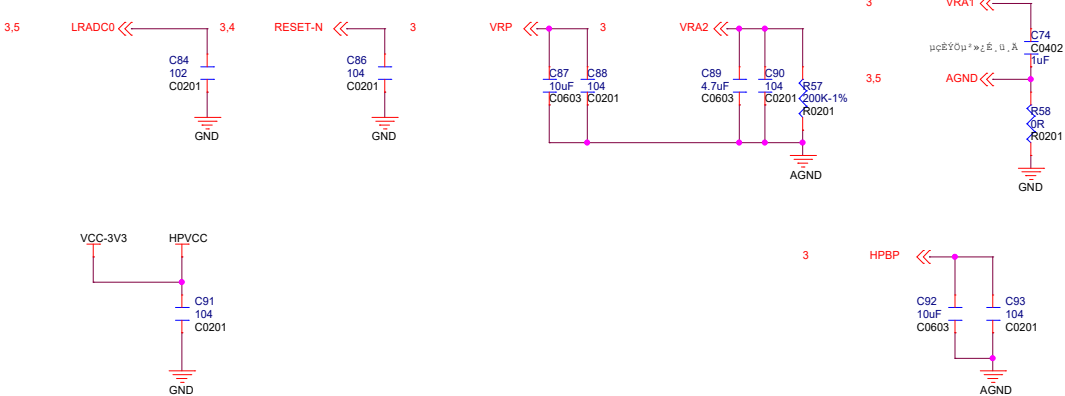
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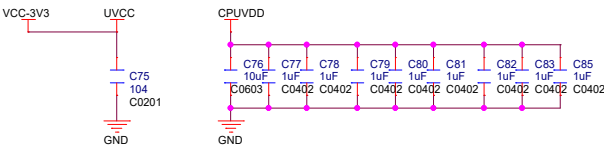
## PIO-INTERFACE



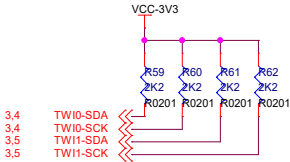
## AUDIO-TP



## USB-CPU



## OTHERS



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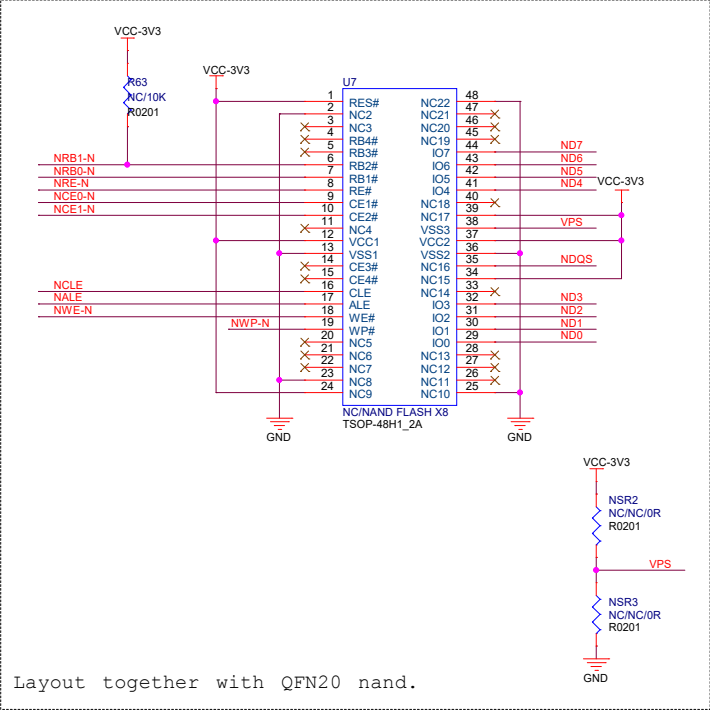
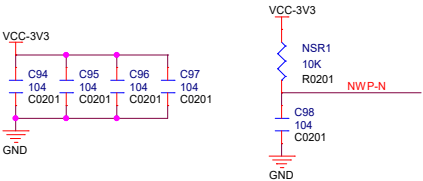
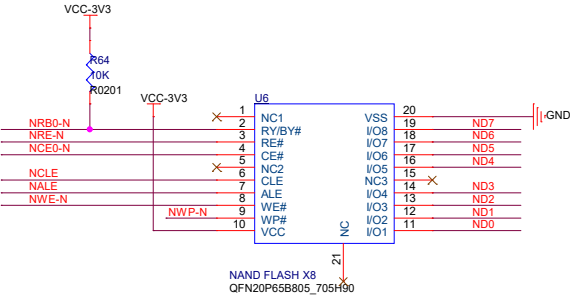
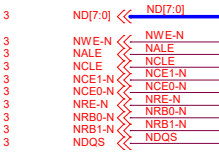
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NAND



Layout together with QFN20 nand.

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