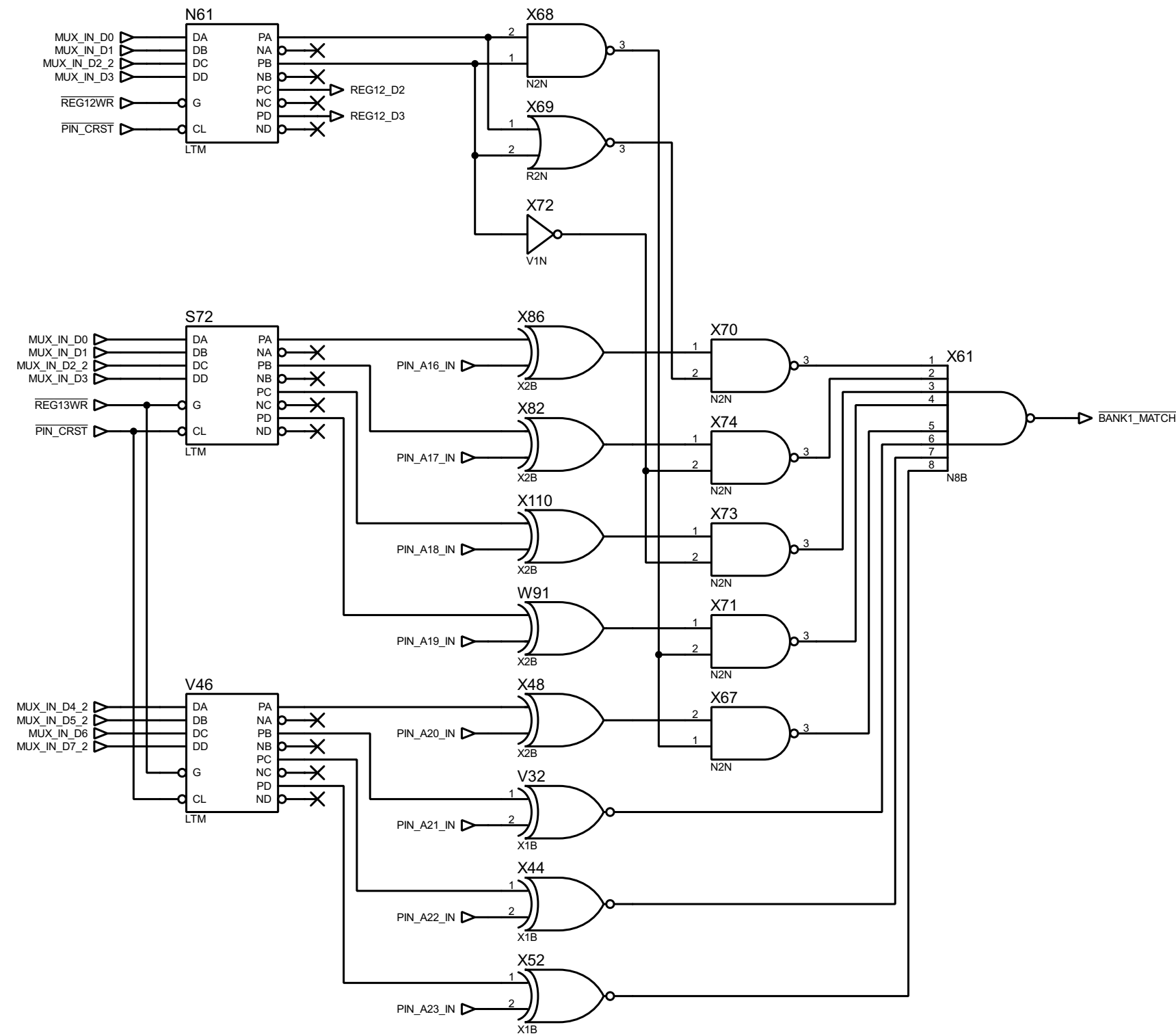
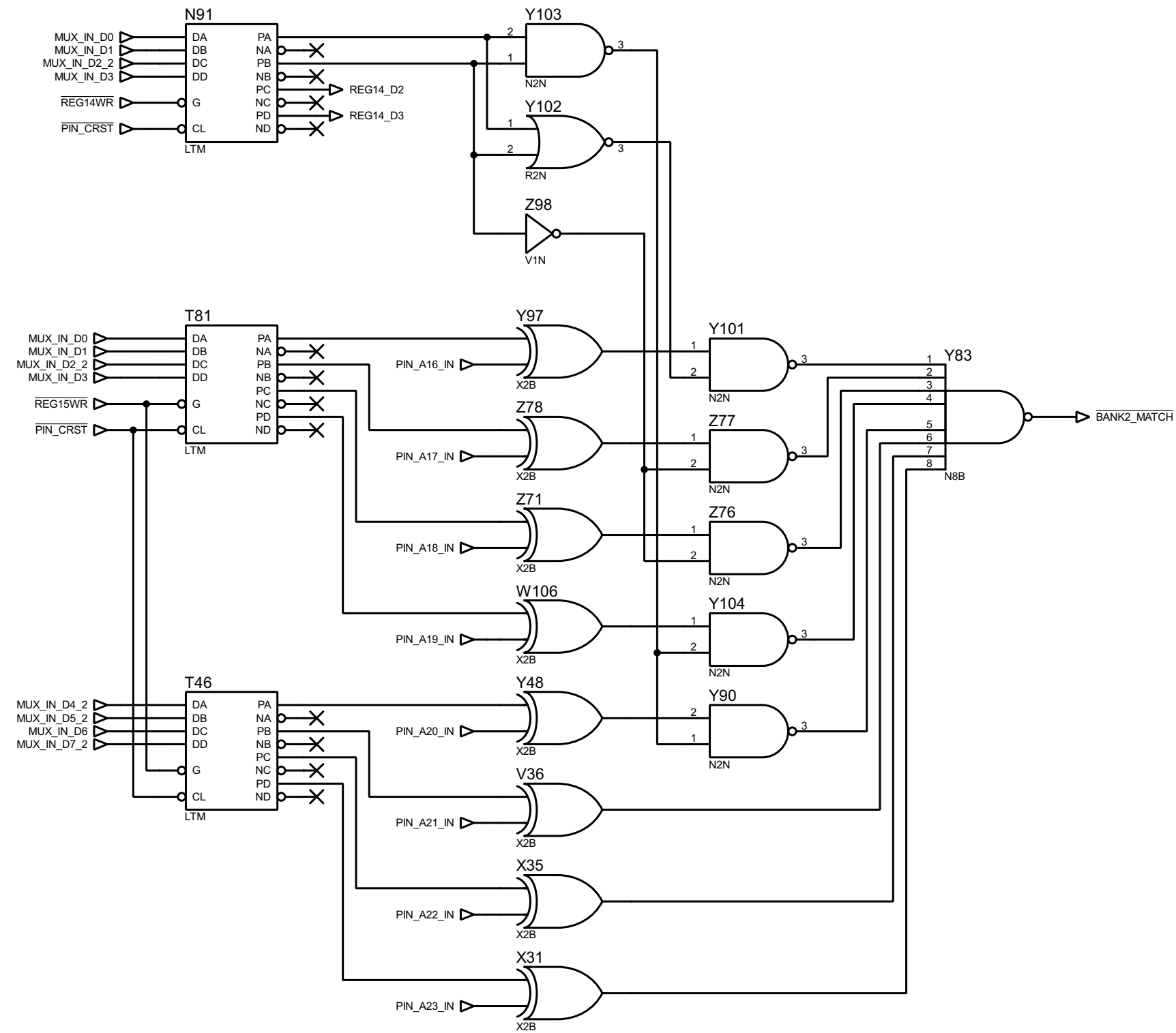
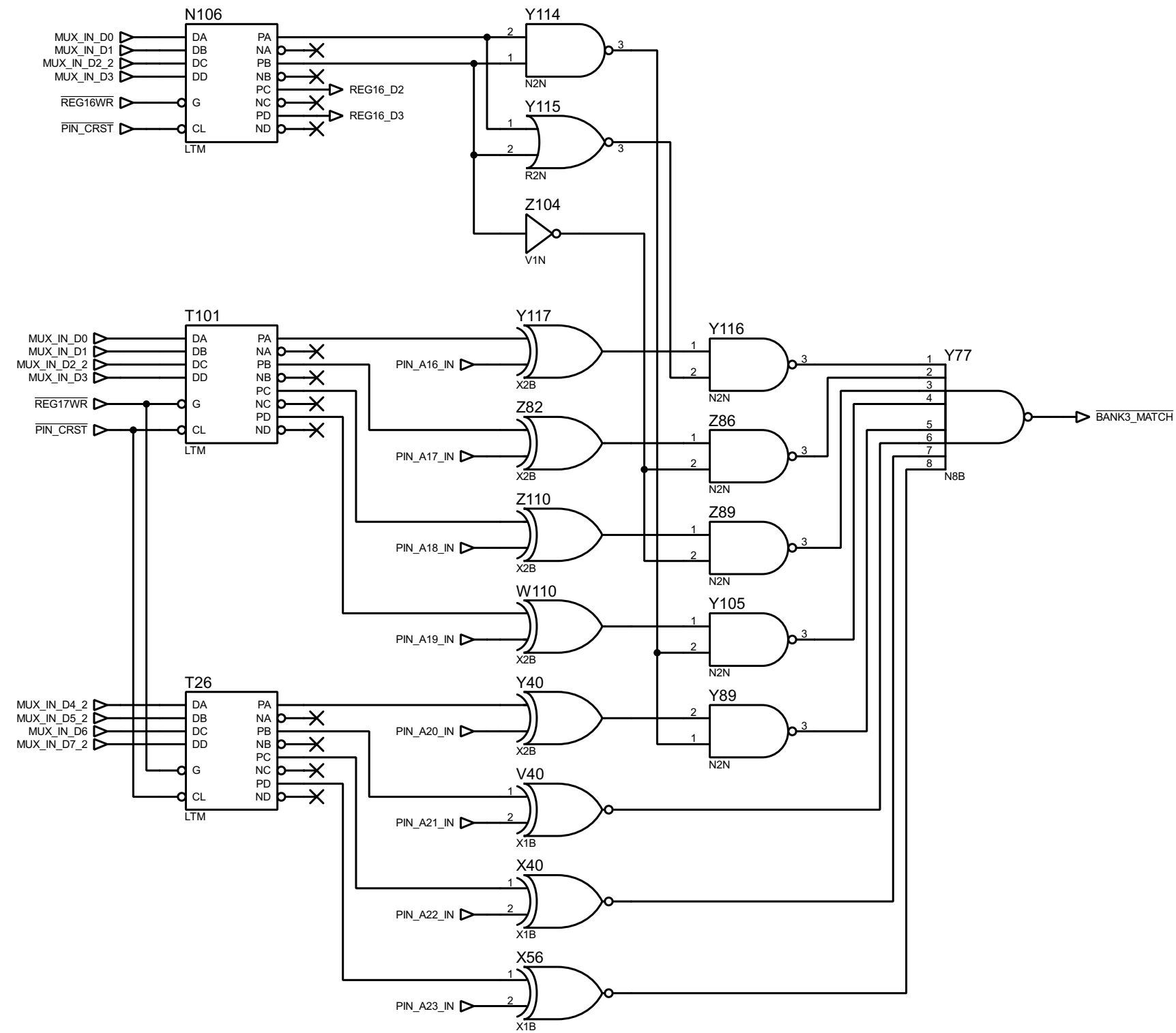


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| FILE NAME:    | 315-5195.pdsprj                      | DATE: | 02/08/2022 |
| DESIGN TITLE: | Sega 315-5195<br>MAPPING REGS BANK 0 | PAGE: | 1 of 18    |
| BY:           | Sean Gonsalves                       | REV:  | A          |

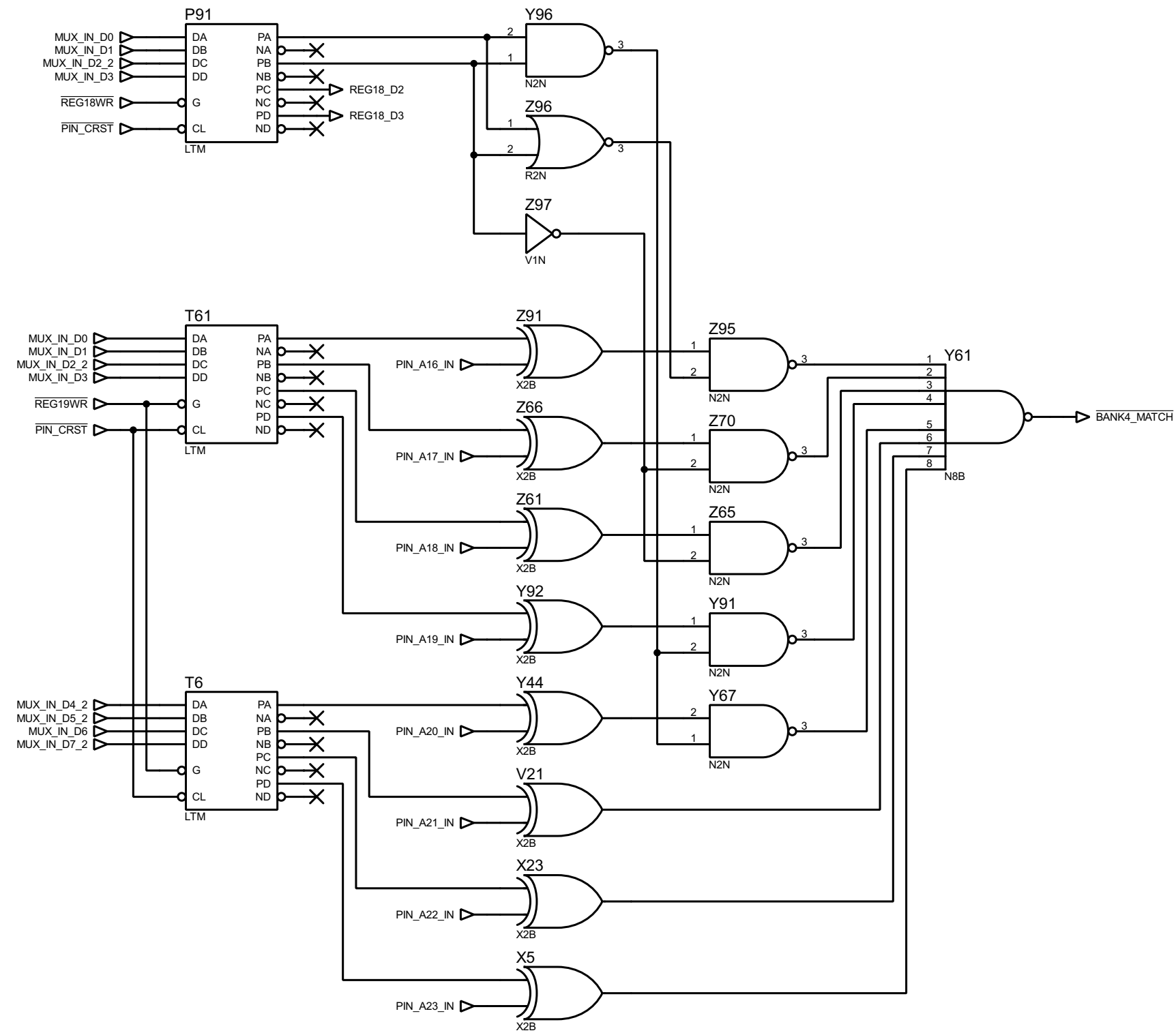




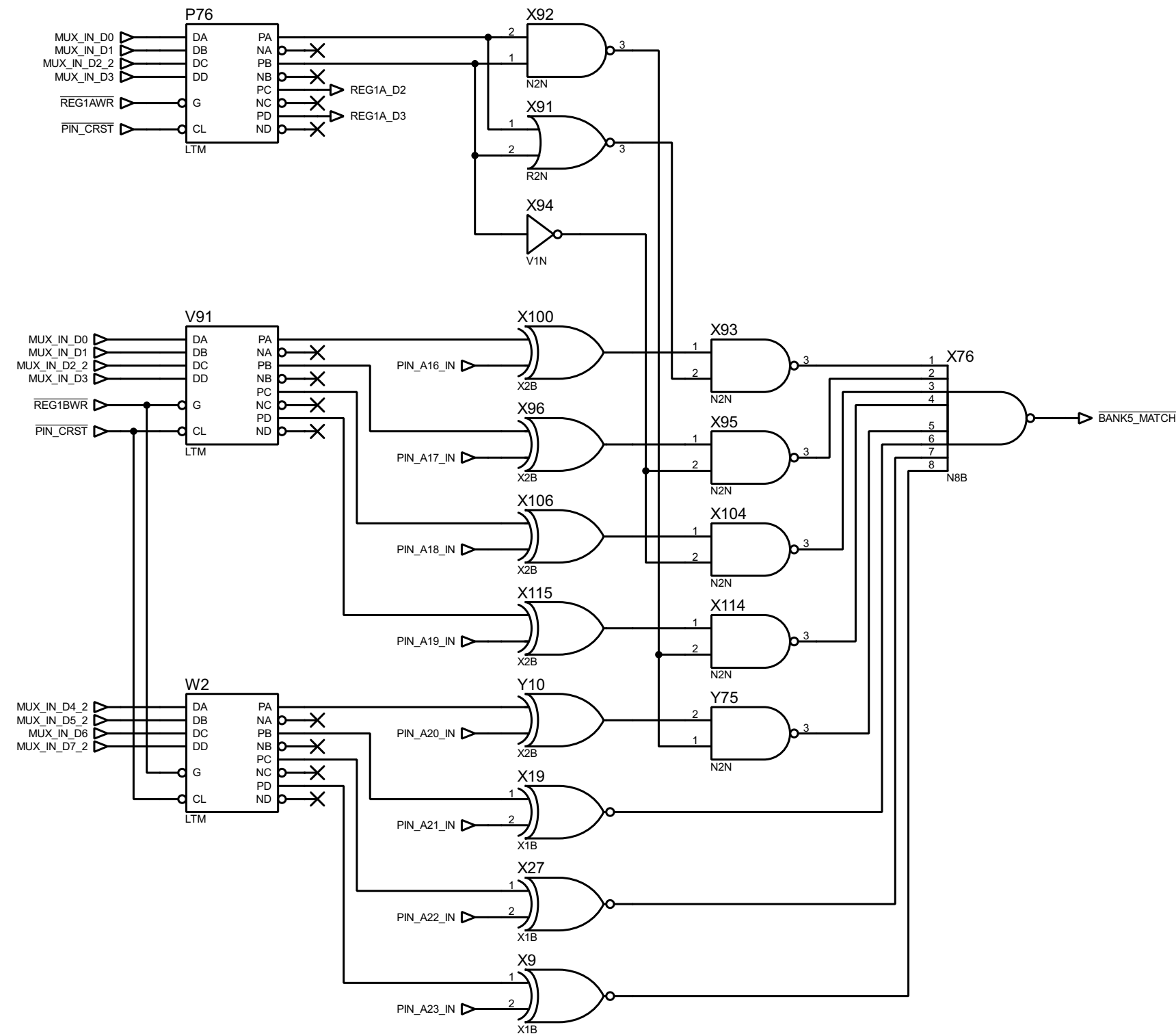
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| DESIGN TITLE: | Sega 315-5195<br>MAPPING REGS BANK 2 | PAGE: | 3 of 18    |
| BY:           | Sean Gonsalves                       | REV:  | A          |



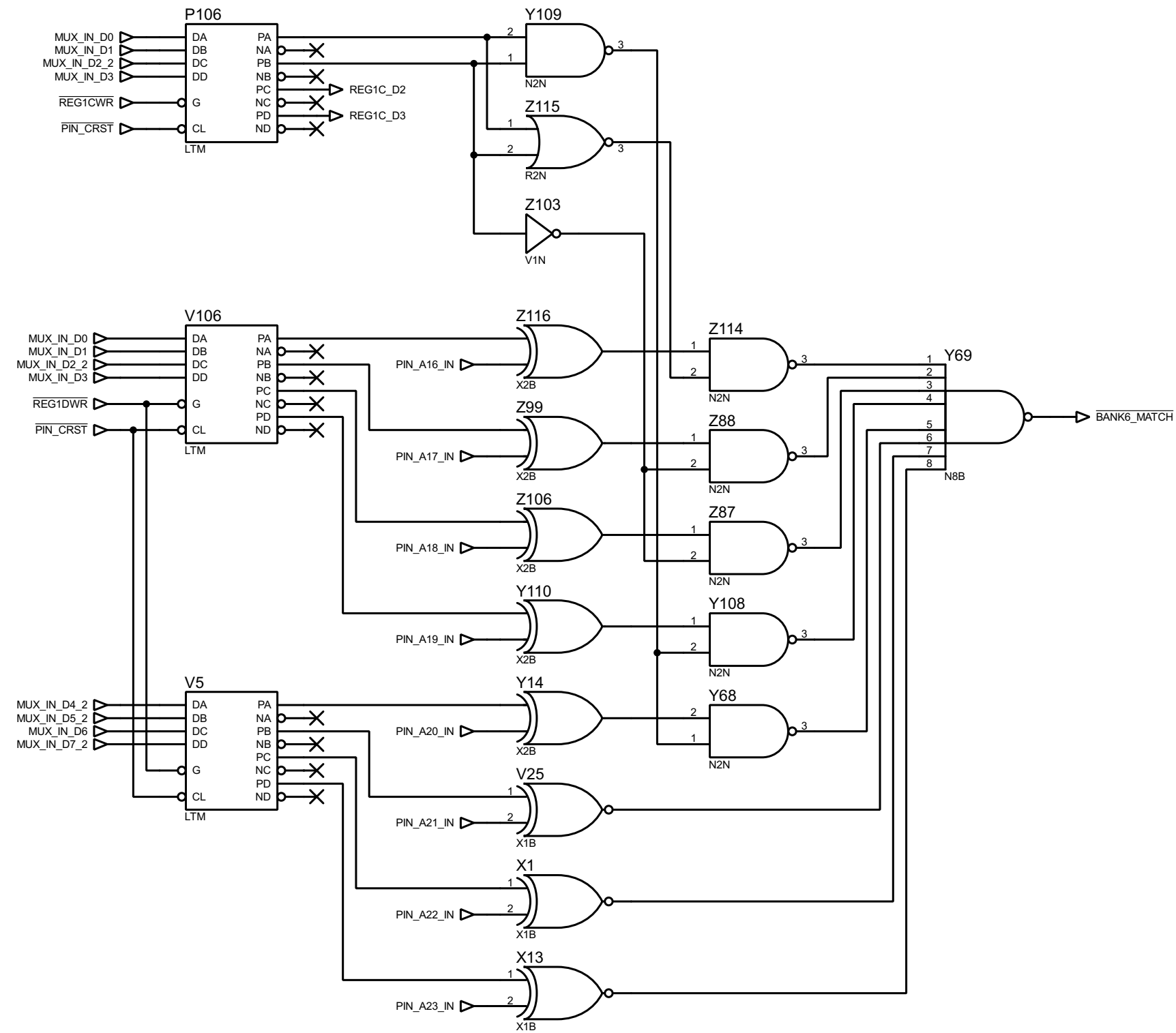
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| DESIGN TITLE:      | Sega 315-5195       | PAGE: | 4 of 18    |
|                    | MAPPING REGS BANK 3 |       |            |
| BY: Sean Gonsalves | REV: A              |       |            |



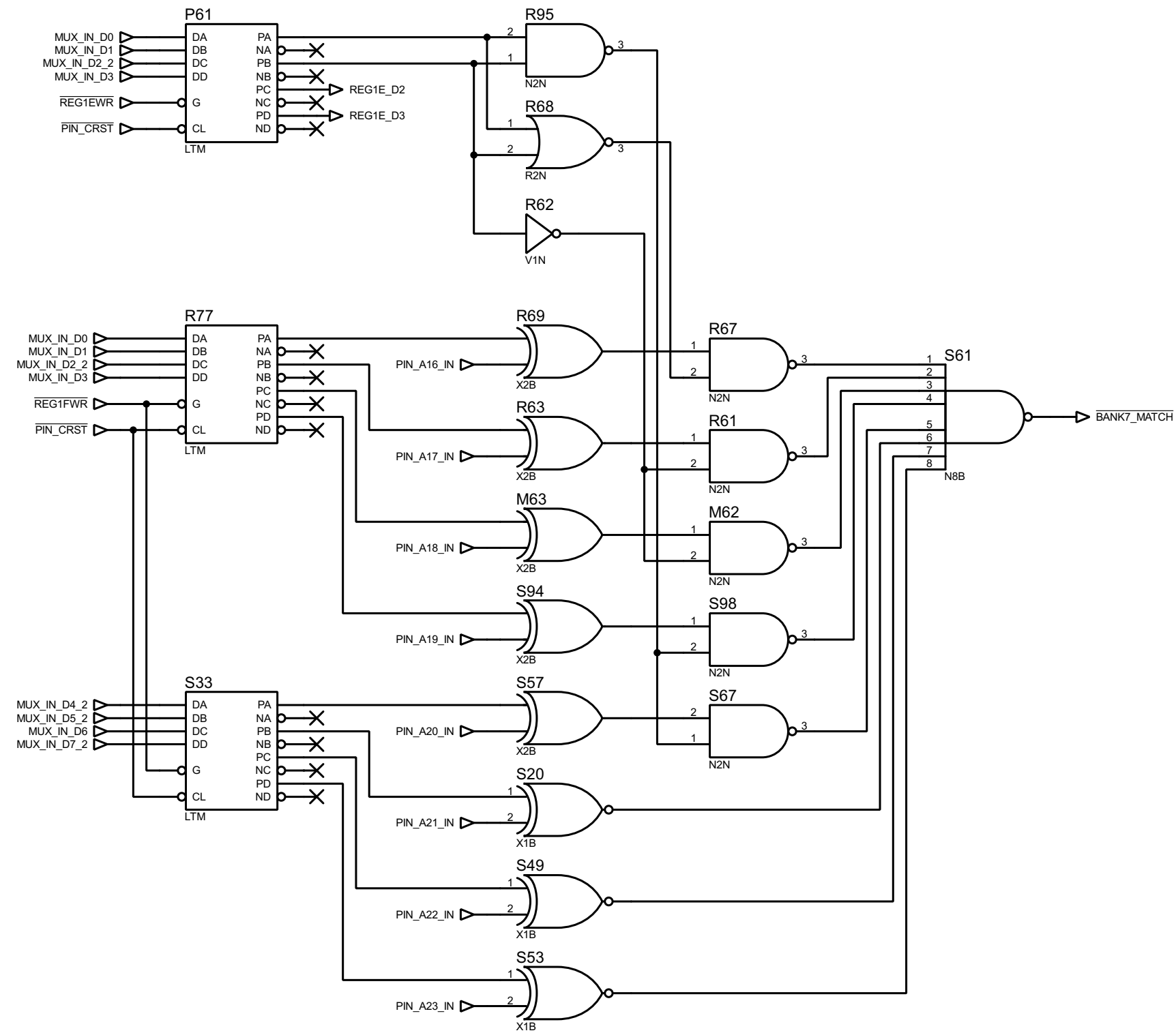
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| DESIGN TITLE:      | Sega 315-5195       | PAGE: | 5 of 18    |
|                    | MAPPING REGS BANK 4 |       |            |
| BY: Sean Gonsalves | REV: A              |       |            |



|               |                                      |       |            |
|---------------|--------------------------------------|-------|------------|
| FILE NAME:    | 315-5195.pdsprj                      | DATE: | 02/08/2022 |
| DESIGN TITLE: | Sega 315-5195<br>MAPPING REGS BANK 5 | PAGE: | 6 of 18    |
| BY:           | Sean Gonsalves                       | REV:  | A          |



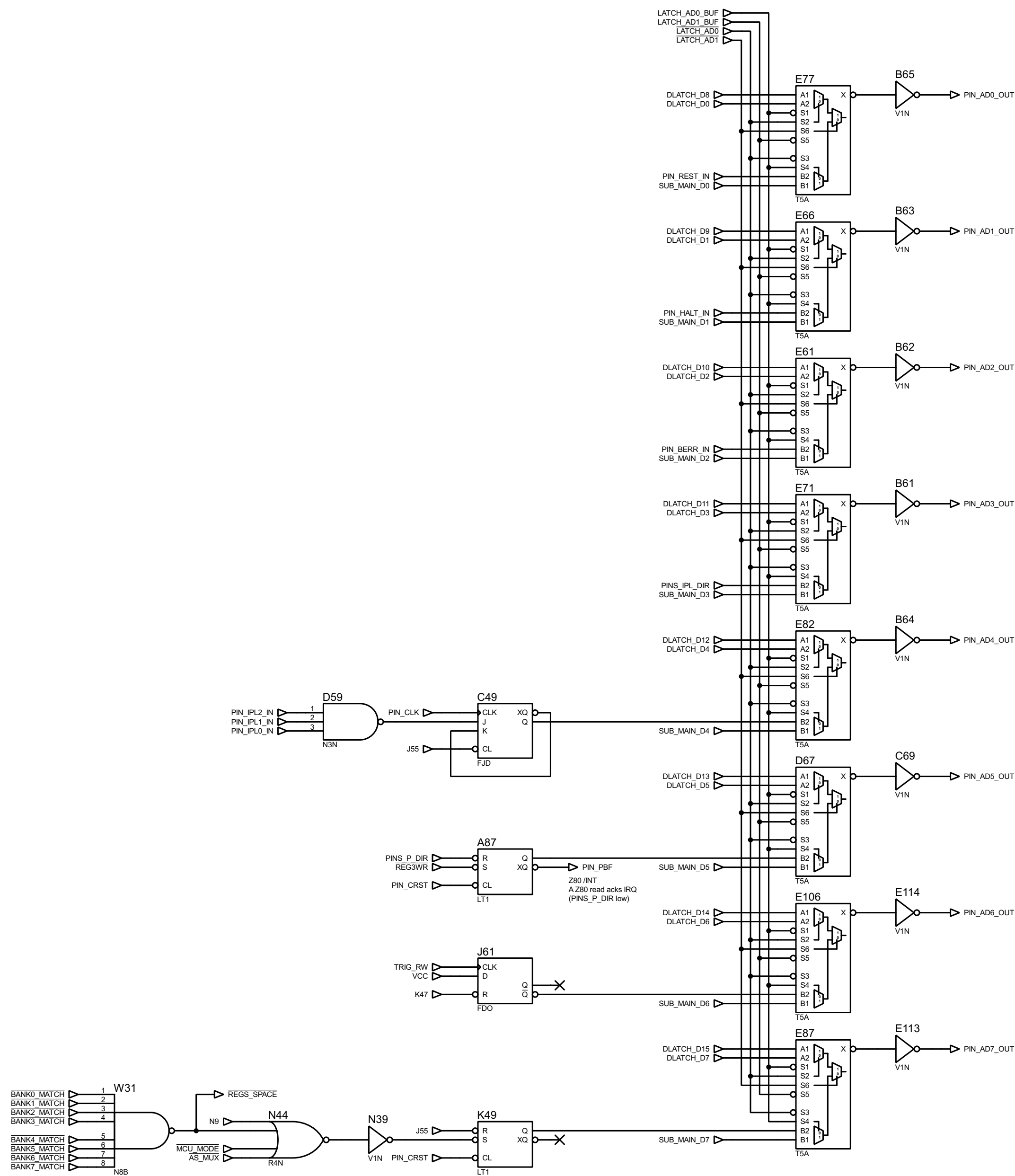
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| DESIGN TITLE:      | Sega 315-5195       | PAGE: | 7 of 18    |
|                    | MAPPING REGS BANK 6 |       |            |
| BY: Sean Gonsalves | REV: A              |       |            |



|                    |                     |       |            |
|--------------------|---------------------|-------|------------|
| FILE NAME:         | 315-5195.pdsprj     | DATE: | 02/08/2022 |
| DESIGN TITLE:      | Sega 315-5195       | PAGE: | 8 of 18    |
|                    | MAPPING REGS BANK 7 |       |            |
| BY: Sean Gonsalves | REV: A              |       |            |







MCU read muxes:  
00: Latch MSB  
01: Latch LSB  
02: Various 68k lines and status  
03: Sub CPU latch data

