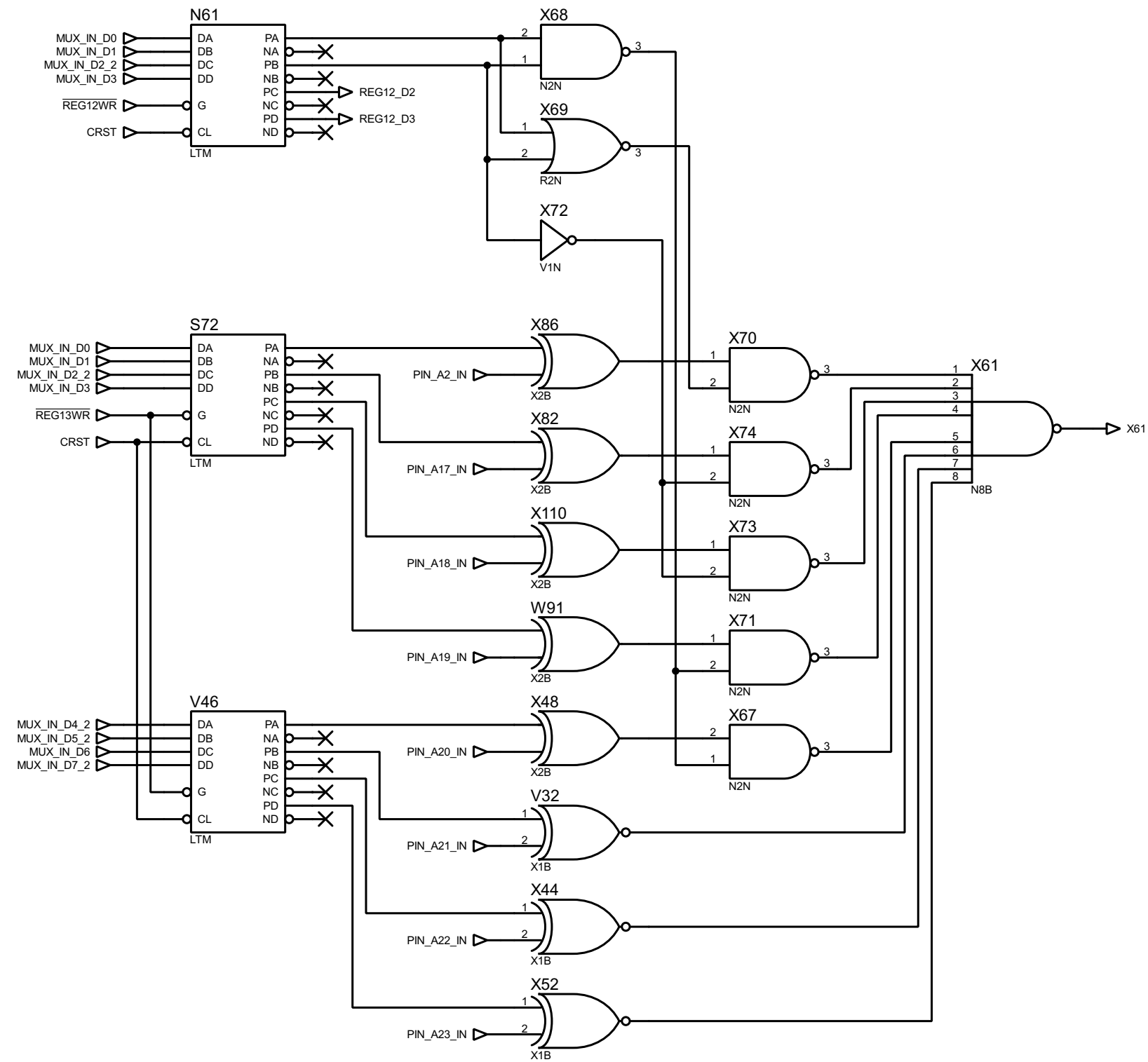
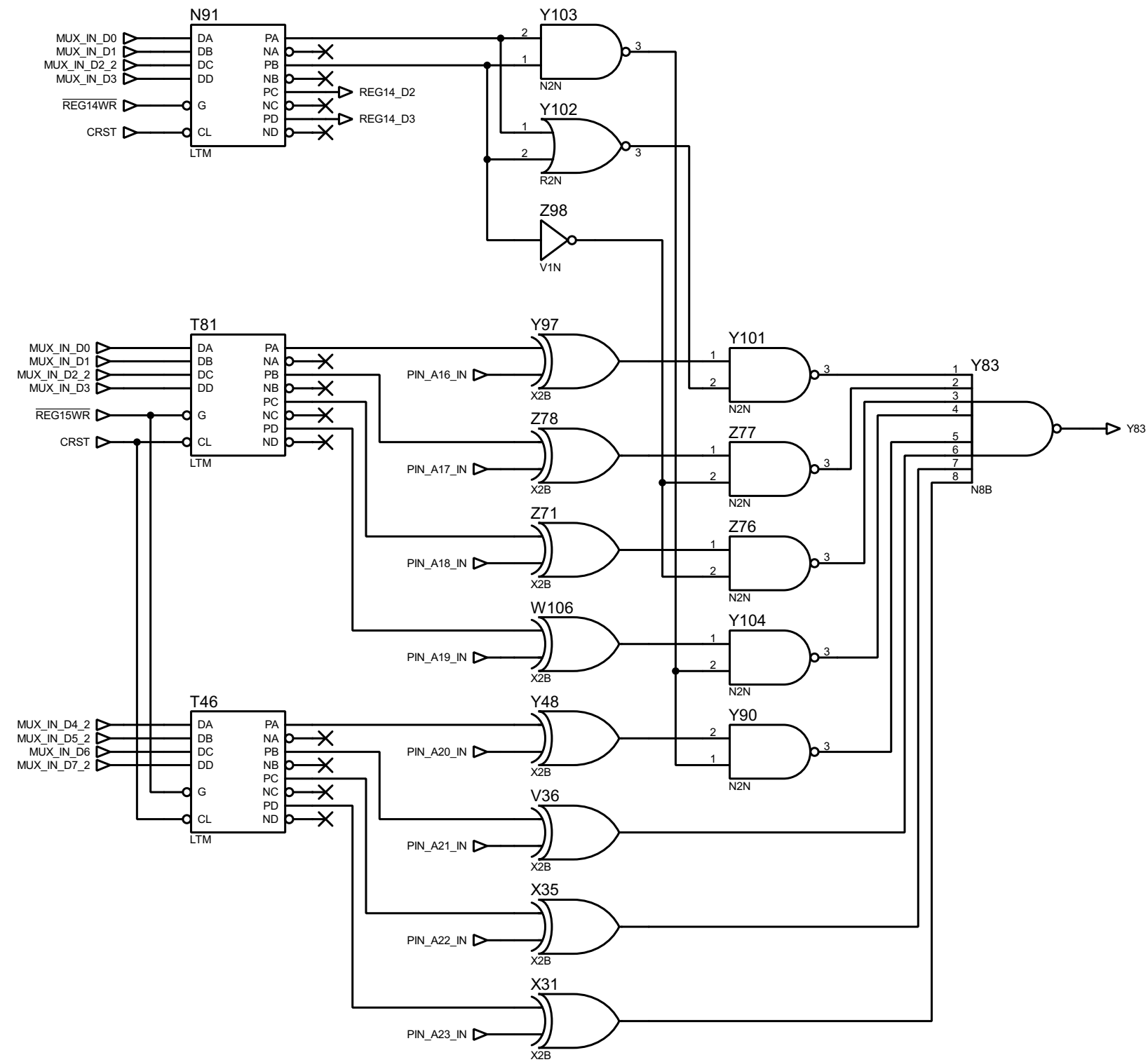
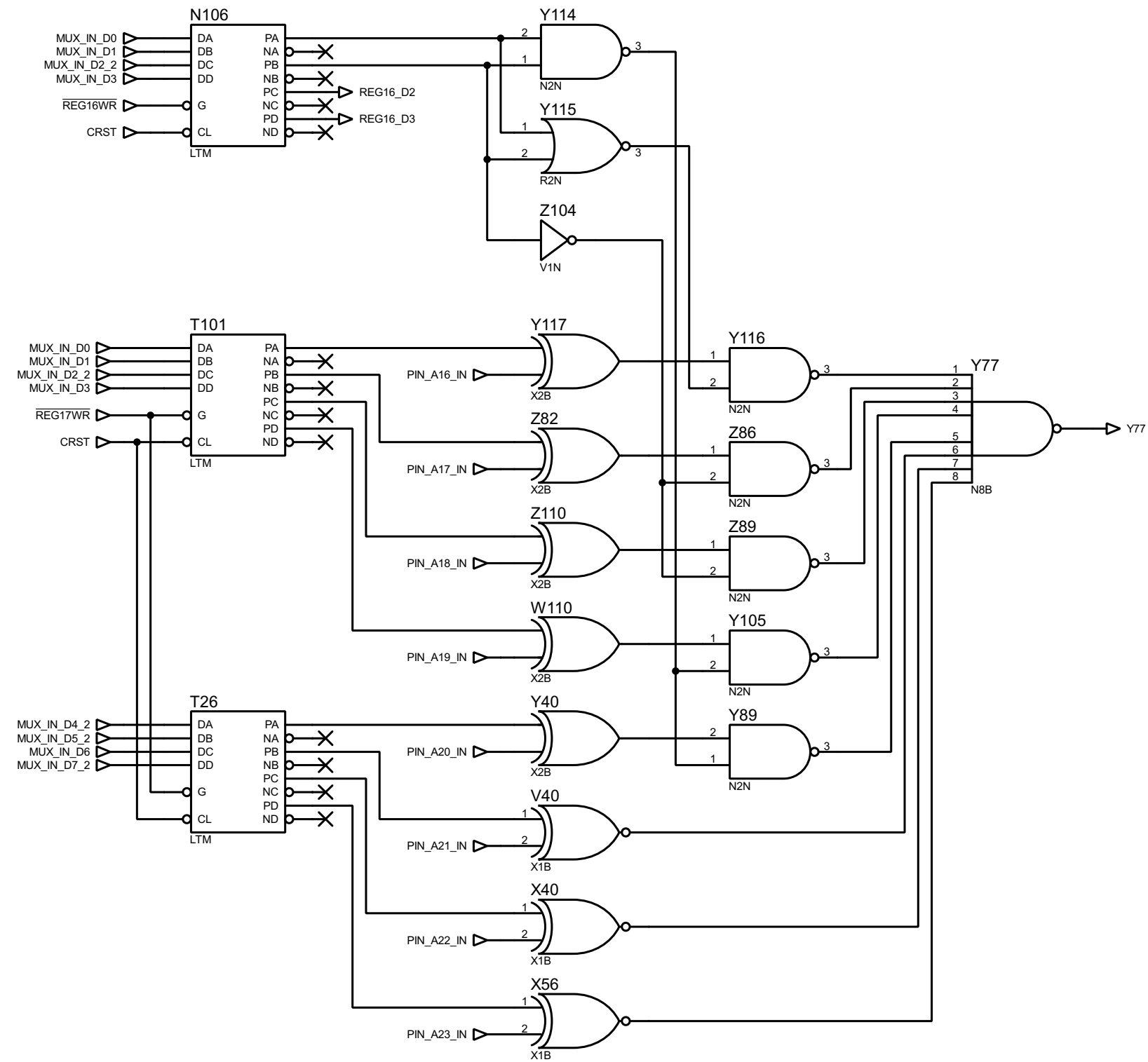


FILE NAME:	315-5195.pdsprj	DATE:	26/07/2021
DESIGN TITLE:	Sega 315-5195	PAGE:	1 of 17
	MAPPING REGS BANK 0		
BY: Sean Gonsalves	REV: A		

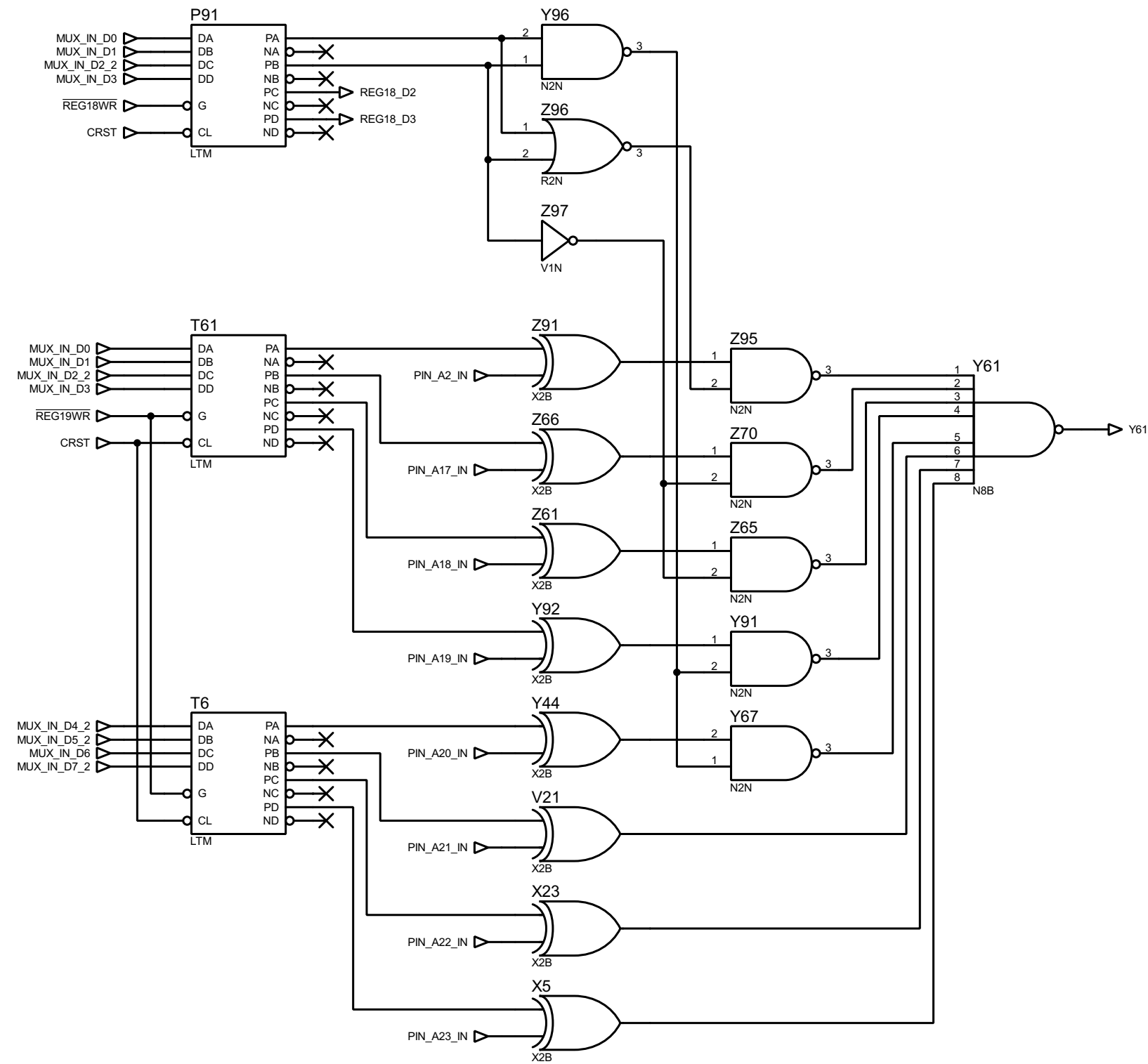


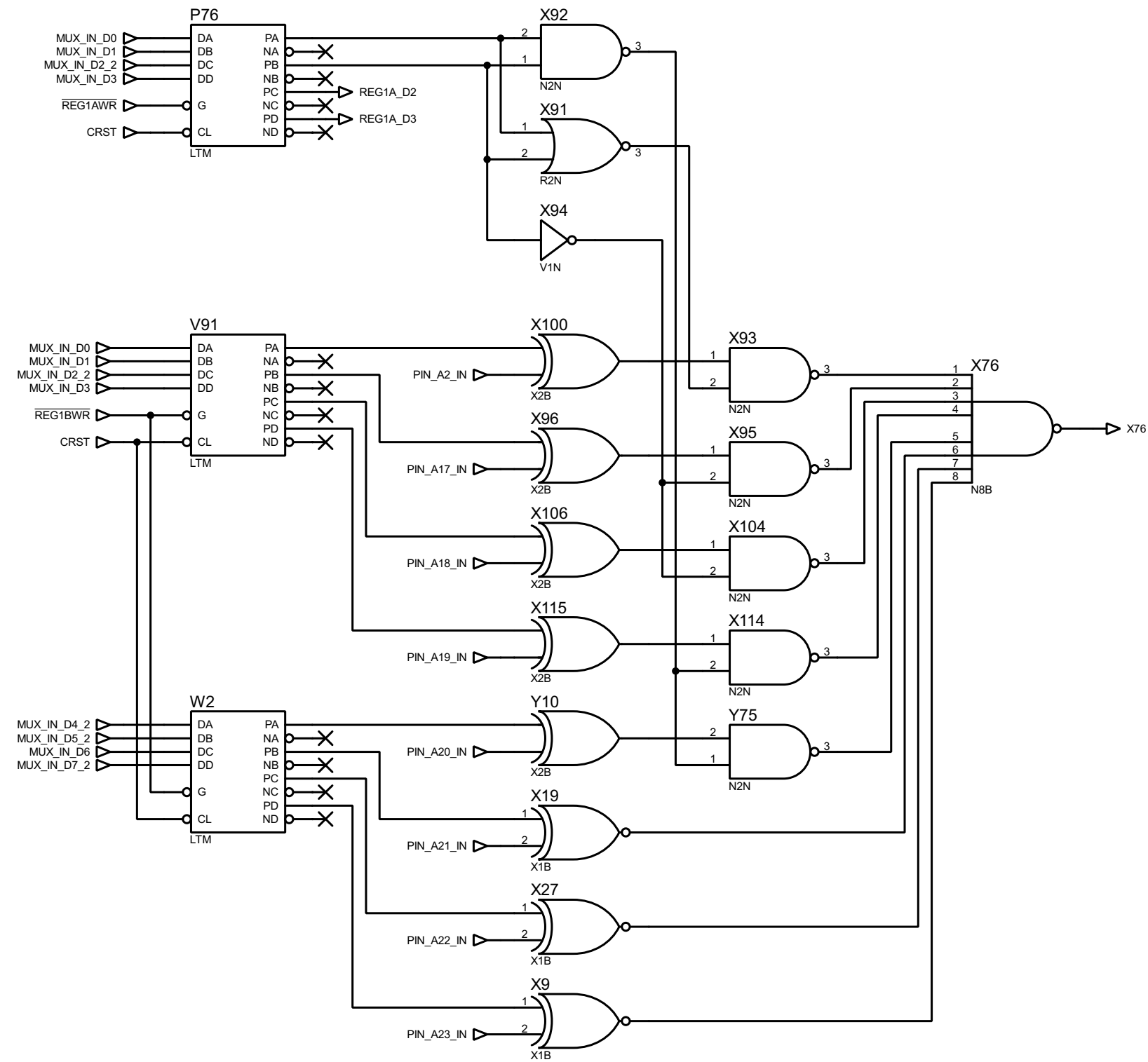
FILE NAME:	315-5195.pdsprj	DATE:	26/07/2021
DESIGN TITLE:	Sega 315-5195	PAGE:	2 of 17
	MAPPING REGS BANK 1		
BY: Sean Gonsalves	REV: A		

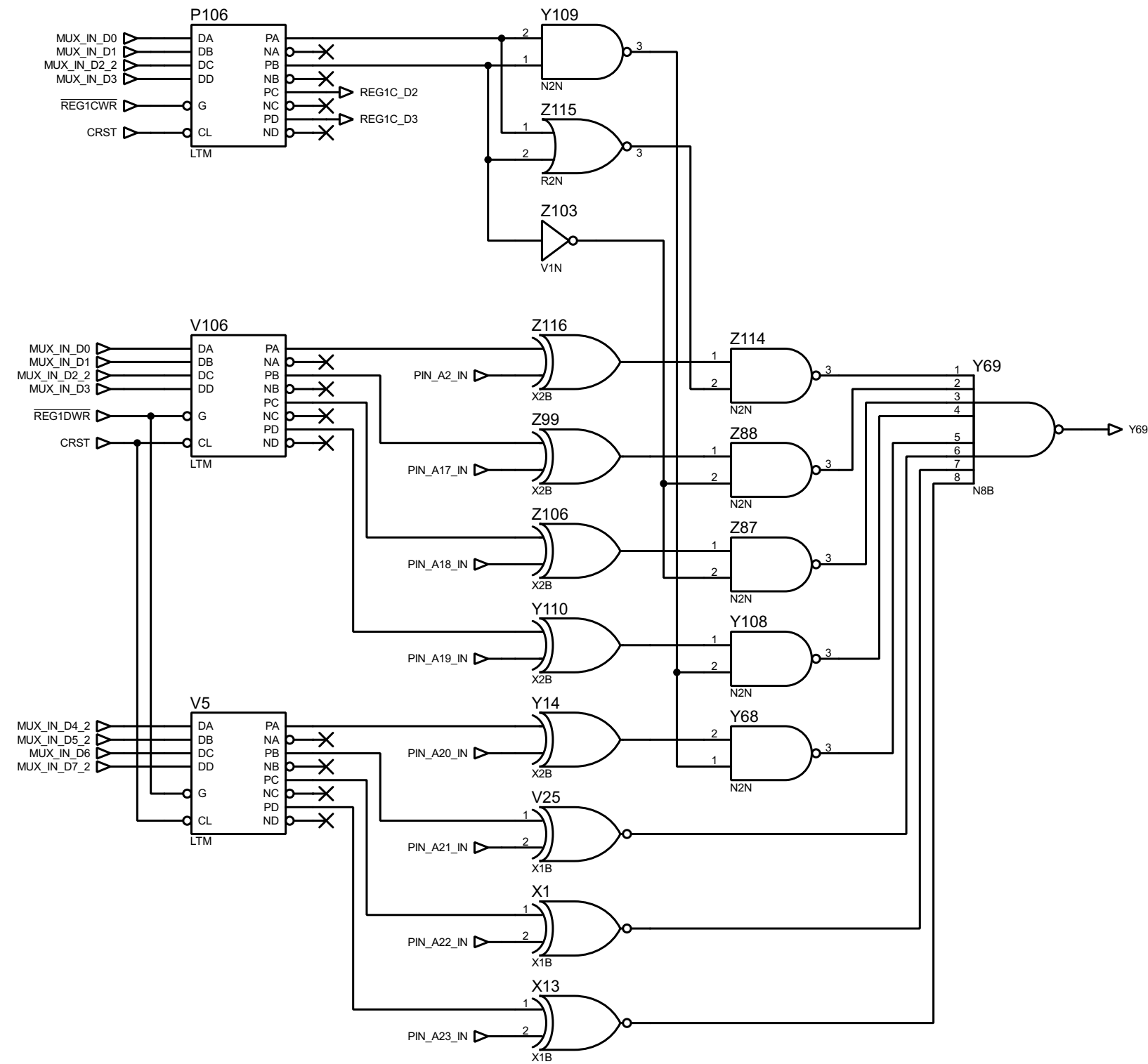




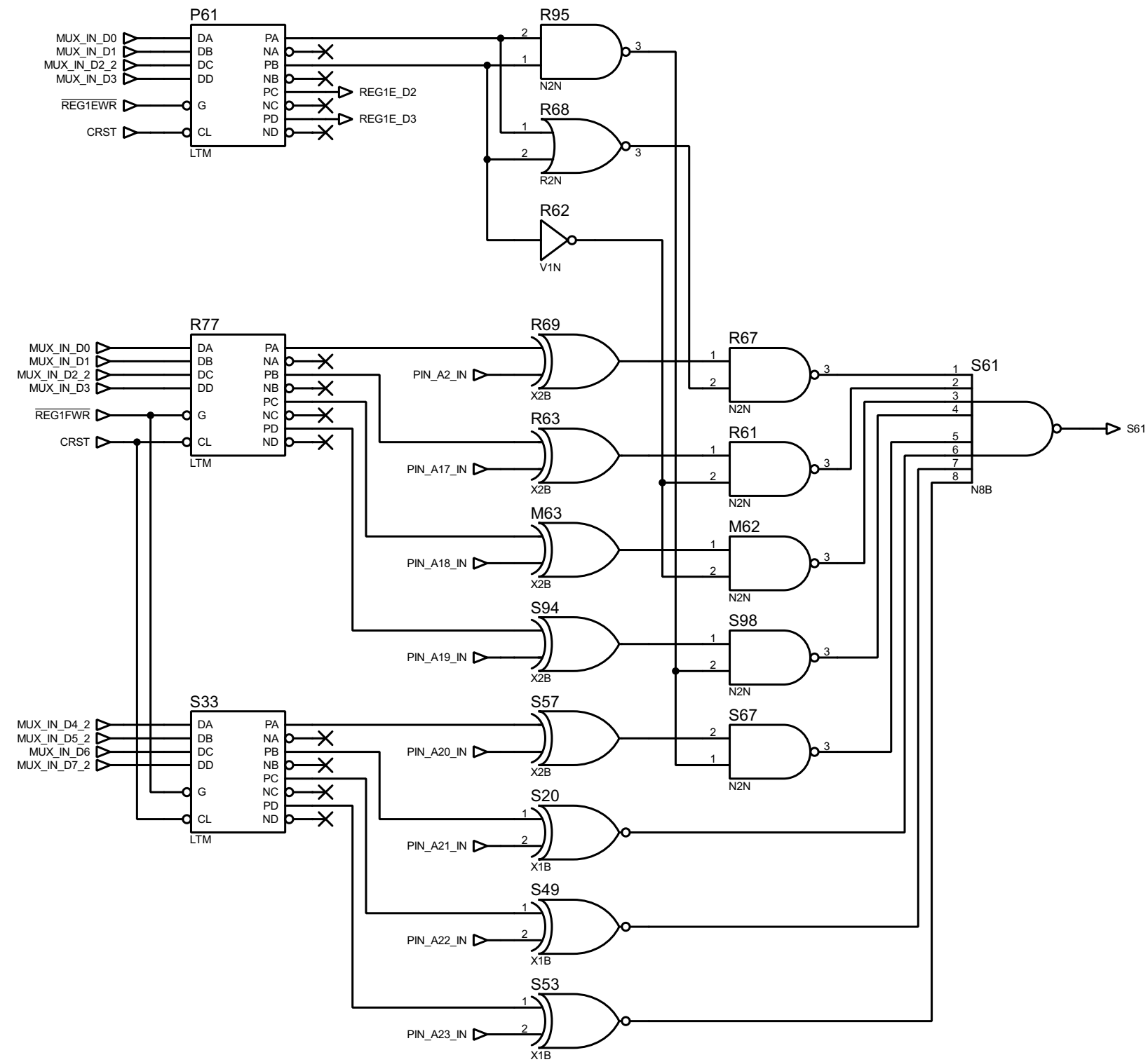
FILE NAME:	315-5195.pdsprj	DATE:	26/07/2021
DESIGN TITLE:	Sega 315-5195	PAGE:	4 of 17
	MAPPING REGS BANK 3		
BY: Sean Gonsalves	REV: A		



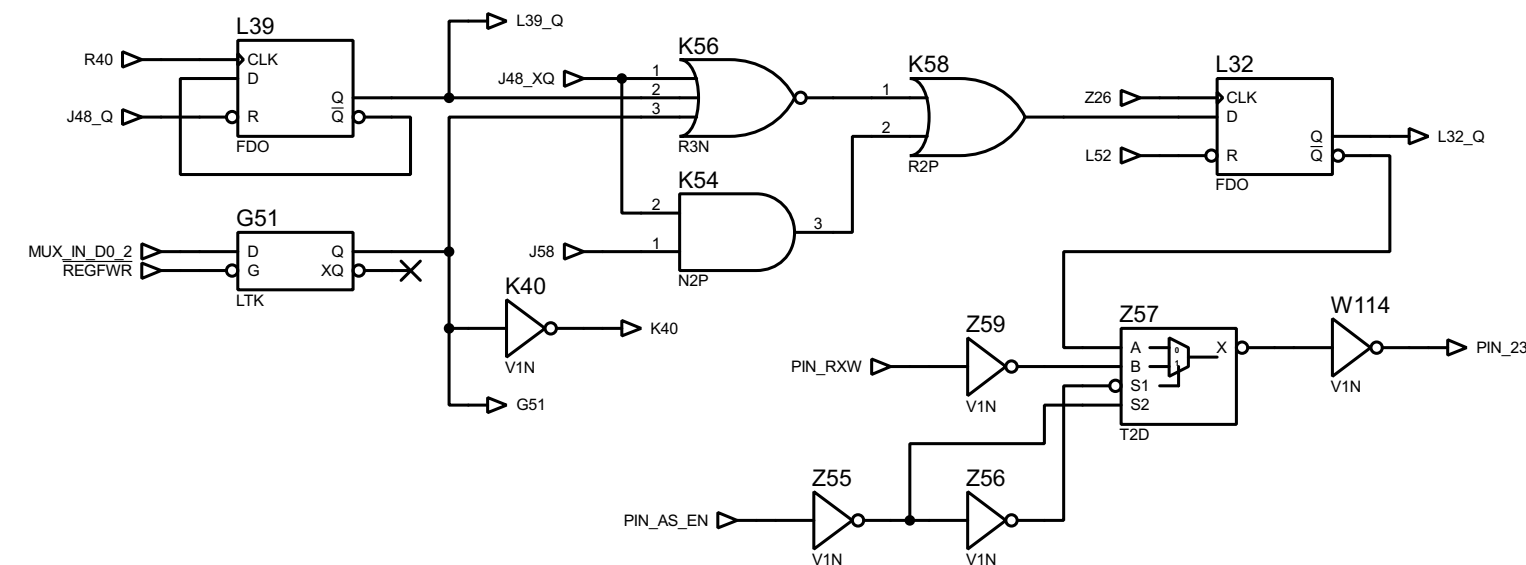
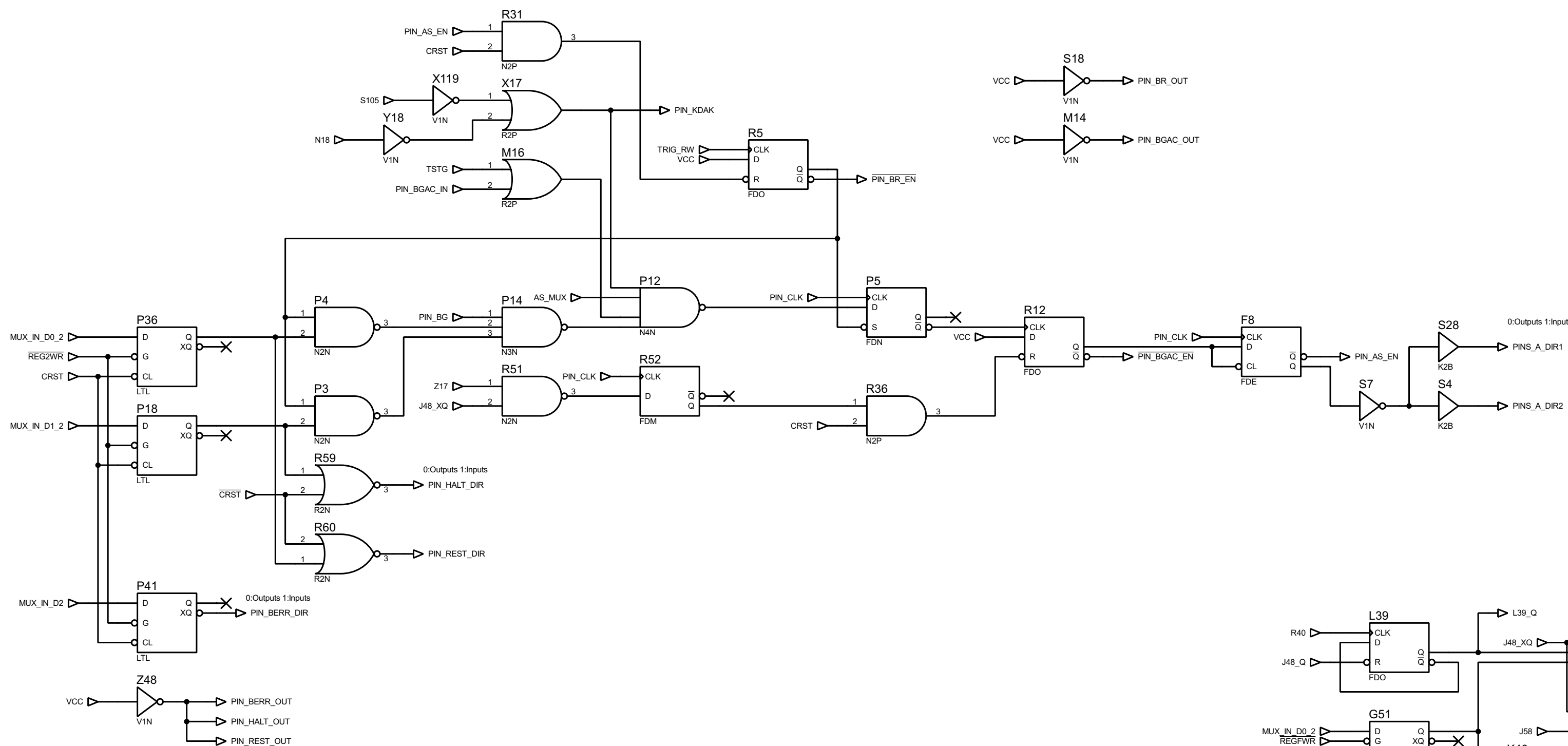
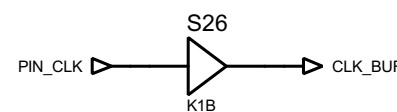
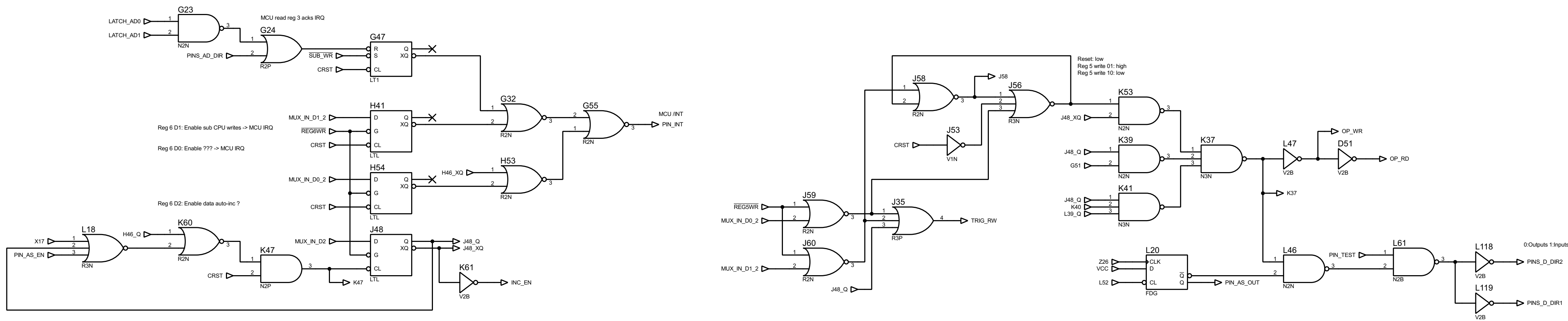


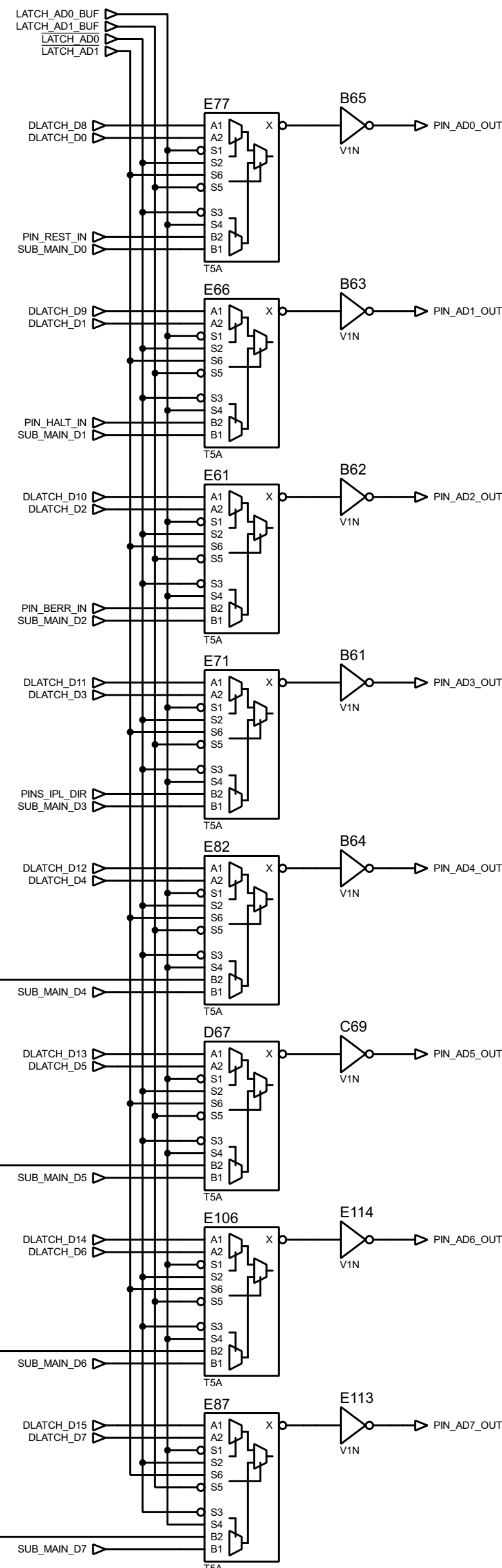
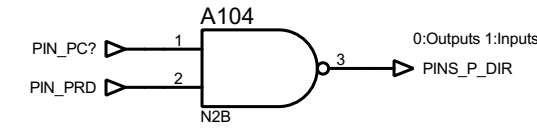
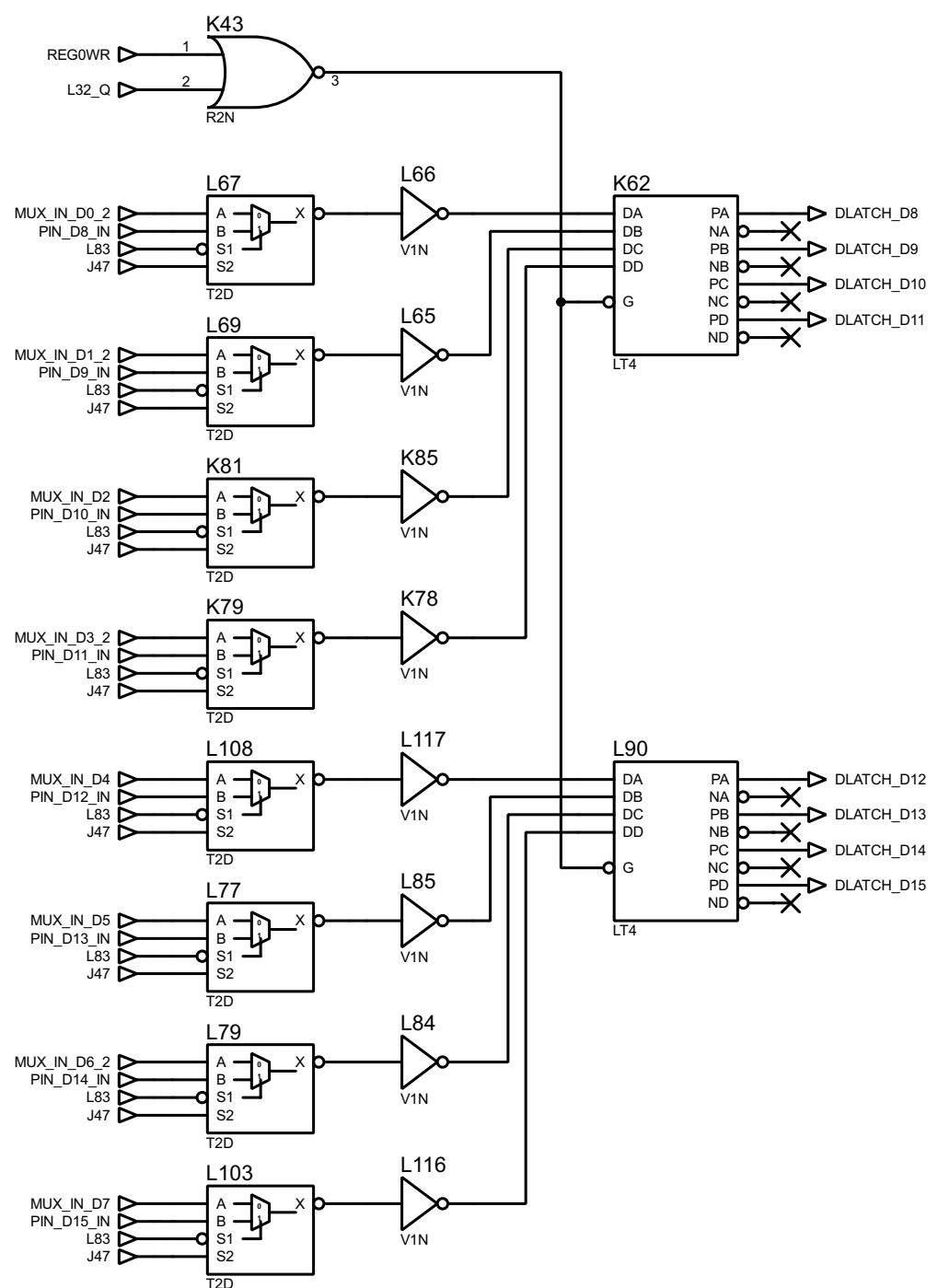
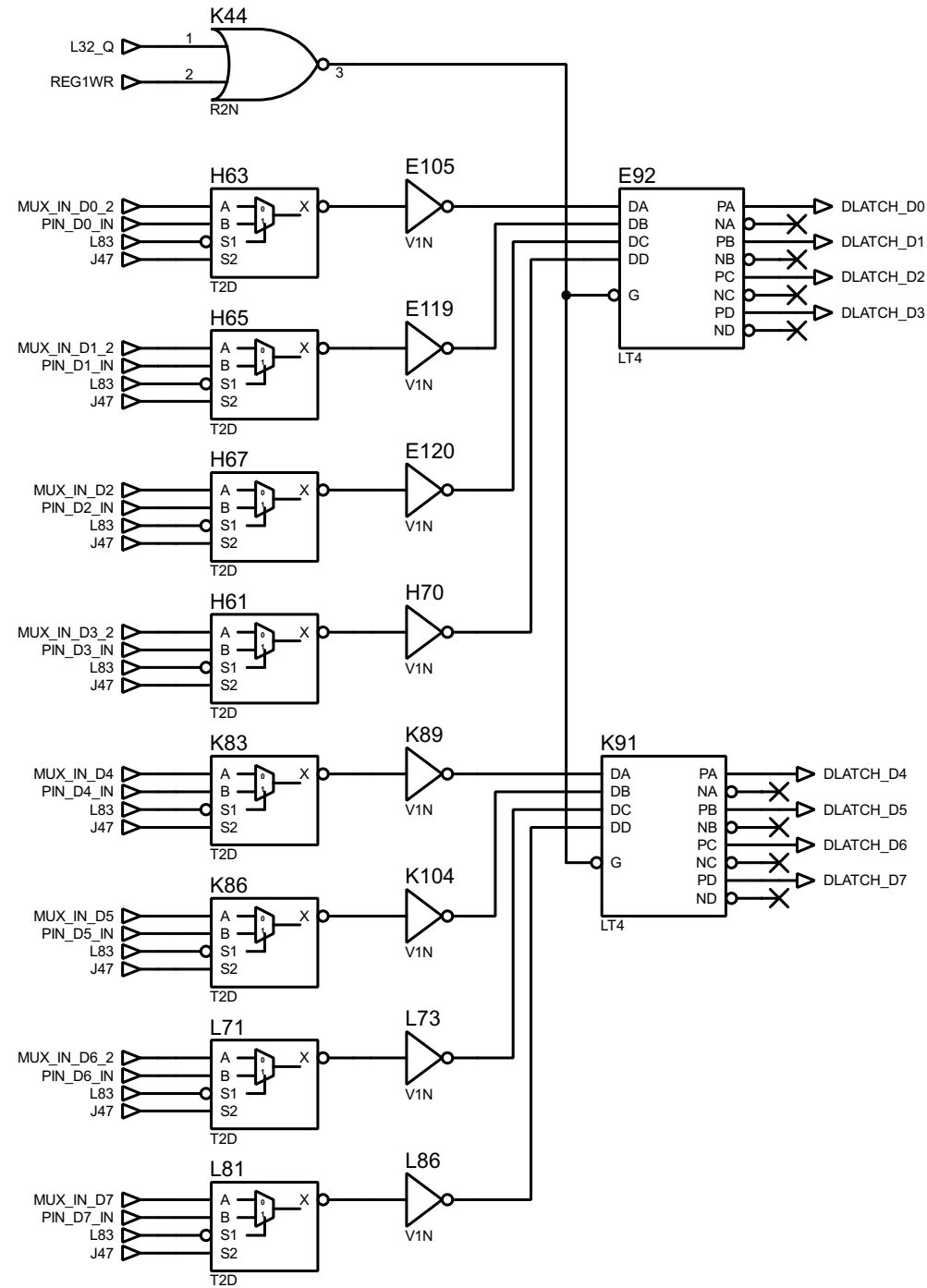
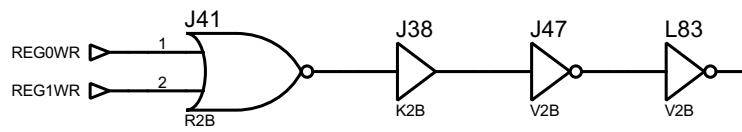


FILE NAME:	315-5195.pdsprj	DATE:	26/07/2021
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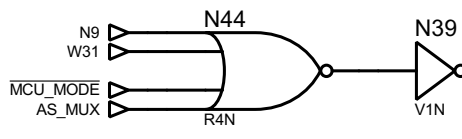
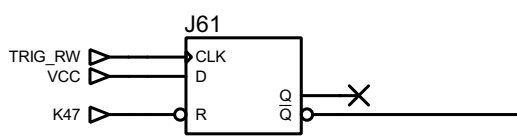
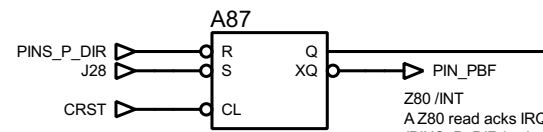
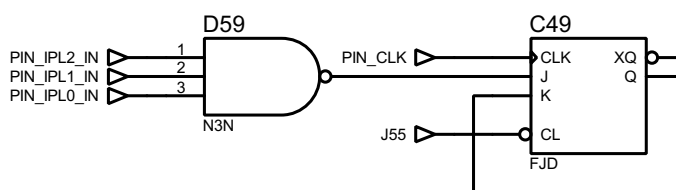


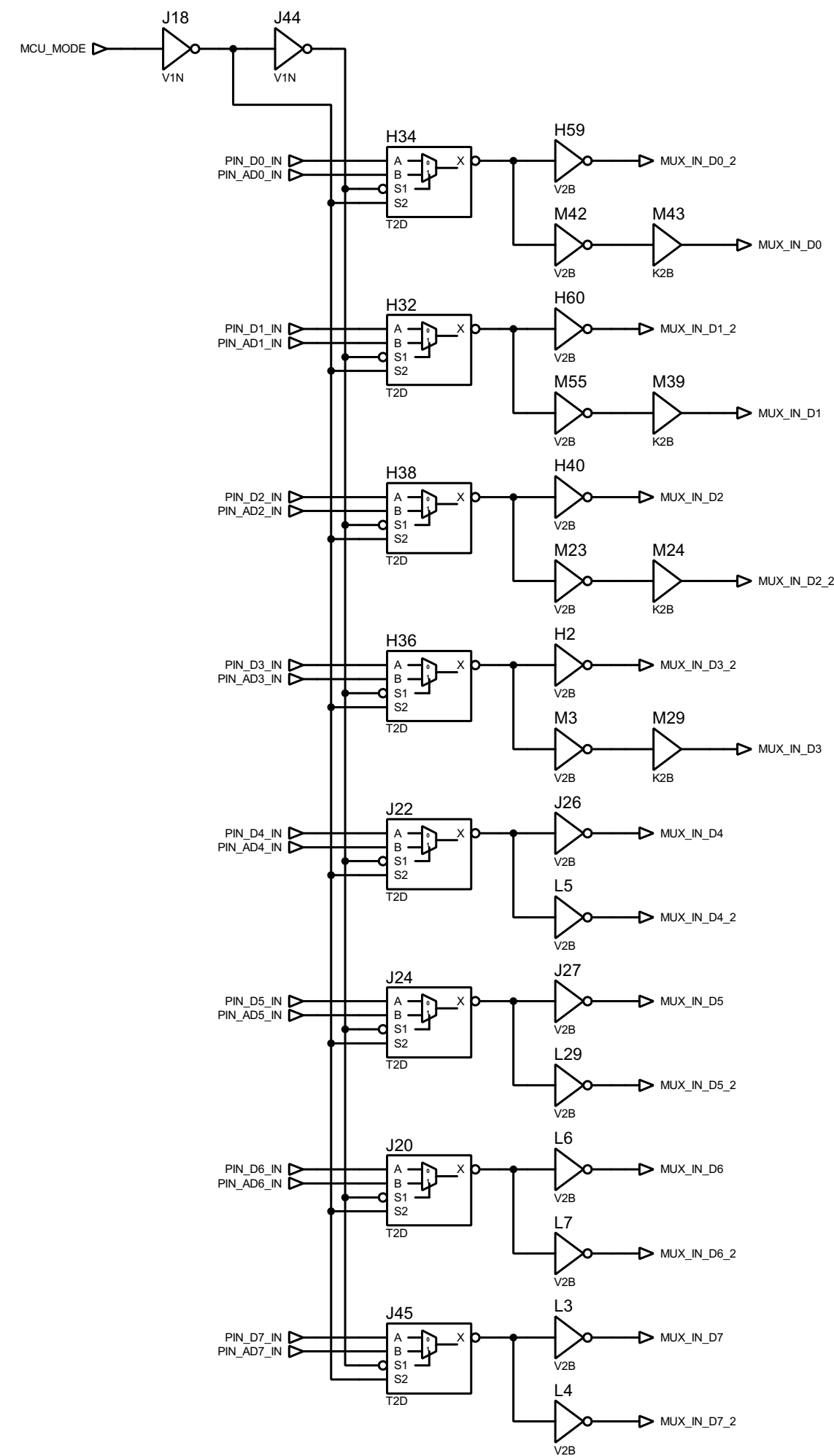
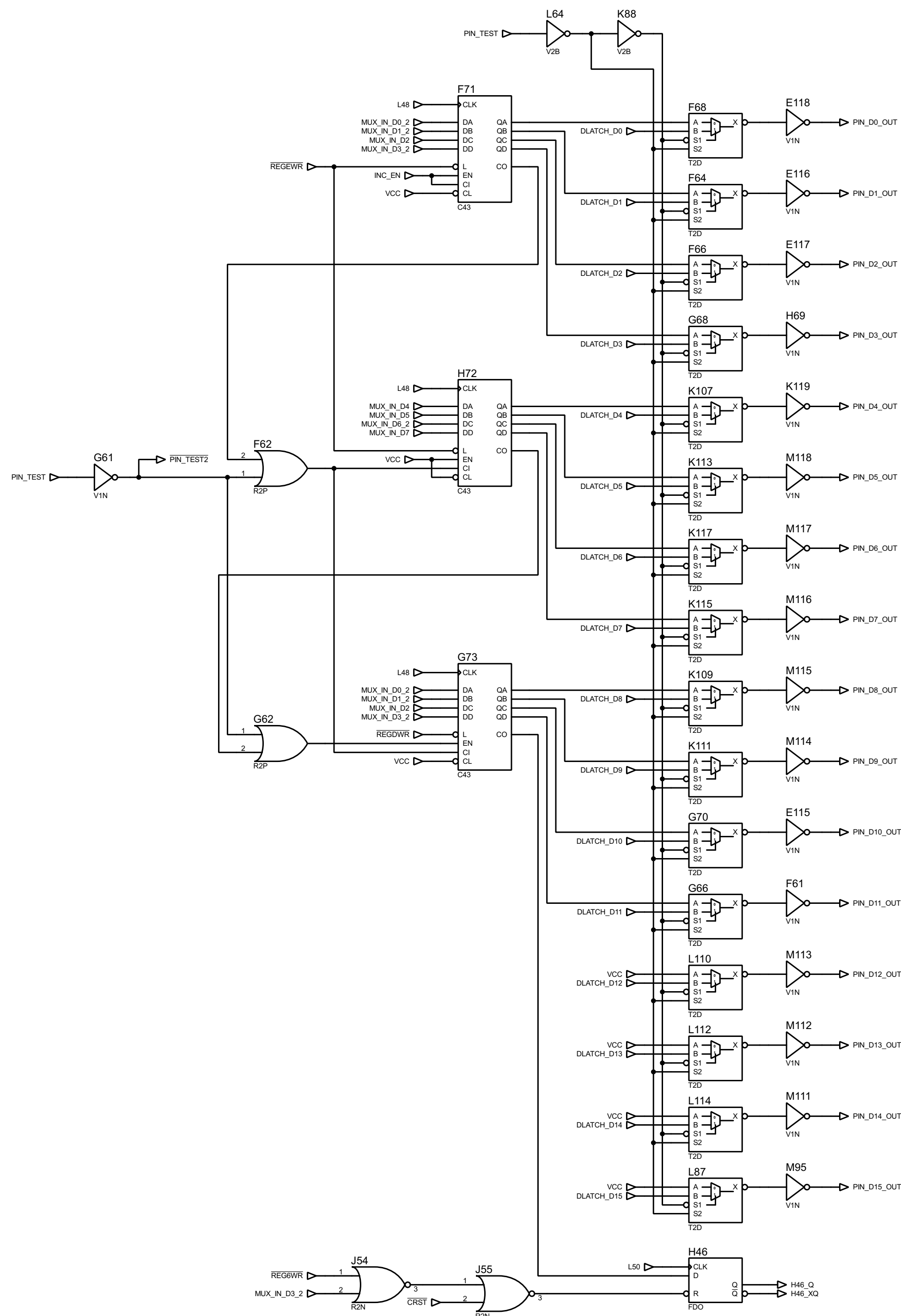
FILE NAME:	315-5195.pdsprj	DATE:	26/07/2021
DESIGN TITLE:	Sega 315-5195 MAPPING REGS BANK 7	PAGE:	8 of 17
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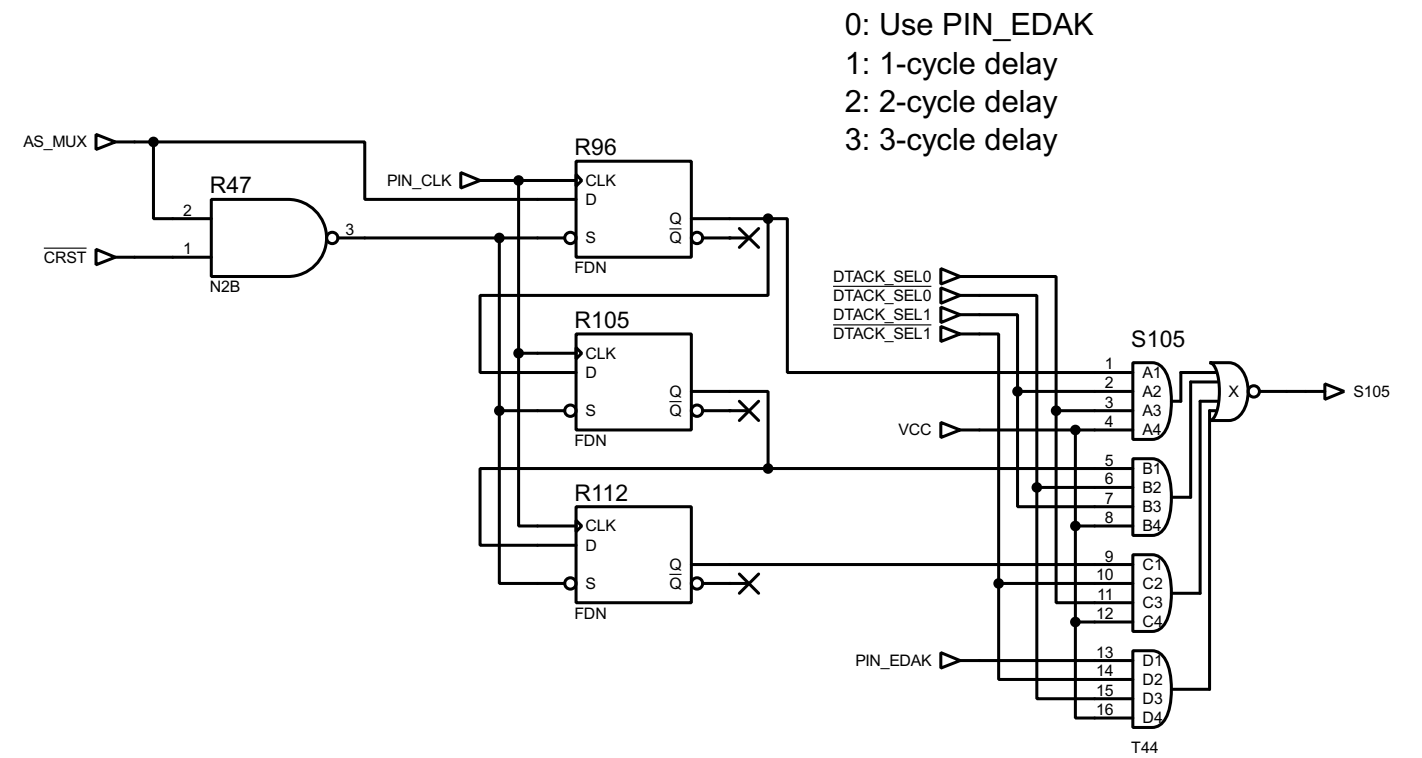
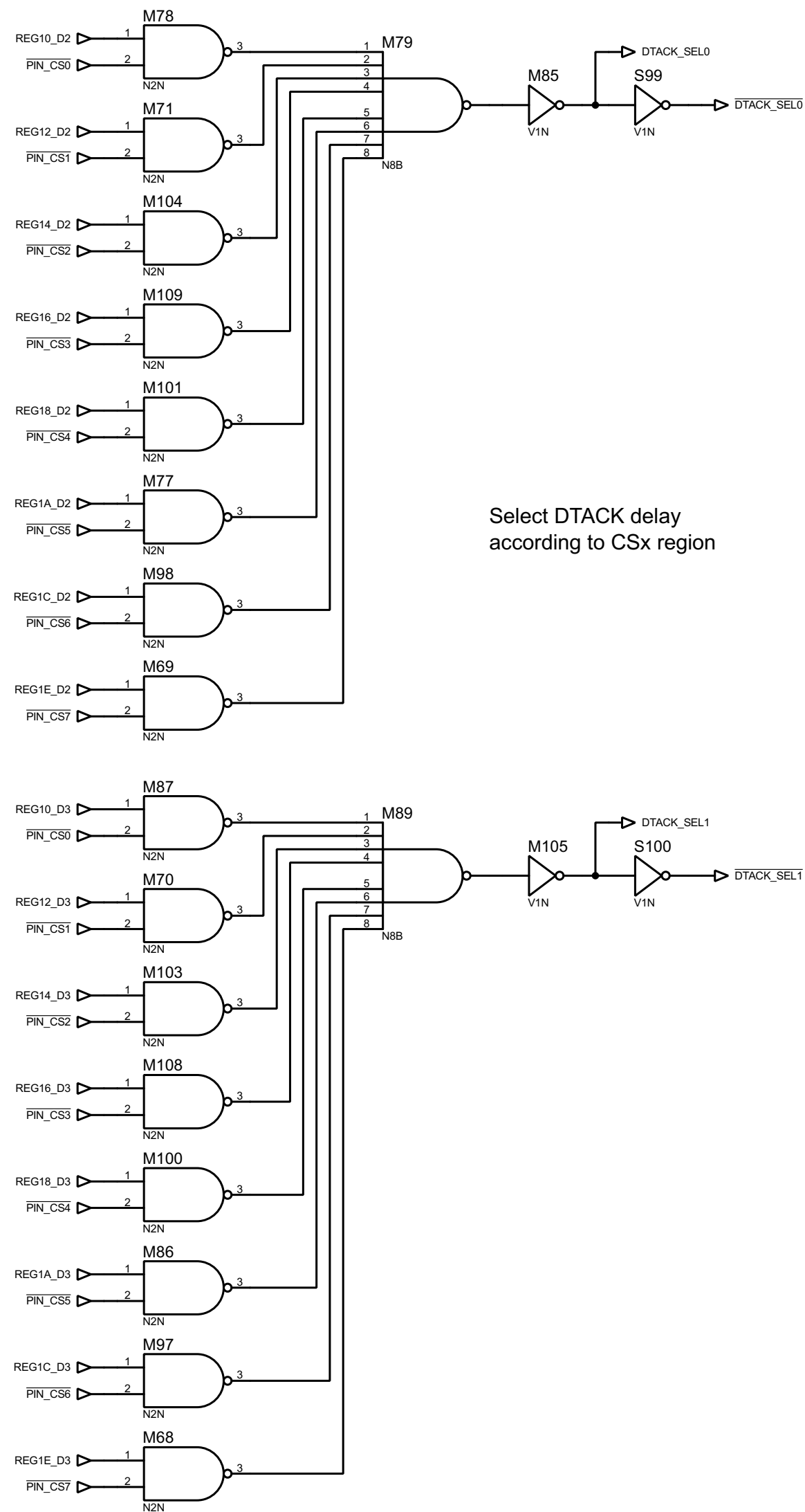


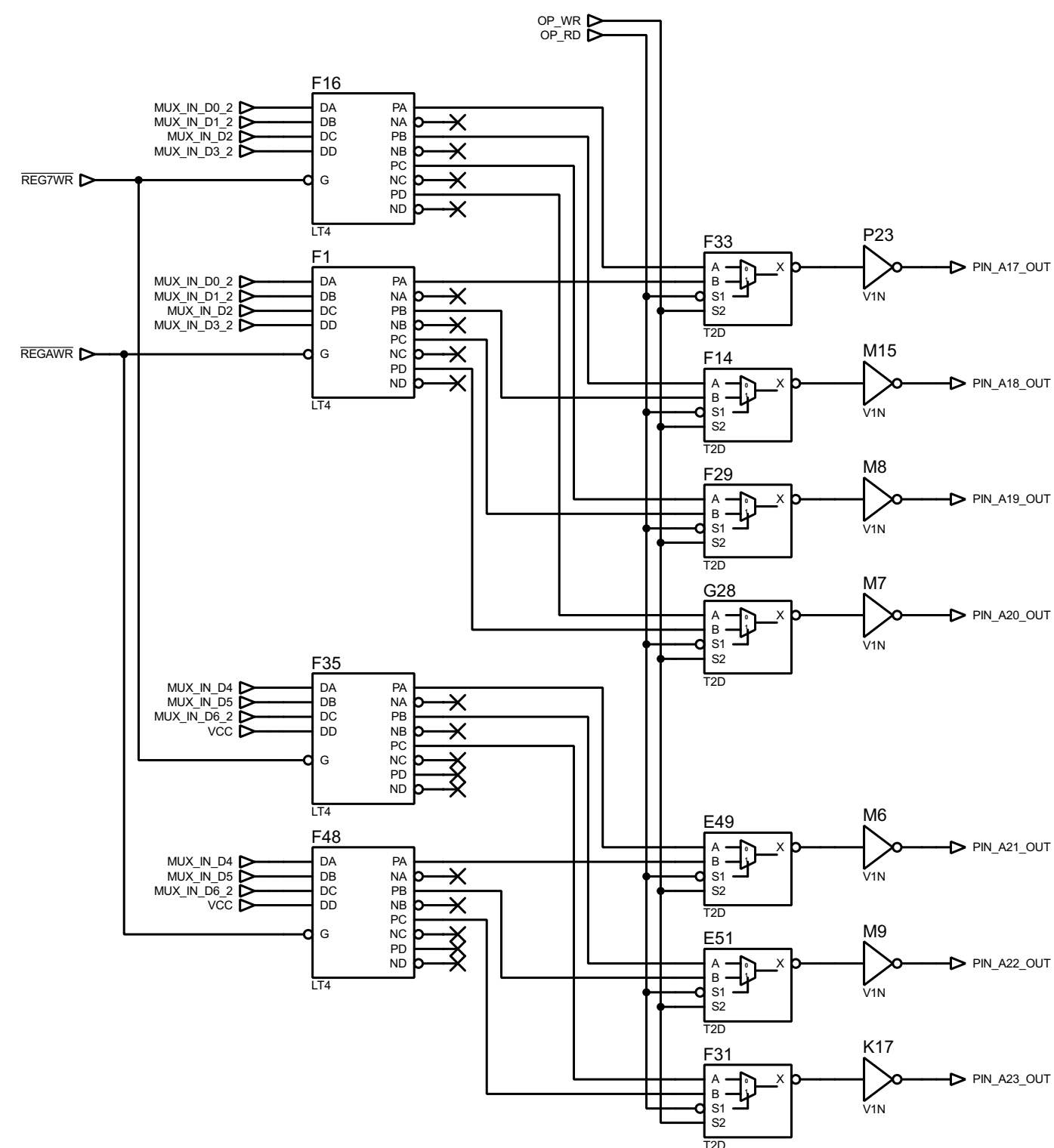
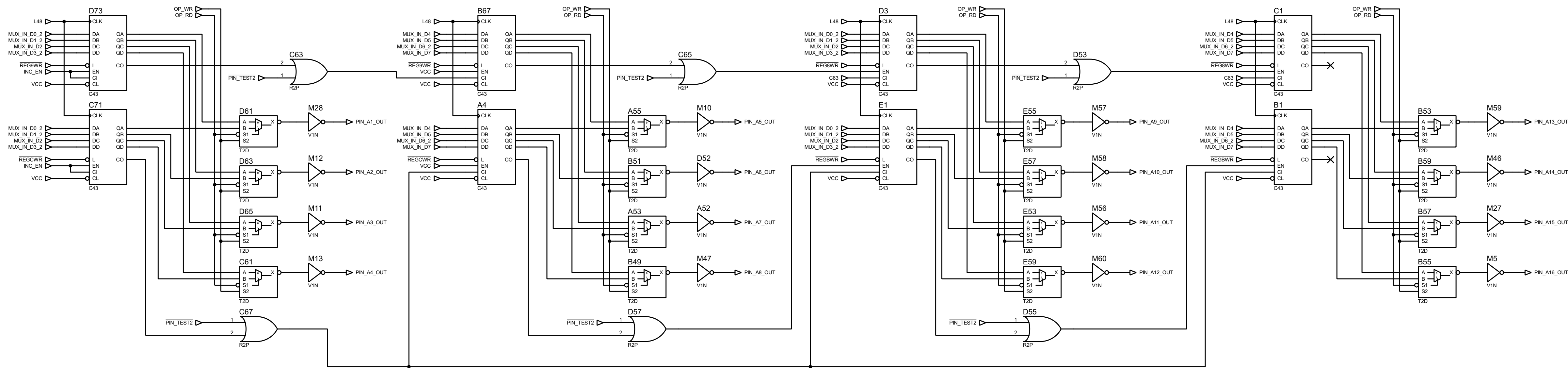


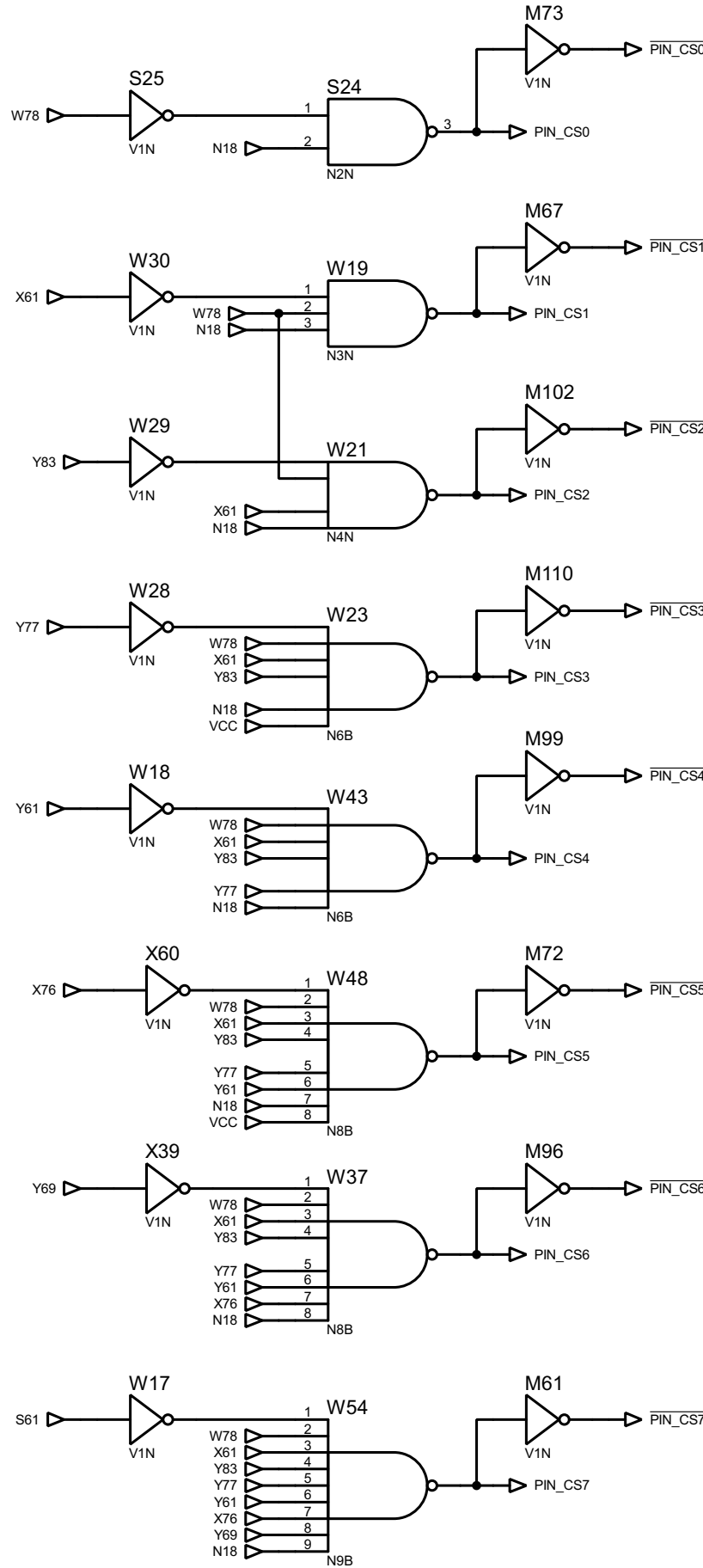
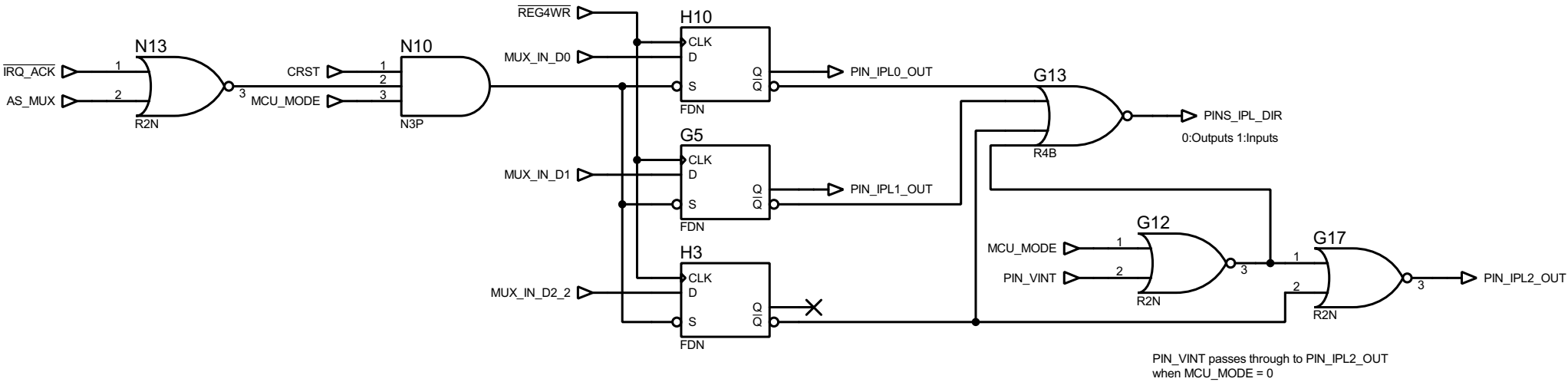
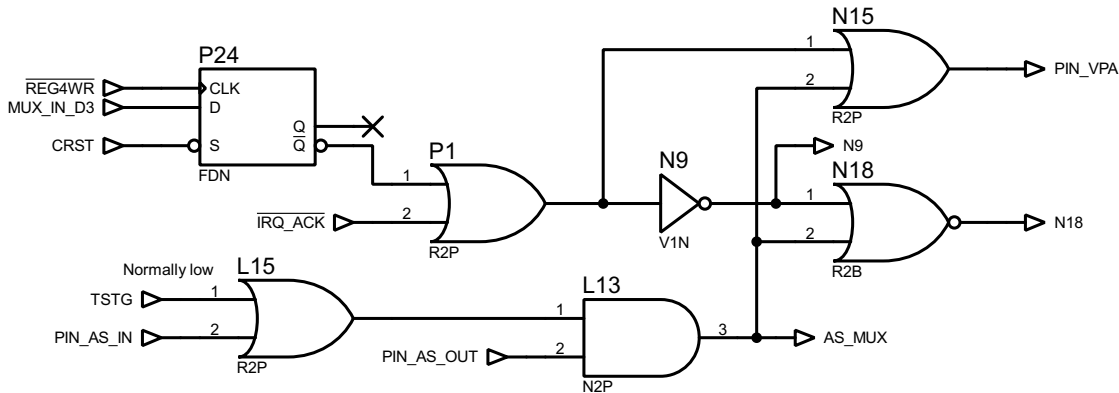
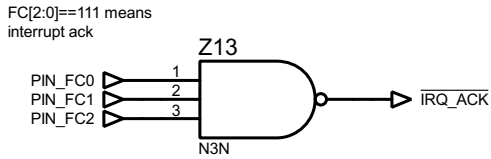
Read 02:
 Bit 0: REST pin
 Bit 1: HALT pin
 Bit 2: BERR pin
 Bit 3: IPL pins direction
 Bit 4: IRQ in
 Bit 5: P pins direction
 Bit 6: Busy with R/W
 Bit 7:

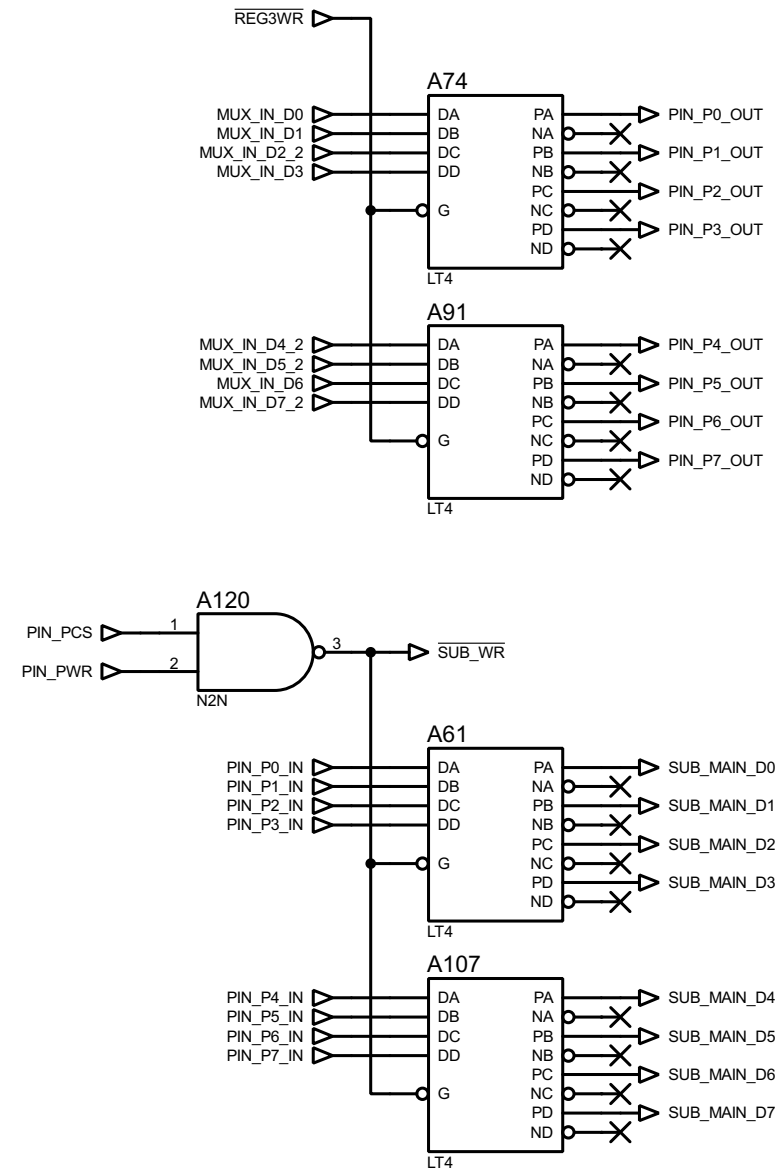












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