

Paradigm BBB Cape Rev. 1

Documentation Package



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Rev 2 Proposal

After brainstorming possible ideas to improve on, we feel that a solution such as below would be better. Rather than restricting the layout to a cape, a larger board should be used. If space is not an issue, more can be incorporated. See below. The schematic could be reused just with a new layout. This will allow for extra connectors.

http://rgb-123.com/wp-content/uploads/2014/10/RJ45_C2_ISO.jpg
https://www.nexlab.net/wp-content/uploads/unixmedia/10/IMG_5428-copia.jpg \

Rev 1. Documentation

Depending on decision made at team meeting, we will scrap this layout and go with the above proposal. Or generate the gerbers and order the current board after creating a pinout diagram and a few more things.

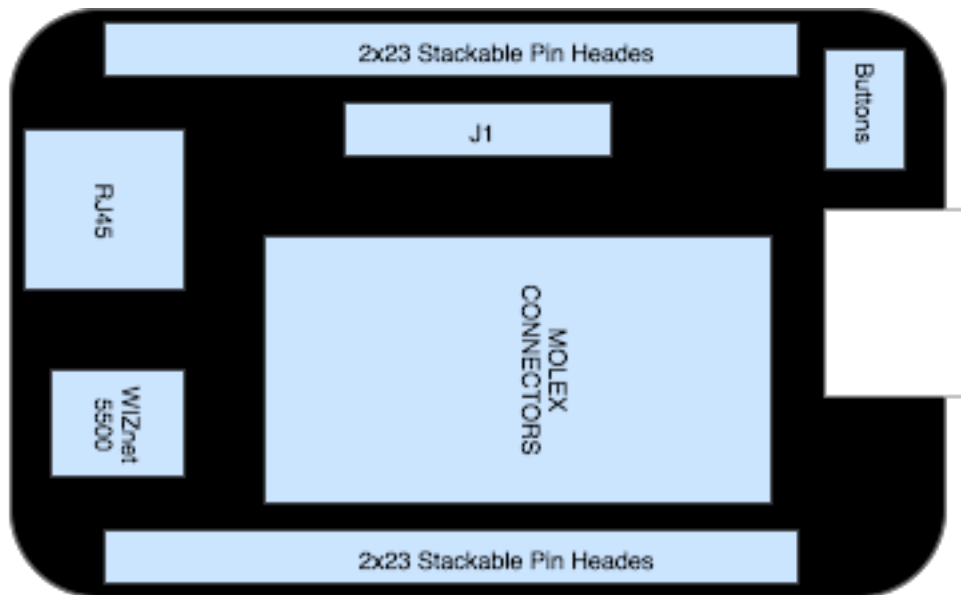
To Be Attached:

- BOM
- Pinout
- Layout

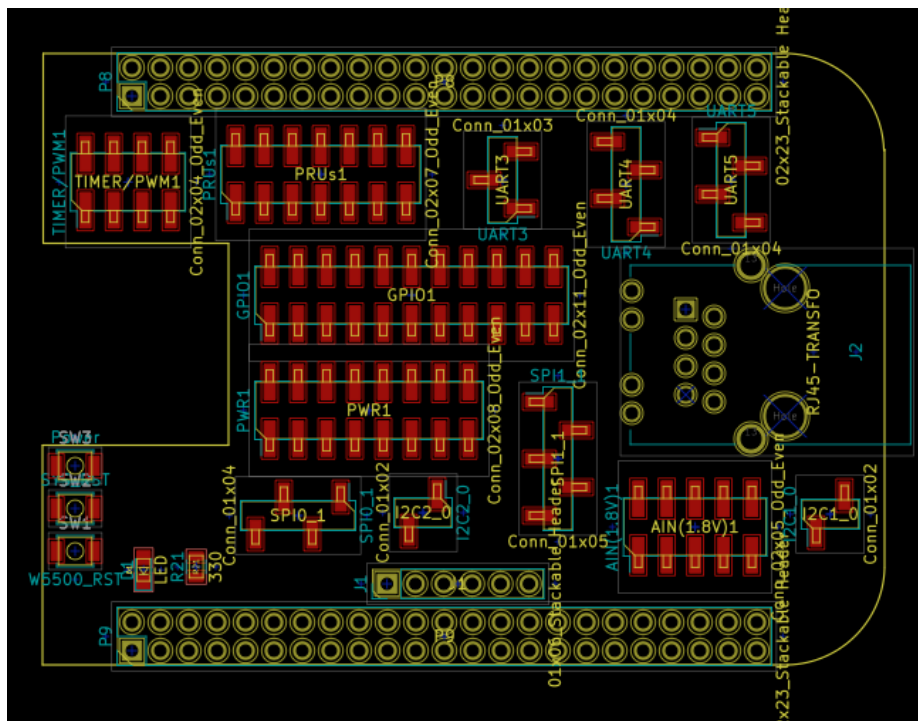
To Do

- Fab and do H/W verification

Block Diagram v1



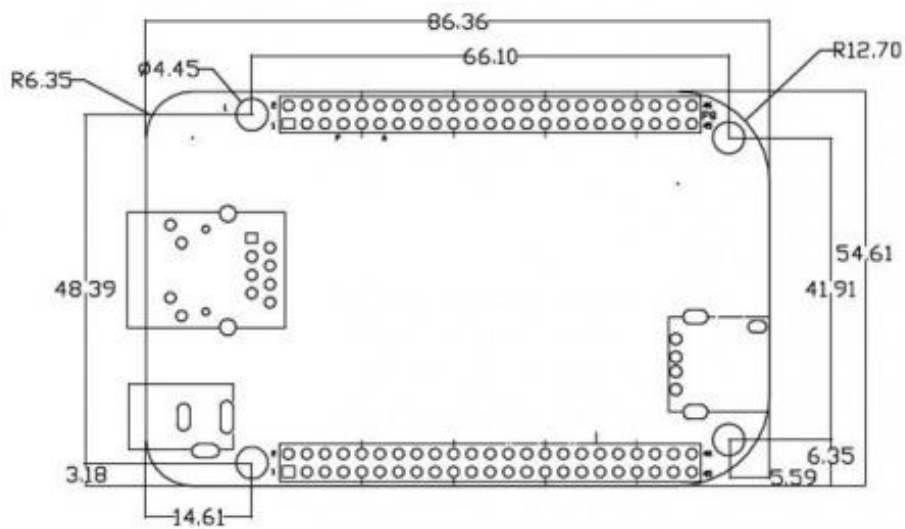
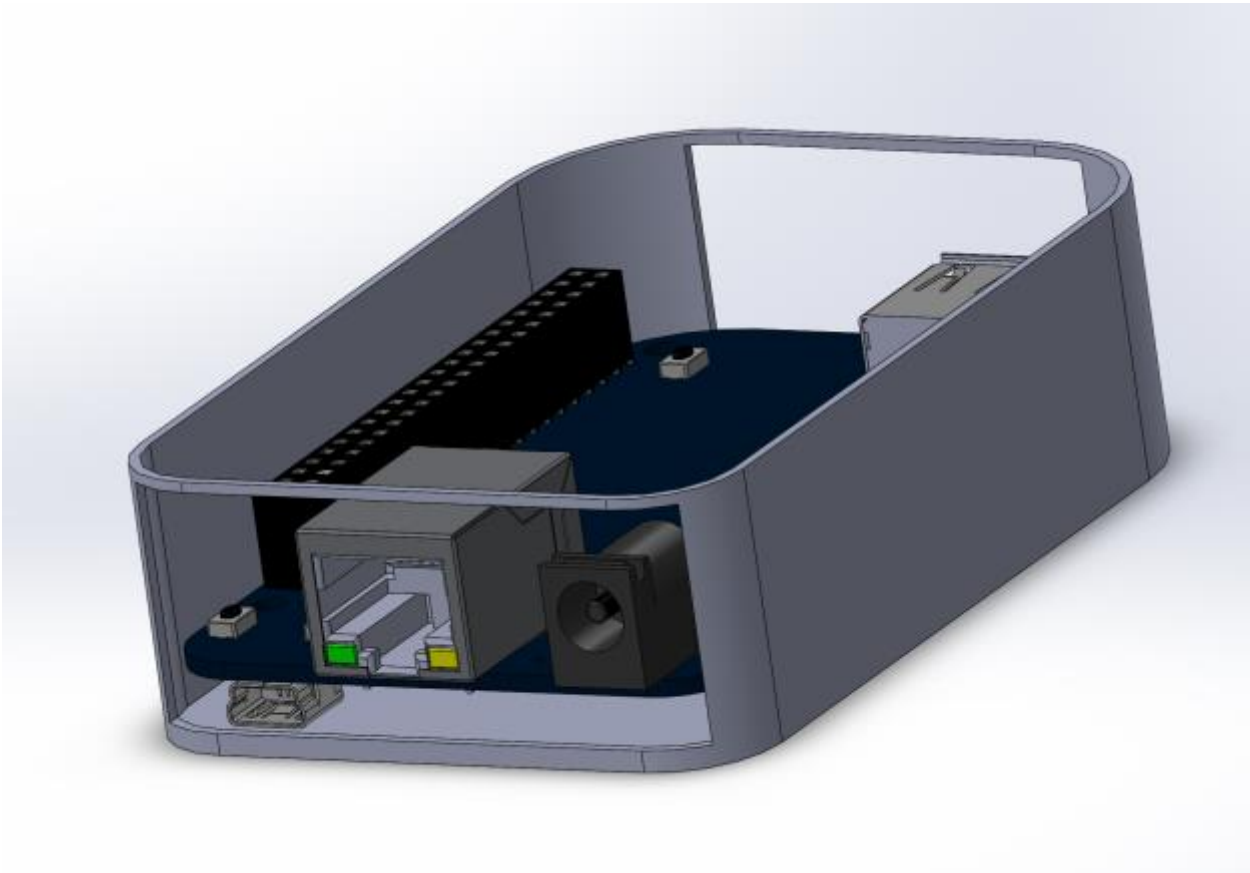
Top Layout (no trace)



WIZnet W5500 and supporting components reside on bottom

3D-Printed Case Rev1

- Mockup, Dimensions will be finalized with calipers and enclosed around port holes.

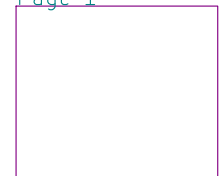


Dimensions need to be verified with calipers

Index:

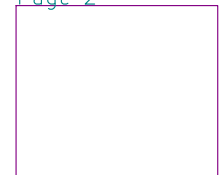
Pins and Connectors.....Page 1
RJ45 and WIZnet W5500.....Page 2

Page 1



Pins and Connectors.sch

Page 2

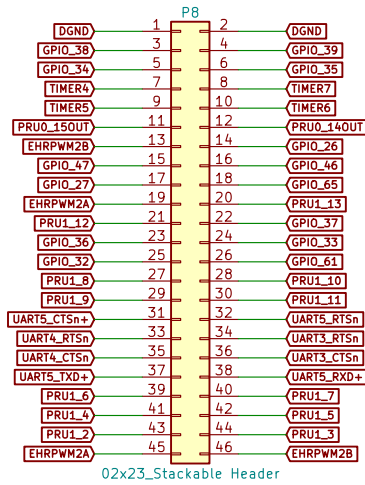
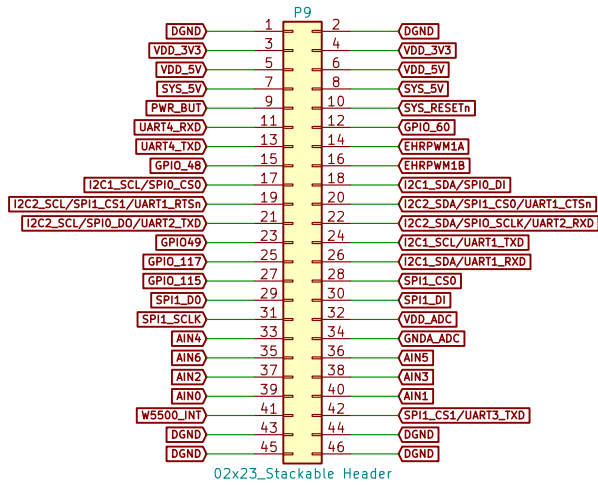


RJ45 and WIZnet W5500.sch

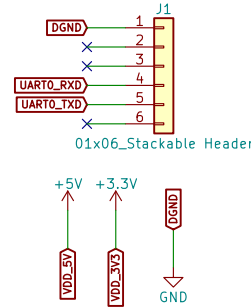


PARADIGM HYPERLOOP

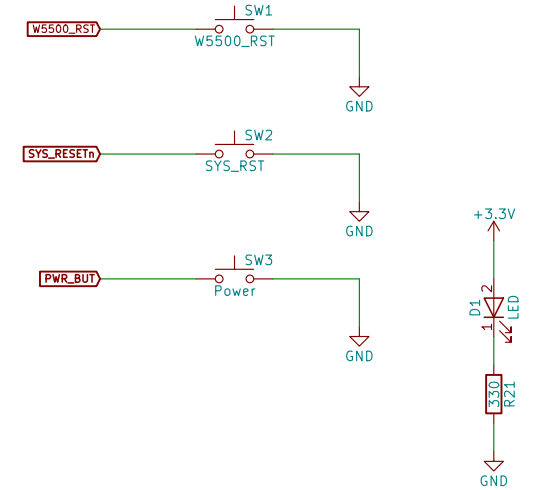
Sheet: /Index		Mitchell Stride	
File: BBB-Cape.sch			
Title: BBB-Cape			
Size: A4	Date: Nov. 10/2017	Rev: 1	
KiCad E.D.A. kicad 4.0.7		Id: 1/3	



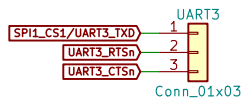
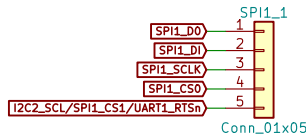
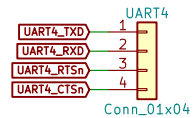
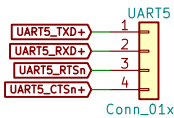
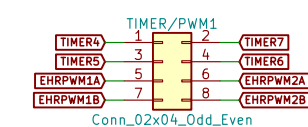
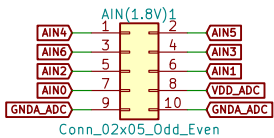
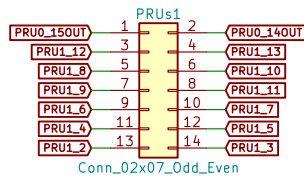
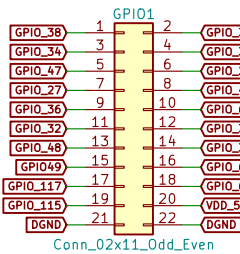
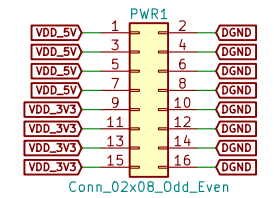
Existing Headers Extended Up



Switches



New Molex Connectors



Sheet: /Page 1/
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