

Sparkroad SCH V1.0

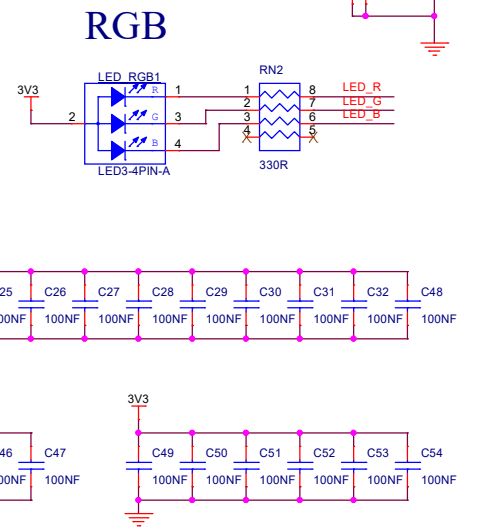
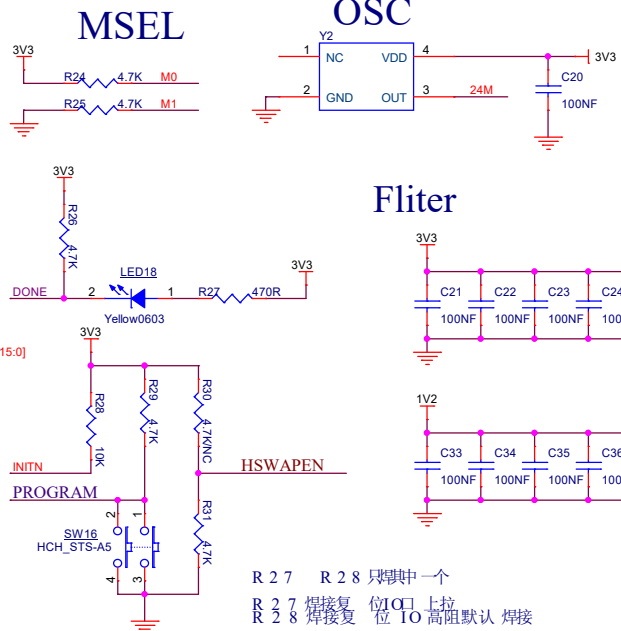
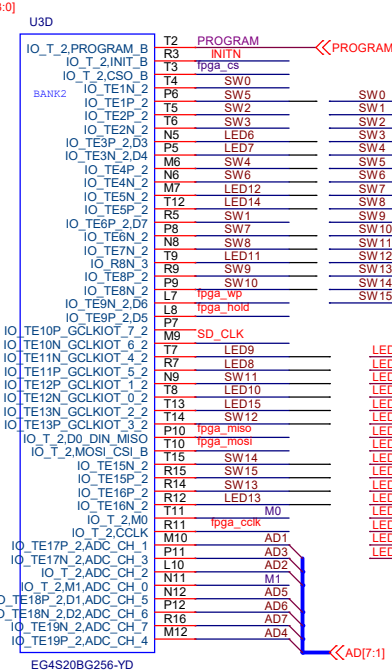
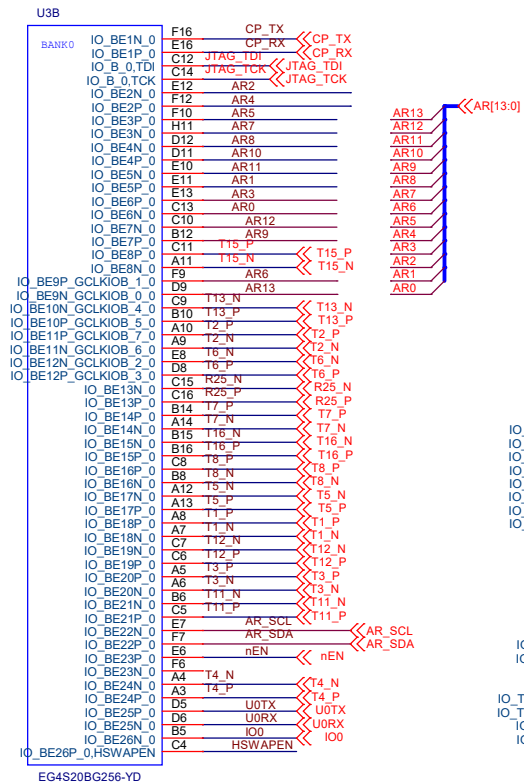
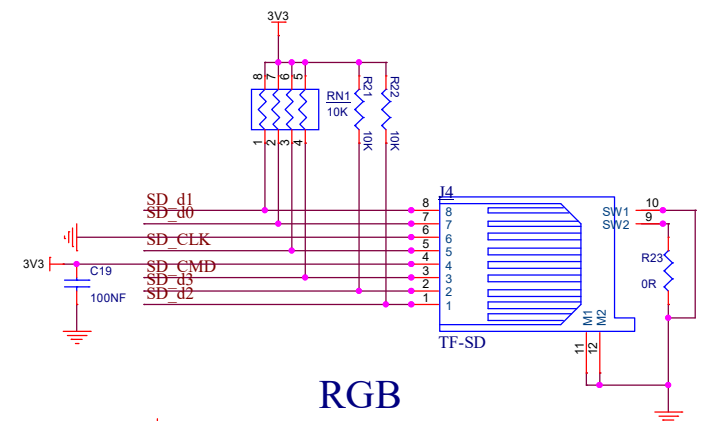
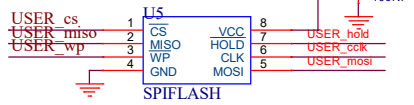
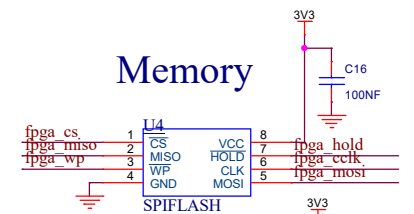
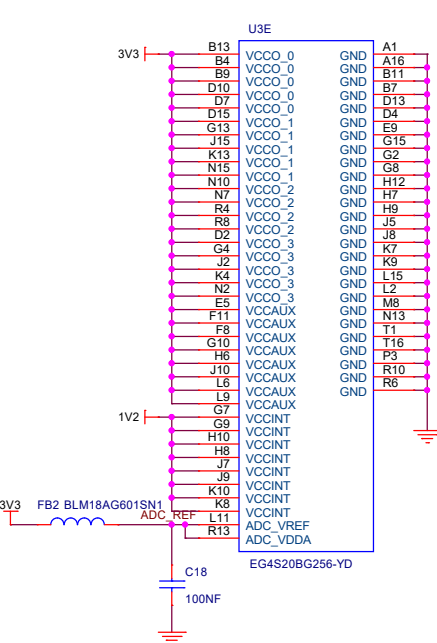
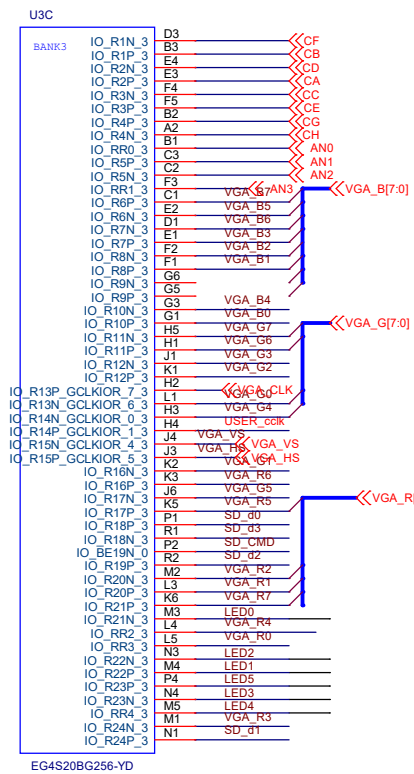
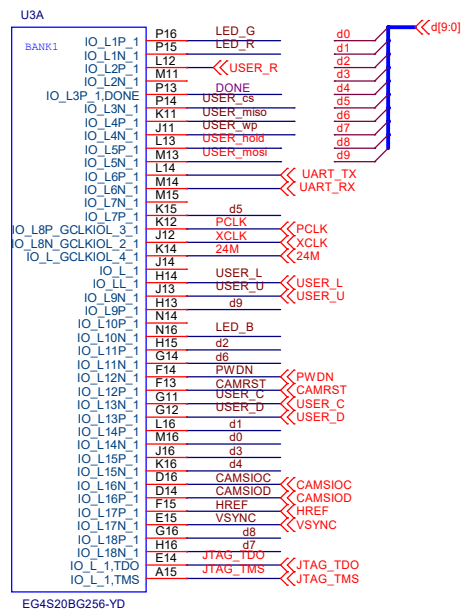
CONTENTS

Schematic Name	Sheet
01. Reversion History	1
02. BLOCK DIAGRAM	2
03. POWER_JTAG_WIFI	3
04. FPGA	4
05. Interface	5
06. LED_SW	6
07. USB_VGA	7

REVISION HISTORY

Date	Author	Version	Comments
2019.07.04	Sparkroad	0.1	Initial Release
2019.07.30	Sparkroad	1.0	
2019.10.01	Sparkroad	1.01	

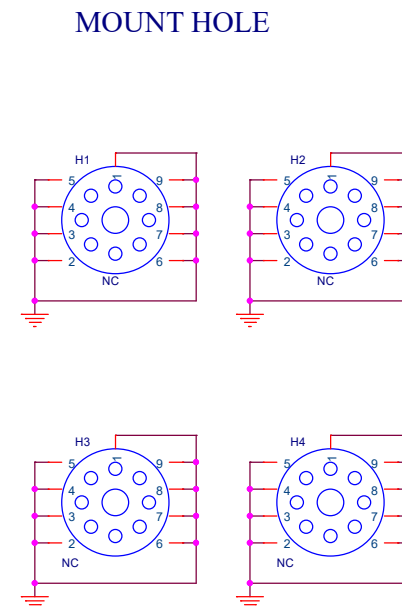
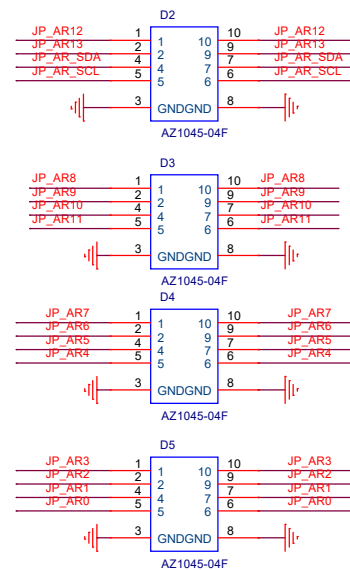




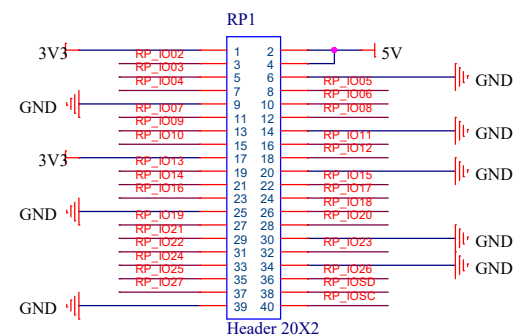
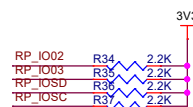
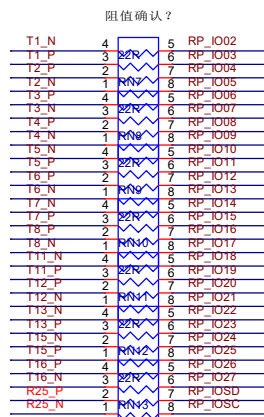
R 2 7 R 2 8 只其中一个

R 2 7 焊接复位IO口上拉

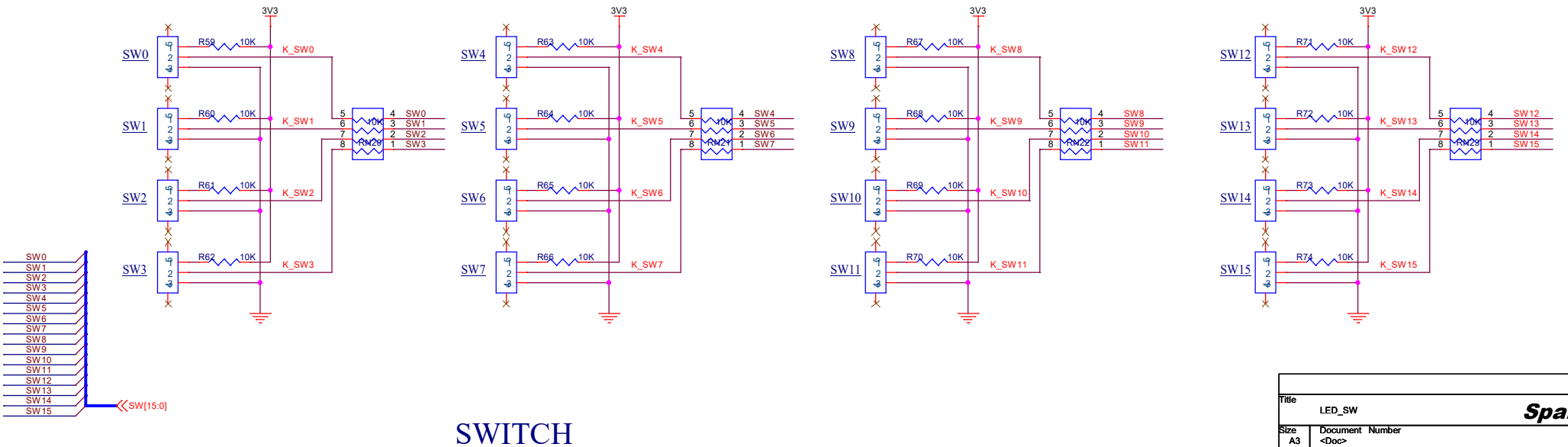
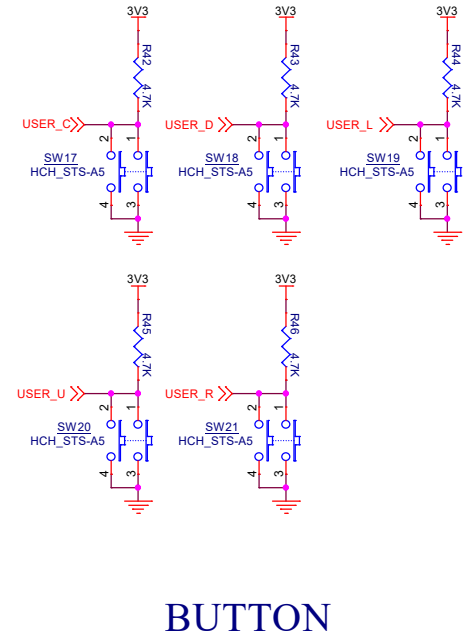
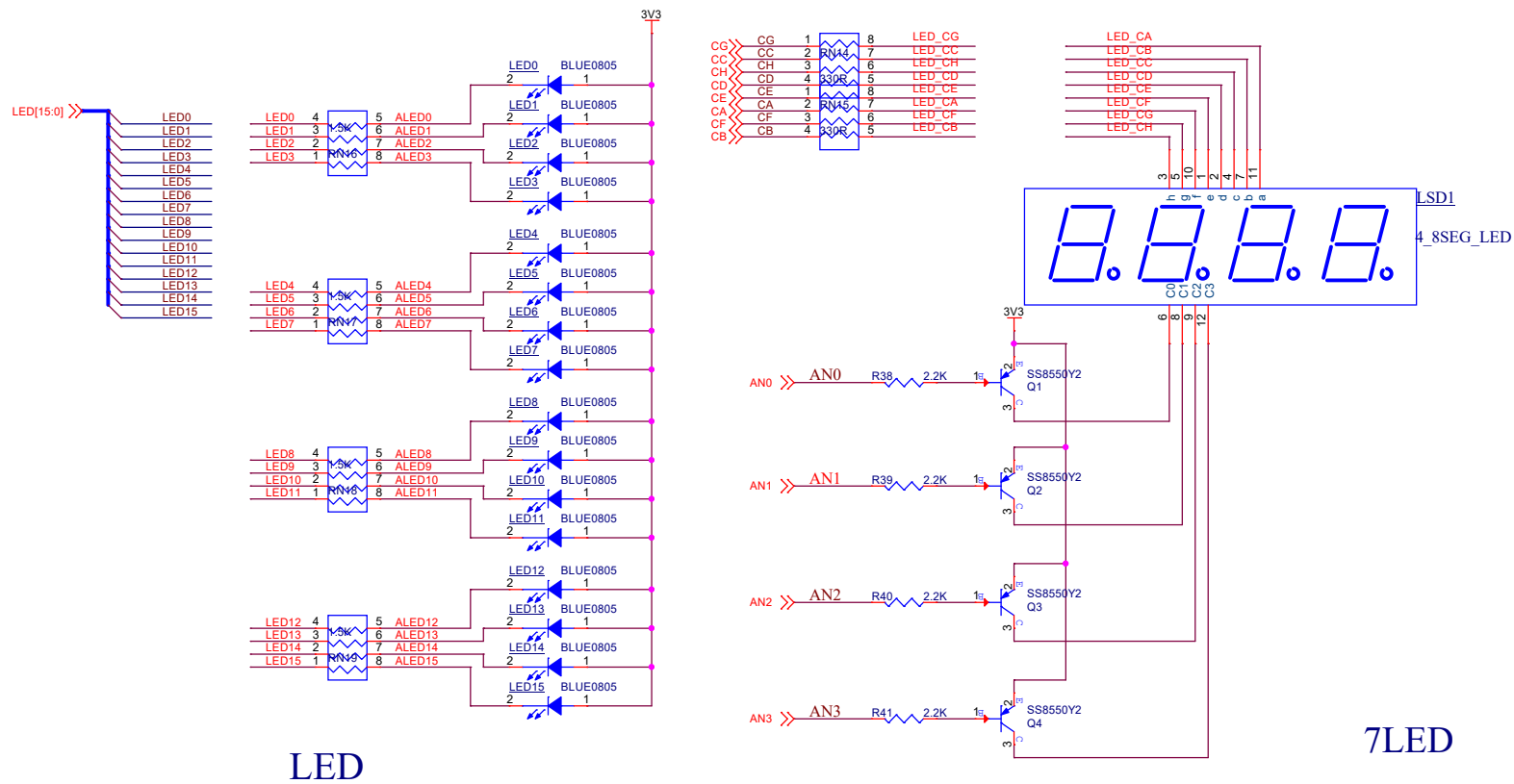
R 2 8 焊接复位IO高阻默认焊接



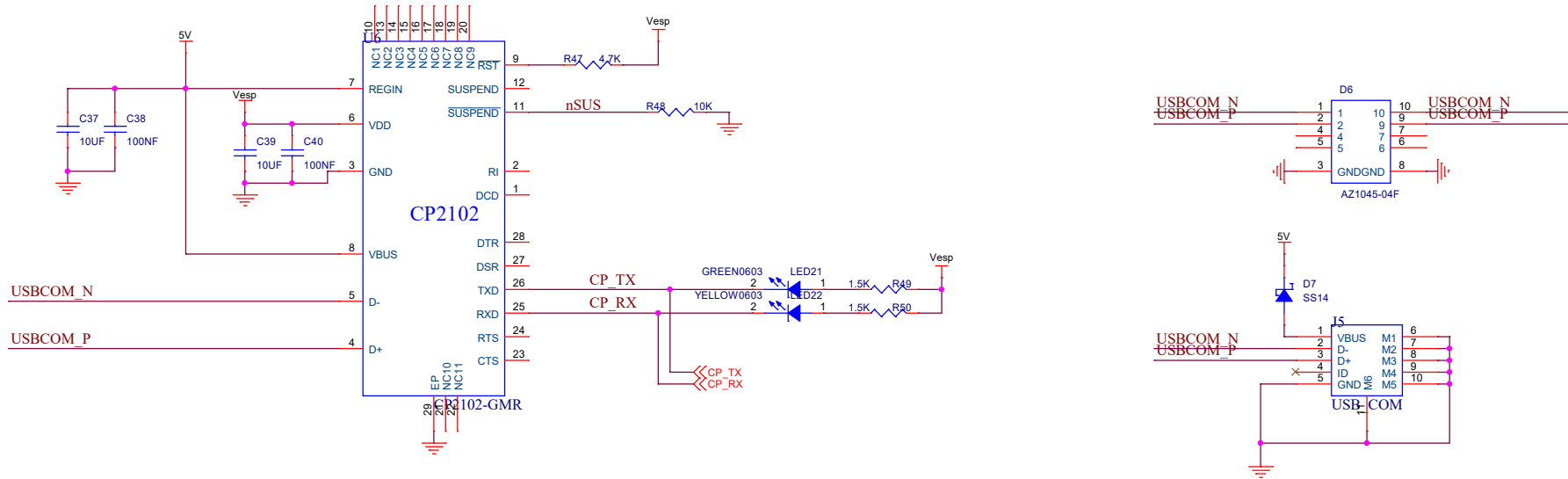
MOUNT HOLE



Title				
Interface		<b>Sparkroad</b>		
Size	Document	Number		Rev
A3	<Doc>			1.0
Date:	Tuesday, July 30, 2019	Sheet	4 of 6	



# USB2UART



# VGA

