_	PORT	AF0		AF1		AF2			AF3			AF4			AF5			AF6			AF7		AF8	Al			AF12	ADC		配置
Ľ	OKI	SYS	TIM1	TIM2	TIM3	TIM4		TIM9	TIM10 TI	M11	I2C1	I2C2	I2C3	SPI1	SPI2	SPI4	SPI3	SPI4	SPI5	SPI3	USART1		USART6	I2C2	I2C3	OTG_FS	SDIO			比量
	0			CH1 / ETR			CH1															CTS						ADC1_IN0	X	
	1			CH2			CH2									MOSI						RTS						ADC1_IN1	X	
	2			CH3			CH3	CH1														TX						ADC1_IN2	X	
	3			CH4			CH4	CH2														RX						ADC1_IN3	X	
	4													NSS			NSS					CK						ADC1_IN4	X	
	5			CH1 / ETR										SCK														ADC1_IN5	X	
	8		BKIN		CH1									MISO														ADC1_IN6	X	
P			CH1N		CH2									MOSI														ADC1_IN7	X	
		MCO1	CH1										SCL								CK					SOF	D1		X	
	9		CH2										SMBA								TX					VBUS	D2		X	LED D
	10		CH3																MOSI		RX		777.7			ID			X	LED_B
	11		CH4															MISO	Mac		CTS		TX			DM			X	LED_G
	12	SDIO	ETR																MISO		RTS		RX			DP			X	LED_R SWDIO
		SCLK																											X	
		JTDI		CH1 / ETR										NSS			NSS				TX								X X	SWCLK
\vdash	0		CH2N	CHI / EIK	CH3					_				NSS			1199		SCK		1.A							ADC1 IN8	X	CS
	1		CH3N		CH3														NSS									ADC1_IN6 ADC1_IN9	X	
		BOOT	CHISIN		CH														1400									ADCI_IN9	X	KEY
		JTDO		CH2										SCK			SCK				RX			SDA					X	SCK
		JTRST		CIIZ	CH1									MISO			MISO				10.1			SDIT	SDA		D0		X	SDO
	5 6 7 8 9	o i i i i			CH2						SMBA			MOSI			MOSI								5511		D3		X	SDI
						CH1					SCL										TX								X	TX
						CH2					SDA										RX						D0		X	RX
P						СНЗ			CH1		SCL								MOSI						SDA		D4		Х	IRQ
						CH4			(SDA				NSS									SDA			D5		X	RST
	10			CH3								SCL			SCK												D7		X	
		,																												
	12		BKIN									SMBA			NSS			NSS		SCK									X	
I	13		CH1N												SCK			SCK											X	
I	14		CH2N												MISO												D6		X	
L	15	RTC	CH3N												MOSI												CK		X	
	13					-																							X	EXTON
P	C 14																												X	WAKE
L	15																												X	