

PORT		AF0 SYS	AF1 TIM1 TIM2		AF2 TIM3 TIM4 TIM5		AF3 TIM9 TIM10 TIM11			AF4 I2C1 I2C2 I2C3			AF5 SPI1 SPI2 SPI4			AF6 SPI3 SPI4 SPI5			AF7 SPI3 USART1 USART2			AF8 USART6	AF9 I2C2 I2C3		AF10 OTG_FS	AF12 SDIO	ADC	配置		
PA	0			CH1 / ETR																								X	UWB_IRQ	
	1			CH2																								X	UWB_EXT	
	2			CH3																								X	UWB_RST	
	3			CH4																								X	UWB_WAK	
	4																											X	UWB_CS	
	5			CH1 / ETR																								X	UWB_SCK	
	6			BKIN																								X	UWB_SDO	
	7			CH1N																								X	UWB_SDI	
	8		MCO1	CH1																								X	IMU_INTB	
	9			CH2																								O	EA9	
	10			CH3																								O	EA10	
	11			CH4																								X	USB_DM	
	12			ETR																								X	USB_DP	
	13		SDIO																									O	SWDIO	
	14		SCLK																									O	SWCLK	
15		JTDI		CH1 / ETR																							O	IMU_CSB		
PB	0		CH2N																									O	EB0	
	1		CH3N																									X	ADC_BAT	
	2																											X	KEY	
	3			CH2																								X	IMU_SCK	
	4		JTDO																									X	IMU_SDO	
	5		JTRST																									X	IMU_SDI	
	6																											O	SWD_TX	
	7																											O	SWD_RX	
	8																											O	IMU_CSM	
	9																											O	IMU_INTM	
	10			CH3																								O	EB10	
		12		BKIN																									O	EB12
		13		CH1N																									O	EB13
		14		CH2N																									O	EB14
	15		CH3N																									O	EB15	
PC	13																											X	LED_B	
	14																											X	LED_G	
	15																											X	LED_R	