

PORT		AF0 SYS	AF1		AF2			AF3			AF4			AF5			AF6			AF7			AF8	AF9		AF10	AF12	ADC	配置		
			TIM1	TIM2	TIM3	TIM4	TIM5	TIM9	TIM10	TIM11	I2C1	I2C2	I2C3	SPI1	SPI2	SPI4	SPI3	SPI4	SPI5	SPI3	USART1	USART2	USART6	I2C2	I2C3	OTG_FS	SDIO				
PA	0			CH1 / ETR			CH1															CTS						ADC1_IN0	O	UWB_RST	
	1			CH2			CH2									MOSI						RTS						ADC1_IN1	O	UWB_IRQ	
	2			CH3			CH3	CH1														TX						ADC1_IN2	O	EXP03	
	3			CH4			CH4	CH2														RX						ADC1_IN3	O	EXP02	
	4													NSS								CK						ADC1_IN4	O	UWB_CSD	
	5			CH1 / ETR										SCK														ADC1_IN5	O	UWB_SCK	
	6			BKIN			CH1							MISO														ADC1_IN6	O	UWB_SDO	
	7			CH1N			CH2							MOSI															ADC1_IN7	O	UWB_SDI
	8	MCO1		CH1									SCL								CK					SOF	D1		X	IMU_CSB	
	9			CH2									SMBA								TX					VBUS	D2		X	USB_VB	
	10			CH3															MOSI			RX				ID			X	USB_ID	
	11			CH4																		CTS				DM			X	USB_DM	
	12			ETR															MISO			RTS			TX	DP			X	USB_DP	
	13	SDIO																											X	SWDIO	
	14	SCLK																											X	SWCLK	
15	JTDI		CH1 / ETR											NSS							TX							O	EXP05		
PB	0		CH2N			CH3													SCK									ADC1_IN8	X	IMU_INTM	
	1		CH3N			CH4													NSS									ADC1_IN9	X	ADC_BAT	
	2	BOOT																											X	KEY	
	3	JTDO		CH2										SCK								RX							O	EXP06	
	4	JTRST				CH1								MISO															O	EXP07	
	5					CH2								MOSI															O	EXP08	
	6						CH1				SMBA																		X	SWD_TX	
	7						CH2				SCL																		X	SWD_RX	
	8						CH3				SDA																		X	UWB_EXT	
	9						CH4				SCL								MOSI										X	UWB_WAK	
	10			CH3					CH1	CH1	SDA		SCL		NSS	SCK								SDA					X	IMU_INTB	
	12			BKIN								SMBA			NSS				NSS	SCK		SCK								X	IMU_CSM
	13			CH1N											SCK				SCK											X	IMU_SCK
	14			CH2N											MISO													D6		X	IMU_SDO
15	RTC		CH3N											MOSI												CK			X	IMU_SDI	
PC	13																												X	LED_B	
	14																												X	LED_G	
	15																												X	LED_R	