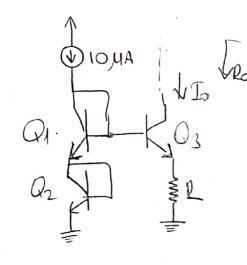
HW-14 Abdullah MENTEDELL (2) For the circuit in Fig P6.144, Assume BJTs 121024001 with high B and WBE = 0.7V at ImA. Find the value of By that will result in Io=10MA () For the design in (a), find Lo assuming B=100 and Va=100V



$$I_{C_1} = I_{S} exp\left(\frac{VBE 1}{VT}\right)$$

$$I_{C_2} = I_{S} exp\left(\frac{VBE 2}{VT}\right)$$

$$I_{C_3} = I_{S} exp\left(\frac{VBE 3}{VT}\right)$$

$$I_{C_4} = I_{C_2} = 10 \mu A$$

$$I_{C_3} = 10 \mu A$$

$$\frac{1}{2} = \frac{1}{2} \left(\frac{1}{2} \right)$$

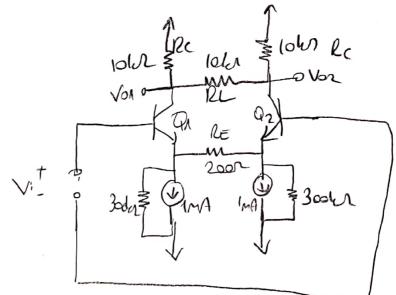
$$\frac{1}{2} = \frac{1}{2} \left(\frac{1}{2} \right)$$

$$\frac{0.4}{26\times10^{-3}} = \ln\left(\frac{10^{-3}}{1s}\right) - \ln\left[1s = 2.02\times10^{-15}A\right]$$

small sig. eq. cir. C1+85+ 1 19m2 Je Tros Dt Viest 173>>> 1 + 1 gmz Po= Viest = (1+9ms Po3)(PIIrms)+G3 $\log = \frac{\sqrt{A}}{f_{C3}} = \frac{100V}{10MA} = 10 M \Omega$ $g_{m3} = \frac{T_{c3}}{V_T} = \frac{10MA}{25mV} = \frac{10 \times 10^{-3}}{25} = 4 \times 10^{-4} \text{ S}$ $\frac{1}{13} = \frac{3}{9m_3} = \frac{100}{4 \times 10^{-4}} = \frac{100 \times 10^{4}}{4} = 25 \times 10^{4} \text{ N}$

lo= (1+ Lix10-4. 10x106) (58.039121/25x1042)+ 10:1062 Ro = (4001), (47,1012) + 1075

Qz. For the differential amplifier shown in Fig. P7.41, identify and sketch the differential half circuit and the common mode half circuit. Find the differential gain, the differential input resistance, the common mode in put resistance, for the transistors B=100 and VA=100V



DC analysis

$$g_{m} = \frac{Ic}{VT} = \frac{ImA}{25mV} = 40 \text{ m} \text{ S}$$

$$G = \frac{VA}{Tc} = \frac{100V}{ImA} = 100 \text{k}$$

$$fit = \frac{\beta}{gm} = \frac{100}{40m} = 2.500L$$

C) Diff- mode-half circ =) virtual gnd Reverse

Common Mode Half Gr

Ac =
$$\frac{\sqrt{61}}{\sqrt{1000}} = \left[-\frac{9m}{1+9mRes} \right] \cdot \left[\frac{1+9mRes}{1+9mRes} \right] \cdot \left[\frac{1+$$

$$MRR = \left| \frac{Ad}{A} \right|$$

Rice= (IT [1+9mRes]

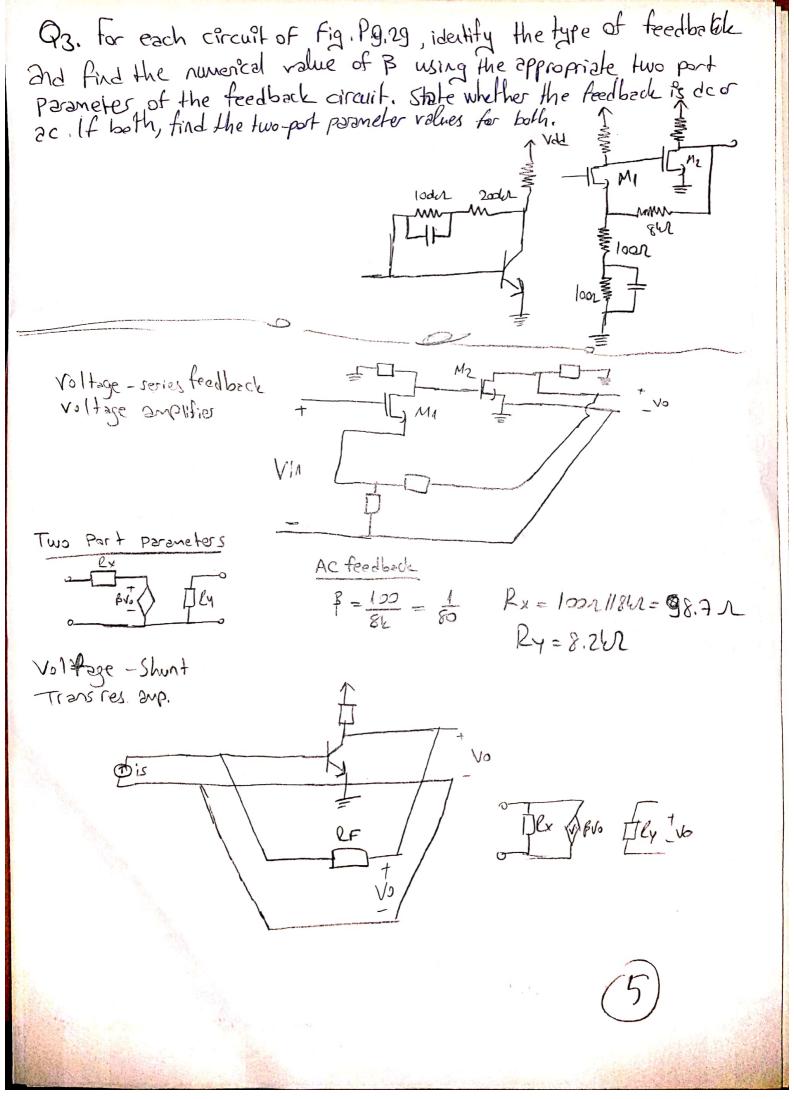
superposition

$$+ \frac{\sqrt{d}}{2}$$

$$\sqrt{01} = Ad \frac{\sqrt{d}}{2}$$

$$\sqrt{02} = -Ad \frac{\sqrt{d}}{2}$$

Stroppilion Von = Adva + Ack, Voz= -Adva+AcVe





B = -1 RF	ac feed.	de feed ill 300la
•	20061	3∞ l N
Rx = RF	20 oler	300 ls
Py = RF	200ler	300lN

Q4. The CMOS amplifier in Fig 14.20 is ofereted at a biasing current of IQ = 50MA, The parameters of the MOSFET's are $k = 10MA/V^2$, |Vm(nmos)| = Vm(pmos) = 40MAVE=IV, W/L = 80 Mm/10 Mm except for Q7, for which WIL =160 Mm/10 Mm. Assure VDD=VSJ=5V

(2) find Ves, gm and ro for all MOSFETS.

(1) Find the low-frequency voltage gain of the amplifier Avo

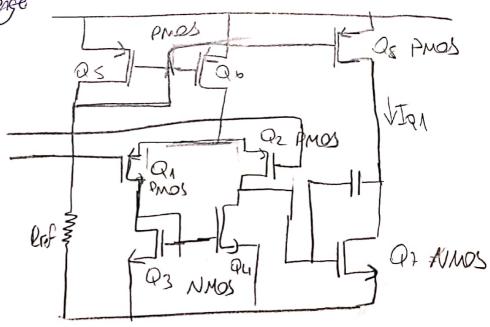
@ Find the value of the external resistance lef. @ Find the value of Conference capacitance Cx that gives a unity-gain bandwith of 1/11z and the corresponding slew rate.

(Find the value of resistance La to be connected in socies with Cx in order to

move the zero frequency to intinity.

f) find the common-mode input voltage range

(g) find the voltage 12990





(2) Systematic DC offset carcellations

$$2 \frac{(\frac{\psi}{2})_{8}}{(\frac{\psi}{2})_{6}} = \frac{(\frac{\psi}{2})_{4}}{(\frac{\psi}{2})_{4}}$$

$$I_{D1} = I_{D2} = I_{D3} = I_{D4} = \frac{I_Q}{2} = 25\mu A$$

 $I_{D8} = I_{D7} = I_Q = 50\mu A$

$$ID = \frac{1}{2} Mn Cox \frac{W}{L} (VGS-Vt)^{2}$$

$$I Mn Cox \frac{W}{L} (VGS-Vt)^{2}$$

$$ID = \frac{1}{2} \mu Cox \frac{W}{L} (Vsc-|V4|)^2$$

$$L_x = \frac{1}{2}M_nC_{0x} = \frac{1}{2}M_pC_{0x} = 19M_A/V^2$$

$$I_{D_1} = \frac{1}{2} M_1 C_{0x} \frac{W}{L} (v_{GS} - V_4)^2$$

$$V_{D_1} = \frac{1}{2} M_1 C_{0x} \frac{W}{L} (v_{GS} - V_4)^2$$

Q1 = Q2 = 05=Q6 = 08 = PMOS

$$25MA = \frac{1}{2}Mn(0x) \frac{W}{L}(VGS-VI)^{2} \qquad Q_{3} = Q_{1} = \frac{1}{2}Mn(0x) \frac{W}{L}(VGS-VI)^{2} \qquad Q_{3} = \frac{1}{2}Mn(0x) \frac{W}{L}$$

$$Q_3 = Q_4 = Q_2 = NMOS$$
 $165 - \sqrt{4} = 0.790$

$$c_0 = \frac{600}{250\mu A} = 1.2 \mu M J L$$

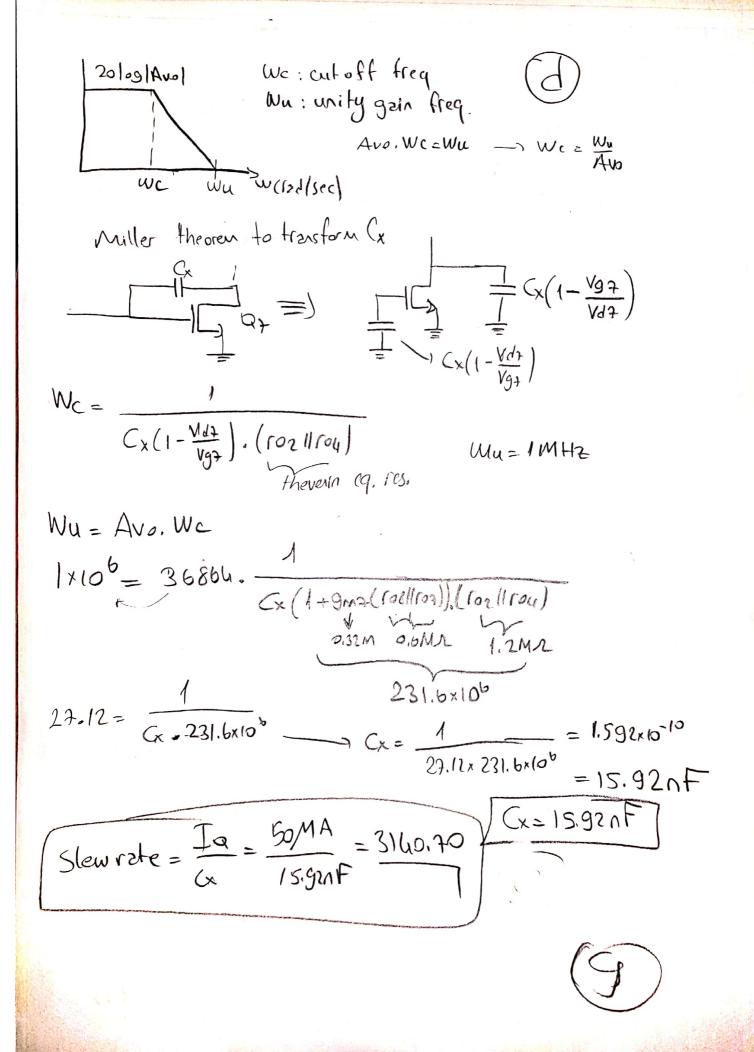
$$Q_2 \rightarrow (VS = VI)^2 = \frac{25}{80}$$

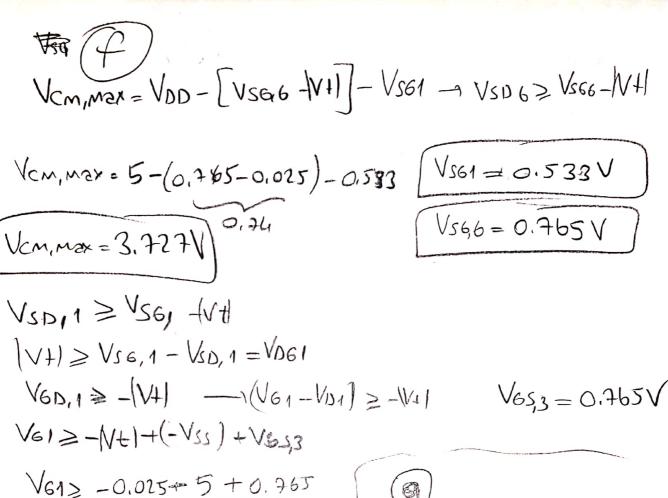
Qt = 10 MA/V. 16. (Ves - V4) = 50 M 1 (Nes-N+) = 30

$$(V_{6s}-V_{+})_{1,2,3,4,7} = \frac{25}{80}$$

$$(V_{6s}-V_{+})_{5,6,8} = \frac{50}{80}$$

$$(V_{6s}-V_{+})_{5,6,8} = \frac{50}{80}$$





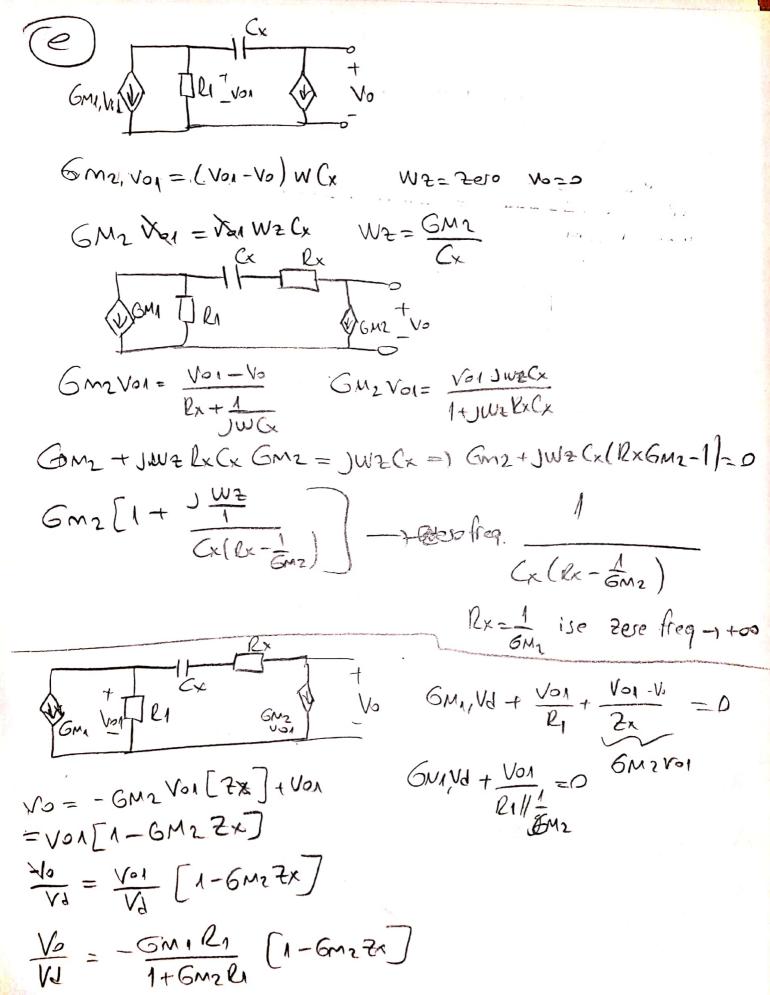
VSD18 > VSE,8-V+1 -108 > 20tores. Vcm, MIn = -4.26V VDS, 72 V65,7 - V+ your 1 Q2 52 lurasp. VO, MEX = VDD - (VSG, 8-1V+1) Vo, min =- VSI + (VGJ, 7 - V+)

Nest = 0, 2331 Vo, Max = 5-(0,765-0,025) V56,8=0.765V VOMEX = 4,26V

Voimin = -5+(0.533V-0.025)

Vom 1 = - 4.497V





(11)

15= -9m, Vd 15 = Vo 15 = 9miNd Vd = 9mi, 9ma. [Rillex] [Rellex] 1+ 9M7 [R1/12x] [R2/12x] 1 Vo = 9m1 9m2 212x 122+2x 1+9mz 12x . 122x 1 9m1 9m7 R, R2 2x2 (R1+2x)(R2+2x)+9M7 R1 R27x 9m, gm7 R, 122 7x2 E1 R2+2x(R1+1R2+9M7)R1R1)+2x2 -, JM721R2>>> P1+R2 = 9m, 9m, R, R, 2x2 21R2+ 2x9m2RR2+ 2x2 - 1000+ -67VA gm2RR2-2rsm - 9malila = Vamero)=4Rila Vo = 9m, 9m2 R, R2 7x2

Vo (2x+Fsm), (2x+9m7R, R2) (9m7R1R2) 2 >> LIRI RA ZN = Rx+ 1 jucx -11 = -9M7 RIR2+ BM 2x + (3m) (2x + 8m) (1+ JWCx (1x+15m)) B = - CsM ") I+JUCX (Rx+gm7R1R2))

CamScanner ile tarandı

1+JWCx(PCX+rsm) (1+JW2(xRx)(1-JwCx(PCX+rsm))? 2 1+JWCx (Rx-1sm) Vo = 9m,9mq. R. R. R. 1+JW(x(Rx-FSM) 1+JW(x(Rx+9mqRill)) $W_{z} = \frac{1}{C_{x}(R_{x}-r_{sm})} \qquad W_{p} = \frac{1}{C_{x}(R_{x}+g_{m}R_{u}R_{u})}$ WP = 1 Cx(1+9m2122). R1 () therein eq. ref. FOR 11/02 FOR 11/04 Miller WZ-1+00 RXXTSM Olur.

Q5. Figure P.1321 shows four oscillator circuits of the Colpills type, complete with bias detail. For each circuit, derive an equation governing circuit operation, and find the frequency of oskillation and the gain coundition that ensures that oscillation in the converse that oscillation Start DI C: Spen C: Short SS equivalent RESSUML RAMJUNOL Zjwol RA-1 OPEN CITCUIT demont $\frac{1}{12} = \frac{1}{2} = \frac{1$ No= 1 VI + CIC2 CI+CI

(15)