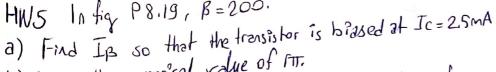
HWS In fig P8.19, B=200.

Abdullah MENTEOGU 171024001



c) Write an equation for C so that the low-trequency

Pole is located at 100 rad/s

$$9m = \frac{1}{V_T} = \frac{1}{V_T} = \frac{1}{V_T} = \frac{1}{200.26 \times 10^{-3}} = \frac{1}{2.5 \times 10^{-3}} = \frac{1}{2.08 \times 1.6 \times 10^{-3}} = \frac{1}{2.08 \times 1.6 \times 10^{-3}} = \frac{1}{2.5 \times 10^{-3}} = \frac{1}{2.08 \times 1.6 \times 10^{-3}} = \frac{1}{2.08 \times 10^{-3}} = \frac{1}{2.0$$

Vin Pes-6000 Prout Re=502

Re=502

Re=502

$$\frac{RE}{1 + RE} = \frac{Cs.(1+9m)}{CsRs + Cs.}$$

$$\frac{Vout}{Vin} = \frac{(1+9mRE).\Gamma\pi}{Rs + (1+9mRE)\Gamma\pi + \frac{1}{Cs}} \cdot \frac{RE}{\frac{1}{gm} + RE} = \frac{Cs.(1+9mRE)\Gamma\pi}{CsRs + Cs.(1+9mRE)\Gamma\pi + 1} \cdot \frac{RE}{\frac{1}{gm} + RE}$$

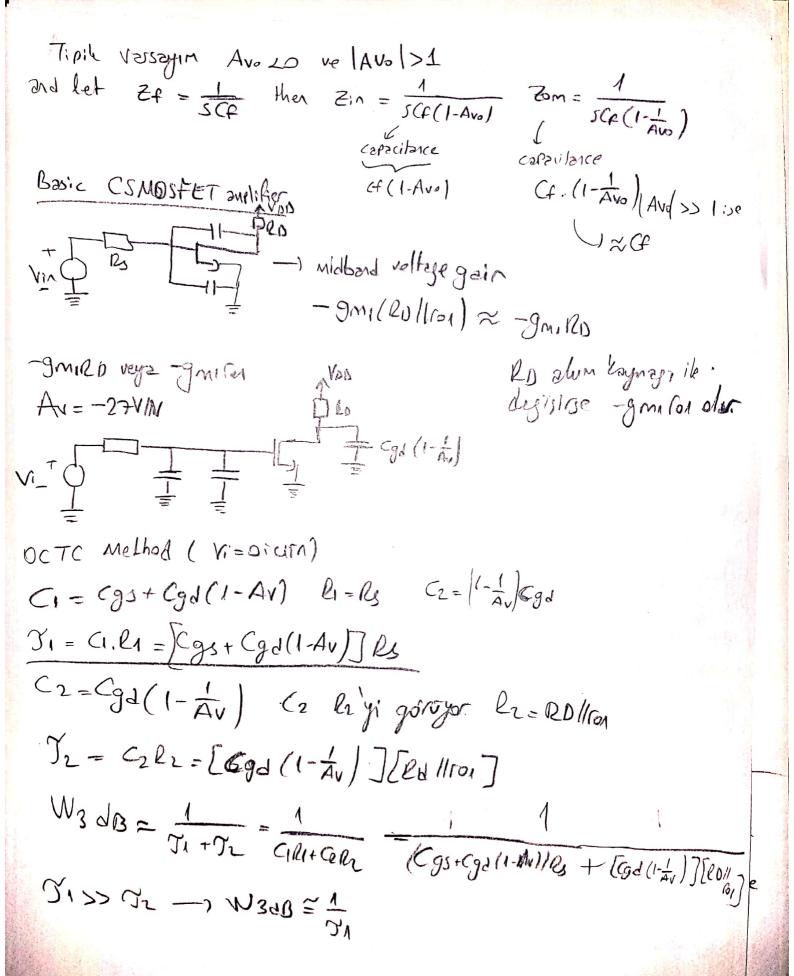
C. (HYMRE), MATC. Rs

$$g_{M=} \frac{I_c}{VT} = \frac{2.5 mA}{26 mV} =$$

Pole =
$$\frac{1}{C.(Hgmle)nHc.ls}$$
 = $\frac{1}{C.(Hgmle)nHc.ls}$ = $\frac{1}{C.(Hgmle)nHc.ls}$ = $\frac{1}{C.(Hsmle)nHc.ls}$ = $\frac{1}{C.(Hsmle)nHc}$ = $\frac{1}{C.(Hsmle)nHsmle)nHsmle}$ = $\frac{1}{C.(Hsmle)nHsmle}$ = $\frac{1}{C.(Hsm$

In a particular MOSFET amplifier for which the unidoand voltage gain between gate and drain is -27 VIV, the NMOS transistor has Cgs =0.3pp and Cgd = 0.1 pf. What input capacitance would you expect? For what range of signal-source resistances can you expect the 3dB frequency to exceed 10MHz? Neglect the effect of le

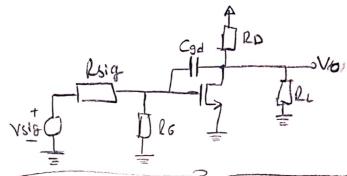
Miller Teoreni haterlatma deve analizi



W326 = 211 f326 = 1 10MHz ls[69s+C92(1-Av)] C95=0.3PF 211×10×106 = 1 PS[0.3×10-12+0.1×10-12(28)] 69d = 0.1PF Co Av= -27V/V Rs = 105 = 105 = 5.134 WERS α p 2 Wads > 21. 10 MHz Olacale ise ls 2 5.136 We Olach P In a fet amplifier, such as that in Fig. 4.49(a). the resistance - of the source Rsig = 1006/2, amplifier input resistance (which is the) due to the brasing network) Rin = 100kl, Ggs = 1pF, Cgd = 0.2pF, gm = 3mA/V ro = 50 ks, Ro = 862 and RL = 1062. Determine the expected 3-dB cutoff frequency for and the Midband gain. In evaluating ways to double for a designer considers the alternatives of changing either Rout or Rin. To raise fit as described what separate change in each would be required? What midbard vallage gain results in each case? SCTC Method Vsg=0 igin NDD -VSS -) 9nd I ise open circuit. Sei = Cci. Rcci (Cci in gordiger direnci =) Rsigerlo -1Ccz ve Cs Los deure Tez = Ccz Reaz -1 Recz (Czz'nin 4 11 =) (RD/10+RL) -1 Cc1 ve Cs Mes = Cs. Res - 1 Res (Cs'n " " =) fm - Carrecce kurz

WL = $\frac{1}{\Im e_1} + \frac{1}{\Im l_1} = \frac{1}{\Im l_2} = \frac{1}{G_1 \cdot (lsig+ll_6)} + \frac{1}{G_2 \cdot (ll_0||r_0+ll_1)} + \frac{1}{G_3}$

High freq. cutoff Cci, Czz ve Cs



Midbard gain = Av = LG (-gm) [Rollle 110] 20, Repasitor withing sortings direct TCgd(1-Av) JH = [Ggs+Cgd (1-Av)]. [Rsig 11eg] Egd (1-1) + open aresult

JHZ= [69d (1-A)][RO/1RL/10] $W_{H} = \frac{\Lambda}{J_{H1} + J_{H2}} \qquad J_{H1} >> J_{H2} \text{ is e} \qquad W_{H} \approx \frac{1}{J_{H1}}$ $= W_{H} = \frac{\Lambda}{[Cgs + Cgd (I - Av)]^{-} [esig || egg]} \qquad e$

 $Av = \frac{lg}{100 \text{ M} + lg} (-3) \cdot \left[\frac{80 \cdot 110 \text{ M}}{18} \right] = \frac{80 \cdot 1150 \text{ M}}{18}$ $Rg = \frac{100 \text{ M}}{18} \cdot \frac{1}{18} = \frac{100 \cdot 16}{18}$ $Av = 1, -3. \cdot \frac{100 \cdot 16}{18} \cdot \frac{80}{18} + 50$ Av= 1,-3. 408.16

Av= -6/2.24

A Hesaphure RG= 1006/2icin Yapılmıskr.

Egs= 1pf Egd=0.2PF Av bulunzcak 12 sig = 100M R&-1 bihamiyor. 9m = 3ms KD = 841 Re=10W

(0 = 50 les

Consider the common-enither amplifier of Fig. P5.159 under the following conditions: Rsig = 5kl, l1=33kl, l2=22kl, RG=39kl, lc=4.7kl, Re = 5.66. Vcc = 5v. The do evitter current can be shown to be IED.3MA 2t which Bo = 120, Po = 300kr, and Px = 50r. Find the input resistence Rin and the mid band gain Am. It the morning the lesse the start ovo Rin and the mid bond gain Am. If the travistor is specified to have for 700 MHz

SCTC method for WL

JC1 = CC1=RCC1

DCCI = Ca ta gordigi dereia

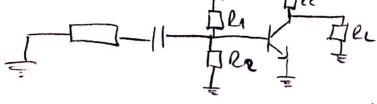
Dec1 = Rsigt (R/11R211rn)

Cain solde gordige direra Rsig 31 = G. . (Rsig + (RI || Rallin)) = Ca. (542+ (3342/12242/1044)

J1 = CC10 (562+5.81662)

J1= Cc1. 10.81662

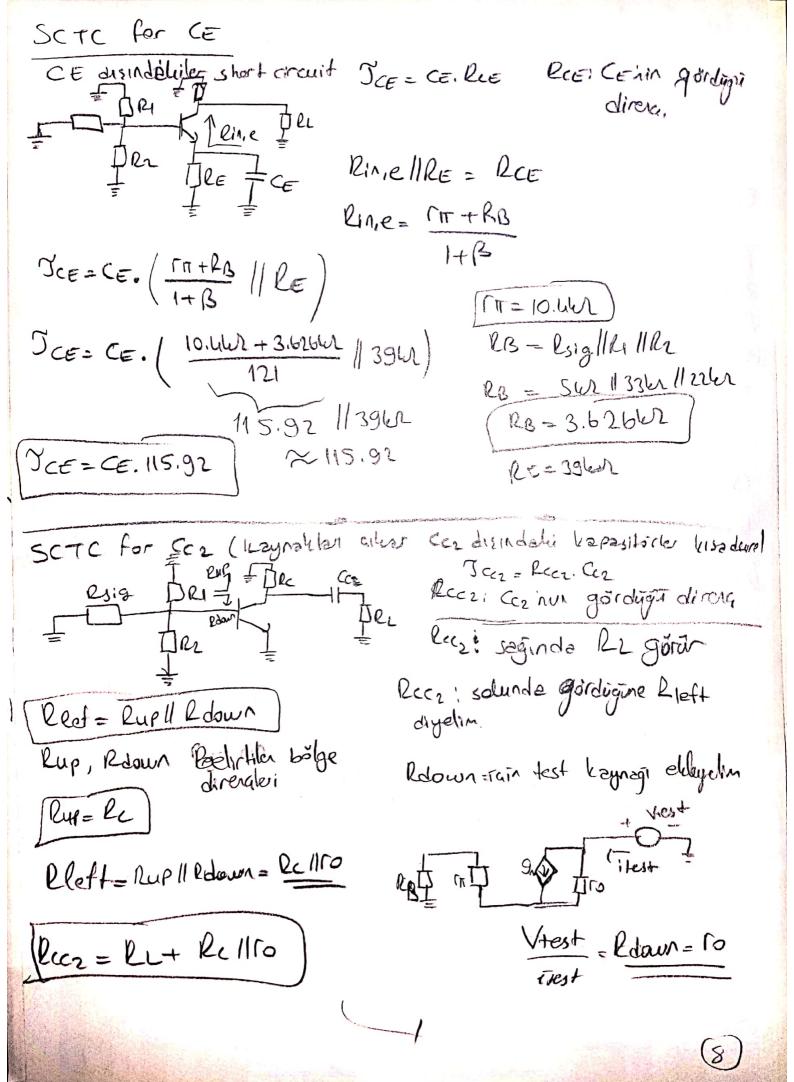
Du Methodde sadec Cc, biraluldi diger keresitoirier short circuits



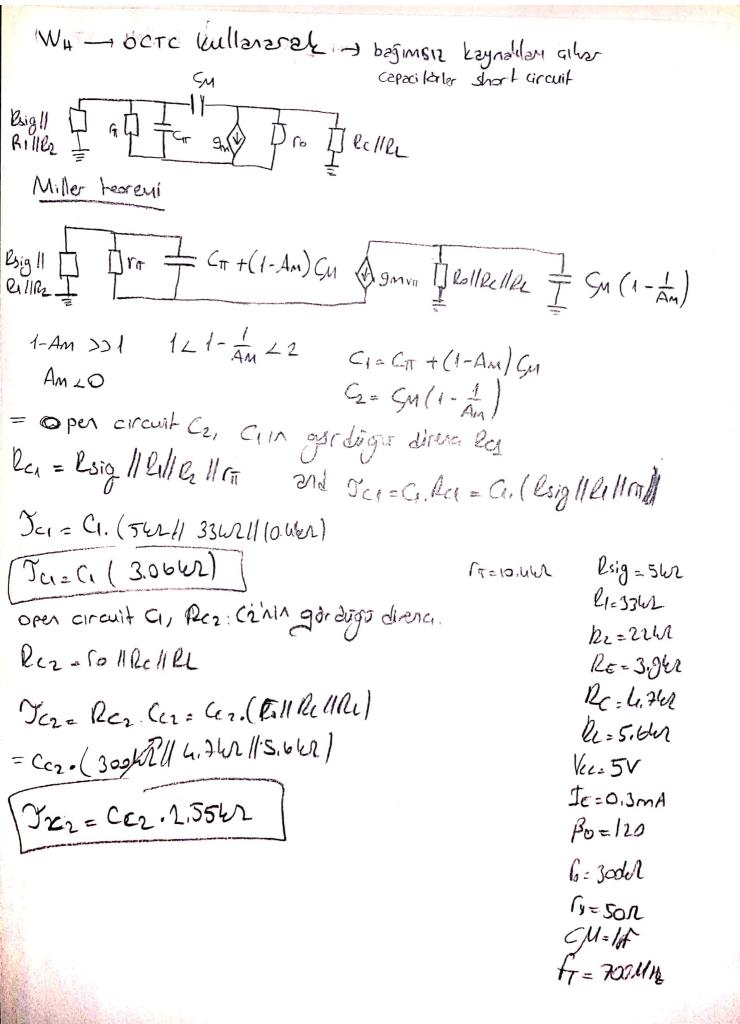
 $T_c = \frac{\beta V_T}{T_c} = \frac{12.26 \times 10^{-3}}{0.3 \times 10^{-3}}$

Ic=IE_

TT=10.462



$$T_{C2} = C_{C2}RC_2 = C_{C2} \cdot (R_1 + R_2 | R_1 | R_2 | R_$$



$$g_m = \frac{f_c}{V_T} = \frac{\beta}{\beta+1} \int_{V_T} \frac{1}{V_T}$$

$$C_{11} + 10^{-12} = \frac{(1.90 \times 10^{-3})}{21.700.10^{6}}$$

C+10-12= 2. 305×10-12

CT = 1.705×10-12 F = 1.305pF

CM= IPF

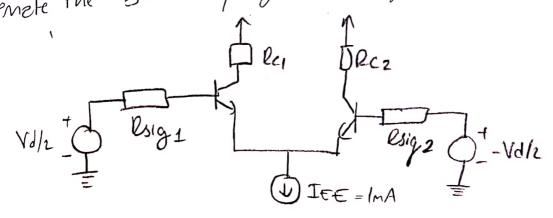
Qb. A BJT differential amplifier operating with a 1-MA current source uses transistors for which B=100, f_ = booMHz, Cu = 0.5pf, and rx = 1001. Each of the collector resistances is 10W, and so is very large. The amplifier is fed in a symmetrical fashion with a source resistance of 10W in series with

each of the two input townows.

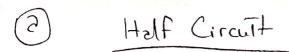
(2) Sketch the differential half-circuit and its high-frequency equivalent circuit.

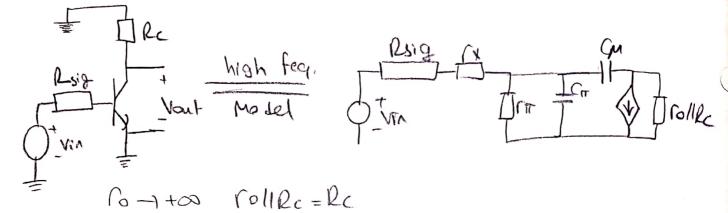
(b) Defermine the low-frequency value of the overall differential garn.

6) Defermine the tow-frequency value the input capacitance and hence (C) Use Miller's theorem to determine the input capacitance and hence estimate the 3-2B frequency and the gain-bandwith product



(11)





$$C_1 = C_{TT} + C_{JU}(1-A)$$
 $C_2 = C_{JU}(1-\frac{1}{A}) = C_{JU}(1+\frac{1}{4mRe})$

OCTC Cz open circult Rc1: C1 in gordypo direnc.

$$C_2 = Rc$$

$$W_H = \frac{1}{\gamma_{CI} + \gamma_{CI}} \approx \frac{1}{\gamma_{CI}} = \frac{1}{\gamma_{$$

Gain-Bandwith Product

$$f_{7} = \frac{9m}{2\pi(C_{M}+C_{\Pi})} - \frac{9m}{2\pi f_{7}} = \frac{0.02}{2\pi f_{7}} = \frac{0.02}{2\pi .600 \times 10^{6}}$$

$$C_M + G_T = 5.305 \times 10^{-12}$$

 $C_T = 5.305 \times 10^{-12} = 0.5 \times 10^{-12} = 1.805 \times 10^{-12} = 0.5 \times 10^{-12}$

(13)