Section Titles

- Reset Circuit
- 512Mb DDR SDRAM (32Mx16)
- Headphones and Line-In
- Composite Video
- POWER/RECOVERY Buttons
- GPIO
- BOOT MODE SELECT
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- MicroSD
- POWER SUPPLY CIRCUIT

Notes (BOOT MODE SELECT area)

LCD_RS must to be pulled
High to enable Resistor Boot Mode.
If pulled low, the i.MX233 will
attempt to boot from OTP.

LCD_D05 is ETM Enable Bit:
IF Low -> ETM disabled
IF High -> ETM enabled

BOOT MODE SELECT Table

BOOT MODE SELECT:		LCD DATA			
BOOT MODE		D03	D02	D01	D00
USB (Default)		0	0	0	0
3.3V I2C Master		0	0	0	1
3.3V SPI Flash 1 Master		0	0	1	0
3.3V SPI Flash 2 Master		0	0	1	1
3.3V NAND		0	1	0	0
DEBUG		0	1	1	0
3.3V SD/MMC 1		1	0	0	1
3.3V SD/MMC 2		1	0	1	0

Power Supply notes

5VDC Only!!!

Main IC

MCIMX233DAG4B_LD

Title Block

iMX233-OLinuXino_Rev_A
09.3.2012
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Bottom text

Future changes is read!!!

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