

M5Stack Unit Scroll I2C Protocol																		V1 (FW Version)	
REG MAP (0x40)		0	1	2	3	4	5	6	7	8	9	A	B	C	D	E	F	2024/2/29	
note																			
Encoder	0x10 W/R	Encoder -byte0	Encoder -byte1	Encoder -byte2	Encoder -byte3													Encoder = Encoder-byte0 + Encoder-byte1 * 256 + Encoder-byte2 * 65536 + Encoder-byte3 * 16777216	
Button	0x20 R	Button																0~1	
RGB	0x30 W/R	NULL	R	G	B													R/G/B	
Reset	0x40 W	Reset																write 1, Reset Encoder value(10H) and Inc Encoder Value(50H)	
Inc Encoder	0x50 R	Inc Encoder -byte0	Inc Encoder -byte1	Inc Encoder -byte2	Inc Encoder -byte3													Inc Encoder = Inc Encoder-byte0 + Inc Encoder-byte1 * 256 + Inc Encoder-byte2 * 65536 + Inc Encoder-byte3 * 16777216 will set to 0 after read	
Encoder AB or BA	0xF0 W/R													Encoder AB or BA				Encoder AB or BA: 0: AB(default) 1: BA	
Bootloader Version	0xF0 R													Bootloader Version				Value: bootloader version	
Firmware Version	0xF0 R														FW Version		Value: firmware version		
I2C Addr	0xF0 R																I2C Addr	Value: I2C Address	