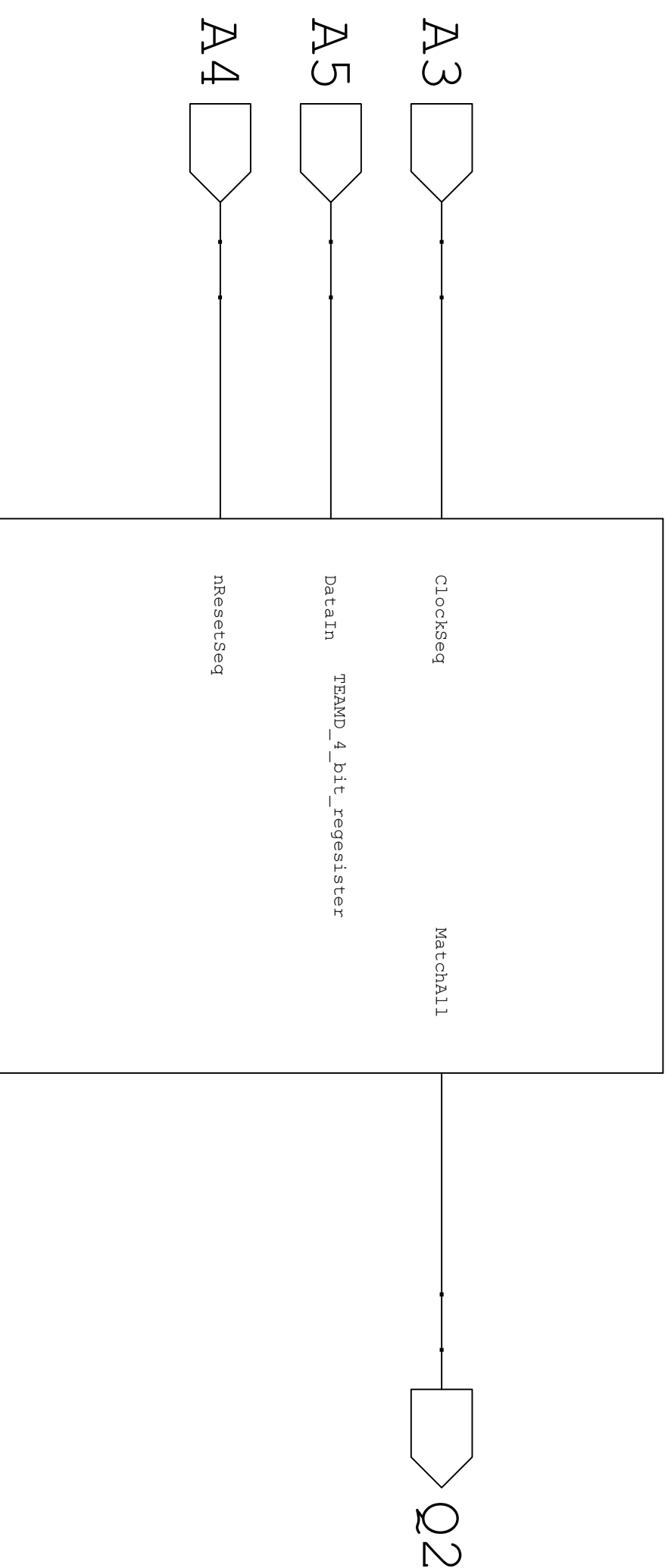
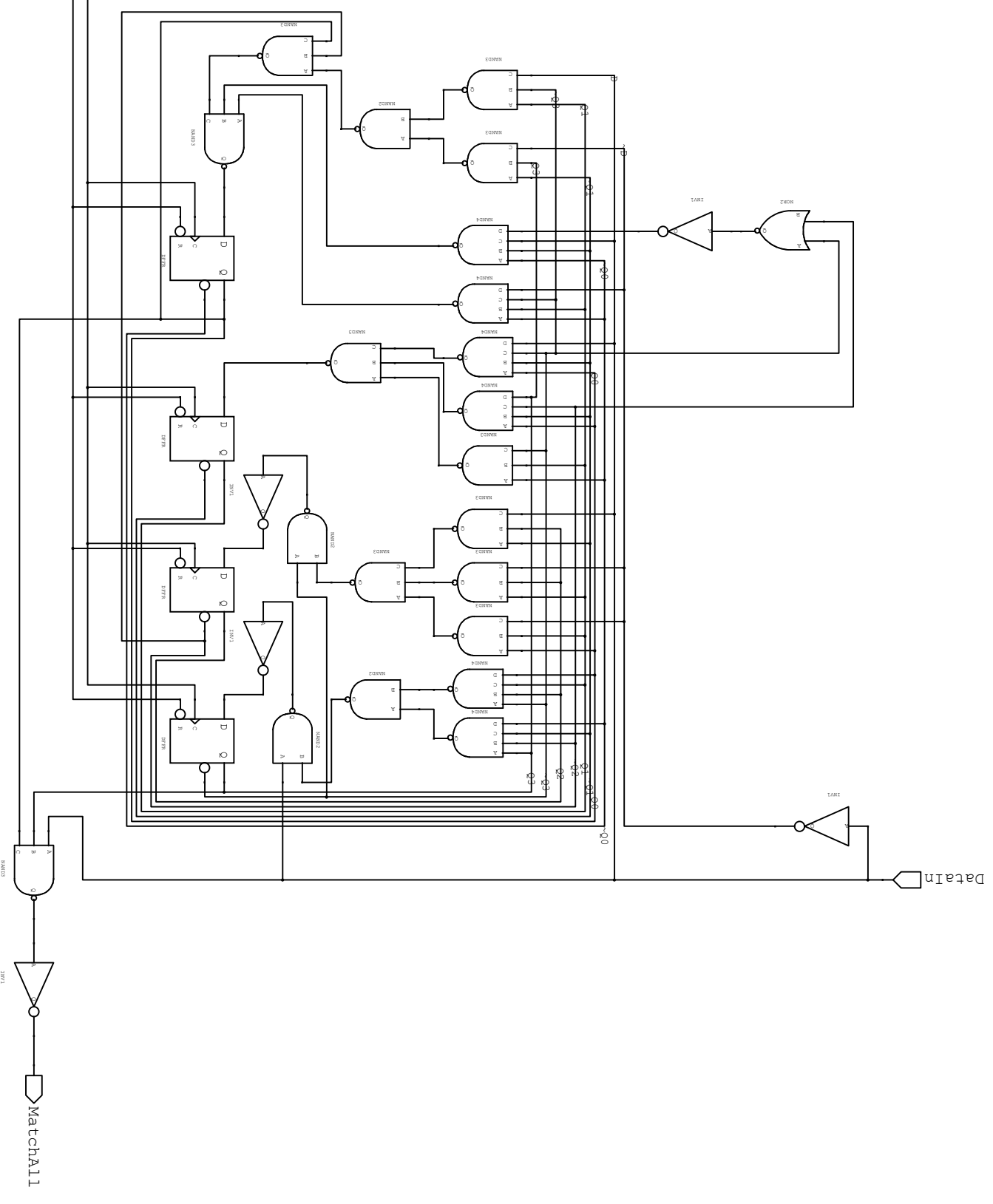
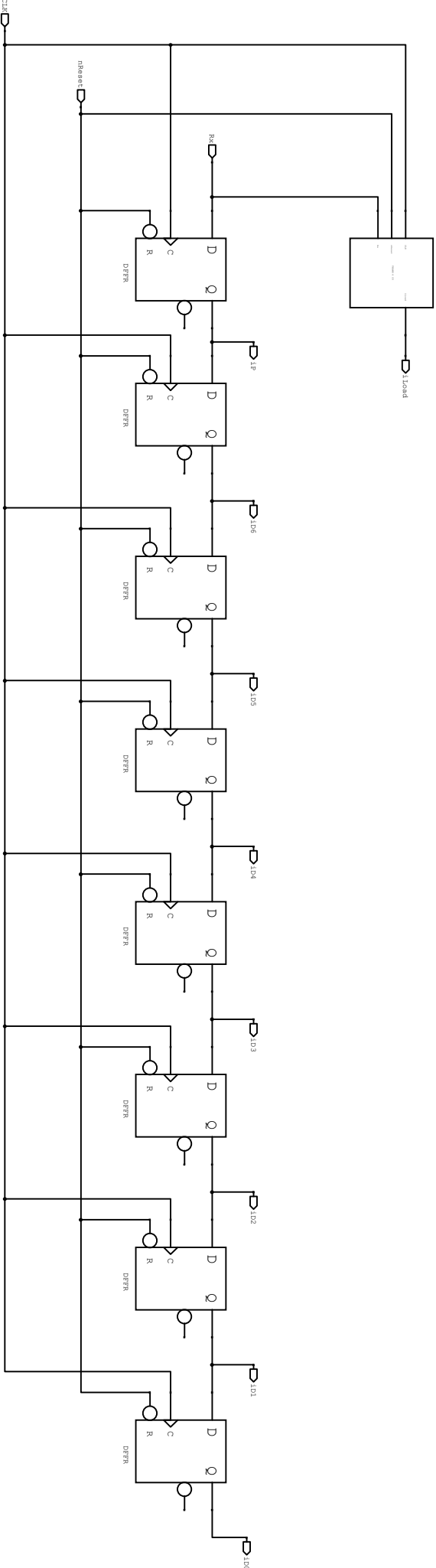


schematic of cell "TEAMD_CIRCUIT1" design "TEAMD_Schematics1"







schematic of cell "TEAMD_CIRCUIT3" design "TEAMD_Schematics1"

