INSTRUCTION	OPCODE	REGISTER	DATA/ADDR H	DATA/ADDR L
LD R1, 0xabcd	0x00	REG[0-7]	0xAB	0xCD
# LD R1, R2	0x01	REG[0-7]	0x00	REG[0-7]
# LD R1, $0xABCD	0x02	REG[0-7]	0xAB	0xCD
# ST R1, $0xABCD	0x10	REG[0-7]	0xAB	0xCD
# STL R1, $0xABCD	0x11	REG[0-7]	0xAB	0xCD
# STH R1, $0xABCD	0x12	REG[0-7]	0xAB	0xCD
# ST R1, R2	0x13	REG[0-7]	0x00	REG[0-7]
# STL R1, R2	0x14	REG[0-7]	0x00	REG[0-7]
# STH R1, R2	0x15	REG[0-7]	0x00	REG[0-7]
# CMP R1, R2	0x20	REG[0-7]	0x00	REG[0-7]
# CMP R1, 0xabcd	0x21	REG[0-7]	0xAB	0xCD
# BEQ label	0x30	0x00	0xAB	0xCD
# BGT label	0x31	0x00	0xAB	0xCD
# BLT label	0x32	0x00	0xAB	0xCD
# BRA label	0x33	0x00	0xAB	0xCD
# ADD R1, 0xabcd	0x40	REG[0-7]	0xAB	0xCD
# SUB R1, 0xabcd	0x41	REG[0-7]	0xAB	0xCD
# ADD R1, R2	0x42	REG[0-7]	0x00	REG[0-7]
# SUB R1, R2	0x43	REG[0-7]	0x00	REG[0-7]
# HALT	0xFE	0xFF	0xFE	0xFF
# NOOP	0xFF	0xFF	0xFF	0xFF