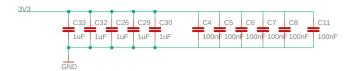
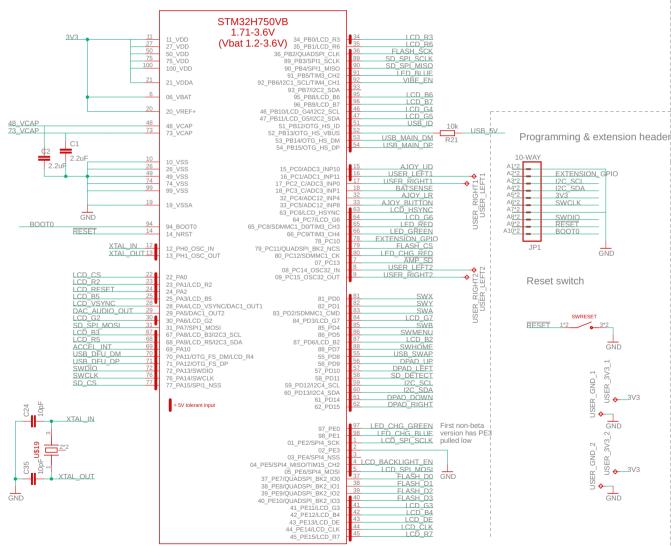
32blit



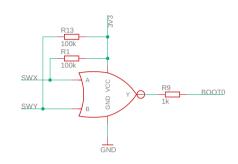
STM32H750VB

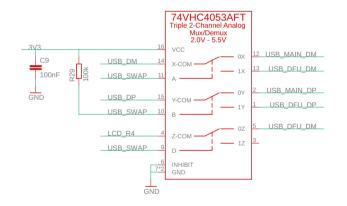


USB DFU logic

To enter USB DFU at boot, an user presses both X and Y buttons at the same time. X and Y input to a NOR gate, which outputs high when the buttons are pressed. The NOR output is wired to BOOT0

When USB_SWAP is high (i.e. not actively driven low by the MCU), USB pins are routed to USB_DFU_DM/DP to enable entering DFU mode





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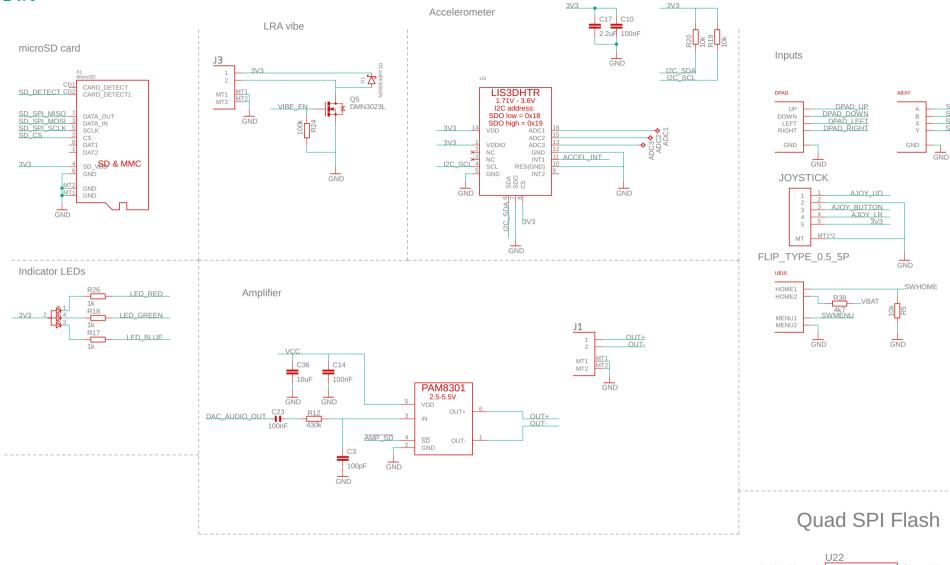
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32blit



AMP TPA301: 150nA in shutdown SD card: up to 100mA, 0.1 - 30mA in idle LCD backlight: ~90mA full, 50uA shutdown Accelerometer: 180uA normal, 1uA shutdown

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Flash W25Q32FV: 20mA full, 10uA idle, 1uA shutdown

LCD: 20mA

NOR gate SN74AHC1G02: 10uA

AP2112 LDO 55uA

STM32: 100mA, 5uA standby

Voltages: STM32: 1.62-3.6V, separate VDD50USB (not on QFP100) XOR SN74AHC1G02 2-5.5V Accel MSA301: 1.62-3.6V Quad flash 2.7-3.6V Amp 2.5-5.5V (deleted: MicroSD: 3V3) USB: 3v3

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SWB SWX

SWY

FLASH_D3

100nF

FLASH SCK

FLASH_D1

SO(D1) WP(D2)

E_PAD

GND

HOLD(D3)

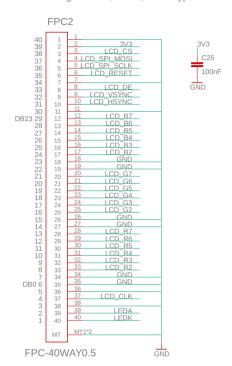
SCK

MT25QL256ABA1EW7-0SIT

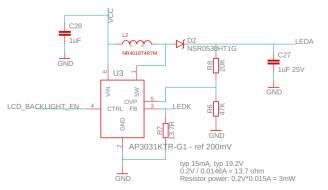
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3.5" LCD connector

Driver: ST7272A Backlight: 6S1P, 19.2V, 15mA typ

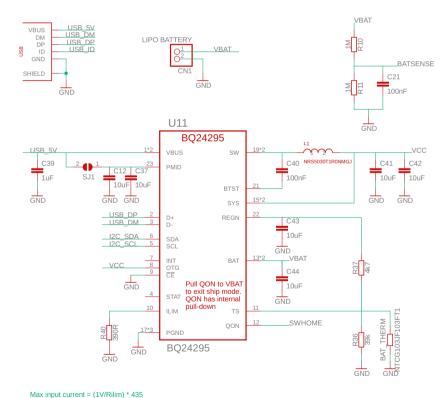


3.5" LCD backlight driver



Battery Charger and Power Path Management USB and Lipo connectors





390R results in a 1.1A max input current. 1.1A/3.7V*5V*0.9A-0.2A = average charge current would be 1.1A

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