USRP Receiver Debugging Diagram

(Top View)

USRP Basic RX
Daughterboard

	Pin	Pin	
+5V	1	2	N/C
CLK1	3	4	D15
D14	5	6	D13
D12	7	8	D11
D10	9	10	D9
D8	11	12	D7
D6	13	14	D5
D4	15	16	D3
D2	17	18	D1
D0	19	20	GND
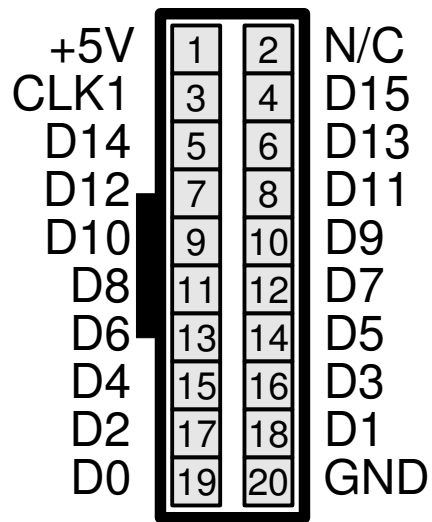
HP Logic Analyzer 100k
Termination Adapter
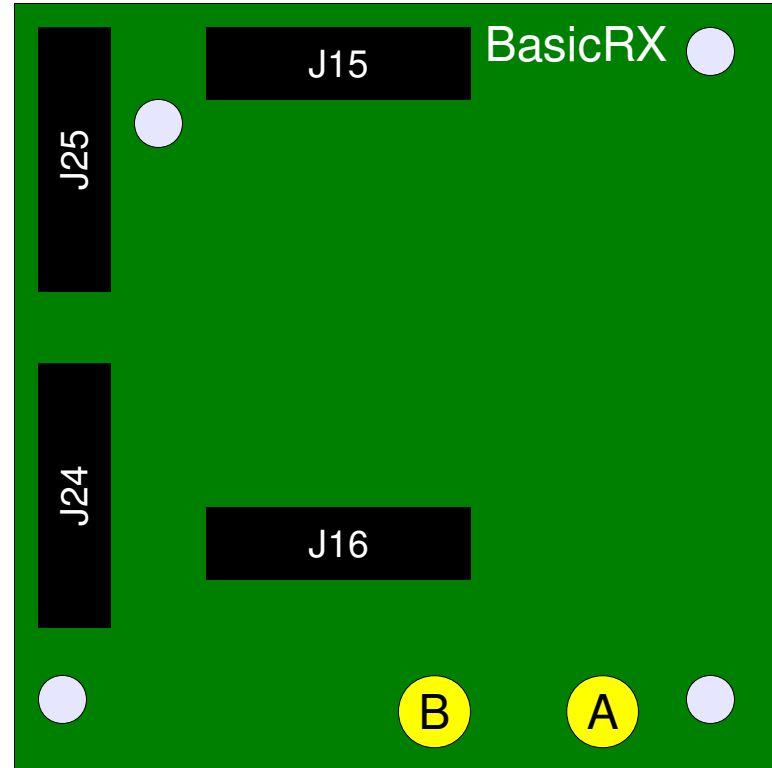
1. Connect Termination Adapter to J15 with Pin1 connected to bottom-right side.