

# Reverse Polish notation

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**Reverse Polish notation** (RPN), also known as **reverse Łukasiewicz notation**, **Polish postfix notation** or simply **postfix notation**, is a mathematical notation in which operators *follow* their operands, in contrast to Polish notation (PN), in which operators *precede* their operands. It does not need any parentheses as long as each operator has a fixed number of operands. The description "Polish" refers to the nationality of logician Jan Łukasiewicz,<sup>[1][2]</sup> who invented Polish notation in 1924.<sup>[3][4][5][6]</sup>

The first computer to use postfix notation, though it long remained essentially unknown outside of Germany, was Konrad Zuse's Z3 in 1941<sup>[7][8][9][10][11][12][13][14][15]</sup> as well as his Z4 in 1945. The reverse Polish scheme was again proposed in 1954 by Arthur Burks, Don Warren, and Jesse Wright<sup>[16]</sup> and was independently reinvented by Friedrich L. Bauer and Edsger W. Dijkstra in the early 1960s to reduce computer memory access and use the stack to evaluate expressions. The algorithms and notation for this scheme were extended by the Australian philosopher and computer scientist Charles L. Hamblin in the mid-1950s.<sup>[17][18][19][20][21][22]</sup>

During the 1970s and 1980s, Hewlett-Packard used RPN in all of their desktop and hand-held calculators, and has continued to use it in some models into the 2020s.<sup>[23][24]</sup> In computer science, reverse Polish notation is used in stack-oriented programming languages such as Forth, STOIC, PostScript, RPL, and Joy.

## Explanation

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In reverse Polish notation, the operators follow their operands. For example, to add 3 and 4 together, the expression is 3 4 + rather than 3 + 4. The expression 3 − 4 + 5 in conventional notation is 3 4 − 5 + in reverse Polish notation: 4 is first subtracted from 3, then 5 is added to it.

The concept of a *stack*, a last-in/first-out construct, is integral to the left-to-right evaluation of RPN. In the example 3 4 −, first the 3 is put onto the stack, then the 4; the 4 is now on top and the 3 below it. The subtraction operator removes the top two items from the stack, performs 3 − 4, and puts the result of −1 onto the stack.

The common terminology is that added items are *pushed* on the stack and removed items are *popped*.

The advantage of reverse Polish notation is that it removes the need for order of operations and parentheses that are required by infix notation and can be evaluated linearly, left-to-right. For example, the infix expression (3 × 4) + (5 × 6) becomes 3 4 × 5 6 × + in reverse Polish notation.

## Practical implications

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In comparison testing of reverse Polish notation with algebraic notation, reverse Polish has been found to lead to faster calculations, for two reasons. The first reason is that reverse Polish calculators do not need expressions to be parenthesized, so fewer operations need to be entered to perform typical calculations. Additionally, users of reverse Polish calculators made fewer mistakes than for other types of calculators.<sup>[25][26]</sup> Later research clarified that the increased speed from reverse Polish notation may be

attributed to the smaller number of keystrokes needed to enter this notation, rather than to a smaller cognitive load on its users.<sup>[27]</sup> However, anecdotal evidence suggests that reverse Polish notation is more difficult for users who previously learned algebraic notation.<sup>[26]</sup>

## Converting from infix notation

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Edsger W. Dijkstra invented the shunting-yard algorithm to convert infix expressions to postfix expressions (reverse Polish notation), so named because its operation resembles that of a railroad shunting yard.

There are other ways of producing postfix expressions from infix expressions. Most operator-precedence parsers can be modified to produce postfix expressions; in particular, once an abstract syntax tree has been constructed, the corresponding postfix expression is given by a simple post-order traversal of that tree.

## Implementations

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### History

The first computer implementing a form of reverse Polish notation (but without the name), was Konrad Zuse's Z3, which he started to construct in 1938 and demonstrated publicly on 12 May 1941.<sup>[28][11][29][13]</sup> In dialog mode, it allowed operators to enter two operands followed by the desired operation.<sup>[7][8][9][10][11][12][13][14][15]</sup> It was destroyed on 21 December 1943 in a bombing raid.<sup>[11]</sup> With Zuse's help a first replica was built in 1961.<sup>[11]</sup> The 1945 Z4 also added a stack.<sup>[30][31]</sup>

Other early computers to implement architectures enabling reverse Polish notation were the English Electric Company's KDF9 machine, which was announced in 1960 and commercially available in 1963,<sup>[32]</sup> and the Burroughs B5000, announced in 1961 and also delivered in 1963:

Presumably, the KDF9 designers drew ideas from Hamblin's GEORGE (General Order Generator),<sup>[17][18][20]</sup> an autocode programming system written for a DEUCE computer installed at the University of Sydney, Australia, in 1957.<sup>[17][18][20][32]</sup>

One of the designers of the B5000, Robert S. Barton, later wrote that he developed reverse Polish notation independently of Hamblin sometime in 1958 after reading a 1954 textbook on symbolic logic by Irving Copi,<sup>[33][34][35]</sup> where he found a reference to Polish notation,<sup>[35]</sup> which made him read the works of Jan Łukasiewicz as well,<sup>[35]</sup> and before he was aware of Hamblin's work.

Friden introduced reverse Polish notation to the desktop calculator market with the EC-130, designed by Robert "Bob" Appleby Ragen,<sup>[36]</sup> supporting a four-level stack<sup>[5]</sup> in June 1963.<sup>[37]</sup> The successor EC-132 added a square root function in April 1965.<sup>[38]</sup> Around 1966, the Monroe Epic calculator supported an unnamed input scheme resembling RPN as well.<sup>[5]</sup>

### Hewlett-Packard

Hewlett-Packard engineers designed the 9100A Desktop Calculator in 1968 with reverse Polish notation<sup>[23]</sup> with only three stack levels with working registers *X* ("keyboard"), *Y* ("accumulate") and visible storage register *Z* ("temporary"),<sup>[39][40]</sup> a reverse Polish notation variant later referred to as *three-level RPN*. This calculator popularized reverse Polish notation among the scientific and engineering communities. The HP-35, the world's first handheld scientific calculator,<sup>[23]</sup> introduced the classical *four-level RPN* with its specific ruleset of the so-called *operational (memory) stack*<sup>[41]</sup> (later also called

*automatic memory stack*<sup>[42][43]</sup>) in 1972.<sup>[44]</sup> In this scheme, the Enter ↑ key duplicates values into Y under certain conditions, and the top register gets duplicated on drops in order to ease some calculations and to save keystrokes.<sup>[43]</sup> HP used reverse Polish notation on every handheld calculator it sold, whether scientific, financial, or programmable, until it introduced the HP-10 adding machine calculator in 1977. By this time, HP was the leading manufacturer of calculators for professionals, including engineers and accountants.

Later calculators with LCD displays in the early 1980s, such as the HP-10C, HP-11C, HP-15C, HP-16C, and the financial HP-12C calculator also used reverse Polish notation. In 1988, Hewlett-Packard introduced a business calculator, the HP-19B, without reverse Polish notation, but its 1990 successor, the HP-19BII, gave users the option of using algebraic or reverse Polish notation again.

Around 1987, HP introduced RPL, an object-oriented successor to reverse Polish notation. It deviates from classical reverse Polish notation by using a stack only limited by the amount of available memory (instead of three or four fixed levels) and which could hold all kinds of data objects (including symbols, strings, lists, matrices, graphics, programs, etc.) instead of just numbers. It also changed the behaviour of the stack to no longer duplicate the top register on drops (since in an unlimited stack there is no longer a top register) and the behaviour of the Enter ↑ key so that it no longer duplicated values into Y, which had shown to sometimes cause confusion among users not familiar with the specific properties of the *automatic memory stack*. From 1990 to 2003, HP manufactured the HP-48 series of graphing RPL calculators, and in 2006 introduced the HP 50g.

As of 2011, Hewlett-Packard was offering the calculator models 12C, 12C Platinum, 17bII+, 20b, 30b, 33s, 35s, 48gII (RPL) and 50g (RPL) which support reverse Polish notation.<sup>[45]</sup> While calculators emulating classical models continue to support classical reverse Polish notation, new reverse Polish notation models feature a variant of reverse Polish notation, where the Enter ↑ key behaves as in RPL. This latter variant is sometimes known as *entry RPN*.<sup>[46]</sup> In 2013, the HP Prime introduced a *128-level* form of entry RPN called *advanced RPN*. In late 2017, the list of active models supporting reverse Polish notation included only the 12C, 12C Platinum, 17bii+, 35s and Prime. On 1 November 2021, Royal Consumer Information Products<sup>[47]</sup> (for the Americas) and the Czechia-based Moravia Consulting<sup>[48]</sup> (for all other markets) became the licensees of HP Development Company, L.P. to continue the production, distribution, marketing and support of HP-branded calculators. By July 2023, only the 12C, 12C Platinum, the freshly released HP 15C Collector's Edition, and the Prime remain active models supporting RPN, with a new version of the 35s vaguely announced.



A promotional Hewlett-Packard "No Equals" hat from the 1980s – both a boast and a reference to RPN

## WP 31S and WP 34S

The community-developed calculators WP 31S and WP 34S, which are based on the HP 20b/HP 30b hardware platform, support Hewlett-Packard-style classical reverse Polish notation with either a four- or an eight-level stack. A seven-level stack had been implemented in the MITS 7400C scientific desktop calculator in 1972<sup>[49][50][51]</sup> and an eight-level stack was already suggested by John A. Ball in 1978.<sup>[5]</sup>

## Sinclair Radionics

In Britain, Clive Sinclair's Sinclair Scientific and Scientific Programmable models used reverse Polish notation.<sup>[52][53]</sup>

## Commodore

In 1974, Commodore produced the Minuteman \*6 (MM6) without an enter key and the Minuteman \*6X (MM6X) with an enter key, both implementing a form of *two-level RPN*. The SR4921 RPN came with a variant of *four-level RPN* with stack levels named X, Y, Z, and W (rather than T) and an Ent key (for "entry"). In contrast to Hewlett-Packard's reverse Polish notation implementation, W filled with 0 instead of its contents being duplicated on stack drops.<sup>[54]</sup>

## Prinztronic

**Prinz** and **Prinztronic** were own-brand trade names of the British Dixons photographic and electronic goods stores retail chain, later rebranded as Currys Digital stores, and became part of DSG International. A variety of calculator models was sold in the 1970s under the Prinztronic brand, all made for them by other companies.

Among these was the PROGRAM<sup>[55]</sup> Programmable Scientific Calculator which featured reverse Polish notation.

## Heathkit

The Aircraft Navigation Computer Heathkit OC-1401/OCW-1401 used *five-level RPN* in 1978.

## Soviet Union

Soviet programmable calculators (MK-52, MK-61, B3-34 and earlier B3-21<sup>[56]</sup> models) used reverse Polish notation for both automatic mode and programming. Modern Russian calculators MK-161<sup>[57]</sup> and MK-152,<sup>[58]</sup> designed and manufactured in Novosibirsk since 2007 and offered by Semico,<sup>[59]</sup> are backwards compatible with them. Their extended architecture is also based on reverse Polish notation.

## Other

Existing implementations using reverse Polish notation include:

- Stack-oriented programming languages such as:
  - Forth
  - STOIC
  - Factor
  - PostScript page description language<sup>[60][61]</sup>
  - BibTeX
  - Befunge
  - Joy
  - IPTSCRAE
  - Lotus 1-2-3 and Lotus Symphony formulas<sup>[62][63]</sup>

- RPL (aka Reverse Polish Language), a programming language for the Commodore PET around 1979/1981
- RPL (aka Reverse Polish Lisp), a programming language for Hewlett-Packard calculators between 1984 and 2015
- RPNL (Reverse Polish Notation Language)<sup>[64][65]</sup>
- Hardware calculators:
  - Some Hewlett-Packard science/engineering and business/finance calculators
  - Semico calculators
  - SwissMicros calculators
  - Some APF calculators as well can use RPN
- Software calculators:
  - Mac OS X Calculator
  - Several Apple iPhone applications e.g. "reverse polish notation calculator"
  - Several Android applications e.g. "RealCalc"
  - Several Windows 10 Mobile applications e.g. "RPN9"
  - Unix system calculator program dc
  - Emacs lisp library package calc
  - Xorg calculator (xcalc)
  - ARPCalc, a powerful scientific/engineering RPN calculator for Windows, Linux and Android that also has a web-browser based version
  - grpn<sup>[66]</sup> scientific/engineering calculator using the GIMP Toolkit (GTK+)
  - F-Correlatives in MultiValue dictionary items
  - RRDtool, a widely used tabulating and graphing software
  - grdmath, a program for algebraic operations on NetCDF grids, part of Generic Mapping Tools (GMT) suite
  - galculator,<sup>[67]</sup> a GTK desktop calculator
  - Mouseless Stack-Calculator<sup>[68]</sup> scientific/engineering calculator including complex numbers
  - rpCalc, a simple reverse polish notation calculator written in Python for Linux and MS Windows and published under the GNU GPLv2 license
  - orpie, RPN calculator for the terminal for real or complex numbers or matrices
  - Qalculate!, a powerful and versatile cross-platform desktop calculator
- Class libraries
  - TRURL,<sup>[69]</sup> a class library for the construction of RPN calculators in Object Pascal

## See also

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- Calculator input methods
- FOCAL keystroke programming
- Stack machine
- Head-directionality parameter
- Scrambling (linguistics)

## References

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1. Łukasiewicz, Jan (1951). "Chapter IV. Aristotle's System in Symbolic Form (section on "Explanation of the Symbolism)". *Aristotle's Syllogistic from the Standpoint of Modern Formal Logic* (1 ed.). p. 78.
2. Łukasiewicz, Jan (1957). *Aristotle's Syllogistic from the Standpoint of Modern Formal Logic* (2 ed.). Oxford University Press. (Reprinted by Garland Publishing in 1987 ISBN 0-8240-6924-2.)
3. Łukasiewicz, Jan (February 1929). *Elementy logiki matematycznej* (in Polish) (1 ed.). Warsaw, Poland: Państwowe Wydawnictwo Naukowe; Łukasiewicz, Jan (1963). *Elements of mathematical logic*. Translated by Wojtasiewicz, Olgierd Adrian [in Polish]. New York, USA: The MacMillan Company. p. 24.
4. Hamblin, Charles Leonard (1962-11-01). "Translation to and from Polish notation" ([https://web.archive.org/web/20221020172114/https://watermark.silverchair.com/5-3-210.pdf?token=AQECAHi208BE49Ooan9kKhW\\_Ercy7Dm3ZL\\_9Cf3qfKAc485ysgAAAsYwggLCBgkqhkiG9w0BBWagggKzMIICrwiBADCCAqgGCSqGSIb3DQEHAATAeBgIghkgBZQMEAS4wEQQMqgrWehLt3Q-vndNgAgEQgIIceS9dAMLeq1AKBwrDN5TQY4gNgYDbjJ5TgZIWaINiDgof7kP40GMNOaOAJKG\\_fk3O3n0hphWohj0964P0ICTTGaBHL8qeej\\_r1vwugGCx8J0jiQbGpEMX0ujPrsUo\\_AKgFkE4sUk\\_9MWZ2gobDpNP1kZ-akNB0R\\_87g5zmCRSwFnUgjRC6t45kCf4zsq4xPTYkL1QCQNXdEPk3WmarvEwgeCzvP8onePqPBIktDZK5ZC6XUq6Q4T3IlikvJoEUwyxn7OW2s-h3C6CKGxztBiTMy9au1PeH1aKV7t5V\\_LWhGqP9Xe2LzhZ2hTMA5iCcoMkbMWhZiOq65FOSajlzw3\\_1ivADCs5zMCzD\\_SXhKtDCQQuRFJmnXBGPzA89QcUcHhtdZIFkaA16p7dWv7SJ0Wkg0\\_mwxyGoL3jwZNQmtTiV2Uzv\\_aC2fmFpl6gPiDnBMwgpslWy8UHU4bhT7mTzbBPwGbHZculzN7bApBRAZbu6yWWuftwgDZiKU9XUciDucNnNiCWd8bhVW9i\\_g-rS6SEAHtfsQ9A-ghgTOCPXEN2f7dZT9HD9BSuhvKWPKoYeFYx67Xn1CbMDpKZnOb2qPgssbSZDoGhVVjTrirBakr8TKjEJMrDz53Sf5qRIPLOARYwbzTD48hk5YDJX4dPPoO3IJ2Uepe5-GIMmCv5rAyHwaA9DzxQ7\\_7fidTQU6EfQXGwgehQwBQsaU2T0zKOikANeFIZ2pEskKqffFftVgy345S0j\\_p7z60\\_39YZHEiX7wA-hssZq\\_BUaE8rDXWuU8y6MQ4DATET83QkyKa7e7rupJ6zZ-hOLLPmyN7GCfUovWBqOcjR\\_a\\_GJozQ](https://web.archive.org/web/20221020172114/https://watermark.silverchair.com/5-3-210.pdf?token=AQECAHi208BE49Ooan9kKhW_Ercy7Dm3ZL_9Cf3qfKAc485ysgAAAsYwggLCBgkqhkiG9w0BBWagggKzMIICrwiBADCCAqgGCSqGSIb3DQEHAATAeBgIghkgBZQMEAS4wEQQMqgrWehLt3Q-vndNgAgEQgIIceS9dAMLeq1AKBwrDN5TQY4gNgYDbjJ5TgZIWaINiDgof7kP40GMNOaOAJKG_fk3O3n0hphWohj0964P0ICTTGaBHL8qeej_r1vwugGCx8J0jiQbGpEMX0ujPrsUo_AKgFkE4sUk_9MWZ2gobDpNP1kZ-akNB0R_87g5zmCRSwFnUgjRC6t45kCf4zsq4xPTYkL1QCQNXdEPk3WmarvEwgeCzvP8onePqPBIktDZK5ZC6XUq6Q4T3IlikvJoEUwyxn7OW2s-h3C6CKGxztBiTMy9au1PeH1aKV7t5V_LWhGqP9Xe2LzhZ2hTMA5iCcoMkbMWhZiOq65FOSajlzw3_1ivADCs5zMCzD_SXhKtDCQQuRFJmnXBGPzA89QcUcHhtdZIFkaA16p7dWv7SJ0Wkg0_mwxyGoL3jwZNQmtTiV2Uzv_aC2fmFpl6gPiDnBMwgpslWy8UHU4bhT7mTzbBPwGbHZculzN7bApBRAZbu6yWWuftwgDZiKU9XUciDucNnNiCWd8bhVW9i_g-rS6SEAHtfsQ9A-ghgTOCPXEN2f7dZT9HD9BSuhvKWPKoYeFYx67Xn1CbMDpKZnOb2qPgssbSZDoGhVVjTrirBakr8TKjEJMrDz53Sf5qRIPLOARYwbzTD48hk5YDJX4dPPoO3IJ2Uepe5-GIMmCv5rAyHwaA9DzxQ7_7fidTQU6EfQXGwgehQwBQsaU2T0zKOikANeFIZ2pEskKqffFftVgy345S0j_p7z60_39YZHEiX7wA-hssZq_BUaE8rDXWuU8y6MQ4DATET83QkyKa7e7rupJ6zZ-hOLLPmyN7GCfUovWBqOcjR_a_GJozQ)) (PDF). *Computer Journal*. 5 (3): 210–213. doi:10.1093/comjnl/5.3.210 (<https://doi.org/10.1093/comjnl/5.3.210>). Archived from the original ([https://watermark.silverchair.com/5-3-210.pdf?token=AQECAHi208BE49Ooan9kKhW\\_Ercy7Dm3ZL\\_9Cf3qfKAc485ysgAAAsYwggLCBgkqhkiG9w0BBWagggKzMIICrwiBADCCAqgGCSqGSIb3DQEHAATAeBgIghkgBZQMEAS4wEQQMqgrWehLt3Q-vndNgAgEQgIIceS9dAMLeq1AKBwrDN5TQY4gNgYDbjJ5TgZIWaINiDgof7kP40GMNOaOAJKG\\_fk3O3n0hphWohj0964P0ICTTGaBHL8qeej\\_r1vwugGCx8J0jiQbGpEMX0ujPrsUo\\_AKgFkE4sUk\\_9MWZ2gobDpNP1kZ-akNB0R\\_87g5zmCRSwFnUgjRC6t45kCf4zsq4xPTYkL1QCQNXdEPk3WmarvEwgeCzvP8onePqPBIktDZK5ZC6XUq6Q4T3IlikvJoEUwyxn7OW2s-h3C6CKGxztBiTMy9au1PeH1aKV7t5V\\_LWhGqP9Xe2LzhZ2hTMA5iCcoMkbMWhZiOq65FOSajlzw3\\_1ivADCs5zMCzD\\_SXhKtDCQQuRFJmnXBGPzA89QcUcHhtdZIFkaA16p7dWv7SJ0Wkg0\\_mwxyGoL3jwZNQmtTiV2Uzv\\_aC2fmFpl6gPiDnBMwgpslWy8UHU4bhT7mTzbBPwGbHZculzN7bApBRAZbu6yWWuftwgDZiKU9XUciDucNnNiCWd8bhVW9i\\_g-rS6SEAHtfsQ9A-ghgTOCPXEN2f7dZT9HD9BSuhvKWPKoYeFYx67Xn1CbMDpKZnOb2qPgssbSZDoGhVVjTrirBakr8TKjEJMrDz53Sf5qRIPLOARYwbzTD48hk5YDJX4dPPoO3IJ2Uepe5-GIMmCv5rAyHwaA9DzxQ7\\_7fidTQU6EfQXGwgehQwBQsaU2T0zKOikANeFIZ2pEskKqffFftVgy345S0j\\_p7z60\\_39YZHEiX7wA-hssZq\\_BUaE8rDXWuU8y6MQ4DATET83QkyKa7e7rupJ6zZ-hOLLPmyN7GCfUovWBqOcjR\\_a\\_GJozQ](https://watermark.silverchair.com/5-3-210.pdf?token=AQECAHi208BE49Ooan9kKhW_Ercy7Dm3ZL_9Cf3qfKAc485ysgAAAsYwggLCBgkqhkiG9w0BBWagggKzMIICrwiBADCCAqgGCSqGSIb3DQEHAATAeBgIghkgBZQMEAS4wEQQMqgrWehLt3Q-vndNgAgEQgIIceS9dAMLeq1AKBwrDN5TQY4gNgYDbjJ5TgZIWaINiDgof7kP40GMNOaOAJKG_fk3O3n0hphWohj0964P0ICTTGaBHL8qeej_r1vwugGCx8J0jiQbGpEMX0ujPrsUo_AKgFkE4sUk_9MWZ2gobDpNP1kZ-akNB0R_87g5zmCRSwFnUgjRC6t45kCf4zsq4xPTYkL1QCQNXdEPk3WmarvEwgeCzvP8onePqPBIktDZK5ZC6XUq6Q4T3IlikvJoEUwyxn7OW2s-h3C6CKGxztBiTMy9au1PeH1aKV7t5V_LWhGqP9Xe2LzhZ2hTMA5iCcoMkbMWhZiOq65FOSajlzw3_1ivADCs5zMCzD_SXhKtDCQQuRFJmnXBGPzA89QcUcHhtdZIFkaA16p7dWv7SJ0Wkg0_mwxyGoL3jwZNQmtTiV2Uzv_aC2fmFpl6gPiDnBMwgpslWy8UHU4bhT7mTzbBPwGbHZculzN7bApBRAZbu6yWWuftwgDZiKU9XUciDucNnNiCWd8bhVW9i_g-rS6SEAHtfsQ9A-ghgTOCPXEN2f7dZT9HD9BSuhvKWPKoYeFYx67Xn1CbMDpKZnOb2qPgssbSZDoGhVVjTrirBakr8TKjEJMrDz53Sf5qRIPLOARYwbzTD48hk5YDJX4dPPoO3IJ2Uepe5-GIMmCv5rAyHwaA9DzxQ7_7fidTQU6EfQXGwgehQwBQsaU2T0zKOikANeFIZ2pEskKqffFftVgy345S0j_p7z60_39YZHEiX7wA-hssZq_BUaE8rDXWuU8y6MQ4DATET83QkyKa7e7rupJ6zZ-hOLLPmyN7GCfUovWBqOcjR_a_GJozQ)) (PDF) on 2022-10-20. (4 pages)

5. Ball, John A. (1978). *Algorithms for RPN calculators* (<https://archive.org/details/algorithmsforrpn0000ball>) (1 ed.). Cambridge, Massachusetts, USA: Wiley-Interscience, John Wiley & Sons, Inc. ISBN 0-471-03070-8. "[...] In their advertisements and also in a letter to me, Hewlett-Packard Company (HP), the best known manufacturer of RPN calculators, says that RPN is based on a suggestion by Jan Łukasiewicz (1878–1956), and that RPN was invented and is patented by HP. Aside from the apparent contradiction in these two statements, I do not think that either of them is quite true. My first experience with RPN involved a nice old Friden EC-130 desktop electronic calculator, circa 1964. The EC-130 has RPN with a push-down stack of four registers, all visible simultaneously on a cathode ray tube display. Furthermore, they are shown upside down, that is, the last-in-first-out register is at the bottom. [...] Around 1966, the Monroe Epic calculator offered RPN with a stack of four, a printer, and either 14 or 42 step programmability. The instruction booklets with these two calculators make no mention of RPN or Jan Łukasiewicz. [...]"
6. Kennedy, John (August 1982). "RPN Perspective" (<http://citeseerx.ist.psu.edu/viewdoc/download?doi=10.1.1.90.6448&rep=rep1&type=pdf>). *PPC Calculator Journal*. Mathematics Department, Santa Monica College, Santa Monica, California, USA. **9** (5): 26–29. CiteSeerX 10.1.1.90.6448 (<https://citeseerx.ist.psu.edu/viewdoc/summary?doi=10.1.1.90.6448>). Archived (<https://web.archive.org/web/20220701223543/http://citeseerx.ist.psu.edu/viewdoc/download?doi=10.1.1.90.6448&rep=rep1&type=pdf>) from the original on 2022-07-01. Retrieved 2022-07-02. (12 pages)
7. Ceruzzi, Paul E. (April 1980). "1941 RPN Computer?" (<https://www.hpcalc.org/hp48/docs/columns/1941rpn.html>). *PPC Calculator Journal*. **7** (3): 25. Archived (<https://web.archive.org/web/20220701172806/https://www.hpcalc.org/hp48/docs/columns/1941rpn.html>) from the original on 2022-07-01. Retrieved 2022-07-01. "The interesting aspect of the programming of the Z-3 was that this code was very similar to that of, say, an HP-25. To perform an operation on two numbers, commands would first be given to recall the numbers from appropriate locations in the memory, followed by the command for the operation. Numbers were automatically positioned in registers in the Arithmetic Unit of the machine so that operations like division and subtraction would proceed in the right order. Results were left in a register in the AU so that long sequences of operations could be carried out. Thus, the Z-3 used a version of RPN that was nearly identical to that used by HP! I have obtained copies of early programs that Zuse had written for the evaluation of a  $5 \times 5$  determinant, and it is possible to run these programs on an HP-41C with almost no modification whatsoever (once the numbers have been placed in the storage registers beforehand). The AU of the Z-3 contained 3 registers, although Zuse never referred to them as a stack, of course. These registers were labelled "f", "a", and "b". All entrance and exit to and from the AU was through the "f" register. This is sort of like the display register of the 41C, which is distinct from the stack. Arithmetic operations were performed on numbers in the a and b registers, so these may be thought of as corresponding to the x and y registers of HP's. Unlike modern computer practice, the actual numbers themselves were moved around the registers, not just a pointer."
8. Ceruzzi, Paul E. (1983). "2. Computers in Germany" (<http://ed-thelen.org/comp-hist/Reckoners-ch-2.html>). *Reckoners - The prehistory of the digital computer, from relays to the stored program concept, 1935–1945* (<http://ed-thelen.org/comp-hist/Reckoners.html#TOC>). Contributions to the study of computer science. Vol. 1 (1 ed.). Westport, Connecticut, USA: Greenwood Press, Congressional Information Service, Inc. p. 0010. ISBN 0-313-23382-9. ISSN 0734-757X (<https://www.worldcat.org/issn/0734-757X>). LCCN 82-20980 (<https://lccn.loc.gov/82-20980>). Archived (<https://web.archive.org/web/20220701223948/http://ed-thelen.org/comp-hist/Reckoners-ch-2.html>) from the original on 2022-07-01. Retrieved 2022-07-02.

9. Rojas, Raúl (April–June 1997). "Konrad Zuse's Legacy: The Architecture of the Z1 and Z3" ([http://ed-thelen.org/comp-hist/Zuse\\_Z1\\_and\\_Z3.pdf](http://ed-thelen.org/comp-hist/Zuse_Z1_and_Z3.pdf)) (PDF). *IEEE Annals of the History of Computing*. **19** (2): 5–16 [7–8]. doi:10.1109/85.586067 (<https://doi.org/10.1109%2F85.586067>). Archived ([https://web.archive.org/web/20220703082408/http://ed-thelen.org/comp-hist/Zuse\\_Z1\\_and\\_Z3.pdf](https://web.archive.org/web/20220703082408/http://ed-thelen.org/comp-hist/Zuse_Z1_and_Z3.pdf)) (PDF) from the original on 2022-07-03. Retrieved 2022-07-03. (12 pages)
10. Zuse, Horst. "2. Dialogfähigkeit der Maschine Z3". Written at Berlin, Germany. In Cremers, Armin B.; Manthey, Rainer; Martini, Peter; Steinhage, Volker (eds.). *Die ergonomischen Erfindungen der Zuse-Maschinen* (<https://subs.emis.de/LNI/Proceedings/Proceedings67/GI-Proceedings.67-42.pdf>) (PDF). INFORMATIK 2005 Informatik LIVE! Band 1, Beiträge der 35. Jahrestagung der Gesellschaft für Informatik e.V. (GI), 19. bis 22. September 2005 in Bonn. Lecture Notes in Informatics (in German). Bonn, Germany: Gesellschaft für Informatik (GI). pp. 200–204 [200–201]. Archived (<https://web.archive.org/web/20220701225356/https://subs.emis.de/LNI/Proceedings/Proceedings67/GI-Proceedings.67-42.pdf>) (PDF) from the original on 2022-07-01. Retrieved 2022-07-02. p. 201: "Dazu stehen die beiden Register R1 und R2 als Kurzspeicher für die Operanden der arithmetischen Operationen zur Verfügung. Gerechnet wird in der umgekehrten polnischen Notation, wie z.B. beim Taschenrechner HP 45 (1972) oder HP11 (1998)." (5 pages)
11. Zuse, Horst, ed. (2008-02-22). "Z3 im Detail" (<http://www.horst-zuse.homepage.t-online.de/z3-detail.html>) [Z3 in details]. *Professor Dr.-Ing. habil. Horst Zuse* (in German). Archived (<https://web.archive.org/web/20220701173220/http://www.horst-zuse.homepage.t-online.de/z3-detail.html>) from the original on 2022-07-01. Retrieved 2022-07-01. "Die Z3 konnte in zwei Betriebsmodi betrieben werden, und zwar in dem Programm- und Dialogmodus. Das Rechnen im Dialog erfolgt wie mit einem Taschenrechner in der umgekehrten polnischen Notation." [1] (<https://web.archive.org/web/20220701173354/http://www.horst-zuse.homepage.t-online.de/foto-scout-zuse-internet-html/fsz-readme-1.pdf>)
12. Bonten, Jo H. M. (2009-05-28) [2009-03-08]. "Fast Calculators: Konrad Zuse's Z1 and Z3" (<http://home.kpn.nl/jhm.bonten/computers/bitsandbytes/wordsizes/crayzuse.htm>). Geldrop, Netherlands. Archived (<https://web.archive.org/web/20220701230722/http://home.kpn.nl/jhm.bonten/computers/bitsandbytes/wordsizes/crayzuse.htm>) from the original on 2022-07-01. Retrieved 2022-07-02. "The computer can be used as a simple hand-held calculator. In this mode besides entering the numeric values the user must enter the instructions and the addresses by pressing their keys. He has to enter the numbers and operators in the reverse Polish notation."
13. Bundesmann, Jan (June 2016). "Zum 75. Geburtstag von Konrad Zuses Z3: Ratterkasten" (<https://www.heise.de/select/ix/2016/6/1464579381888967#literaturverzeichnis>). Report / Jubiläum. *ix* (in German). Vol. 2016, no. 6. Heise Verlag. p. 94. Archived (<https://web.archive.org/web/20220701174549/https://www.heise.de/select/ix/2016/6/1464579381888967#literaturverzeichnis>) from the original on 2022-07-01. Retrieved 2022-07-01. "Zum Eingeben der Zahlen stand eine Tastatur bereit (Dezimalzahlen, Gleitkommadarstellung). Anweisungen gaben Nutzer in umgekehrter polnischer Notation: zuerst die Argumente, um Register zu befüllen, dann der auszuführende Operator."
14. "Die Computerwelt von Konrad Zuse - Auf den Spuren eines EDV-Genies" (<https://www.weltderfertigung.de/assets/konrad-zuse-museum.pdf>) (PDF). Die Welt der technischen Museen. *Welt der Fertigung* (in German). Vol. 2018, no. 2. 2018. pp. 32–35. ISSN 2194-9239 (<https://www.worldcat.org/issn/2194-9239>). Archived (<https://web.archive.org/web/20191017185004/http://weltderfertigung.de/assets/konrad-zuse-museum.pdf>) (PDF) from the original on 2019-10-17. Retrieved 2022-07-02. pp. 32–33: "Er hat wohl auch als erster die vom polnischen Mathematiker Jan Lukasiewicz entwickelte ›polnische Notation‹ weiterentwickelt und daraus die ›umgekehrte polnische Notation‹ (UPN) ersonnen, da diese in seinen Rechnern verwendet wird: zunächst werden die Werte eingegeben, danach die gewünschte Rechenoperation ausgelöst. Klammern werden auf diese Weise vermieden." (4 pages)



15. Tremmel, Sylvester (2021-11-21). "Computergeschichte: Zuse Z3 "im Test" " (<https://www.heise.de/tests/Computergeschichte-Zuse-Z3-im-Test-6275272.html>). *c't magazin*. Heise Verlag. Archived (<https://web.archive.org/web/20220301130344/https://www.heise.de/tests/Computergeschichte-Zuse-Z3-im-Test-6275272.html>) from the original on 2022-03-01. Retrieved 2022-07-01. "Über die I/O-Einheit kann man die Z3 als reine Rechenmaschine einsetzen, Operationen nimmt sie dann in der praktischen – wenn auch gewöhnungsbedürftigen – umgekehrten polnischen Notation entgegen. Werte im Speicher ablegen (oder von dort laden) kann man so allerdings nicht."
16. Burks, Arthur Walter; Warren, Don W.; Wright, Jesse B. (1954). "An Analysis of a Logical Machine Using Parenthesis-Free Notation". *Mathematical Tables and Other Aids to Computation*. **8** (46): 53–57. doi:10.2307/2001990 (<https://doi.org/10.2307%2F2001990>). JSTOR 2001990 (<https://www.jstor.org/stable/2001990>).
17. Hamblin, Charles Leonard (May 1957). *An Addressless Coding Scheme based on Mathematical Notation* (Typescript). New South Wales University of Technology.
18. Hamblin, Charles Leonard (June 1957). "An addressless coding scheme based on mathematical notation". *Proceedings of the First Australian Conference on Computing and Data Processing*. Salisbury, South Australia: Weapons Research Establishment.
19. Hamblin, Charles Leonard (1957). "Computer Languages". *The Australian Journal of Science* (20?): 135–139; Hamblin, Charles Leonard (November 1985). "Computer Languages". *The Australian Computer Journal* (Reprint). **17** (4): 195–198.
20. Hamblin, Charles Leonard (1958). *GEORGE IA and II: A semi-translation programming scheme for DEUCE: Programming and Operation Manual* (<http://members.iinet.net.au/~dgreen/deuce/GEORGEProgrammingManual.pdf>) (PDF). School of Humanities, University of New South Wales, Kensington, New South Wales. Archived ([https://web.archive.org/web/20200404093021/http://members.iinet.net.au/~dgreen/deuce/GEORGEProgrammingManual.p](https://web.archive.org/web/20200404093021/http://members.iinet.net.au/~dgreen/deuce/GEORGEProgrammingManual.pdf)[df](http://members.iinet.net.au/~dgreen/deuce/GEORGEProgrammingManual.pdf)) (PDF) from the original on 2020-04-04. Retrieved 2020-07-27.
21. McBurney, Peter (2008-12-06). "Charles L. Hamblin and his work" (<https://web.archive.org/web/20081206093044/http://www.csc.liv.ac.uk/~peter/hamblin.html>). Archived from the original (<http://www.csc.liv.ac.uk/~peter/hamblin.html>) on 2008-12-06.
22. McBurney, Peter (2008-07-27). "Charles L. Hamblin: Computer Pioneer" (<https://web.archive.org/web/20081207005233/http://www.csc.liv.ac.uk/~peter/this-month/this-month-3-030303.html>). Archived from the original (<http://www.csc.liv.ac.uk/~peter/this-month/this-month-3-030303.html>) on 2008-12-07. "[...] Hamblin soon became aware of the problems of (a) computing mathematical formulae containing brackets, and (b) the memory overhead in having dealing with memory stores each of which had its own name. One solution to the first problem was Jan Łukasiewicz's Polish notation, which enables a writer of mathematical notation to instruct a reader the order in which to execute the operations (e.g. addition, multiplication, etc) without using brackets. Polish notation achieves this by having an operator (+, ×, etc) precede the operands to which it applies, e.g., +ab, instead of the usual, a+b. Hamblin, with his training in formal logic, knew of Łukasiewicz's work. [...]"
23. Osborne, Thomas E. (2010) [1994]. "Tom Osborne's Story in His Own Words" ([http://www.hp9825.com/html/osborne\\_s\\_story.html](http://www.hp9825.com/html/osborne_s_story.html)). Steve Leibson. Archived ([https://web.archive.org/web/20220404222610/http://www.hp9825.com/html/osborne\\_s\\_story.html](https://web.archive.org/web/20220404222610/http://www.hp9825.com/html/osborne_s_story.html)) from the original on 2022-04-04. Retrieved 2016-01-01. "[...] I changed the architecture to use RPN (Reverse Polish Notation), which is the ideal notation for programming environment in which coding efficiency is critical. In the beginning, that change was not well received... [...]"
24. Peterson, Kristina (2011-05-04). "Wall Street's Cult Calculator Turns 30" (<https://web.archive.org/web/20150316030830/https://www.wsj.com/articles/SB10001424052748703841904576257440326458056>). *The Wall Street Journal*. Archived from the original (<https://www.wsj.com/articles/SB10001424052748703841904576257440326458056>) on 2015-03-16. Retrieved 2015-12-06.

25. Kasprzyk, Dennis Michael; Drury, Colin G.; Bialas, Wayne F. (1979) [1978-09-25]. "Human behaviour and performance in calculator use with Algebraic and Reverse Polish Notation". *Ergonomics*. Department of Industrial Engineering, State University of New York at Buffalo, Amherst, New York, USA: Taylor & Francis. **22** (9): 1011–1019. doi:10.1080/00140137908924675 (<https://doi.org/10.1080%2F00140137908924675>).
26. Agate, Seb J.; Drury, Colin G. (March 1980). "Electronic calculators: which notation is the better?" (<https://vdocuments.mx/electronic-calculators-which-notation-is-the-better.html>). *Applied Ergonomics*. Department of Industrial Engineering, University at Buffalo, State University of New York, USA: IPC Business Press. **11** (1): 2–6. doi:10.1016/0003-6870(80)90114-3 (<https://doi.org/10.1016%2F0003-6870%2880%2990114-3>). PMID 15676368 (<https://pubmed.ncbi.nlm.nih.gov/15676368>). 0003-6870/80/01 0002-05. Archived (<https://archive.today/2018.09.22-202441/https://vdocuments.mx/electronic-calculators-which-notation-is-the-better.html>) from the original on 2018-09-22. Retrieved 2018-09-22. "In terms of practical choice between calculators, it would appear that RPN is faster and more accurate overall but particularly for more complex problems." (5 pages)
27. Hoffman, Errol; Ma, Patrick; See, Jason; Yong, Chee Kee; Brand, Jason; Poulton, Matthew (1994). "Calculator logic: when and why is RPN superior to algebraic?". *Applied Ergonomics*. **25** (5): 327–333. doi:10.1016/0003-6870(94)90048-5 (<https://doi.org/10.1016%2F0003-6870%2894%2990048-5>).
28. "Rechenhilfe für Ingenieure" (<https://web.archive.org/web/20090213222711/http://www2.tu-berlin.de/alumni/parTU/00dez/zuse.htm>). *Alumni-Magazin der Technischen Universität Berlin* (in German). Vol. 2, no. 3. Technische Universität Berlin. December 2000. Archived from the original (<http://www2.tu-berlin.de/alumni/parTU/00dez/zuse.htm>) on 2009-02-13.
29. "An einem 12. Mai" (<https://web.archive.org/web/20130530040033/http://www.dhm.de/gaeste/luise/tagesfakten/tf05/0512.htm>) (in German). Deutsches Historisches Museum (German Historical Museum). Archived from the original (<http://www.dhm.de/gaeste/luise/tagesfakten/tf05/0512.htm>) on 2013-05-30.
30. Blaauw, Gerrit Anne; Brooks, Jr., Frederick Phillips (1997). *Computer architecture: Concepts and evolution*. Boston, Massachusetts, USA: Addison-Wesley Longman Publishing Co., Inc.
31. LaForest, Charles Eric (April 2007). "2.1 Lukasiewicz and the First Generation: 2.1.2 Germany: Konrad Zuse (1910–1995); 2.2 The First Generation of Stack Computers: 2.2.1 Zuse Z4". *Second-Generation Stack Computer Architecture* ([http://fpgacpu.ca/publications/Second-Generation\\_Stack\\_Computer\\_Architecture.pdf](http://fpgacpu.ca/publications/Second-Generation_Stack_Computer_Architecture.pdf)) (PDF) (thesis). Waterloo, Canada: University of Waterloo. pp. 8, 11. Archived ([https://web.archive.org/web/20220120155616/http://fpgacpu.ca/publications/Second-Generation\\_Stack\\_Computer\\_Architecture.pdf](https://web.archive.org/web/20220120155616/http://fpgacpu.ca/publications/Second-Generation_Stack_Computer_Architecture.pdf)) (PDF) from the original on 2022-01-20. Retrieved 2022-07-02. (178 pages)

32. Beard, Bob (Autumn 1997) [1996-10-01]. "The KDF9 Computer — 30 Years On" (<http://www.cs.man.ac.uk/CCS/Archive/Resurrection/pdf/res18.pdf>) (PDF). *Resurrection - The Bulletin of the Computer Conservation Society*. No. 18. Computer Conservation Society (CCS). pp. 7–15. ISSN 0958-7403 (<https://www.worldcat.org/issn/0958-7403>). Archived (<https://web.archive.org/web/20200727140754/http://www.cs.man.ac.uk/CCS/Archive/Resurrection/pdf/res18.pdf>) (PDF) from the original on 2020-07-27. Retrieved 2020-07-27. "[...] The KDF9 is remarkable because it is believed to be the first zero-address instruction format computer to have been announced (in 1960). It was first delivered at about the same time (early 1963) as the other famous zero-address computer, the **Burroughs B5000** in America. Like many modern pocket calculators, a zero-address machine allows the use of Reverse Polish arithmetic; this offers certain advantages to compiler writers. It is believed that the attention of the English Electric team was first drawn to the zero-address concept through contact with **George** (General Order Generator), an autocode programming system written for a **Deuce** computer by the **University of Sydney**, Australia, in the latter half of the 1950s. George used Reversed Polish, and the KDF9 team were attracted to this convention for the pragmatic reason of wishing to enhance performance by minimising accesses to main store. This may be contrasted with the more "theoretical" line taken independently by **Burroughs**. Besides a hardware **nesting store** or stack - the basic mechanism of a zero-address computer - the KDF9 had other groups of central registers for improving performance which gave it an interesting internal structure. [...]" [2] (<https://web.archive.org/web/20200427075718/http://www.cs.man.ac.uk/CCS/res/res18.htm#c>) (NB. This is an edited version of a talk given to North West Group of the Society at the Museum of Science and Industry, Manchester, UK on 1996-10-01.)
33. Galler, Bernard A.; Rosin, Robert F., eds. (1986) [1985-09-06]. *The Burroughs B 5000 Conference - OH 98* (<https://web.archive.org/web/20120422070048/http://conservancy.umn.edu/bitstream/107105/1/oh098b5c.pdf>) (PDF). Marina Del Ray Hotel, Marina Del Ray, California, USA: Charles Babbage Institute, The Center for the History of Information Processing, University of Minnesota, Minneapolis, USA. Archived from the original (<http://conservancy.umn.edu/bitstream/107105/1/oh098b5c.pdf>) (PDF) on 2012-04-22. Retrieved 2013-02-27. A New Approach to the Design of a Digital Computer (1961)
34. "The Burroughs B5000 Conference (1985)" (<http://special.lib.umn.edu/cbi/oh/pdf.phtml?id=21>). 2023-06-17. p. 49.
35. Galler, Bernard A.; Rosin, Robert F., eds. (1985-09-06). "Oral History: Burroughs B5000 Conference" (<http://purl.umn.edu/107105>). Marina del Rey, California, USA, archived by the Charles Babbage Institute, University of Minnesota, Minneapolis: **AFIPS / Burroughs Corporation**. OH 98.
36. "1928–2012 Obituary Condolences Robert (Bob) Ragen" (<http://www.legacy.com/obituaries/insidebayarea/obituary.aspx?n=robert-ragen-bob&pid=158717663>). *Legacy.com*. 2012-07-23. Archived (<https://web.archive.org/web/20171218233505/http://www.legacy.com/obituaries/name/robert-ragen-obituary?pid=1000000158717663>) from the original on 2017-12-18. Retrieved 2016-01-01. "[...] Bob holds over 80 patents awarded during his work as Director of RD for **Friden**, and **Singer** and as Senior Project Engineer at **Xerox**. He retired from Xerox RD in 1990. He is responsible for the development of the first commercial electronic calculator, the **Friden 130**, which has been displayed at the **Smithsonian**. [...]"
37. "Friden EC-130 Electronic Calculator" (<http://www.oldcalculatormuseum.com/friden130.html>). *www.oldcalculatormuseum.com*. 2020-08-09. Archived (<https://web.archive.org/web/20221020175311/https://www.oldcalculatormuseum.com/friden130.html>) from the original on 2022-10-20. Retrieved 2018-03-21.
38. "Friden EC-132 Electronic Calculator" (<http://www.oldcalculatormuseum.com/friden132.html>). *www.oldcalculatormuseum.com*. 2022-07-15. Archived (<https://web.archive.org/web/20221020175254/https://www.oldcalculatormuseum.com/friden132.html>) from the original on 2022-10-20. Retrieved 2018-03-21.

39. Monnier, Richard E. (September 1968). "A New Electronic Calculator with Computerlike Capabilities" (<http://www.hparchive.com/Journals/HPJ-1968-09.pdf>) (PDF). *Hewlett-Packard Journal*. Palo Alto, California, USA: Hewlett-Packard. 20 (1): 3–9. Archived (<https://web.archive.org/web/20221020175450/http://www.hparchive.com/Journals/HPJ-1968-09.pdf>) (PDF) from the original on 2022-10-20. Retrieved 2016-01-03.
40. "hp 9100A Calculator" (<http://archive.computerhistory.org/resources/text/HP/HP.9100A.1968.102646164.pdf>) (PDF) (marketing brochure). Hewlett-Packard. 1968. pp. 8–10. Archived (<https://web.archive.org/web/2021102222725/http://archive.computerhistory.org/resources/text/HP/HP.9100A.1968.102646164.pdf>) (PDF) from the original on 2021-10-22. Retrieved 2013-01-26.
41. *HP35 User's Manual*. Hewlett-Packard. p. i. "[...] The operational stack and reverse Polish (Łukasiewicz) notation used in the HP-35 are the most efficient way known to computer science for evaluating mathematical expressions. [...]"
42. *HP-42S RPN Scientific Calculator – Owner's Manual* (<http://www.hp41.net/forum/files/hp41net/manuel-hp42s-us.pdf>) (PDF) (1 ed.). Corvallis, Oregon, USA: Hewlett-Packard Co. June 1988. p. 3. 00042-90001. Archived (<https://web.archive.org/web/20170917215457/http://www.hp41.net/forum/files/hp41net/manuel-hp42s-us.pdf>) (PDF) from the original on 2017-09-17. Retrieved 2017-09-17.
43. "Section 3: The Automatic Memory Stack, LAST X, and Data Storage". *Hewlett-Packard HP-15C Owner's Handbook* (<http://www.hp.com/ctg/Manual/c03030589.pdf>) (PDF). 2.4. Hewlett-Packard Development Company, LP. September 2011. pp. 32–46. 00015-90001. Archived (<https://web.archive.org/web/20170917224935/http://www.hp.com/ctg/Manual/c03030589.pdf>) (PDF) from the original on 2017-09-17. Retrieved 2015-12-05.
44. Laporte, Jacques (2014-05-22). "The slide rule killer: a milestone in computer history" (<http://web.archive.org/web/20150211194800/http://jacques-laporte.org/HP%2035%20Saga.htm>). Archived from the original (<http://www.jacques-laporte.org/HP%2035%20Saga.htm>) on 2015-02-11. Retrieved 2016-01-01.
45. "HP Calculators" (<http://www8.hp.com/us/en/products/calculators/index.html>).
46. Nelson, Richard J. (April 2012). "HP RPN Evolves" (<http://h20331.www2.hp.com/hpsub/downloads/S07%20HP%20RPN%20Evolves%20V5b.pdf>) (PDF). *HP Solve*. Hewlett-Packard Development Company, L.P. (27): 42–45. Archived (<https://web.archive.org/web/20221020173134/http://h20331.www2.hp.com/hpsub/downloads/S07%20HP%20RPN%20Evolves%20V5b.pdf>) (PDF) from the original on 2022-10-20. Retrieved 2022-10-20. [3] ([https://web.archive.org/save/http://h20331.www2.hp.com/hpsub/downloads/HP\\_Calculator\\_eNL\\_04\\_April\\_2012%2520\(2\).pdf](https://web.archive.org/save/http://h20331.www2.hp.com/hpsub/downloads/HP_Calculator_eNL_04_April_2012%2520(2).pdf)) (4 of 56 pages)
47. <https://hpofficesupply.com/>
48. <https://hpcalcs.com/>
49. "A new standard!... The 7400 scientific & engineering calculator" (<https://worldradiohistory.com/Archive-Radio-Electronics/70s/1972/Radio-Electronics-1972-12.pdf>) (PDF). *Radio-Electronics - For men with ideas in electronics* (Advertisement). Vol. 43, no. 12. New York, USA: Gernsback Publications, Inc. December 1972. p. 17. Archived (<https://web.archive.org/web/20221228161558/https://worldradiohistory.com/Archive-Radio-Electronics/70s/1972/Radio-Electronics-1972-12.pdf>) (PDF) from the original on 2022-12-28. Retrieved 2022-12-28. p. 17: "DATA STORAGE: 2 Auxiliary Storage Registers plus up to 7 push-up Stack Registers. [...] 7400A 3 Registers Kit \$299.95 Assembled \$379.95 [...] 7400B 5 Registers Kit \$319.95 Assembled \$399.95 [...] 7400C 7 Registers Kit \$339.95 Assembled \$419.95"
50. Berger, Ivan (May 1973). "New calculator kits: From pocket minis to versatile desk models" (<https://books.google.com/books?id=htQDAAAAMBAJ&pg=PA151>). *Popular Mechanics*. Hearst Magazines: 152. Retrieved 2017-04-29.

51. "MITS 7400 Scientific/Engineering Calculator" (<http://www.oldcalculatormuseum.com/w-mits-7400.html>). Archived (<https://web.archive.org/web/20170430004710/http://www.oldcalculatormuseum.com/w-mits7400.jpg>) from the original on 2017-04-30. Retrieved 2017-04-30. (NB. Shows a photo of the MITS 7400, but the text erroneously refers to the later algebraic 7440 model instead of the 7400A/B/C models.)
52. Shirriff, Ken. "Reversing Sinclair's amazing 1974 calculator hack – half the ROM of the HP-35" ([http://files.righto.com/calculator/sinclair\\_scientific\\_simulator.html](http://files.righto.com/calculator/sinclair_scientific_simulator.html)). Archived ([https://web.archive.org/web/20220826223841/http://files.righto.com/calculator/sinclair\\_scientific\\_simulator.html](https://web.archive.org/web/20220826223841/http://files.righto.com/calculator/sinclair_scientific_simulator.html)) from the original on 2022-08-26. Retrieved 2013-12-09.
53. Sharwood, Simon (2013-09-02). "Google chap reverse engineers Sinclair Scientific Calculator" ([https://www.theregister.co.uk/2013/09/02/google\\_chap\\_reverse\\_engineers\\_sinclair\\_scientific\\_calculator/](https://www.theregister.co.uk/2013/09/02/google_chap_reverse_engineers_sinclair_scientific_calculator/)). *The Register*. Archived ([https://web.archive.org/web/20221020172550/https://www.theregister.com/2013/09/02/google\\_chap\\_reverse\\_engineers\\_sinclair\\_scientific\\_calculator/](https://web.archive.org/web/20221020172550/https://www.theregister.com/2013/09/02/google_chap_reverse_engineers_sinclair_scientific_calculator/)) from the original on 2022-10-20. Retrieved 2013-12-09.
54. *SR4921 RPN Reverse Notation Scientific Calculator Instruction Manual* (<http://www.wass.net/manuals/Commodore%20SR4921R.pdf>) (PDF). Palo Alto, California, USA: Commodore Business Machines, Inc. Archived (<https://web.archive.org/web/20170625231745/http://www.wass.net/manuals/Commodore%20SR4921R.pdf>) (PDF) from the original on 2017-06-25. Retrieved 2022-10-16.
55. "Prinztronic Program" (<http://www.vintagecalculators.com/html/program.html>). *www.vintagecalculators.com*. Retrieved 2018-03-21.
56. Elektronika B3-21 (<http://www.rskey.org/detail.asp?manufacturer=Elektronika&model=B3-21>) page on RSkey.org
57. Elektronika MK-161 (<http://www.rskey.org/detail.asp?manufacturer=Elektronika&model=MK-161>) page on RSkey.org
58. "Elektronika MK-61/52 and 152/161: small tech review (En) - Кон-Тики" (<http://arbinada.com/pmk/node/56>). *arbinada.com*. Retrieved 2018-03-21.
59. "НПП СЕМИКО - вычислительная техника и устройства автоматизации" (<http://mk.semico.ru/>). *mk.semico.ru*. Retrieved 2018-03-21.
60. Geschke, Charles (1986) [1985]. Preface. *PostScript Language Tutorial and Cookbook* (<https://archive.org/details/postscriptlangua00adobrich>). By Adobe Systems Incorporated (27th printing, August 1998, 1st ed.). Addison Wesley Publishing Company. ISBN 0-201-10179-3. 9-780201-101799. (NB. This book is informally called "blue book" due to its blue cover.)
61. Adobe Systems Incorporated (February 1999) [1985]. *PostScript Language Reference Manual* (<https://www.adobe.com/products/postscript/pdfs/PLRM.pdf>) (PDF) (1st printing, 3rd ed.). Addison-Wesley Publishing Company. ISBN 0-201-37922-8. Archived (<https://web.archive.org/web/20170218093716/https://www.adobe.com/products/postscript/pdfs/PLRM.pdf>) (PDF) from the original on 2017-02-18. Retrieved 2017-02-18. (NB. This book is informally called "red book" due to its red cover.)
62. Born, Günter [in German] (December 2000). "Kapitel 1. LOTUS 1-2-3-Format (WKS/WK1)" [Chapter 1. Lotus 1-2-3 WKS/WK1 format]. *Dateiformate – Eine Referenz – Tabellenkalkulation, Text, Grafik, Multimedia, Sound und Internet* (<http://www.aboutvb.de/bas/formate/pdf/wks.pdf>) [*File formats – a reference – spreadsheets, text, graphics, multimedia, sound and internet*] (PDF) (in German). Bonn, Germany: Galileo Computing. ISBN 3-934358-83-7. Archived (<https://web.archive.org/web/20161129191803/http://www.aboutvb.de/bas/formate/pdf/wks.pdf>) (PDF) from the original on 2016-11-29. Retrieved 2016-11-28.

63. Born, Günter [in German] (December 2000). "Kapitel 2. LOTUS 1-2-3-Format (WK3)" [Chapter 2. Lotus 1-2-3 WK3 format]. *Dateiformate – Eine Referenz – Tabellenkalkulation, Text, Grafik, Multimedia, Sound und Internet* (<http://www.aboutvb.de/bas/formate/pdf/wk3.pdf>) [*File formats – a reference – spreadsheets, text, graphics, multimedia, sound and internet*] (PDF) (in German). Bonn, Germany: Galileo Computing. ISBN 3-934358-83-7. Archived (<http://web.archive.org/web/20161129183043/http://www.aboutvb.de/bas/formate/pdf/wk3.pdf>) (PDF) from the original on 2016-11-29. Retrieved 2016-11-28.
64. Feichtinger, Herwig (1987). *Arbeitsbuch Mikrocomputer* (in German) (2 ed.). Munich, Germany: Franzis-Verlag GmbH. pp. 427–428. ISBN 3-7723-8022-0. (NB. According to this book, a 4 KB compiler was available from Lifeboat Software for CP/M.)
65. Wostrack, Gustav (January 1989). *RPNL. Eine FORTH ähnliche Sprache mit strukturunterstützenden Sprachkonstrukten* (in German). Wolf-Detlef Luther, Gens. ISBN 978-3-88707022-9.
66. "Katharina & Paul Wilkins' Home Page" (<http://lashwhip.com/grpn.html>). *lashwhip.com*. Retrieved 2018-03-21.
67. "galculator - a GTK 2 / GTK 3 algebraic and RPN calculator" (<https://galculator.sourceforge.net>). *galculator.sourceforge.net*. Retrieved 2018-03-21.
68. Schrijver, Frans. "Home - mouseless Stack-Calculator" (<http://www.stack-calculator.com/>). *www.stack-calculator.com*. Retrieved 2018-03-21.
69. Dietrich, Johannes W. (2019-07-24). "TRURL RPN Engine" (<https://zenodo.org/record/3350851>). *Zenodo*. doi:10.5281/zenodo.3257689 (<https://doi.org/10.5281%2Fzenodo.3257689>). Retrieved 2022-07-02.

## Further reading

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- "Everything you've always wanted to know about RPN but were afraid to pursue – Comprehensive manual for scientific calculators – Corvus 500 – APF Mark 55 – OMRON 12-SR and others" (<http://www.wass.net/manuals/Everything%20RPN.pdf>) (PDF). T. K. Enterprises. 1976. Archived (<https://web.archive.org/web/20170624162722/http://www.wass.net/manuals/Everything%20RPN.pdf>) (PDF) from the original on 2017-06-24. Retrieved 2017-06-24. (NB. The book's cover title contains a typographical error reading "APS Mark 55" instead of the correct "APF Mark 55".)
- "Advanced Calculator Logic HP RPN/Algebraic: A Comparative Analysis" (<http://archive.computerhistory.org/resources/access/text/2016/05/102651973-05-01-acc.pdf>) (PDF). Corvallis, Oregon, USA: Hewlett-Packard Corporation. 1979. 5953-1930. Archived (<https://web.archive.org/web/20221226175054/http://archive.computerhistory.org/resources/access/text/2016/05/102651973-05-01-acc.pdf>) (PDF) from the original on 2022-12-26. Retrieved 2022-12-26. (13 pages)
- Kreifeldt, John G.; McCarthy, Mary E. (1981-10-15) [1981-06-16/18]. Written at Department of Engineering Design, Tufts University, Medford, Massachusetts, USA. *Interruption as a test of the user-computer interface* (<https://ntrs.nasa.gov/api/citations/19820005848/downloads/19820005848.pdf>) (PDF). Proceedings of the Seventeenth Annual Conference on Manual Control. University of California, Los Angeles, California, USA: Jet Propulsion Laboratory / Office of Naval Research / NASA. pp. 655–667. 02155, N82-13721, 82N13721, 19820005848, JPL 81-95. Archived (<https://web.archive.org/web/20220130063534/https://ntrs.nasa.gov/api/citations/19820005848/downloads/19820005848.pdf>) (PDF) from the original on 2022-01-30. Retrieved 2018-09-22. [4] (<https://books.google.com/books?id=dHZKAQAAMAAJ>) (13 of 702 pages)
- Kreifeldt, John G. (October 1981). "Hand Calculator Performance Under Interrupted Operation". *Proceedings of the Human Factors Society Annual Meeting*. Department of Engineering Design, Tufts University, Medford, Massachusetts, USA. **25** (1): 329–332.

doi:10.1177/107118138102500187 (<https://doi.org/10.1177%2F107118138102500187>).  
S2CID 106904297 (<https://api.semanticscholar.org/CorpusID:106904297>). (4 pages)

- Hicks, David G. (2013) [1995]. "What is RPN?" (<http://hpmuseum.org/rpn.htm>). The Museum of HP Calculators (MoHPC). Archived (<https://web.archive.org/web/20170624165003/http://hpmuseum.org/rpn.htm>) from the original on 2017-06-24. Retrieved 2015-09-12.
- Redin, James (2005-02-12) [1997-10-05]. "RPN or DAL? A brief analysis of Reverse Polish Notation against Direct Algebraic Logic" ([http://www.xnumber.com/xnumber/rpn\\_or\\_adl.htm](http://www.xnumber.com/xnumber/rpn_or_adl.htm)). Archived ([https://web.archive.org/web/20170624164945/http://www.xnumber.com/xnumber/rpn\\_or\\_adl.htm](https://web.archive.org/web/20170624164945/http://www.xnumber.com/xnumber/rpn_or_adl.htm)) from the original on 2017-06-24. Retrieved 2015-09-12.
- Brown, Bob (2015-06-05) [April 2001]. "Postfix Notation Mini-Lecture" ([http://bbrown.kennesaw.edu/web\\_lectures/postfix/](http://bbrown.kennesaw.edu/web_lectures/postfix/)). Information Technology Department, College of Computing and Software Engineering, Kennesaw State University. Archived ([https://web.archive.org/web/20170624164922/http://ksuweb.kennesaw.edu/faculty/rbrow211/web\\_lectures/postfix/](https://web.archive.org/web/20170624164922/http://ksuweb.kennesaw.edu/faculty/rbrow211/web_lectures/postfix/)) from the original on 2017-06-24. Retrieved 2015-09-12.
- Wirth, Niklaus (2005-06-15) [2005-02-02]. "Good Ideas, Through the Looking Glass" ([http://www.inf.ethz.ch/~wirth/Articles/GoodIdeas\\_origFig.pdf](http://www.inf.ethz.ch/~wirth/Articles/GoodIdeas_origFig.pdf)) (PDF). Zürich, Switzerland. Archived ([https://web.archive.org/web/20170624164254/https://www.inf.ethz.ch/personal/wirth/Articles/GoodIdeas\\_origFig.pdf](https://web.archive.org/web/20170624164254/https://www.inf.ethz.ch/personal/wirth/Articles/GoodIdeas_origFig.pdf)) (PDF) from the original on 2017-06-24. Retrieved 2015-09-12.
- Vanderbeek, Greg (June 2007). *Order of Operations and RPN* (<https://digitalcommons.unl.edu/cgi/viewcontent.cgi?article=1045&context=mathmidexppap>) (Expository paper). Master of Arts in Teaching (MAT) Exam Expository Papers. Lincoln, USA: University of Nebraska. Paper 46. Archived (<https://web.archive.org/web/20200614191608/https://digitalcommons.unl.edu/cgi/viewcontent.cgi?article=1045&context=mathmidexppap>) from the original on 2020-06-14. Retrieved 2020-06-14.
- Klaver, Hans (2014). "RPN Tutorial, incl. some things HP did not tell" (<http://hansklav.home.xs4all.nl/rpn/index.html>). Archived (<https://web.archive.org/web/20170624165035/https://hansklav.home.xs4all.nl/rpn/index.html>) from the original on 2017-06-24. Retrieved 2015-09-12.

## External links

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- Rosettacode.org ([http://rosettacode.org/wiki/Parsing/RPN\\_calculator\\_algorithm](http://rosettacode.org/wiki/Parsing/RPN_calculator_algorithm)) providing many implementations in several programming languages.
  - <http://rpn.codeplex.com/> Implementation of RPN with custom functions support and flexible list of operators.
  - [https://xrjunque.nom.es/ConvertAlg2RPN\\_RPL.aspx](https://xrjunque.nom.es/ConvertAlg2RPN_RPL.aspx) Free online Algebraic expression to RPN Converter
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