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1 GeNN Documentation

GeNN is a software package to enable neuronal network simulations on NVIDIA GPUs by code generation. Models are defined in a simple C-style API and the code for running them on either GPU or CPU hardware is generated by GeNN. GeNN can also be used through external interfaces. Currently there are interfaces for SpineML and SpineCreator, a Python interface (PyGeNN) and a Brian interface via Brian2GeNN.

GeNN is currently developed and maintained by

```
Dr James Knight (contact James)

James Turner (contact James)

Prof. Thomas Nowotny (contact Thomas)
```

Project homepage is http://genn-team.github.io/genn/.

The development of GeNN is partially supported by the EPSRC (grant numbers EP/P006094/1 - Brains on Board and EP/J019690/1 - Green Brain Project).

Note

This documentation is under construction. If you cannot find what you are looking for, please contact the project developers.

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2 Installation

You can download GeNN either as a zip file of a stable release or a snapshot of the most recent stable version or the unstable development version using the Git version control system.

2.1 Downloading a release

Point your browser to https://github.com/genn-team/genn/releases and download a release from the list by clicking the relevant source code button. Note that GeNN is only distributed in the form of source code due to its code generation design. Binary distributions would not make sense in this framework and hence are not provided. After downloading continue to install GeNN as described in the Installing GeNN section below.

2.2 Obtaining a Git snapshot

If it is not yet installed on your system, download and install Git (http://git-scm.com/). Then clone the GeNN repository from Github

```
git clone https://github.com/genn-team/genn.git
```

The github url of GeNN in the command above can be copied from the HTTPS clone URL displayed on the GeNN Github page (https://github.com/genn-team/genn).

This will clone the entire repository, including all open branches. By default git will check out the master branch which contains the source version upon which the next release will be based. There are other branches in the repository that are used for specific development purposes and are opened and closed without warning.

An alternative to using git is to download the full content of GeNN sources by clicking on the "Download ZIP" button on the bottom right of the GeNN Github page (https://github.com/genn-team/genn).

2.3 Installing GeNN

Installing GeNN comprises a few simple steps to create the GeNN development environment:

Note

While GeNN models are normally simulated using CUDA on NVIDIA GPUs, if you want to use GeNN on a machine without an NVIDIA GPU, you can skip steps v and vi and use GeNN in "CPU ONLY" mode.

- (i) If you have downloaded a zip file, unpack GeNN.zip in a convenient location. Otherwise enter the directory where you downloaded the Git repository.
- (ii) Add GeNN's "bin" directory to your path, e.g. if you are running Linux or Mac OS X and extracted/downloaded GeNN to \$HOME/GeNN, then you can add:

```
export PATH=$PATH:$HOME/GeNN/bin
```

to your login script (e.g. .profile or .bashrc). If you are using WINDOWS, the path should be a windows path as it will be interpreted by the Visual C++ compiler cl, and environment variables are best set using SETX in a Windows cmd window. To do so, open a Windows cmd window by typing cmd in the search field of the start menu, followed by the enter key. In the cmd window type:

```
setx PATH "C:\Users\me\GeNN\bin;%PATH%"
```

where C:\Users\me\GeNN is the path to your GeNN directory.

- (iv) Install the C++ compiler on the machine, if not already present. For Windows, download Microsoft Visual Studio Community Edition from https://www.visualstudio.com/en-us/downloads/download-visual-studio-vs.←aspx. When installing Visual Studio, one should select the 'Desktop development with C++' configuration and the 'Windows 8.1 SDK' and 'Windows Universal CRT' individual components. Mac users should download and set up Xcode from https://developer.apple.com/xcode/index.html, Linux users should install the GNU compiler collection gcc and g++ from their Linux distribution repository, or alternatively from https://gcc.←gnu.org/index.html
- (v) If your machine has a GPU and you haven't installed CUDA already, obtain a fresh installation of the NVIDIA CUDA toolkit from https://developer.nvidia.com/cuda-downloads Again, be sure to pick CUDA and C++ compiler versions which are compatible with each other. The latest C++ compiler need not necessarily be compatible with the latest CUDA toolkit.
- (vi) Set the CUDA_PATH variable if it is not already set by the system, by putting

```
export CUDA_PATH=/usr/local/cuda
```

in your login script (or, if CUDA is installed in a non-standard location, the appropriate path to the main CUDA directory). For most people, this will be done by the CUDA install script and the default value of /usr/local/cuda is fine. In Windows, usually CUDA_PATH is already set after installing the CUDA toolkit. If not, set this variable with:

```
setx CUDA_PATH C:\path\to\cuda
```

This normally completes the installation. Windows users must close and reopen their command window to ensure variables set using SETX are initialised.

If you are using GeNN in Windows, the Visual Studio development environment must be set up within every instance of the CMD.EXE command window used. One can open an instance of CMD.EXE with the development environment already set up by navigating to Start - All Programs - Microsoft Visual Studio - Visual Studio Tools - x64 Native Tools Command Prompt. You may also wish to create a shortcut for this tool on the desktop, for convenience.

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3 Quickstart 3

3 Quickstart

GeNN is based on the idea of code generation for the code that simulates neuronal network models, either on GPU or CPU. At the same time, it leaves a lot of freedom to users, how to use the generated code in their final applications. To facilitate the use of GeNN on the background of this philosophy, it comes with a number of complete examples containing both the model description code that is used by GeNN for code generation and the "user side code" to run the generated model as well as save the results. Some of the example models, such as the Insect olfaction model, uses a <code>generate_run</code> executable which automates the building and simulation of the model. Using these executables, running the complete examples should be achievable in a few minutes. The necessary steps are described below.

3.1 Running an Example Model

3.1.1 Unix

In order to build the <code>generate_run</code> executable as well as other additional tools required for the model, open a shell and navigate to the <code>userproject/MBody1_project</code> directory. Then type

make

to generate an executable that you can invoke with

```
./generate_run test1
```

or, if you don't have an NVIDIA GPU and are running GeNN in CPU_ONLY mode, you can instead invoke this executable with

```
./generate_run --cpu-only test1
```

3.1.2 Windows

While GeNN can be used from within Visual Studio, in this example we will use a cmd window. Open a Visual Studio cmd window via Start: All Programs: Visual Studio: Tools: x86 Native Tools Command Prompt, and navigate to the userproject\tools directory. Then compile the additional tools and the generate_run executable for creating and running the project:

to generate an executable that you can invoke with

```
generate_run test1
```

or, if you don't have an NVIDIA GPU and are running GeNN in CPU_ONLY mode, you can instead invoke this executable with

```
generate_run --cpu-only test1
```

3.1.3 Visualising results

These steps build and simulate a model of the locust olfactory system with default parameters of 100 projection neurons, 1000 Kenyon cells, 20 lateral horn interneurons and 100 output neurons in the mushroom body lobes.

Note

If the model isn't build in CPU_ONLY mode it will be simulated on an automatically chosen GPU.

The generate_run executable generates input patterns and writes them to a file, compiles and runs the model using these files as inputs and finally output the resulting spiking activity. For more information of the options passed to this command see the Insect olfaction model section. The results of the simulation can be plotted with

```
python plot.py test1
```

The MBody1 example is already a highly integrated example that showcases many of the features of GeNN and how to program the user-side code for a GeNN application. More details in the User Manual .

3.2 How to use GeNN for New Projects

Creating and running projects in GeNN involves a few steps ranging from defining the fundamentals of the model, inputs to the model, details of the model like specific connectivity matrices or initial values, running the model, and analyzing or saving the data.

GeNN code is generally created by passing the C++ model file (see below) directly to the genn-buildmodel script. Another way to use GeNN is to create or modify a script or executable such as $userproject/MBody1_\leftrightarrow project/generate_run.cc$ that wraps around the other programs that are used for each of the steps listed above. In more detail, the GeNN workflow consists of:

- 1. Either use external programs to generate connectivity and input files to be loaded into the user side code at runtime or generate these matrices directly inside the user side code.
- 2. Generating the model simulation code using <code>genn-buildmodel.sh</code> (On Linux or Mac) or <code>genn-buildmodel.bat</code> (on Windows). For example, inside the <code>generate_run</code> engine used by the MBody1_project, the following command is executed on Linux:

```
genn-buildmodel.sh MBodyl.cc
```

or, if you don't have an NVIDIA GPU and are running GeNN in CPU_ONLY mode, the following command is executed:

```
genn-buildmodel.sh -c MBody1.cc
```

The genn-buildmodel script compiles the GeNN code generator in conjunction with the user-provided model description model/MBodyl.cc. It then executes the GeNN code generator to generate the complete model simulation code for the model.

3. Provide a build script to compile the generated model simulation and the user side code into a simulator executable (in the case of the MBody1 example this consists the file MBody1Sim.cc). On Linux or Mac a suitable GNU makefile can be created by running:

```
{\tt genn-create-user-project.sh~MBody1~MBody1Sim.cc}
```

And on Windows an MSBuild project can be created by running:

```
genn-create-user-project.bat MBody1 MBody1Sim.cc
```

4. Compile the simulator executable by invoking GNU make on Linux or Mac:

```
make clean all
```

or MSbuild on Windows:

```
msbuild MBody1.sln /t:MBody1 /p:Configuration=Release
```

5. Finally, run the resulting stand-alone simulator executable. In the MBody1 example, this is called MBody1 on Linux and MBody1_Release.exe on Windows.

3.3 Defining a New Model in GeNN

According to the work flow outlined above, there are several steps to be completed to define a neuronal network model.

- 1. The neuronal network of interest is defined in a model definition file, e.g. Example1.cc.
- 2. Within the the model definition file Example1.cc, the following tasks need to be completed:
 - a) The GeNN file modelSpec.h needs to be included,

```
#include "modelSpec.h"
```

b) The values for initial variables and parameters for neuron and synapse populations need to be defined, e.g.

would define the (homogeneous) parameters for a population of Poisson neurons.

Note

The number of required parameters and their meaning is defined by the neuron or synapse type. Refer to the User Manual for details. We recommend, however, to use comments like in the above example to achieve maximal clarity of each parameter's meaning.

If heterogeneous parameter values are required for a particular population of neurons (or synapses), they need to be defined as "variables" rather than parameters. See the User Manual for how to define new neuron (or synapse) types and the Defining a new variable initialisation snippet section for more information on initialising these variables to hetererogenous values.

c) The actual network needs to be defined in the form of a function modelDefinition, i.e.

```
void modelDefinition(ModelSpec &model);
```

Note

The name modelDefinition and its parameter of type ModelSpec& are fixed and cannot be changed if GeNN is to recognize it as a model definition.

d) Inside modelDefinition(), The time step DT needs to be defined, e.g.

```
model.setDT(0.1);
```

Note

All provided examples and pre-defined model elements in GeNN work with units of mV, ms, nF and muS. However, the choice of units is entirely left to the user if custom model elements are used.

MBody1.cc shows a typical example of a model definition function. In its core it contains calls to Model← Spec::addNeuronPopulation and ModelSpec::addSynapsePopulation to build up the network. For a full range of options for defining a network, refer to the User Manual.

- 3. The programmer defines their own "user-side" modeling code similar to the code in userproject/M↔ Body1_project/model/MBody1Sim.cc. In this code,
 - a) They manually define the connectivity matrices between neuron groups. Refer to the Synaptic matrix types section for the required format of connectivity matrices for dense or sparse connectivities.
 - b) They define input patterns (e.g. for Poisson neurons like in the MBody1 example) or individual initial values for neuron and / or synapse variables.

Note

The initial values given in the modelDefinition are automatically applied homogeneously to every individual neuron or synapse in each of the neuron or synapse groups.

- c) They use stepTime () to run one time step on either the CPU or GPU depending on the options passed to genn-buildmodel.
- d) They use functions like <code>copyStateFromDevice()</code> etc to transfer the results from GPU calculations to the main memory of the host computer for further processing.
- e) They analyze the results. In the most simple case this could just be writing the relevant data to output files.

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4 Examples

for Windows users, or:

GeNN comes with a number of complete examples. At the moment, there are seven such example projects provided with GeNN.

4.1 Single compartment Izhikevich neuron(s)

```
Izhikevich neuron(s) without any connections
This is a minimal example, with only one neuron population (with more or less
neurons depending on the command line, but without any synapses). The neurons
are Izhikevich neurons with homogeneous parameters across the neuron population.
This example project contains a helper executable called "generate_run",
which compiles and executes the model.
To compile it, navigate to genn/userproject/OneComp_project and type:
msbuild ..\userprojects.sln /t:generate one comp runner /p:Configuration=Release
for Windows users, or:
make
for Linux, Mac and other UNIX users.
USAGE
generate_run [OPTIONS] <outname>
Mandatory arguments:
outname: The base name of the output location and output files
Optional arguments:
--debug: Builds a debug version of the simulation and attaches the debugger
--cpu-only: Uses CPU rather than CUDA backend for GeNN
--timing: Uses GeNN's timing mechanism to measure performance and displays it at the end of the simulation
--ftype: Sets the floating point precision of the model to either float or double (defaults to float)
--gpu-device: Sets which GPU device to use for the simulation (defaults to -1 which picks automatically)
--num-neurons: Number of neurons to simulate (defaults to 1)
For a first minimal test, using these defaults and recording results with a base name of 'test', the system may
generate_run.exe test
```

```
./generate_run test

for Linux, Mac and other UNIX users.

This would create a set of tonic spiking Izhikevich neurons with no connectivity, receiving a constant identical 4 nA input.

Another example of an invocation that runs the simulation using the CPU rather than GPU, records timing information and 4 neurons would be:

generate_run.exe --cpu-only --timing --num_neurons=4 test

for Windows users, or:

./generate_run --cpu-only --timing --num_neurons=4 test

for Linux, Mac and other UNIX users.
```

4.2 Izhikevich neurons driven by Poisson input spike trains:

Izhikevich network receiving Poisson input spike trains _____ In this example project there is again a pool of non-connected Izhikevich model neurons that are connected to a pool of Poisson input neurons with a fixed probability. This example project contains a helper executable called "generate_run", which compiles and executes the model. To compile it, navigate to genn/userproject/PoissonIzh_project and type: msbuild ..\userprojects.sln /t:generate_poisson_izh_runner /p:Configuration=Release for Windows users, or: for Linux, Mac and other UNIX users. USAGE generate_run [OPTIONS] <outname> Mandatory arguments: outname: The base name of the output location and output files Optional arguments: --debug: Builds a debug version of the simulation and attaches the debugger --cpu-only: Uses CPU rather than CUDA backend for GeNN --timing: Uses GeNN's timing mechanism to measure performance and displays it at the end of the simulation --ftype: Sets the floating point precision of the model to either float or double (defaults to float) --gpu-device: Sets which GPU device to use for the simulation (defaults to -1 which picks automatically) --num-poisson: Number of Poisson sources to simulate (defaults to 100) --num-izh: Number of Izhikievich neurons to simulate (defaults to 10) --pconn: Probability of connection between each pair of poisson sources and neurons (defaults to 0.5) -- qscale: Scaling of synaptic conductances (defaults to 2) --sparse: Use sparse rather than dense data structure to represent connectivity An example invocation of generate_run using these defaults and recording results with a base name of 'test':

generate_run.exe test

for Windows users, or:
 ./generate_run test

```
for Linux, Mac and other UNIX users.

This will generate a network of 100 Poisson neurons with 20 Hz firing rate connected to 10 Izhikevich neurons with a 0.5 probability.

The same network with sparse connectivity can be used by adding the --sparse flag to the command line. Another example of an invocation that runs the simulation using the CPU rather than GPU, records timing information and uses sparse connectivity would be:

generate_run.exe --cpu-only --timing --sparse test

for Windows users, or:

./generate_run --cpu-only --timing --sparse test

for Linux, Mac and other UNIX users.

Izhikevich neuron model:[1]

4.3 Pulse-coupled Izhikevich network

Pulse-coupled Izhikevich network

This example model is inspired by simple thalamo-cortical network of Izhikevich with an excitatory and an inhibitory population of spiking neurons that are
```

randomly connected. It creates a pulse-coupled network with 80% excitatory 20% inhibitory connections, each connecting to a fixed number of neurons with sparse connectivity. To compile it, navigate to genn/userproject/Izh_sparse_project and type: msbuild ..\userprojects.sln /t:generate_izh_sparse_runner /p:Configuration=Release for Windows users, or: make for Linux, Mac and other UNIX users. USAGE generate_run [OPTIONS] <outname> Mandatory arguments: outname: The base name of the output location and output files Optional arguments: --debug: Builds a debug version of the simulation and attaches the debugger --cpu-only: Uses CPU rather than CUDA backend for GeNN --timing: Uses GeNN's timing mechanism to measure performance and displays it at the end of the simulation --ftype: Sets the floating point precision of the model to either float or double (defaults to float) --gpu-device: Sets which GPU device to use for the simulation (defaults to -1 which picks automatically) --num-neurons: Number of neurons (defaults to 10000) --num-connections: Number of connections per neuron (defaults to 1000) --gscale: General scaling of synaptic conductances (defaults to 1.0) An example invocation of generate_run using these defaults and recording results with a base name of 'test' wo

generate_run.exe test
for Windows users, or:
 ./generate_run test
for Linux, Mac and other UNIX users.
This would create a pulse coupled network of 8000 excitatory 2000 inhibitory

Izhikevich neurons, each making 1000 connections with other neurons, generating

```
a mixed alpha and gamma regime. For larger input factor, there is more input current and more irregular activity, for smaller factors less and less and more sparse activity. The synapses are of a simple pulse-coupling type. The results of the simulation are saved in the directory 'outdir_output'.

Another example of an invocation that runs the simulation using the CPU rather than GPU, records timing information and doubles the number of neurons would be:

generate_run.exe --cpu-only --timing --num_neurons=20000 test

for Windows users, or:

./generate_run --cpu-only --timing --num_neurons=20000 test

for Linux, Mac and other UNIX users.
```

4.4 Izhikevich network with delayed synapses

Izhikevich neuron model: [1]

```
Izhikevich network with delayed synapses
```

This example project demonstrates the synaptic delay feature of GeNN. It creates a network of three Izhikevich neuron groups, connected all-to-all with fast, medium and slow synapse groups. Neurons in the output group only spike if they are simultaneously innervated by the input neurons, via slow synapses, and the interneurons, via faster synapses.

Izhikevich neuron model: [1]

4.5 Insect olfaction model

Locust olfactory system (Nowotny et al. 2005)

This project implements the insect olfaction model by Nowotny et al. that demonstrates self-organized clustering of odours in a simulation of the insect antennal lobe and mushroom body. As provided, the model works with conductance based Hodgkin-Huxley neurons and several different synapse types, conductance based (but pulse-coupled) excitatory synapses, graded inhibitory synapses and synapses with a simplified STDP rule. This example project contains a helper executable called "generate_run", which prepares input pattern data, before compiling and executing the model.

To compile it, navigate to genn/userproject/MBodyl_project and type: msbuild ..\userprojects.sln /t:generate_mbody1_runner /p:Configuration=Release for Windows users, or: make for Linux, Mac and other UNIX users. USAGE generate_run [OPTIONS] <outname> Mandatory arguments: outname: The base name of the output location and output files Optional arguments: --debug: Builds a debug version of the simulation and attaches the debugger --cpu-only: Uses CPU rather than CUDA backend for GeNN --timing: Uses GeNN's timing mechanism to measure performance and displays it at the end of the simulation --ftype: Sets the floating point precision of the model to either float or double (defaults to float) --gpu-device: Sets which GPU device to use for the simulation (defaults to -1 which picks automatically) --num-al: Number of neurons in the antennal lobe (AL), the input neurons to this model (defaults to 100) --num-kc: Number of Kenyon cells (KC) in the "hidden layer" (defaults to 1000) --num-lhi: Number of lateral horn interneurons, implementing gain control (defaults to 20) --num-dn: Number of decision neurons (DN) in the output layer (defaults to 100) --gscale: A general rescaling factor for synaptic strength (defaults to 0.0025) --bitmask: Use bitmasks to represent sparse PN->KC connectivity rather than dense connectivity --delayed-synapses: Rather than use constant delays of DT throughough, use delays of (5 * DT) ms on KC->DN and

An example invocation of generate_run using these defaults and recording results with a base name of 'test' wo

generate_run.exe test

for Windows users, or:

./generate_run test

for Linux, Mac and other UNIX users.

of (3 * DT) ms on DN->DN synapse populations

Such a command would generate a locust olfaction model with 100 antennal lobe neurons, 1000 mushroom body Kenyon cells, 20 lateral horn interneurons and 100 mushroom body output neurons, and launch a simulation of it on a CUDA-enabled GPU using single precision floating point numbers. All output files will be prefixed with "test" and will be created under the "test" directory. The model that is run is defined in 'model/MBodyl.cc', debugging is switched off and the model would be simulated using float (single precision floating point) variables.

In more details, what generate_run program does is:

- a) use other tools to generate input patterns.
- b) build the source code for the model by writing neuron numbers into ./model/sizes.h, and executing "genn-buildmodel.sh ./model/MBodyl.cc.
- c) compile the generated code by invoking "make clean && make" running the code, e.g. "./classol_sim r1".

Another example of an invocation that runs the simulation using CPU rather than GPU, records timing information and uses bitmask connectivity would be:

```
generate_run.exe --cpu-only --timing --bitmask test
for Windows users, or:
./generate_run --cpu-only --timing --bitmask test
for Linux, Mac and other UNIX users.
As provided, the model outputs 'test.dn.st', 'test.kc.st', 'test.lhi.st' and 'test.pn.st' files
which contain the spiking activity observed in each population in the simulation. There are two
columns in this ASCII file, the first one containing the time of a spike and the second one,
the ID of the neuron that spiked. MATLAB users can use the scripts in the 'matlab' directory to plot
the results of a simulation and Python users can use the plot_spikes.py script in userproject/python.
For more about the model itself and the scientific insights gained from it, see Nowotny et al. referenced belo
MODEL INFORMATION
For information regarding the locust olfaction model implemented in this example project, see:
T. Nowotny, R. Huerta, H. D. I. Abarbanel, and M. I. Rabinovich Self-organization in the
olfactory system: One shot odor recognition in insects, Biol Cyber, 93 (6): 436-446 (2005),
doi:10.1007/s00422-005-0019-7
Locust olfactory system (Nowotny et al. 2005)
_____
This project implements the insect olfaction model by Nowotny et
al. that demonstrates self-organized clustering of odours in a
simulation of the insect antennal lobe and mushroom body. As provided
the model works with conductance based Hodgkin-Huxley neurons and
several different synapse types, conductance based (but pulse-coupled)
excitatory synapses, graded inhibitory synapses and synapses with a
simplified STDP rule. This example project contains a helper executable called "generate_run", which
prepares input pattern data, before compiling and
executing the model.
To compile it, navigate to genn/userproject/MBody1_project and type:
msbuild ..\userprojects.sln /t:generate_mbody1_runner /p:Configuration=Release
for Windows users, or:
make
for Linux, Mac and other UNIX users.
USAGE
generate_run [OPTIONS] <outname>
Mandatory arguments:
outname: The base name of the output location and output files
Optional arguments:
--debug: Builds a debug version of the simulation and attaches the debugger
--cpu-only: Uses CPU rather than CUDA backend for GeNN
--timing: Uses GeNN's timing mechanism to measure performance and displays it at the end of the simulation
--ftype: Sets the floating point precision of the model to either float or double (defaults to float)
--gpu-device: Sets which GPU device to use for the simulation (defaults to -1 which picks automatically)
--num-al: Number of neurons in the antennal lobe (AL), the input neurons to this model (defaults to 100)
--num-kc: Number of Kenyon cells (KC) in the "hidden layer" (defaults to 1000)
--num-lhi: Number of lateral horn interneurons, implementing gain control (defaults to 20)
--num-dn: Number of decision neurons (DN) in the output layer (defaults to 100)
--gscale: A general rescaling factor for synaptic strength (defaults to 0.0025)
--bitmask: Use bitmasks to represent sparse PN->KC connectivity rather than dense connectivity
--delayed-synapses: Rather than use constant delays of DT throughough, use delays of (5 * DT) ms on KC->DN and
An example invocation of generate_run using these defaults and recording results with a base name of 'test' wo
```

```
generate_run.exe test
for Windows users, or:
./generate_run test
for Linux, Mac and other UNIX users.
Such a command would generate a locust olfaction model with 100 antennal lobe neurons,
1000 mushroom body Kenyon cells, 20 lateral horn interneurons and 100 mushroom body
output neurons, and launch a simulation of it on a CUDA-enabled GPU using single
precision floating point numbers. All output files will be prefixed with "test"
and will be created under the "test" directory. The model that is run is defined
in 'model/MBodyl.cc', debugging is switched off and the model would be simulated using
float (single precision floating point) variables.
In more details, what generate_run program does is:
a) use another tools to generate input patterns.
b) build the source code for the model by writing neuron numbers into
   ./model/sizes.h, and executing "genn-buildmodel.sh ./model/MBody1.cc.
c) compile the generated code by invoking "make clean && make"
   running the code, e.g. "./classol_sim r1".
Another example of an invocation that runs the simulation using the CPU rather than GPU,
records timing information and uses bitmask connectivity would be:
generate_run.exe --cpu-only --timing --bitmask test
for Windows users, or:
./generate_run --cpu-only --timing --bitmask test
for Linux, Mac and other UNIX users.
As provided, the model outputs 'test.dn.st', 'test.kc.st', 'test.lhi.st' and 'test.pn.st' files which contain
the spiking activity observed in each population in the simulation, There are two
columns in this ASCII file, the first one containing the time of
a spike and the second one the ID of the neuron that spiked. Users
of matlab can use the scripts in the 'matlab' directory to plot
the results of a simulation and users of python can use the plot_spikes.py script in userproject/python.
For more about the model itself and the scientific insights gained from it see Nowotny et al. referenced below
MODEL INFORMATION
```

For information regarding the locust olfaction model implemented in this example project, see:

T. Nowotny, R. Huerta, H. D. I. Abarbanel, and M. I. Rabinovich Self-organization in the olfactory system: One shot odor recognition in insects, Biol Cyber, 93 (6): 436-446 (2005), doi:10.1007/s00422-005-0019-7

Nowotny insect olfaction model: [6]; Traub-Miles Hodgkin-Huxley neuron model: [10]

4.6 Voltage clamp simulation to estimate Hodgkin-Huxley parameters

Genetic algorithm for tracking parameters in a HH model cell

This example simulates a population of Hodgkin-Huxley neuron models using GeNN and evolves them with a simple guided random search (simple GA) to mimic the dynamics of a separate Hodgkin-Huxley neuron that is simulated on the CPU. The parameters of the CPU simulated "true cell" are drifting according to a user-chosen protocol: Either one of the parameters gNa, ENa, gKd, EKd, gleak, Eleak, Cmem are modified by a sinusoidal addition (voltage parameters) or factor (conductance or capacitance) protocol 0-6. For protocol 7 all 7 parameters undergo a random walk concurrently.

To compile it, navigate to genn/userproject/HHVclampGA_project and type:

```
msbuild ..\userprojects.sln /t:generate_hhvclamp_runner /p:Configuration=Release
for Windows users, or:s
make
for Linux, Mac and other UNIX users.
USAGE
generate_run [OPTIONS] <outname>
Mandatory arguments:
outname: The base name of the output location and output files
Optional arguments:
 -debug: Builds a debug version of the simulation and attaches the debugger
--cpu-only: Uses CPU rather than CUDA backend for GeNN
--timing: Uses GeNN's timing mechanism to measure performance and displays it at the end of the simulation
--ftype: Sets the floating point precision of the model to either float or double (defaults to float)
--gpu-device: Sets which GPU device to use for the simulation (defaults to -1 which picks automatically)
--protocol: Which changes to apply during the run to the parameters of the "true cell" (defaults to -1 which m
--num-pops: Number of neurons in the tracking population (defaults to 5000)
--total-time: Time in ms how long to run the simulation (defaults to 1000 ms)
An example invocation of generate_run is:
generate run.exe test1
for Windows users, or:
./generate_run test1
for Linux, Mac and other UNIX users.
This will simulate 5000 Hodgkin-Huxley neurons on the GPU which will, for 1000 ms, be matched to a
Hodgkin-Huxley neuron. The output files will be written into a directory of the name test1_output,
which will be created if it does not yet exist.
Another example of an invocation that records timing information for the the simulation and runs it for 10000
generate_run.exe --timing --total-time 10000
for Windows users, or:
./generate_run --timing --total-time 10000
for Linux, Mac and other UNIX users.
```

Traub-Miles Hodgkin-Huxley neuron model: [10]

4.7 A neuromorphic network for generic multivariate data classification

for Windows users (add -d for a debug build), or:

```
genn-buildmodel.sh Model_Schmuker_2014_classifier.cc
for Linux, Mac and other UNIX users.
You would only have to do this at the start, or when you change your actual network model,
i.e. on editing the file, Model_Schmuker_2014_classifier.cc
Then to compile the experiment and the GeNN created C/CUDA code type:
msbuild Schmuker2014_classifier.vcxproj /p:Configuration=Release
for Windows users (change Release to Debug if using debug mode), or:
for Linux, Mac and other UNIX users (add DEBUG=1 if using debug mode).
Once it compiles you should be able to run the classifier against the included Iris dataset by typing:
Schmuker2014\_classifier .
for Windows users, or:
./experiment .
for Linux, Mac and other UNIX systems.
This is how it works roughly. The experiment (experiment.cu) controls the experiment at a high level.
It mostly does this by instructing the classifier (Schmuker2014_classifier.cu) which does the grunt work.
So the experiment first tells the classifier to set up the GPU with the model and the synapse data.
Then it chooses the training and test set data.
It runs through the training set with plasticity ON, telling the classifier to run with the specfied
observation and collecting the classifier decisions.
```

Then it runs through the test set with plasticity OFF and collects the results in various reporting files.

At the highest level, it also has a loop where you can cycle through a list of parameter values e.g. some threshold value for the classifier to use. It will then report on the performance for each value. You should be aware that some parameter changes won't actually affect the classifier unless you invoke a re-initialisation of some sort. E.g. anything to do with VRs will require the input data cache to be reset between values, anything to do with non-plastic synapse weights won't get cleared down until you upload a changed set to the GPU etc.

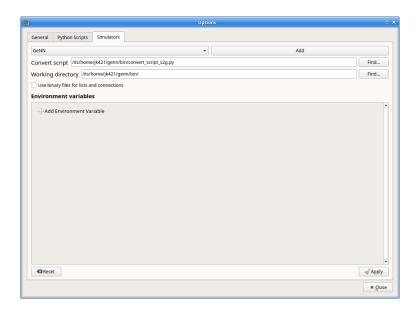
You should also note there is no option currently to run on CPU, this is not due to the demanding task, but just hasn't been tweaked yet to allow for this (a small change).

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5 SpineML and SpineCreator

GeNN now supports simulating models built using SpineML and includes scripts to fully integrate it with the SpineCreator graphical editor on Linux, Mac and Windows. After installing GeNN using the instructions in Installation, build SpineCreator for your platform.

From SpineCreator, select Edit->Settings->Simulators and add a new simulator using the following settings (replacing "/home/j/jk/jk421/genn" with the GeNN installation directory on your own system):



If you would like SpineCreator to use GeNN in CPU only mode, add an environment variable called "GENN_SPI↔ NEML_CPU_ONLY".

The best way to get started using SpineML with GeNN is to experiment with some example models. A number are available here although the "Striatal model" uses features not currently supported by GeNN and the two "Brette Benchmark" models use a legacy syntax no longer supported by SpineCreator (or GeNN). Once you have loaded a model, click "Expts" from the menu on the left hand side of SpineCreator, choose the experiment you would like to run and then select your newly created GeNN simulator in the "Setup Simulator" panel:



Now click "Run experiment" and, after a short time, the results of your GeNN simulation will be available for plotting by clicking the "Graphs" option in the menu on the left hand side of SpineCreator.

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6 Brian interface (Brian2GeNN)

GeNN can simulate models written for the Brian simulator via the Brian2GeNN interface [9]. In order for Brian2GeNN to find GeNN, GeNN must either be in your path with the location of the CUDA libraries configured via the CUDA_PATH environment variable (as described in Installation) or the devices.genn.path and devices.genn.cuda_backend.cuda_pathBrian preferences should be set.

To use GeNN to simulate a Brian script, import the brian2genn package and switch Brian to the genn device. As an example, the following Python script will simulate Leaky-integrate-and-fire neurons with varying input currents to construct an f/l curve:

```
from brian2 import *
import brian2genn
set_device('genn')

n = 1000
duration = 1*second
tau = 10*ms
eqs = '''
dv/dt = (v0 - v) / tau : volt (unless refractory)
v0 : volt
```

Of course, your simulation should be more complex than the example above to actually benefit from the performance gains of using a GPU via GeNN.

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7 Python interface (PyGeNN)

As well as being able to build GeNN models and user code directly from C++, you can also access all GeNN features from Python. The pygenn_genn_model.GeNNModel class provides a thin wrapper around
ModelSpec as well as providing support for loading and running simulations; and accessing their state.
SynapseGroup, NeuronGroup and CurrentSource are similarly wrapped by the pygenn.genn_groups.SynapseGroup, pygenn.genn_groups.NeuronGroup and pygenn.genn_groups.
CurrentSource classes respectively.

PyGeNN can be built from source on Windows, Mac and Linux following the instructions in the README file in the pygenn directory of the GeNN repository. However, if you have a relatively recent version of Python and CUDA, we recommend that you instead downloading a suitable 'wheel' from our releases page (all wheels are build for CUDA 10 except for on Mac OS which is built for CUDA 9). These can then be installed using e.g. pip install pygenn-0.2.1-cp27-cp27mu-linux_x86_64.whl for a Linux system with Python 2.7. On Windows we recommend using the Python 3 version of Anaconda.

The following example shows how PyGeNN can be easily interfaced with standard Python packages such as numpy and matplotlib to plot 4 different Izhikevich neuron regimes:

```
import numpy as np
import matplotlib.pvplot as plt
from pygenn.genn_model import GeNNModel
# Create a single-precision GeNN model
model = GeNNModel("float", "pygenn")
# Set simulation timestep to 0.1ms
model.dT = 0.1
\# Initialise Izhikevich
Variable parameters - arrays will be automatically uploaded izk_init = {"V": -65.0,
              "U": -20.0,
              "a": [0.02,
                               0.1,
                                        0.02,
                                                 0.02],
              "b": [0.2,
                               0.2,
                                        0.2,
                                                 0.2],
              "c": [-65.0,
                               -65.0, -50.0,
             "d": [8.0,
                               2.0,
                                        2.0,
                                                 4.01}
# Add neuron populations and current source to model
pop = model.add_neuron_population("Neurons", 4, "IzhikevichVariable", {}, izk_init) model.add_current_source("CurrentSource", "DC", "Neurons", {"amp": 10.0}, {})
# Build and load model
model.build()
model.load()
# Create a numpy view to efficiently access the membrane voltage from Python
voltage_view = pop.vars["V"].view
# Simulate
v = None
while model.t < 200.0:
    model.step_time()
    model.pull_state_from_device("Neurons")
    v = np.copy(voltage view) if v is None else np.vstack((v, voltage view))
# Create plot
figure, axes = plt.subplots(4, sharex=True)
# Plot voltages
for i, t in enumerate(["RS", "FS", "CH", "IB"]):
```

```
axes[i].set_title(t)
axes[i].set_ylabel("V [mV]")
axes[i].plot(np.arange(0.0, 200.0, 0.1), v[:,i])
axes[-1].set_xlabel("Time [ms]")
# Show plot
plt.show()
```

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8 Release Notes

Release Notes for GeNN v4.3.2 (PyGeNN 0.4.2)

This release fixes several small issues found in the 4.3.1 release.

Bug fixes:

- 1. Fixed bug when simulating models with very small timesteps.
- 2. Fixed bug in code generator where synapse groups with procedural variables could be incorrectly merged.
- 3. Fixed bug in code generator where references to parameters in sparse connectivity initialisation snippet row build state variables were not found.
- 4. Fixed issue with PyGeNN and custom sparse connectivity init snippets that led to segfaults.
- 5. Fixed bug that prevented PyGeNN being used with procedural connectivity.
- 6. In models with a large number of populations, the CUDA constant cache could overflow. This has been fixed and, if your application has additional constant cache requirements, these can be added to <code>GENN_PREFE</code>
 <code>RENCES.constantCacheOverhead</code>.

Release Notes for GeNN v4.3.1 (PyGeNN 0.4.1)

This release fixes several small issues found in the 4.3.0 release.

Bug fixes:

- 1. Fixed reference-counting bugs in PyGeNN that prevented multiple models from being instantiated.
- 2. Fixed PyGeNN interface for downloading connectivity from GPU.
- 3. Fixed host initialisation of sparse connectivity initialisation snippet extra global parameters using the CPU backend.
- 4. Upgraded third-party logging library to fix issues compiling SpineML generator with Visual Studio 2019.
- 5. Fixed bug in new code generator that didn't disambiguate between pre or postsynaptic neuron parameters and synapse parameters with the same name.

Release Notes for GeNN v4.3.0 (PyGeNN 0.4.0)

This release adds a number of significant new features to GeNN as well as making small improvements to PyGeNN. It also includes a number of bug fixes that have been identified since the 4.2.1 release.

User Side Changes

- 1. Previously GeNN performed poorly with large numbers of populations. This version includes a new code generator which effectively solves this problem (see [3]).
- 2. InitSparseConnectivitySnippet::Base row build state and NeuronModels::Base additional input variables could previously only be initialised with a numeric value. Now they can be initialised with a code string supporting substitutions etc.
- 3. Added GeNN implementation of cortical microcircuit model [7] to userprojects (discussed further in [2]). Also demonstrates how to dynamically load GeNN models rather than linking against them.
- 4. Previously one pushed states and spikes to and from device in PyGeNN using methods like pygenn. ← genn_model.GeNNModel.push_current_spikes_to_device which was somewhat cumbersome. These have now been wrapped in methods like pygenn.genn_groups.NeuronGroup.push ← _current_spikes_to_device which is somewhat nicer.
- 5. The CodeGenerator::generateAll function now returns memory estimates which are, in turn, returned from pygenn.genn_model.GeNNModel.build.
- 6. To better support batching of inputs into multiple instances of the same model, added ModelSpec::add← SlaveSynapsePopulation to add synapse populations which share per-synapse state with a 'master' synapse group.
- 7. Added extra global parameters to variable initialisation snippets can be used for lookup table style functionality.
- 8. Added support for host initialisation of sparse connectivity initialisation snippet extra global parameters. This allows host-based initialisation to be encapsulated within an InitSparseConnectivitySnippet :: Base class.

Bug fixes:

- 1. Fixed issues preventing spike recorder classes from compiling with GCC 4.9, thanks to Christoph Ostrau for this one!
- 2. The initialisers for pre and postsynaptic weight update model variables were not searched for references to an RNG when determining whether a neuron group requires an initialisation RNG.
- 3. Fixed issue with PyGeNN and custom var init snippets that led to segfaults.

Release Notes for GeNN v4.2.1 (PyGeNN 0.3.1)

This release fixes several small issues including several relating to Brian2GeNN compatibility.

User Side Changes

 Added -s option to genn-buildmodel.bat on Windows to turn off Visual C++ additional security checks (SDL), allowing Brian2GeNN libraries to be included in code generator.

Bug fixes:

- 1. Fixed bug where \$(sT_pre) and \$(sT_post) were incorrect when accessed in weight update model pre and postsynaptic spike code respectively when using the single-threaded CPU backend.
- 2. Fixed a corner case where valid models might result in compiler errors about Isyn not being defined.
- 3. Fixed a bug preventing multiple include paths being passed to genn-buildmodel.bat on Windows.

Release Notes for GeNN v4.2.0 (PyGeNN 0.3)

This release adds a number of new features to GeNN and its Python interface as well as fixing a number of bugs that have been identified since the 4.1.0 release.

User Side Changes

- 1. Kernel timings can now be enabled from python with pygenn.genn_model.GeNNModel. ← timing_enabled and subsequently accessed with pygenn.genn_model.GeNNModel. ← neuron_update_time, pygenn.genn_model.GeNNModel.init_time, pygenn.genn ← _model.GeNNModel.presynaptic_update_time, pygenn.genn_model.GeNNModel. ← postsynaptic_update_time, pygenn.genn_model.GeNNModel.synapse_dynamics_ ← time and pygenn.genn_model.GeNNModel.init_sparse_time.
- 2. Backends now generate getFreeDeviceMemBytes() function to allow free device memory to be queried from user simulation code. This is also exposed to Python via Gennumodel.free_device_
 mem_bytes property.
- 3. GeNN preferences are now fully exposed to PyGeNN by passing kwargs to pygenn.genn_model.Ge⊷ NNModel.__init__.
- 4. Logging level can now be seperately specified for GeNN, the code generator, the SpineML generator and the backend and is accessible from PyGeNN.
- 5. CodeGenerator::PreferencesBase::enableBitmaskOptimisations flag enables an alternative algorithm for updating synaptic matrices implemented with SynapseMatrixConnectivityee::BITMASK which performs better on smaller GPUs and CPUs. If you are manually initialising matrices this adds padding to align words to rows of the matrix.
- 6. SynapseMatrixConnectivity::PROCEDURAL and SynapseMatrixWeight::PROCEDURAL allow connectivity and synaptic weights to be generated on the fly rather than stored in memory.
- 7. CodeGenerator::PreferencesBase::automaticCopy flag allows models to be built without the need for explicitly copying data between host and device. For CUDA backend this uses unified memory (https://devblogs.nvidia.com/unified-memory-cuda-beginners/).
- 8. Speed of code compilation can be improved by building using multiple threads. This is now done everywhere where make or MSBuild is invocated automatically.

Bug fixes:

- 1. Fixed several bugs in extra global parameter implementation in PyGeNN.
- 2. Floating point min and max should be calculated with fmax and fmin in code snippets fixed in several models and user projects.
- 3. Fixed issues with version of numpy required in PyGeNN (previously held back by an issue with PyNN)

Release Notes for GeNN v4.1.0

This release adds a number of new features to GeNN and its SpineML interface as well as fixing a number of bugs that have been identified since the 4.0.2 release.

User Side Changes

1. The SpineML simulator could previously only be used as a standalone application. This functionality is now provided by the spineml_simulator library and can be used via the SpineMLSimulator::Simulator class.

- 2. When declaring a model's variables using SET_VAR, they can be marked as read-only by adding a 3rd parameter set to VarAccess::READ_ONLY to enable further optimisations. See Defining your own neuron type for more details.
- 3. Previously, unless models were very large or had very high spike rates, using SynapseGroup::
 SpanType::PRESYNAPTIC typically resulted in poor performance. When using the CUDA backend,
 SynapseGroup::setNumThreadsPerSpike can now be used to increase parallelism.
- 4. There were useful helpers for recording spikes (SpikeRecorder) and timing (Timer, Timer← Accumulate) in "userproject\include" which were not easily usable to user projects. genn-create-userproject. ⇔ sh and genn-create-userproject.bat now have a "-u" option which puts this in the include path of the generated project.
- 5. Timing information generated when ModelSpec::setTiming is enabled was not accesible to SpineML models. This is now exposed through the SpineMLSimulator::Simulator class.
- 6. Neuron population state variables were not easily accessible if the populations had incoming or outgoing connections with synaptic delays. Additional helper functions are now generated. See Creating and simulating a network model for more details.
- 7. SpineML interface will now use heterogeneous dendritic delay system introduced in GeNN 3.2.0 if required.
- 8. Add CodeGenerator::CUDA::Preferences::generateLineInfo option to output CUDA line info for profiling.
- 9. CUDA backend supports half datatype allowing memory savings through reduced precision. Host C++ code does not support half-precision types so such state variables must have their location set to Var← Location::DEVICE.
- 10. If ModelSpec::setDefaultNarrowSparseIndEnabled is set on a model or SynapseGroup← ::setNarrowSparseIndEnabled is set on an individual synapse population with sparse connectivity, 16-bit numbers will be used for postsynaptic indices, almost halving memory requirements.
- 11. Manual selection of CUDA devices is now exposed to PyGeNN via the pygenn.genn_model.GeNN← Model.selected_gpu property.

Bug fixes:

- 1. Fixed incomaptibilies with GCC 4.9
- 2. Fixed bug that occured if derived parameters were used in spike-like-event threshold conditions.
- 3. Fixed bug that occured when merging of postsynaptic models is enabled and GeNN decides to employ specific CUDA optimizations.
- 4. Increase maximum supported CUDA kernel grid size a bug was limiting this to 65536.
- 5. Fixed bugs in timing system when used with synapse dynamics kernels.

Release Notes for GeNN v4.0.2

This release fixes several small issues with the generation of binary wheels for Python:

Bug fixes:

- 1. There was a conflict between the versions of numpy used to build the wheels and the version required for the PyGeNN packages
- 2. Wheels were renamed to include the CUDA version which broke them.

Release Notes for GeNN v4.0.1

This release fixes several small bugs found in GeNN 4.0.0 and implements some small features:

User Side Changes

- 1. Improved detection and handling of errors when specifying model parameters and values in PyGeNN.
- 2. SpineML simulator is now implemented as a library which can be used directly from user applications as well as from command line tool.

Bug fixes:

- 1. Fixed typo in pygenn.genn_model.GennModel.push_var_to_device function in PyGeNN.
- 2. Fixed broken support for Visual C++ 2013.
- 3. Fixed zero-copy mode.
- 4. Fixed typo in tutorial 2.

Release Notes for GeNN v4.0.0

This release is the result of a second round of fairly major refactoring which we hope will make GeNN easier to use and allow it to be extended more easily in future. However, especially if you have been using GeNN 2.XX syntax, it breaks backward compatibility.

User Side Changes

- 1. Totally new build system make install can be used to install GeNN to a system location on Linux and Mac and Windows projects work much better in the Visual Studio IDE.
- 2. Python interface now supports Windows and can be installed using binary 'wheels' (see Python interface (PyGeNN) for more details).
- 3. No need to call initGeNN() at start and model.finalize() at end of all models.
- 4. Initialisation system simplified if you specify a value or initialiser for a variable or sparse connectivity, it will be initialised by your chosen backend. If you mark it as uninitialised, it is up to you to initialize it in user code between the calls to initialize() and initializeSparse() (where it will be copied to device).
- 5. genn-create-user-project helper scripts to create Makefiles or MSBuild projects for building user code
- 6. State variables can now be pushed and pulled individually using the pull<var name><neuron or synapse name>FromDevice() and push<var name><neuron or synapse name>To← Device() functions.
- 7. Management of extra global parameter arrays has been somewhat automated (see Extra Global Parameters for more details).
- 8. GENN_PREFERENCES is no longer a namespace it's a global struct so members need to be accessed with . rather than ::.
- 9. NeuronGroup, SynapseGroup, CurrentSource and NNmodel all previously exposed a lot of methods that the user wasn't *supposed* to call but could. These have now all been made protected and are exposed to GeNN internals using derived classes (NeuronGroupInternal, SynapseGroupInternal, CurrentSourceInternal, ModelSpecInternal) that make them public using using directives.

- 10. Auto-refractory behaviour was controlled using GENN_PREFERENCES::autoRefractory, this is now controlled on a per-neuron-model basis using the SET_NEEDS_AUTO_REFRACTORY macro.
- 11. The functions used for pushing and pulling have been unified somewhat this means that copyState←
 ToDevice and copyStateFromDevice functions no longer copy spikes and pus<neuron or
 synapse name>SpikesToDevice and pull<neuron or synapse name>SpikesFrom←
 Device no longer copy spike times or spike-like events.
- 12. Standard models of leaky-integrate-and-fire neuron (NeuronModels::LIF) and of exponentially shaped postsynaptic current (PostsynapticModels::ExpCurr) have been added.
- 13. When a model is built using the CUDA backend, the device it was built for is stored using it's PCI bus ID so it will always use the same device.

Deprecations

- 1. Yale-format sparse matrices are no longer supported.
- 2. GeNN 2.X syntax for implementing neuron and synapse models is no longer supported.
- 3. \$(addtoinSyn) = X; \$(updatelinsyn); idiom in weight update models has been replaced by function style
 \$ (addToInSyn, X);.

Release Notes for GeNN v3.3.0

This release is intended as the last service release for GeNN 3.X.X. Fixes for serious bugs **may** be backported if requested but, otherwise, development will be switching to GeNN 4.

User Side Changes

- 1. Postsynaptic models can now have Extra Global Parameters.
- 2. Gamma distribution can now be sampled using \$ (gennrand_gamma, a). This can be used to initialise variables using InitVarSnippet::Gamma.
- 3. Experimental Python interface All features of GeNN are now exposed to Python through the pygenn module (see Python interface (PyGeNN) for more details).

Bug fixes:

- 1. Devices with Streaming Multiprocessor version 2.1 (compute capability 2.0) now work correctly in Windows.
- 2. Seeding of on-device RNGs now works correctly.
- 3. Improvements to accuracy of memory usage estimates provided by code generator.

Release Notes for GeNN v3.2.0

This release extends the initialisation system introduced in 3.1.0 to support the initialisation of sparse synaptic connectivity, adds support for networks with more sophisticated models of synaptic plasticity and delay as well as including several other small features, optimisations and bug fixes for certain system configurations. This release supports $GCC \ge 4.9.1$ on Linux, Visual Studio ≥ 2013 on Windows and recent versions of Clang on Mac OS X.

User Side Changes

Sparse synaptic connectivity can now be initialised using small snippets of code run either on GPU or CPU.
 This can save significant amounts of initialisation time for large models. See Sparse connectivity initialisation for more details.

- 2. New 'ragged matrix' data structure for representing sparse synaptic connections supports initialisation using new sparse synaptic connecivity initialisation system and enables future optimisations. See Synaptic matrix types for more details.
- 3. Added support for pre and postsynaptic state variables for weight update models to allow more efficient implementatation of trace based STDP rules. See Defining a new weight update model for more details.
- 4. Added support for devices with Compute Capability 7.0 (Volta) to block-size optimizer.
- 5. Added support for a new class of 'current source' model which allows non-synaptic input to be efficiently injected into neurons. See Current source models for more details.
- 6. Added support for heterogeneous dendritic delays. See Defining a new weight update model for more details.
- 7. Added support for (homogeneous) synaptic back propagation delays using SynapseGroup::setBack← PropDelaySteps.
- 8. For long simulations, using single precision to represent simulation time does not work well. Added N← Nmodel::setTimePrecision to allow data type used to represent time to be set independently.

Optimisations

- 1. GENN_PREFERENCES::mergePostsynapticModels flag can be used to enable the merging together of postsynaptic models from a neuron population's incoming synapse populations improves performance and saves memory.
- 2. On devices with compute capability > 3.5 GeNN now uses the read only cache to improve performance of postsynaptic learning kernel.

Bug fixes:

- 1. Fixed bug enabling support for CUDA 9.1 and 9.2 on Windows.
- 2. Fixed bug in SynDelay example where membrane voltage went to NaN.
- 3. Fixed bug in code generation of SCALAR_MIN and SCALAR_MAX values.
- 4. Fixed bug in substitution of trancendental functions with single-precision variants.
- 5. Fixed various issues involving using spike times with delayed synapse projections.

Release Notes for GeNN v3.1.1

This release fixes several small bugs found in GeNN 3.1.0 and implements some small features:

User Side Changes

1. Added new synapse matrix types SPARSE_GLOBALG_INDIVIDUAL_PSM, DENSE_GLOBALG_IND

IVIDUAL_PSM and BITMASK_GLOBALG_INDIVIDUAL_PSM to handle case where synapses with no individual state have a postsynaptic model with state variables e.g. an alpha synapse. See Synaptic matrix types for more details.

Bug fixes

- 1. Correctly handle aliases which refer to other aliases in SpineML models.
- 2. Fixed issues with presynaptically parallelised synapse populations where the postsynaptic population is small enough for input to be accumulated in shared memory.

Release Notes for GeNN v3.1.0

This release builds on the changes made in 3.0.0 to further streamline the process of building models with GeNN and includes several bug fixes for certain system configurations.

User Side Changes

- Support for simulating models described using the SpineML model description language with GeNN (see SpineML and SpineCreator for more details).
- 2. Neuron models can now sample from uniform, normal, exponential or log-normal distributions these calls are translated to cuRAND when run on GPUs and calls to the C++11 < random > library when run on CPU. See Defining your own neuron type for more details.
- 3. Model state variables can now be initialised using small *snippets* of code run either on GPU or CPU. This can save significant amounts of initialisation time for large models. See Defining a new variable initialisation snippet for more details.
- 4. New MSBuild build system for Windows makes developing user code from within Visual Studio much more streamlined. See Debugging suggestions for more details.

Bug fixes:

- 1. Workaround for bug found in Glibc 2.23 and 2.24 which causes poor performance on some 64-bit Linux systems (namely on Ubuntu 16.04 LTS).
- 2. Fixed bug encountered when using extra global variables in weight updates.

Release Notes for GeNN v3.0.0

This release is the result of some fairly major refactoring of GeNN which we hope will make it more user-friendly and maintainable in the future.

User Side Changes

- 1. Entirely new syntax for defining models hopefully terser and less error-prone (see updated documentation and examples for details).
- 2. Continuous integration testing using Jenkins automated testing and code coverage calculation calculated automatically for Github pull requests etc.
- 3. Support for using Zero-copy memory for model variables. Especially on devices such as NVIDIA Jetson TX1 with no physical GPU memory this can significantly improve performance when recording data or injecting it to the simulation from external sensors.

Release Notes for GeNN v2.2.3

This release includes minor new features and several bug fixes for certain system configurations.

User Side Changes

- 1. Transitioned feature tests to use Google Test framework.
- 2. Added support for CUDA shader model 6.X

Bug fixes:

- 1. Fixed problem using GeNN on systems running 32-bit Linux kernels on a 64-bit architecture (Nvidia Jetson modules running old software for example).
- 2. Fixed problem linking against CUDA on Mac OS X El Capitan due to SIP (System Integrity Protection).
- 3. Fixed problems with support code relating to its scope and usage in spike-like event threshold code.
- 4. Disabled use of C++ regular expressions on older versions of GCC.

Release Notes for GeNN v2.2.2

This release includes minor new features and several bug fixes for certain system configurations.

User Side Changes

- 1. Added support for the new version (2.0) of the Brian simulation package for Python.
- 2. Added a mechanism for setting user-defined flags for the C++ compiler and NVCC compiler, via GENN_PR← EFERENCES.

Bug fixes:

- 1. Fixed a problem with atomicAdd() redefinitions on certain CUDA runtime versions and GPU configurations
- 2. Fixed an incorrect bracket placement bug in code generation for certain models.
- 3. Fixed an incorrect neuron group indexing bug in the learning kernel, for certain models.
- 4. The dry-run compile phase now stores temporary files in the current directory, rather than the temp directory, solving issues on some systems.
- 5. The LINK_FLAGS and INCLUDE_FLAGS in the common windows makefile include 'makefile_commin
 win.mk' are now appended to, rather than being overwritten, fixing issues with custom user makefiles on
 Windows.

Release Notes for GeNN v2.2.1

This bugfix release fixes some critical bugs which occur on certain system configurations.

Bug fixes:

- 1. (important) Fixed a Windows-specific bug where the CL compiler terminates, incorrectly reporting that the nested scope limit has been exceeded, when a large number of device variables need to be initialised.
- 2. (important) Fixed a bug where, in certain circumstances, outdated generateALL objects are used by the Makefiles, rather than being cleaned and replaced by up-to-date ones.
- (important) Fixed an 'atomicAdd' redeclared or missing bug, which happens on certain CUDA architectures when using the newest CUDA 8.0 RC toolkit.

4. (minor) The SynDelay example project now correctly reports spike indexes for the input group.

Please refer to the full documentation for further details, tutorials and complete code documentation.

Release Notes for GeNN v2.2

This release includes minor new features, some core code improvements and several bug fixes on GeNN v2.1.

User Side Changes

1. GeNN now analyses automatically which parameters each kernel needs access to and these and only these are passed in the kernel argument list in addition to the global time t. These parameters can be a combination of extraGlobalNeuronKernelParameters and extraGlobalSynapseKernelParameters in either neuron or synapse kernel. In the unlikely case that users wish to call kernels directly, the correct call can be found in the stepTimeGPU() function.

Reflecting these changes, the predefined Poisson neurons now simply have two extraGlobalNeuron \leftarrow Parameter rates and offset which replace the previous custom pointer to the array of input rates and integer offset to indicate the current input pattern. These extraGlobalNeuronKernelParameters are passed to the neuron kernel automatically, but the rates themselves within the array are of course not updated automatically (this is exactly as before with the specifically generated kernel arguments for Poisson neurons).

The concept of "directInput" has been removed. Users can easily achieve the same functionality by adding an additional variable (if there are individual inputs to neurons), an extraGlobalNeuronParameter (if the input is homogeneous but time dependent) or, obviously, a simple parameter if it's homogeneous and constant.

Note

The global time variable "t" is now provided by GeNN; please make sure that you are not duplicating its definition or shadowing it. This could have severe consequences for simulation correctness (e.g. time not advancing in cases of over-shadowing).

- 2. We introduced the namespace GENN_PREFERENCES which contains variables that determine the behaviour of GeNN.
- 3. We introduced a new code snippet called "supportCode" for neuron models, weightupdate models and post-synaptic models. This code snippet is intended to contain user-defined functions that are used from the other code snippets. We advise where possible to define the support code functions with the CUDA keywords "__ host__ __device__" so that they are available for both GPU and CPU version. Alternatively one can define separate versions for **host** and **device** in the snippet. The snippets are automatically made available to the relevant code parts. This is regulated through namespaces so that name clashes between different models do not matter. An exception are hash defines. They can in principle be used in the supportCode snippet but need to be protected specifically using ifndef. For example

```
#ifndef clip(x)
#define clip(x) x > 10.0? 10.0 : x
#endif
```

Note

If there are conflicting definitions for hash defines, the one that appears first in the GeNN generated code will then prevail.

- 4. The new convenience macros spikeCount_XX and spike_XX where "XX" is the name of the neuron group are now also available for events: spikeEventCount_XX and spikeEvent_XX. They access the values for the current time step even if there are synaptic delays and spikes events are stored in circular queues.
- 5. The old buildmodel.[sh|bat] scripts have been superseded by new genn-buildmodel.[sh|bat] scripts. These scripts accept UNIX style option switches, allow both relative and absolute model file paths, and allow the user to specify the directory in which all output files are placed (-o <path>). Debug (-d), CPU-only (-c) and show help (-h) are also defined.

6. We have introduced a CPU-only "-c" genn-buildmodel switch, which, if it's defined, will generate a GeNN version that is completely independent from CUDA and hence can be used on computers without CUDA installation or CUDA enabled hardware. Obviously, this then can also only run on CPU. CPU only mode can either be switched on by defining CPU_ONLY in the model description file or by passing appropriate parameters during the build, in particular

```
genn-buildmodel.[sh|bat] \<modelfile\> -c
make release CPU ONLY=1
```

- 7. The new genn-buildmodel "-o" switch allows the user to specify the output directory for all generated files the default is the current directory. For example, a user project could be in '/home/genn_project', whilst the GeNN directory could be '/usr/local/genn'. The GeNN directory is kept clean, unless the user decides to build the sample projects inside of it without copying them elsewhere. This allows the deployment of GeNN to a read-only directory, like '/usr/local' or 'C:\Program Files'. It also allows multiple users i.e. on a compute cluster to use GeNN simultaneously, without overwriting each other's code-generation files, etcetera.
- 8. The ARM architecture is now supported e.g. the NVIDIA Jetson development platform.
- 9. The NVIDIA CUDA SM_5* (Maxwell) architecture is now supported.
- 10. An error is now thrown when the user tries to use double precision floating-point numbers on devices with architecture older than SM 13, since these devices do not support double precision.
- 11. All GeNN helper functions and classes, such as toString() and NNmodel, are defined in the header files at genn/lib/include/, for example stringUtils.h and modelSpec.h, which should be individually included before the functions and classes may be used. The functions and classes are actually implementated in the static library genn\lib\lib\genn.lib\(Windows)\) or genn/lib/lib/libgenn.a (Mac, Linux), which must be linked into the final executable if any GeNN functions or classes are used.
- 12. In the modelDefinition() file, only the header file modelSpec.h should be included i.e. not the source file modelSpec.cc. This is because the declaration and definition of NNmodel, and associated functions, has been separated into modelSpec.h and modelSpec.cc, respectively. This is to enable NNmodel code to be precompiled separately. Henceforth, only the header file modelSpec.h should be included in model definition files!
- 13. In the modelDefinition() file, DT is now preferrably defined using model.setDT(<val>);, rather than #defineDT(<val>), in order to prevent problems with DT macro redefinition. For backward-compatibility reasons, the old #defineDT(<val>) method may still be used, however users are advised to adopt the new method.
- 14. In preparation for multi-GPU support in GeNN, we have separated out the compilation of generated code from user-side code. This will eventually allow us to optimise and compile different parts of the model with different CUDA flags, depending on the CUDA device chosen to execute that particular part of the model. As such, we have had to use a header file definitions.h as the generated code interface, rather than the runner.cc file. In practice, this means that user-side code should include myModel_COD← E/definitions.h, rather than myModel_CODE/runner.cc. Including runner.cc will likely result in pages of linking errors at best!

Developer Side Changes

- Blocksize optimization and device choice now obtain the ptxas information on memory usage from a CUDA driver API call rather than from parsing ptxas output of the nvcc compiler. This adds robustness to any change in the syntax of the compiler output.
- 2. The information about device choice is now stored in variables in the namespace GENN_PREFERENCES. This includes chooseDevice, optimiseBlockSize, optimizeCode, debugCode, showPtxcInfo, defaultDevice. asGoodAsZero has also been moved into this namespace.
- 3. We have also introduced the namespace GENN_FLAGS that contains unsigned int variables that attach names to numeric flags that can be used within GeNN.

- 4. The definitions of all generated variables and functions such as pullXXXStateFromDevice etc, are now generated into definitions.h. This is useful where one wants to compile separate object files that cannot all include the full definitions in e.g. "runnerGPU.cc". One example where this is useful is the brian2genn interface.
- 5. A number of feature tests have been added that can be found in the featureTests directory. They can be run with the respective runTests.sh scripts. The cleanTests.sh scripts can be used to remove all generated code after testing.

Improvements

- 1. Improved method of obtaining ptxas compiler information on register and shared memory usage and an improved algorithm for estimating shared memory usage requirements for different block sizes.
- 2. Replaced pageable CPU-side memory with page-locked memory. This can significantly speed up simulations in which a lot of data is regularly copied to and from a CUDA device.
- 3. GeNN library objects and the main generateALL binary objects are now compiled separately, and only when a change has been made to an object's source, rather than recompiling all software for a minor change in a single source file. This should speed up compilation in some instances.

Bug fixes:

- 1. Fixed a minor bug with delayed synapses, where delaySlot is declared but not referenced.
- 2. We fixed a bug where on rare occasions a synchronisation problem occurred in sparse synapse populations.
- 3. We fixed a bug where the combined spike event condition from several synapse populations was not assembled correctly in the code generation phase (the parameter values of the first synapse population over-rode the values of all other populations in the combined condition).

Please refer to the $\verb|full|$ documentation for further details, tutorials and complete code documentation.

Release Notes for GeNN v2.1

This release includes some new features and several bug fixes on GeNN v2.0.

User Side Changes

- 1. Block size debugging flag and the asGoodAsZero variables are moved into include/global.h.
- 2. NGRADSYNAPSES dynamics have changed (See Bug fix #4) and this change is applied to the example projects. If you are using this synapse model, you may want to consider changing model parameters.
- 3. The delay slots are now such that NO_DELAY is 0 delay slots (previously 1) and 1 means an actual delay of 1 time step.
- 4. The convenience function convertProbabilityToRandomNumberThreshold(float *, uint64_t *, int) was changed so that it actually converts firing probability/timestep into a threshold value for the GeNN random number generator (as its name always suggested). The previous functionality of converting a rate in kHz into a firing threshold number for the GeNN random number generator is now provided with the name convertRateToRandomNumberThreshold(float *, uint64_t *, int)
- 5. Every model definition function modelDefinition() now needs to end with calling NNmodel←::finalize() for the defined network model. This will lock down the model and prevent any further changes to it by the supported methods. It also triggers necessary analysis of the model structure that should only be performed once. If the finalize() function is not called, GeNN will issue an error and exit before code generation.

6. To be more consistent in function naming the pull\<SYNAPSENAME\>FromDevice and push\<S\U0044 YNAPSENAME\>ToDevice have been renamed to pull\<SYNAPSENAME\>StateFromDevice and push\<SYNAPSENAME\>StateToDevice. The old versions are still supported through macro definitions to make the transition easier.

- 7. New convenience macros are now provided to access the current spike numbers and identities of neurons that spiked. These are called spikeCount_XX and spike_XX where "XX" is the name of the neuron group. They access the values for the current time step even if there are synaptic delays and spikes are stored in circular queues.
- 8. There is now a pre-defined neuron type "SPIKECOURCE" which is empty and can be used to define PyNN style spike source arrays.
- 9. The macros FLOAT and DOUBLE were replaced with GENN_FLOAT and GENN_DOUBLE due to name clashes with typedefs in Windows that define FLOAT and DOUBLE.

Developer Side Changes

1. We introduced a file definitions.h, which is generated and filled with useful macros such as spkQuePtrShift which tells users where in the circular spike queue their spikes start.

Improvements

- 1. Improved debugging information for block size optimisation and device choice.
- 2. Changed the device selection logic so that device occupancy has larger priority than device capability version.
- 3. A new HH model called TRAUBMILES_PSTEP where one can set the number of inner loops as a parameter is introduced. It uses the TRAUBMILES_SAFE method.
- 4. An alternative method is added for the insect olfaction model in order to fix the number of connections to a maximum of 10K in order to avoid negative conductance tails.
- 5. We introduced a preprocessor define directive for an "int_" function that translates floating points to integers.

Bug fixes:

- 1. AtomicAdd replacement for old GPUs were used by mistake if the model runs in double precision.
- 2. Timing of individual kernels is fixed and improved.
- 3. More careful setting of maximum number of connections in sparse connectivity, covering mixed dense/sparse network scenarios.
- 4. NGRADSYNAPSES was not scaling correctly with varying time step.
- 5. Fixed a bug where learning kernel with sparse connectivity was going out of range in an array.
- 6. Fixed synapse kernel name substitutions where the "dd" prefix was omitted by mistake.

Please refer to the full documentation for further details, tutorials and complete code documentation.

Release Notes for GeNN v2.0

Version 2.0 of GeNN comes with a lot of improvements and added features, some of which have necessitated some changes to the structure of parameter arrays among others.

User Side Changes

- 1. Users are now required to call initGeNN () in the model definition function before adding any populations to the neuronal network model.
- 2. glbscnt is now call glbSpkCnt for consistency with glbSpkEvntCnt.
- 3. There is no longer a privileged parameter Epre. Spike type events are now defined by a code string spk EvntThreshold, the same way proper spikes are. The only difference is that Spike type events are specific to a synapse type rather than a neuron type.
- 4. The function setSynapseG has been deprecated. In a GLOBALG scenario, the variables of a synapse group are set to the initial values provided in the modeldefinition function.
- 5. Due to the split of synaptic models into weightUpdateModel and postSynModel, the parameter arrays used during model definition need to be carefully split as well so that each side gets the right parameters. For example, previously

would define the parameter array of three parameters, Erev, Epre, and tau_S for a synapse of type NSYNAPSE. This now needs to be "split" into

i.e. parameters <code>Erev</code> and <code>tau_S</code> are moved to the post-synaptic model and its parameter array of two parameters. <code>Epre</code> is discontinued as a parameter for <code>NSYNAPSE</code>. As a consequence the weightupdate model of <code>NSYNAPSE</code> has no parameters and one can pass <code>NULL</code> for the parameter array in <code>addSynapse</code> <code>Population</code>. The correct parameter lists for all defined neuron and synapse model types are listed in the <code>User Manual</code>.

Note

If the parameters are not redefined appropriately this will lead to uncontrolled behaviour of models and likely to segmentation faults and crashes.

- 6. Advanced users can now define variables as type scalar when introducing new neuron or synapse types. This will at the code generation stage be translated to the model's floating point type (ftype), float or double. This works for defining variables as well as in all code snippets. Users can also use the expressions SCALAR_MAX and SCALAR_MIN for FLT_MIN, FLT_MAX, DBL_MIN and DBL_MAX, respectively. Corresponding definitions of scalar, SCALAR_MIN and SCALAR_MAX are also available for user-side code whenever the code-generated file runner.cc has been included.
- 7. The example projects have been re-organized so that wrapper scripts of the <code>generate_run</code> type are now all located together with the models they run instead of in a common <code>tools</code> directory. Generally the structure now is that each example project contains the wrapper script <code>generate_run</code> and a <code>model</code> subdirectory which contains the model description file and the user side code complete with Makefiles for Unix and Windows operating systems. The generated code will be deposited in the <code>model</code> subdirectory in its <code>own modelname_CODE</code> folder. Simulation results will always be deposited in a new sub-folder of the main project directory.
- 8. The addSynapsePopulation (...) function has now more mandatory parameters relating to the introduction of separate weightupdate models (pre-synaptic models) and postynaptic models. The correct syntax for the addSynapsePopulation (...) can be found with detailed explanations in teh User Manual.
- We have introduced a simple performance profiling method that users can employ to get an overview over the differential use of time by different kernels. To enable the timers in GeNN generated code, one needs to declare

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```
networkmodel.setTiming(TRUE);
```

This will make available and operate GPU-side cudeEvent based timers whose cumulative value can be found in the double precision variables <code>neuron_tme</code>, <code>synapse_tme</code> and <code>learning_tme</code>. They measure the accumulated time that has been spent calculating the neuron kernel, synapse kernel and learning kernel, respectively. CPU-side timers for the simulation functions are also available and their cumulative values can be obtained through

```
float x= sdkGetTimerValue(&neuron_timer);
float y= sdkGetTimerValue(&synapse_timer);
float z= sdkGetTimerValue(&learning_timer);
```

The Insect olfaction model example shows how these can be used in the user-side code. To enable timing profiling in this example, simply enable it for GeNN:

```
model.setTiming(TRUE);
```

in MBody1.cc's modelDefinition function and define the macro TIMING in classol_sim.h

```
#define TIMING
```

This will have the effect that timing information is output into $OUTNAME_output/OUTNAME.$ timingprofile.

Developer Side Changes

- 1. allocateSparseArrays() has been changed to take the number of connections, connN, as an argument rather than expecting it to have been set in the Connetion struct before the function is called as was the arrangement previously.
- 2. For the case of sparse connectivity, there is now a reverse mapping implemented with revers index arrays and a remap array that points to the original positions of variable values in teh forward array. By this mechanism, revers lookups from post to pre synaptic indices are possible but value changes in the sparse array values do only need to be done once.
- 3. SpkEvnt code is no longer generated whenever it is not actually used. That is also true on a somewhat finer granularity where variable queues for synapse delays are only maintained if the corresponding variables are used in synaptic code. True spikes on the other hand are always detected in case the user is interested in them.

Please refer to the full documentation for further details, tutorials and complete code documentation.

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```

9 User Manual

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- · Postsynaptic integration methods
- · Current source models
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- · Sparse connectivity initialisation

9.2 Introduction

GeNN is a software library for facilitating the simulation of neuronal network models on NVIDIA CUDA enabled GPU hardware. It was designed with computational neuroscience models in mind rather than artificial neural networks. The main philosophy of GeNN is two-fold:

- GeNN relies heavily on code generation to make it very flexible and to allow adjusting simulation code to the model of interest and the GPU hardware that is detected at compile time.
- 2. GeNN is lightweight in that it provides code for running models of neuronal networks on GPU hardware but it leaves it to the user to write a final simulation engine. It so allows maximal flexibility to the user who can use any of the provided code but can fully choose, inspect, extend or otherwise modify the generated code. They can also introduce their own optimisations and in particular control the data flow from and to the GPU in any desired granularity.

This manual gives an overview of how to use GeNN for a novice user and tries to lead the user to more expert use later on. With that we jump right in.

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9.3 Defining a network model

A network model is defined by the user by providing the function

```
void modelDefinition(ModelSpec &model)
```

in a separate file, such as MyModel.cc. In this function, the following tasks must be completed:

1. The name of the model must be defined:

```
model.setName("MyModel");
```

- 2. Neuron populations (at least one) must be added (see Defining neuron populations). The user may add as many neuron populations as they wish. If resources run out, there will not be a warning but GeNN will fail. However, before this breaking point is reached, GeNN will make all necessary efforts in terms of block size optimisation to accommodate the defined models. All populations must have a unique name.
- 3. Synapse populations (zero or more) can be added (see Defining synapse populations). Again, the number of synaptic connection populations is unlimited other than by resources.

9.3.1 Defining neuron populations

Neuron populations are added using the function

```
model.addNeuronPopulation<NeuronModel>(name, num, paramValues, varInitialisers);
```

where the arguments are:

- NeuronModel: Template argument specifying the type of neuron model These should be derived off NeuronModels::Base and can either be one of the standard models or user-defined (see Neuron models).
- const string &name: Unique name of the neuron population
- \bullet unsigned int size: number of neurons in the population
- NeuronModel::ParamValues paramValues: Parameters of this neuron type
- NeuronModel::VarValues varInitialisers: Initial values or initialisation snippets for variables of this neuron type

The user may add as many neuron populations as the model necessitates. They must all have unique names. The possible values for the arguments, predefined models and their parameters and initial values are detailed Neuron models below.

9.3.2 Defining synapse populations

Synapse populations are added with the function

where the arguments are

- WeightUpdateModel: Template parameter specifying the type of weight update model. These should be derived off WeightUpdateModels::Base and can either be one of the standard models or user-defined (see Weight update models).
- PostsynapticModel: Template parameter specifying the type of postsynaptic integration model. These should be derived off PostsynapticModels::Base and can either be one of the standard models or user-defined (see Postsynaptic integration methods).
- const string &name: The name of the synapse population
- unsigned int mType: How the synaptic matrix is stored. See Synaptic matrix types for available options.
- unsigned int delay: Homogeneous (axonal) delay for synapse population (in terms of the simulation time step DT).
- const string preName: Name of the (existing!) pre-synaptic neuron population.
- const string postName: Name of the (existing!) post-synaptic neuron population.
- WeightUpdateModel::ParamValues weightParamValues: The parameter values (common to all synapses of the population) for the weight update model.
- WeightUpdateModel::VarValues weightVarInitialisers: Initial values or initialisation snippets for the weight update model's state variables
- WeightUpdateModel::PreVarValues weightPreVarInitialisers: Initial values or initialisation snippets for the weight update model's presynaptic state variables
- WeightUpdateModel::PostVarValues weightPostVarInitialisers: Initial values or initialisation snippets for the weight update model's postsynaptic state variables
- PostsynapticModel::ParamValues postsynapticParamValues: The parameter values (common to all postsynaptic neurons) for the postsynaptic model.

- PostsynapticModel::VarValues postsynapticVarInitialisers: Initial values or initialisation snippets for variables for the postsynaptic model's state variables
- InitSparseConnectivitySnippet::Init connectivityInitialiser: Optional argument, specifying the initialisation snippet for synapse population's sparse connectivity (see Sparse connectivity initialisation).

The ModelSpec::addSynapsePopulation() function returns a pointer to the newly created SynapseGroup object which can be further configured, namely with:

• SynapseGroup::setMaxConnections() and SynapseGroup::setMaxSourceConnections() to configure the maximum number of rows and columns respectively allowed in the synaptic matrix - this can improve performance and reduce memory usage when using SynapseMatrixConnectivity::SPARSE connectivity (see Synaptic matrix types).

Note

When using a sparse connectivity initialisation snippet, these values are set automatically.

- SynapseGroup::setMaxDendriticDelayTimesteps() sets the maximum dendritic delay (in terms of the simulation time step DT) allowed for synapses in this population. No values larger than this should be passed to the delay parameter of the addToDenDelay function in user code (see Defining a new weight update model).
- SynapseGroup::setSpanType() sets how incoming spike processing is parallelised for this synapse group. The default SynapseGroup::SpanType::POSTSYNAPTIC is nearly always the best option, but SynapseGroup::SpanType::PRESYNAPTIC may perform better when there are large numbers of spikes every timestep or very few postsynaptic neurons.

Note

If the synapse matrix uses one of the "GLOBALG" types then the global value of the synapse parameters are taken from the initial value provided in weightVarInitialisers therefore these must be constant rather than sampled from a distribution etc.

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9.4 Neuron models

There is a number of predefined models which can be used with the ModelSpec::addNeuronGroup function:

NeuronModels::RulkovMap

• NeuronModels::Izhikevich

· NeuronModels::IzhikevichVariable

· NeuronModels::LIF

• NeuronModels::SpikeSource

• NeuronModels::PoissonNew

NeuronModels::TraubMiles

NeuronModels::TraubMilesFast

NeuronModels::TraubMilesAlt

• NeuronModels::TraubMilesNStep

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9.4.1 Defining your own neuron type

In order to define a new neuron type for use in a GeNN application, it is necessary to define a new class derived from NeuronModels::Base. For convenience the methods this class should implement can be implemented using macros:

- DECLARE_MODEL(TYPE, NUM_PARAMS, NUM_VARS): declared the boilerplate code required for the model e.g. the correct specialisations of NewModels::ValueBase used to wrap the neuron model parameters and values.
- SET_SIM_CODE(SIM_CODE): where SIM_CODE contains the code for executing the integration of the
 model for one time stepWithin this code string, variables need to be referred to by \$(NAME), where NA

 ME is the name of the variable as defined in the vector varNames. The code may refer to the predefined
 primitives DT for the time step size and for the total incoming synaptic current. It can also refer to a unique
 ID (within the population) using .
- SET_THRESHOLD_CONDITION_CODE(THRESHOLD_CONDITION_CODE) defines the condition for true spike detection.
- SET_PARAM_NAMES() defines the names of the model parameters. If defined as NAME here, they can then be referenced as \$(NAME) in the code string. The length of this list should match the NUM_PARAM specified in DECLARE_MODEL. Parameters are assumed to be always of type double.
- SET_VARS() defines the names, type strings (e.g. "float", "double", etc) and (optionally) access mode of the neuron state variables. The type string "scalar" can be used for variables which should be implemented using the precision set globally for the model with ModelSpec::setPrecision. The variables defined here as NAME can then be used in the syntax \$(NAME) in the code string. If the access mode is set to VarAccess::Replace of the code string of the code string. If the access mode is set to VarAccess::Replace of the code string of the
- SET_NEEDS_AUTO_REFRACTORY() defines whether the neuron should include an automatic refractory
 period to prevent it emitting spikes in successive timesteps.

For example, using these macros, we can define a leaky integrator $au rac{dV}{dt} = -V + I_{
m syn}$ solved using Euler's method:

```
class LeakyIntegrator : public NeuronModels::Base
{
public:
    DECLARE_MODEL(LeakyIntegrator, 1, 1);

    SET_SIM_CODE("$(V) += (-$(V) +$(Isyn)) *(DT/$(tau));");

    SET_THRESHOLD_CONDITION_CODE("$(V) >= 1.0");

    SET_PARAM_NAMES({"tau"});

    SET_VARS({{"V", "scalar", VarAccess::READ_WRITE}});
};
```

Additionally "dependent parameters" can be defined. Dependent parameters are a mechanism for enhanced efficiency when running neuron models. If parameters with model-side meaning, such as time constants or conductances always appear in a certain combination in the model, then it is more efficient to pre-compute this combination and define it as a dependent parameter.

For example, because the equation defining the previous leaky integrator example has an algebraic solution, it can be more accurately solved as follows - using a derived parameter to calculate $\exp\left(\frac{-t}{\tau}\right)$:

```
class LeakyIntegrator2 : public NeuronModels::Base
{
public:
    DECLARE_MODEL(LeakyIntegrator2, 1, 1);

    SET_SIM_CODE("$(V) = $(Isyn) - $(ExpTC)*($(Isyn) - $(V));");

    SET_THRESHOLD_CONDITION_CODE("$(V) >= 1.0");

    SET_PARAM_NAMES({"tau"});
```

GeNN provides several additional features that might be useful when defining more complex neuron models.

9.4.1.1 Support code

Support code enables a code block to be defined that contains supporting code that will be utilized in multiple pieces of user code. Typically, these are functions that are needed in the sim code or threshold condition code. If possible, these should be defined as __host__ __device__ functions so that both GPU and CPU versions of GeNN code have an appropriate support code function available. The support code is protected with a namespace so that it is exclusively available for the neuron population whose neurons define it. Support code is added to a model using the SET_SUPPORT_CODE() macro, for example:

```
SET_SUPPORT_CODE("__device__ __host__ scalar mysin(float x){ return sin(x); }");
```

9.4.1.2 Extra global parameters

Extra global parameters are parameters common to all neurons in the population. However, unlike the standard neuron parameters, they can be varied at runtime meaning they could, for example, be used to provide a global reward signal. These parameters are defined by using the SET_EXTRA_GLOBAL_PARAMS() macro to specify a list of variable names and type strings (like the SET_VARS() macro). For example:

```
SET_EXTRA_GLOBAL_PARAMS({{"R", "float"}});
```

These variables are available to all neurons in the population. They can also be used in synaptic code snippets; in this case it need to be addressed with a _pre or _post postfix.

For example, if the model with the "R" parameter was used for the pre-synaptic neuron population, the weight update model of a synapse population could have simulation code like:

where we have assumed that the weight update model has a variable x and our synapse type will only be used in conjunction with pre-synaptic neuron populations that do have the extra global parameter R. If the pre-synaptic population does not have the required variable/parameter, GeNN will fail when compiling the kernels.

9.4.1.3 Additional input variables

Normally, neuron models receive the linear sum of the inputs coming from all of their synaptic inputs through the \$(inSyn) variable. However neuron models can define additional input variables - allowing input from different synaptic inputs to be combined non-linearly. For example, if we wanted our leaky integrator to operate on the the product of two input currents, it could be defined as follows:

Where the SET_ADDITIONAL_INPUT_VARS() macro defines the name, type and its initial value before postsynaptic inputs are applyed (see section Postsynaptic integration methods for more details).

9.4.1.4 Random number generation

Many neuron models have probabilistic terms, for example a source of noise or a probabilistic spiking mechanism. In GeNN this can be implemented by using the following functions in blocks of model code:

- \$ (gennrand_uniform) returns a number drawn uniformly from the interval [0.0, 1.0]

- \$ (gennrand_normal) returns a number drawn from a normal distribution with a mean of 0 and a standard deviation of 1.
- \$ (gennrand_exponential) returns a number drawn from an exponential distribution with $\lambda = 1$.
- \$ (gennrand_log_normal, MEAN, STDDEV) returns a number drawn from a log-normal distribution with the specified mean and standard deviation.
- \$ (gennrand_gamma, ALPHA) returns a number drawn from a gamma distribution with the specified shape.

Once defined in this way, new neuron models classes, can be used in network descriptions by referring to their type e.g.

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9.5 Weight update models

Currently 4 predefined weight update models are available:

- · WeightUpdateModels::StaticPulse
- WeightUpdateModels::StaticPulseDendriticDelay
- WeightUpdateModels::StaticGraded
- · WeightUpdateModels::PiecewiseSTDP

For more details about these built-in synapse models, see [5].

9.5.1 Defining a new weight update model

Like the neuron models discussed in Defining your own neuron type, new weight update models are created by defining a class. Weight update models should all be derived from WeightUpdateModel::Base and, for convenience, the methods a new weight update model should implement can be implemented using macros:

- SET_DERIVED_PARAMS(), SET_PARAM_NAMES(), SET_VARS() and SET_EXTRA_GLOBAL_PARAM
 S() perform the same roles as they do in the neuron models discussed in Defining your own neuron type.
- DECLARE_WEIGHT_UPDATE_MODEL(TYPE, NUM_PARAMS, NUM_VARS, NUM_PRE_VARS, NUM_ POST_VARS) is an extended version of DECLARE_MODEL() which declares the boilerplate code required for a weight update model with pre and postsynaptic as well as per-synapse state variables.
- SET_PRE_VARS() and SET_POST_VARS() define state variables associated with pre or postsynaptic neurons rather than synapses. These are typically used to efficiently implement *trace* variables for use in STDP learning rules [4]. Like other state variables, variables defined here as NAME can be accessed in weight update model code strings using the \$(NAME) syntax.
- SET_SIM_CODE(SIM_CODE): defines the simulation code that is used when a true spike is detected. The update is performed only in timesteps after a neuron in the presynaptic population has fulfilled its threshold detection condition. Typically, spikes lead to update of synaptic variables that then lead to the activation of input into the post-synaptic neuron. Most of the time these inputs add linearly at the post-synaptic neuron. This is assumed in GeNN and the term to be added to the activation of the post-synaptic neuron should be applied using the the \$(addToInSyn, weight) function. For example

```
SET_SIM_CODE(
    "$(addToInSyn, $(inc));\n"
```

where "inc" is the increment of the synaptic input to a post-synaptic neuron for each pre-synaptic spike. The simulation code also typically contains updates to the internal synapse variables that may have contributed to . For an example, see WeightUpdateModels::StaticPulse for a simple synapse update model and Weight UpdateModels::PiecewiseSTDP for a more complicated model that uses STDP. To apply input to the post-synaptic neuron with a dendritic (i.e. between the synapse and the postsynaptic neuron) delay you can instead use the \$(addToInSynDelay, weight, delay) function. For example

```
SET_SIM_CODE(
    "$(addToInSynDelay, $(inc), $(delay));");
```

where, once again, inc is the magnitude of the input step to apply and delay is the length of the dendritic delay in timesteps. By implementing delay as a weight update model variable, heterogeneous synaptic delays can be implemented. For an example, see WeightUpdateModels::StaticPulseDendriticDelay for a simple synapse update model with heterogeneous dendritic delays.

Note

When using dendritic delays, the **maximum** dendritic delay for a synapse populations must be specified using the SynapseGroup::setMaxDendriticDelayTimesteps() function.

• SET_EVENT_THRESHOLD_CONDITION_CODE(EVENT_THRESHOLD_CONDITION_CODE) defines a condition for a synaptic event. This typically involves the pre-synaptic variables, e.g. the membrane potential:

```
SET_EVENT_THRESHOLD_CONDITION_CODE("$(V_pre) > -0.02");
```

Whenever this expression evaluates to true, the event code set using the SET_EVENT_CODE() macro is executed. For an example, see WeightUpdateModels::StaticGraded.

- SET_EVENT_CODE(EVENT_CODE) defines the code that is used when the event threshold condition is met (as set using the SET_EVENT_THRESHOLD_CONDITION_CODE() macro).
- SET_LEARN_POST_CODE(LEARN_POST_CODE) defines the code which is used in the learnSynapses Post kernel/function, which performs updates to synapses that are triggered by post-synaptic spikes. This is typically used in STDP-like models e.g. WeightUpdateModels::PiecewiseSTDP.
- SET_SYNAPSE_DYNAMICS_CODE(SYNAPSE_DYNAMICS_CODE) defines code that is run for each synapse, each timestep i.e. unlike the others it is not event driven. This can be used where synapses have internal variables and dynamics that are described in continuous time, e.g. by ODEs. However using this mechanism is typically computationally very costly because of the large number of synapses in a typical network. By using the \$(addtoinsyn), \$(updatelinsyn) and \$(addToDenDelay) mechanisms discussed in the context of SET_SIM_CODE(), the synapse dynamics can also be used to implement continuous synapses for rate-based models.
- SET_PRE_SPIKE_CODE() and SET_POST_SPIKE_CODE() define code that is called whenever there is a pre or postsynaptic spike. Typically these code strings are used to update any pre or postsynaptic state variables.
- SET_NEEDS_PRE_SPIKE_TIME(PRE_SPIKE_TIME_REQUIRED) and SET_NEEDS_POST_SPIKE_TI

 ME(POST_SPIKE_TIME_REQUIRED) define whether the weight update needs to know the times of the spikes emitted from the pre and postsynaptic populations. For example an STDP rule would be likely to require:

```
SET_NEEDS_PRE_SPIKE_TIME(true);
SET_NEEDS_POST_SPIKE_TIME(true);
```

All code snippets, aside from those defined with SET_PRE_SPIKE_CODE () and SET_POST_SPIKE_COD \leftarrow E (), can be used to manipulate any synapse variable and so learning rules can combine both time-drive and event-driven processes.

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9.6 Postsynaptic integration methods

There are currently 3 built-in postsynaptic integration methods:

PostsynapticModels::ExpCurr

· PostsynapticModels::ExpCond

· PostsynapticModels::DeltaCurr

9.6.1 Defining a new postsynaptic model

The postsynaptic model defines how synaptic activation translates into an input current (or other input term for models that are not current based). It also can contain equations defining dynamics that are applied to the (summed) synaptic activation, e.g. an exponential decay over time.

In the same manner as to both the neuron and weight update models discussed in Defining your own neuron type and Defining a new weight update model, postsynamic model definitions are encapsulated in a class derived from PostsynapticModels::Base. Again, the methods that a postsynaptic model should implement can be implemented using the following macros:

- DECLARE_MODEL(TYPE, NUM_PARAMS, NUM_VARS), SET_DERIVED_PARAMS(), SET_PARAM_N
 AMES(), SET_VARS() perform the same roles as they do in the neuron models discussed in Defining your own neuron type.
- SET_DECAY_CODE(DECAY_CODE) defines the code which provides the continuous time dynamics for the summed presynaptic inputs to the postsynaptic neuron. This usually consists of some kind of decay function.
- SET_APPLY_INPUT_CODE(APPLY_INPUT_CODE) defines the code specifying the conversion from synaptic inputs to a postsynaptic neuron input current. e.g. for a conductance model:

```
SET\_APPLY\_INPUT\_CODE("$(Isyn) += $(inSyn) * ($(E) - $(V))");
```

where \$(E) is a postsynaptic model parameter specifying reversal potential and \$(V) is the variable containing the postsynaptic neuron's membrane potential. As discussed in Built-in Variables in GeNN, \$(Isyn) is the built in variable used to sum neuron input. However additional input variables can be added to a neuron model using the SET_ADDITIONAL_INPUT_VARS() macro (see Defining your own neuron type for more details).

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9.7 Current source models

There is a number of predefined models which can be used with the ModelSpec::addCurrentSource function:

- CurrentSourceModels::DC
- · CurrentSourceModels::GaussianNoise

9.7.1 Defining your own current source model

In order to define a new current source type for use in a GeNN application, it is necessary to define a new class derived from CurrentSourceModels::Base. For convenience the methods this class should implement can be implemented using macros:

DECLARE_MODEL(TYPE, NUM_PARAMS, NUM_VARS), SET_DERIVED_PARAMS(), SET_PARAM_N
 AMES(), SET_VARS() perform the same roles as they do in the neuron models discussed in Defining your own neuron type.

• SET_INJECTION_CODE(INJECTION_CODE): where INJECTION_CODE contains the code for injecting current into the neuron every simulation timestep. The \$(injectCurrent,) function is used to inject current.

For example, using these macros, we can define a uniformly distributed noisy current source:

```
class UniformNoise : public CurrentSourceModels::Base
{
public:
    DECLARE_MODEL(UniformNoise, 1, 0);

    SET_SIM_CODE("$(injectCurrent, $(gennrand_uniform) * $(magnitude));");

    SET_PARAM_NAMES({"magnitude"});
};
```

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9.8 Synaptic matrix types

Synaptic matrix types are made up of two components: SynapseMatrixConnectivity and SynapseMatrixWeight. SynapseMatrixConnectivity defines what data structure is used to store the synaptic matrix:

- SynapseMatrixConnectivity::DENSE stores synaptic matrices as a dense matrix. Large dense matrices require a large amount of memory and if they contain a lot of zeros it may be inefficient.
- SynapseMatrixConnectivity::SPARSE stores synaptic matrices in a(padded) 'ragged array' format. In general, this is less efficient to traverse using a GPU than the dense matrix format but does result in significant memory savings for large matrices. Ragged matrix connectivity is stored using several variables whose names, like state variables, have the name of the synapse population appended to them:
 - 1. const unsigned int maxRowLength: a constant set via the SynapseGroup::setMaxconnections method which specifies the maximum number of connections in any given row (this is the width the structure is padded to).
 - 2. unsigned int *rowLength (sized to number of presynaptic neurons): actual length of the row of connections associated with each presynaptic neuron
 - 3. unsigned int *ind (sized to maxRowLength * number of presynaptic neurons) ← : Indices of corresponding postsynaptic neurons concatenated for each presynaptic neuron. For example, consider a network of two presynaptic neurons connected to three postsynaptic neurons: 0th presynaptic neuron connected to 1st and 2nd postsynaptic neurons, the 1st presynaptic neuron connected only to the 0th neuron. The struct RaggedProjection should have these members, with indexing from 0 (where X represents a padding value):

```
maxRowLength = 2
ind = [1 2 0 X]
rowLength = [2 1]
```

Weight update model variables associated with the sparsely connected synaptic population will be kept in an array using the same indexing as ind. For example, a variable called g will be kept in an array such as: g=[g] Pre0-Post1 g pre0-post2 g pre1-post0 X]

• SynapseMatrixConnectivity::BITMASK is an alternative sparse matrix implementation where which synapses within the matrix are present is specified as a binary array (see Insect olfaction model). This structure is somewhat less efficient than the SynapseMatrixConnectivity::SPARSE and SynapseMatrixConnectivity::RAGGED formats and doesn't allow individual weights per synapse. However it does require the smallest amount of GPU memory for large networks.

Furthermore the SynapseMatrixWeight defines how

SynapseMatrixWeight::INDIVIDUAL allows each individual synapse to have unique weight update model variables. Their values must be initialised at runtime and, if running on the GPU, copied across from the user side code, using the pushXXXXXStateToDevice function, where XXXX is the name of the synapse population.

9.9 Variable initialisation 41

• SynapseMatrixWeight::INDIVIDUAL_PSM allows each postsynapic neuron to have unique post synaptic model variables. Their values must be initialised at runtime and, if running on the GPU, copied across from the user side code, using the pushxxxxxxstateToDevice function, where XXXX is the name of the synapse population.

• SynapseMatrixWeight::GLOBAL saves memory by only maintaining one copy of the weight update model variables. This is automatically initialized to the initial value passed to ModelSpec::addSynapsePopulation.

Only certain combinations of SynapseMatrixConnectivity and SynapseMatrixWeight are sensible therefore, to reduce confusion, the SynapseMatrixType enumeration defines the following options which can be passed to Model Spec::addSynapsePopulation:

- SynapseMatrixType::SPARSE_GLOBALG
- SynapseMatrixType::SPARSE_GLOBALG_INDIVIDUAL_PSM
- SynapseMatrixType::SPARSE_INDIVIDUALG
- SynapseMatrixType::DENSE GLOBALG
- SynapseMatrixType::DENSE_GLOBALG_INDIVIDUAL_PSM
- SynapseMatrixType::DENSE INDIVIDUALG
- SynapseMatrixType::BITMASK GLOBALG
- SynapseMatrixType::BITMASK_GLOBALG_INDIVIDUAL_PSM

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9.9 Variable initialisation

Neuron, weight update and postsynaptic models all have state variables which GeNN can automatically initialise.

Previously we have shown variables being initialised to constant values such as:

state variables can also be left *uninitialised* leaving it up to the user code to initialise them between the calls to initialize() and initializeSparse():

or initialised using one of a number of predefined variable initialisation snippets:

- InitVarSnippet::Uniform
- InitVarSnippet::Normal
- InitVarSnippet::Exponential
- InitVarSnippet::Gamma

For example, to initialise a parameter using values drawn from the normal distribution:

9.9.1 Defining a new variable initialisation snippet

Similarly to neuron, weight update and postsynaptic models, new variable initialisation snippets can be created by simply defining a class in the model description. For example, when initialising excitatory (positive) synaptic weights with a normal distribution they should be clipped at 0 so the long tail of the normal distribution doesn't result in negative weights. This could be implemented using the following variable initialisation snippet which redraws until samples are within the desired bounds:

```
class NormalPositive : public InitVarSnippet::Base
{
public:
    DECLARE_SNIPPET(NormalPositive, 2);

SET_CODE(
        "scalar normal;"
        "do\n"
        "{\n"
        " normal = $(mean) + ($(gennrand_normal) * $(sd));\n"
        "} while (normal < 0.0);\n"
        "$(value) = normal;\n");

SET_PARAM_NAMES({"mean", "sd"});
};
IMPLEMENT_SNIPPET(NormalPositive);</pre>
```

Within the snippet of code specified using the SET_CODE () macro, when initialisising neuron and postaynaptic model state variables, the \$(id) variable can be used to access the id of the neuron being initialised. Similarly, when initialising weight update model state variables, the \$(id_pre) and \$(id_post) variables can used to access the ids of the pre and postsynaptic neurons connected by the synapse being initialised.

9.9.2 Variable locations

Once you have defined **how** your variables are going to be initialised you need to configure **where** they will be allocated. By default memory is allocated for variables on both the GPU and the host. However, the following alternative 'variable locations' are available:

- VarLocation::DEVICE Variables are only allocated on the GPU, saving memory but meaning that they can't easily be copied to the host best for internal state variables.
- VarLocation::HOST_DEVICE Variables are allocated on both the GPU and the host the default.
- VarLocation::HOST_DEVICE_ZERO_COPY Variables are allocated as 'zero-copy' memory accessible to the host and GPU - useful on devices such as Jetson TX1 where physical memory is shared between the GPU and CPU.

Note

'Zero copy' memory is only supported on newer embedded systems such as the Jetson TX1 where there is no physical seperation between GPU and host memory and thus the same block of memory can be shared between them.

These modes can be set as a model default using ModelSpec::setDefaultVarLocation or on a pervariable basis using one of the following functions:

- NeuronGroup::setSpikeLocation
- NeuronGroup::setSpikeEventLocation
- NeuronGroup::setSpikeTimeLocation
- · NeuronGroup::setVarLocation
- SynapseGroup::setWUVarLocation
- SynapseGroup::setWUPreVarLocation

- SynapseGroup::setWUPostVarLocation
- SynapseGroup::setPSVarLocation
- SynapseGroup::setInSynVarLocation

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9.10 Sparse connectivity initialisation

Synaptic connectivity implemented using SynapseMatrixConnectivity::SPARSE and SynapseMatrixConnectivity::

BITMASK can be automatically initialised.

This can be done using one of a number of predefined sparse connectivity initialisation snippets:

- InitSparseConnectivitySnippet::OneToOne
- InitSparseConnectivitySnippet::FixedProbability
- InitSparseConnectivitySnippet::FixedProbabilityNoAutapse

For example, to initialise synaptic connectivity with a 10% connection probability (allowing connections between neurons with the same id):

9.10.1 Defining a new sparse connectivity snippet

Similarly to variable initialisation snippets, sparse connectivity initialisation snippets can be created by simply defining a class in the model description.

For example, the following sparse connectivity initialisation snippet could be used to initialise a 'ring' of connectivity where each neuron is connected to a number of subsequent neurons specified using the numNeighbours parameter:

```
class Ring : public InitSparseConnectivitySnippet::Base
public:
    DECLARE_SNIPPET(Ring, 1);
    SET_ROW_BUILD_STATE_VARS({{"offset", {"unsigned int", 1}}});
    SET ROW BUILD CODE (
         const unsigned int target = ($(id_pre) + offset) % $(num_post); \n"
        "$(addSynapse, target);\n"
        "offset++;\n"
        "if(offset > (unsigned int)(numNeighbours)) {n"
           $ (endRow); \n"
        "}\n");
    SET_PARAM_NAMES({"numNeighbours"});
    SET_CALC_MAX_ROW_LENGTH_FUNC(
        [](unsigned int numPre, unsigned int numPost, const std::vector<double> &pars)
            return (unsigned int)pars[0];
        });
    SET_CALC_MAX_COL_LENGTH_FUNC (
        [](unsigned int numPre, unsigned int numPost, const std::vector<double> &pars)
            return (unsigned int)pars[0];
IMPLEMENT_SNIPPET(Ring);
```

Each *row* of sparse connectivity is initialised independantly by running the snippet of code specified using the Stet_ROW_BUILD_CODE () macro within a loop. The \$(num_post) variable can be used to access the number of

neurons in the postsynaptic population and the \$(id_pre) variable can be used to access the index of the presynaptic neuron associated with the row being generated. The SET_ROW_BUILD_STATE_VARS() macro can be used to initialise state variables outside of the loop - in this case offset which is used to count the number of synapses created in each row. Synapses are added to the row using the \$(addSynapse, target) function and iteration is stopped using the \$(endRow) function. To avoid having to manually call SynapseGroup::setMaxConnections and SynapseGroup::setMaxSourceConnections, sparse connectivity snippets can also provide code to calculate the maximum row and column lengths this connectivity will result in using the SET_CALC_MAX_ROW_LENGTH_F\to UNC() and SET_CALC_MAX_COL_LENGTH_FUNC() macros. Alternatively, if the maximum row or column length is constant, the SET_MAX_ROW_LENGTH() and SET_MAX_COL_LENGTH() shorthand macros can be used.

9.10.2 Sparse connectivity locations

Once you have defined **how** sparse connectivity is going to be initialised, similarly to variables, you can control **where** it is allocated. This is controlled using the same VarLocations options described in section Variable locations and can either be set using the model default specified with ModelSpec::setDefault SparseConnectivityLocation or on a per-synapse group basis using SynapseGroup::setSparsectonnectivityLocation.

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10 Tutorial 1

In this tutorial we will go through step by step instructions how to create and run your first GeNN simulation from scratch.

10.1 The Model Definition

In this tutorial we will use a pre-defined Hodgkin-Huxley neuron model (NeuronModels::TraubMiles) and create a simulation consisting of ten such neurons without any synaptic connections. We will run this simulation on a GPU and save the results - firstly to stdout and then to file.

The first step is to write a model definition function in a model definition file. Create a new directory and, within that, create a new empty file called tenHHModel.cc using your favourite text editor, e.g.

```
>> emacs tenHHModel.cc &
```

Note

The ">>" in the example code snippets refers to a shell prompt in a unix shell, do not enter them as part of your shell commands.

The model definition file contains the definition of the network model we want to simulate. First, we need to include the GeNN model specification code modelSpec.h. Then the model definition takes the form of a function named modelDefinition that takes one argument, passed by reference, of type ModelSpec. Type in your tenH \leftarrow HModel.cc file:

```
// Model definintion file tenHHModel.cc
#include "modelSpec.h"

void modelDefinition(ModelSpec &model)
{
    // definition of tenHHModel
```

Two standard elements to the 'modelDefinition function are setting the simulation step size and setting the name of the model:

```
model.setDT(0.1);
model.setName("tenHHModel");
```

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Note

With this we have fixed the integration time step to 0.1 in the usual time units. The typical units in GeNN are ms, mV, nF, and μ S. Therefore, this defines DT= 0.1 ms.

Making the actual model definition makes use of the ModelSpec::addNeuronPopulation and ModelSpec::add SynapsePopulation member functions of the ModelSpec object. The arguments to a call to ModelSpec::add NeuronPopulation are

- NeuronModel: template parameter specifying the neuron model class to use
- const std::string &name: the name of the population
- unsigned int size: The number of neurons in the population
- const NeuronModel::ParamValues ¶mValues: Parameter values for the neurons in the population
- const NeuronModel::VarValues &varInitialisers: Initial values or initialisation snippets for variables of this neuron type

We first create the parameter and initial variable arrays,

Note

The comments are obviously only for clarity, they can in principle be omitted. To avoid any confusion about the meaning of parameters and variables, however, we recommend strongly to always include comments of this type.

Having defined the parameter values and initial values we can now create the neuron population,

This completes the model definition in this example. The complete tenHHModel.cc file now should look like this:

This model definition suffices to generate code for simulating the ten Hodgkin-Huxley neurons on the a GPU or CPU. The second part of a GeNN simulation is the user code that sets up the simulation, does the data handling for input and output and generally defines the numerical experiment to be run.

10.2 Building the model

To use GeNN to build your model description into simulation code, use a terminal to navigate to the directory containing your tenHHModel.cc file and, on Linux or Mac, type:

```
>> genn-buildmodel.sh tenHHModel.cc
```

Alternatively, on Windows, type:

```
>> genn-buildmodel.bat tenHHModel.cc
```

If you don't have an NVIDIA GPU and are running GeNN in CPU_ONLY mode, you can invoke genn-buildmodel with a -c option so, on Linux or Mac:

```
>> genn-buildmodel.sh -c tenHHModel.cc
or on Windows:
```

```
>> genn-buildmodel.bat -c tenHHModel.cc
```

If GeNN has been added to your path and CUDA_PATH is correctly configured, you should see some compile output ending in Model build complete

10.3 User Code

GeNN will now have generated the code to simulate the model for one timestep using a function stepTime(). To make use of this code, we need to define a minimal C/C++ main function. For the purposes of this tutorial we will initially simply run the model for one simulated second and record the final neuron variables into a file. Open a new empty file tenHHSimulation.cc in an editor and type

```
// tenHHModel simulation code
#include "tenHHModel_CODE/definitions.h"
int main()
{
    allocateMem();
    initialize();
    return 0;
}
```

This boiler plate code includes the header file for the generated code definitions.h in the subdirectory $ten\leftarrow HHModel_CODE$ where GeNN deposits all generated code (this corresponds to the name passed to the Model \leftarrow Spec::setName function). Calling allocateMem() allocates the memory structures for all neuron variables and initialize() launches a GPU kernel which initialise all state variables to their initial values. Now we can use the generated code to integrate the neuron equations provided by GeNN for 1000ms. To do so, we add after initialize();

Note

The t variable is provided by GeNN to keep track of the current simulation time in milliseconds.

```
while (t < 1000.0f) {
    stepTime();
}</pre>
```

and we need to copy the result back to the host before outputting it to stdout (this will do nothing if you are running the model on a CPU),

```
pullPoplStateFromDevice();
for (int j= 0; j < 10; j++) {
    std::cout << VPopl[j] << " ";
    std::cout << mPopl[j] << " ";
    std::cout << hPopl[j] << " ";
    std::cout << nPopl[j] << std::endl;
}</pre>
```

pullPop1StateFromDevice() copies all relevant state variables of the Pop1 neuron group from the GPU to the CPU main memory. Then we can output the results to stdout by looping through all 10 neurons and outputting the state variables VPop1, mPop1, hPop1, nPop1.

Note

The naming convention for variables in GeNN is the variable name defined by the neuron type, here TraubMiles defining V, m, h, and n, followed by the population name, here Pop1.

This completes the user code. The complete tenHHSimulation.cc file should now look like

```
// tenHHModel simulation code
#include "tenHHModel_CODE/definitions.h"
int main()
{
    allocateMem();
    initialize();
    while (t < 1000.0f) {
        stepTime();
    }
    pullPoplStateFromDevice();

    for (int j= 0; j < 10; j++) {
        std::cout << VPopl[j] << " ";
        std::cout << mPopl[j] << " ";
        std::cout << hPopl[j] << " ";
        std::cout << nPopl[j] << " ";
        std::cout << nPopl[j] << std::endl;
    }
    return 0;
}</pre>
```

10.4 Building the simulator (Linux or Mac)

On Linux and Mac, GeNN simulations are typically built using a simple Makefile which can be generated with the following command:

```
genn-create-user-project.sh tennHHModel tenHHSimulation.cc
```

This defines that the model is named tennHHModel and the simulation code is given in the file $tenHH \leftarrow Simulation.cc$ that we completed above. Now type

make

10.5 Building the simulator (Windows)

So that projects can be easily debugged within the Visual Studio IDE (see section Debugging suggestions for more details), Windows projects are built using an MSBuild script typically with the same title as the final executable. A suitable solution and project can be generated automatically with the following command:

```
genn-create-user-project.bat tennHHModel tenHHSimulation.cc
```

his defines that the model is named tennHHModel and the simulation code is given in the file $tenHH \leftarrow Simulation.cc$ that we completed above. Now type

msbuild tennHHModel.sln /p:Configuration=Release /t:tennHHModel

10.6 Running the Simulation

You can now execute your newly-built simulator on Linux or Mac with

./tennHHModel

Or on Windows with

tennHHModel_Release

The output you obtain should look like

```
-63.7838 0.0350042 0.336314 0.563243 -63.7838 0.0350042 0.336314 0.563243 -63.7838 0.0350042 0.336314 0.563243 -63.7838 0.0350042 0.336314 0.563243 -63.7838 0.0350042 0.336314 0.563243 -63.7838 0.0350042 0.336314 0.563243 -63.7838 0.0350042 0.336314 0.563243 -63.7838 0.0350042 0.336314 0.563243 -63.7838 0.0350042 0.336314 0.563243 -63.7838 0.0350042 0.336314 0.563243 -63.7838 0.0350042 0.336314 0.563243 -63.7838 0.0350042 0.336314 0.563243 -63.7838 0.0350042 0.336314 0.563243 -63.7838 0.0350042 0.336314 0.563243
```

10.7 Reading

This is not particularly interesting as we are just observing the final value of the membrane potentials. To see what is going on in the meantime, we need to copy intermediate values from the device and save them into a file. This can be done in many ways but one sensible way of doing this is to replace the calls to stepTime in tenHHSimulation.cc with something like this:

```
std::ofstream os("tenHH_output.V.dat");
while (t < 1000.0f) {
    stepTime();

    pullVPop1FromDevice();

    os << t << " ";
    for (int j= 0; j < 10; j++) {
        os << VPop1[j] << " ";
    }
    os << std::endl;
}
os.close();</pre>
```

Note

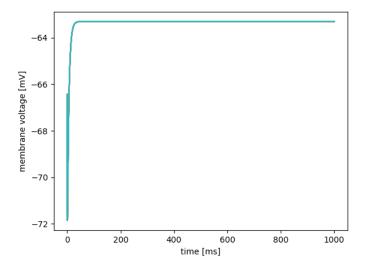
t is a global variable updated by the GeNN code to keep track of elapsed simulation time in ms. we switched from using pullPop1StateFromDevice() to pullVPop1FromDevice() as we are now only interested in the membrane voltage of the neuron.

You will also need to add:

```
#include <fstream>
```

to the top of tenHHSimulation.cc. After building the model; and building and running the simulator as described above there should be a file tenHH_output.V.dat in the same directory. If you plot column one (time) against the subsequent 10 columns (voltage of the 10 neurons), you should observe dynamics like this:

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However so far, the neurons are not connected and do not receive input. As the NeuronModels::TraubMiles model is silent in such conditions, the membrane voltages of the 10 neurons will simply drift from the -60mV they were initialised at to their resting potential.

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11 Tutorial 2

In this tutorial we will learn to add synapsePopulations to connect neurons in neuron groups to each other with synaptic models. As an example we will connect the ten Hodgkin-Huxley neurons from tutorial 1 in a ring of excitatory synapses.

First, copy the files from Tutorial 1 into a new directory and rename the tenHHModel.cc to tenHHRing Model.cc and tenHHSimulation.cc to tenHHRingSimulation.cc, e.g. on Linux or Mac:

```
>> cp -r tenHH_project tenHHRing_project
>> cd tenHHRing_project
>> mv tenHHModel.cc tenHHRingModel.cc
>> mv tenHHSimulation.cc tenHHRingSimulation.cc
```

Finally, to reduce confusion we should rename the model itself. Open tenHHRingModel.cc, change the model name inside,

```
model.setName("tenHHRing");
```

11.1 Defining the Detailed Synaptic Connections

We want to connect our ten neurons into a ring where each neuron connects to its neighbours. In order to initialise this connectivity we need to add a sparse connectivity initialisation snippet at the top of tenhHRingModel.cc:

The SET_ROW_BUILD_CODE code string will be called to generate each row of the synaptic matrix (connections coming from a single presynaptic neuron) and, in this case, each row consists of a single synapses from the presynaptic neuron \$(id_pre) to \$(id_pre) + 1 (the modulus operator is used to ensure that the final connection between neuron 9 and 0 is made correctly). In order to allow GeNN to better optimise the generated code we also provide a maximum row length. In this case each row always contains only one synapse but, when more complex connectivity is used, the number of neurons in the pre and postsynaptic population as well as any parameters used to configure the snippet can be accessed from this function.

Note

When defining GeNN code strings, the \$(VariableName) syntax is used to refer to variables provided by GeNN and the \$(FunctionName, Parameter1,...) syntax is used to call functions provided by GeNN.

11.2 Adding Synaptic connections

Now we need additional initial values and parameters for the synapse and post-synaptic models. We will use the standard WeightUpdateModels::StaticPulse weight update model and PostsynapticModels::ExpCond post-synaptic model. They need the following initial variables and parameters:

```
WeightUpdateModels::StaticPulse::VarValues s_ini(
    -0.2); // 0 - g: the synaptic conductance value

PostsynapticModels::ExpCond::ParamValues ps_p(
    1.0,    // 0 - tau_S: decay time constant for S [ms]
    -80.0); // 1 - Erev: Reversal potential
```

Note

the WeightUpdateModels::StaticPulse weight update model has no parameters and the PostsynapticModels.::ExpCond post-synaptic model has no state variables.

We can then add a synapse population at the end of the modelDefinition(...) function,

```
model.addSynapsePopulation<WeightUpdateModels::StaticPulse
    , PostsynapticModels::ExpCond>(
    "Poplself", SynapseMatrixType::SPARSE_GLOBALG, 10,
    "Popl", "Popl",
    {}, s_ini,
    ps_p, {},
    initConnectivity<Ring>());
```

The addSynapsePopulation parameters are

- WeightUpdateModel: template parameter specifying the type of weight update model (derived from Weight
 — UpdateModels::Base).
- PostsynapticModel: template parameter specifying the type of postsynaptic model (derived from PostsynapticModels::Base).
- name string containing unique name of synapse population.
- mtype how the synaptic matrix associated with this synapse population should be represented. Here
 SynapseMatrixType::SPARSE_GLOBALG means that there will be sparse connectivity and each connection
 will have the same weight (-0.2 as specified previously).
- delayStep integer specifying number of timesteps of propagation delay that spikes travelling through this synapses population should incur (or NO DELAY for none)
- · src string specifying name of presynaptic (source) population
- trg string specifying name of postsynaptic (target) population
- weightParamValues parameters for weight update model wrapped in WeightUpdateModel::ParamValues object.

- weightVarInitialisers initial values or initialisation snippets for the weight update model's state variables wrapped in a WeightUpdateModel::VarValues object.
- postsynapticParamValues parameters for postsynaptic model wrapped in PostsynapticModel::ParamValues object.
- postsynapticVarInitialisers initial values or initialisation snippets for the postsynaptic model wrapped in PostsynapticModel::VarValues object.
- connectivityInitialiser snippet and any paramaters (in this case there are none) used to initialise the synapse population's sparse connectivity.

Adding the addSynapsePopulation command to the model definition informs GeNN that there will be synapses between the named neuron populations, here between population Pop1 and itself. At this point our model definition file tenHHRingModel.cc should look like this

```
// Model definition file tenHHRing.cc
#include "modelSpec.h"
class Ring : public InitSparseConnectivitySnippet::Base
public:
    DECLARE_SNIPPET(Ring, 0);
    SET_ROW_BUILD_CODE(
         "$(addSynapse, ($(id_pre) + 1) % $(num_post));\n"
"$(endRow);\n");
    SET_MAX_ROW_LENGTH(1);
IMPLEMENT SNIPPET (Ring):
void modelDefinition(ModelSpec &model)
     // definition of tenHHRing
    model.setDT(0.1);
    model.setName("tenHHRing");
    NeuronModels::TraubMiles::ParamValues p(
                 // 0 - gNa: Na conductance in muS
                      // 1 - ENa: Na equi potential in mV
                     // 2 - gK: K conductance in muS

// 3 - EK: K equi potential in mV
         1.43.
         -95.0,
                    // 4 - g1: leak conductance in mus
// 5 - E1: leak equi potential in mV
// 6 - Cmem: membr. capacity density in nF
         0.02672,
         -63.563.
         0.143);
    NeuronModels::TraubMiles::VarValues ini(
                        // 0 - membrane potential V // 1 - prob. for Na channel activation m
         -60.0,
0.0529324,
                         // 2 - prob. for not Na channel blocking h
         0.3176767,
                         // 3 - prob. for K channel activation n
         0.5961207);
    model.addNeuronPopulation<NeuronModels::TraubMiles>("Pop1",
       10, p, ini);
    WeightUpdateModels::StaticPulse::VarValues s ini(
          -0.2); // 0 - g: the synaptic conductance value
    PostsynapticModels::ExpCond::ParamValues ps_p(
         1.0, // 0 - tau_S: decay time constant for S [ms] -80.0); // 1 - Erev: Reversal potential
    model.addSvnapsePopulation<
       WeightUpdateModels::StaticPulse,
       PostsynapticModels::ExpCond>(
         "Pop1self", SynapseMatrixType::SPARSE_GLOBALG, 100,
         "Pop1", "Pop1",
         {}, s_ini,
         ps p, {},
         initConnectivity<Ring>());
```

We can now build our new model:

```
>> genn-buildmodel.sh tenHHRingModel.cc
```

Note

Again, if you don't have an NVIDIA GPU and are running GeNN in CPU_ONLY mode, you can instead build with the -c option as described in Tutorial 1.

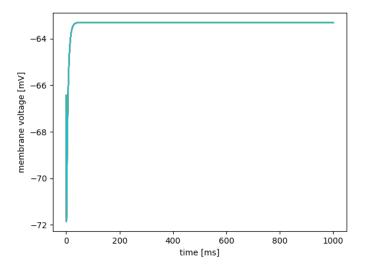
Now we can open the tenHHRingSimulation.cc file and update the file name of the model includes to match the name we set previously:

```
// tenHHRingModel simulation code
#include "tenHHRing_CODE/definitions.h"
```

Additionally, we need to add a call to a second initialisation function to main() after we call initialize():

```
initializeSparse();
```

This initializes any variables associated with the sparse connectivity we have added (and will also copy any manually initialised variables to the GPU). Then, after using the <code>genn-create-user-project</code> tool to create a new project with a model name of <code>tenhhRing</code> and using <code>tenhhRingSimulation.cc</code> rather than <code>tenhhesimulation.cc</code>, we can build and run our new simulator in the same way we did in Tutorial 1. However, even after all our hard work, if we plot the content of the first column against the subsequent 10 columns of <code>tenhesimple.V.dat</code> it looks very similar to the plot we obtained at the end of Tutorial 1.



This is because none of the neurons are spiking so there are no spikes to propagate around the ring.

11.3 Providing initial stimuli

We can use a NeuronModels::SpikeSource to inject an initial spike into the first neuron in the ring during the first timestep to start spikes propagating. Firstly we need to define another sparse connectivity initialisation snippet at the top of tenHHRingModel.cc which simply creates a single synapse on the first row of the synaptic matrix:

We then need to add it to the network by adding the following to the end of the modelDefinition(...) function:

and finally inject a spike in the first timestep (in the same way that the t variable is provided by GeNN to keep track of the current simulation time in milliseconds, iT is provided to keep track of it in timesteps):

```
if(iT == 0) {
    spikeCount_Stim = 1;
    spike_Stim[0] = 0;
    pushStimCurrentSpikesToDevice();
}
```

Note

 $spike_Stim[n]$ is used to specify the indices of the neurons in population Stim spikes which should emit spikes where $n \in [0, spikeCount_Stim)$.

At this point our user code tenHHRingModel.cc should look like this

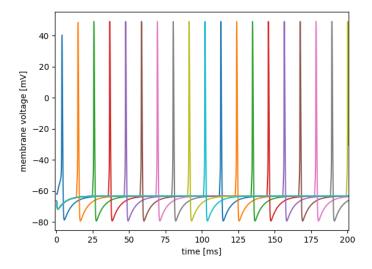
```
// Model definintion file tenHHRing.cc
#include "modelSpec.h"
class Ring : public InitSparseConnectivitySnippet::Base
public:
    DECLARE_SNIPPET(Ring, 0);
    SET ROW BUILD CODE (
         "$(addSynapse, ($(id_pre) + 1) % $(num_post));\n"
        "$ (endRow); \n");
    SET_MAX_ROW_LENGTH(1);
IMPLEMENT_SNIPPET(Ring);
class FirstToFirst : public InitSparseConnectivitySnippet::Base
public:
    DECLARE_SNIPPET(FirstToFirst, 0);
    SET_ROW_BUILD_CODE(
         "if($(id_pre) == 0) {\n"
           $ (addSynapse, $ (id_pre)); \n"
        "}\n"
        "$ (endRow); \n");
    SET_MAX_ROW_LENGTH(1);
IMPLEMENT SNIPPET(FirstToFirst);
void modelDefinition(ModelSpec &model)
    // definition of tenHHRing
    model.setDT(0.1);
    model.setName("tenHHRing");
    NeuronModels::TraubMiles::ParamValues p(
                    // 0 - gNa: Na conductance in muS
         7.15,
                     // 1 - ENa: Na equi potential in mV
         1.43,
                     // 2 - gK: K conductance in muS
                    // 3 - EK: K equi potential in mV
        -95.0.
                    // 4 - gl: leak conductance in muS
// 5 - El: leak equi potential in mV
        0.02672.
         -63.563,
                     // 6 - Cmem: membr. capacity density in nF
        0.143);
    NeuronModels::TraubMiles::VarValues ini(
                   // 0 - membrane potential V // 1 - prob. for Na channel activation m
        -60.0,
        0.0529324.
                        // 2 - prob. for not Na channel blocking h
        0.3176767.
        0.5961207);
                       // 3 - prob. for K channel activation n
```

```
model.addNeuronPopulation<NeuronModels::TraubMiles>("Pop1",
  10, p, ini);
model.addNeuronPopulation<NeuronModels::SpikeSource>("Stim"
  , 1, {}, {});
WeightUpdateModels::StaticPulse::VarValues s_ini(
      -0.2); // 0 - g: the synaptic conductance value
PostsynapticModels::ExpCond::ParamValues ps_p(
    1.0, // 0 - tau_S: decay time constant for S [ms] -80.0); // 1 - Erev: Reversal potential
model.addSynapsePopulation<
  WeightUpdateModels::StaticPulse,
  PostsynapticModels::ExpCond>(
    "Poplself", SynapseMatrixType::SPARSE_GLOBALG, 100,
"Popl", "Popl",
{}, s_ini,
ps_p, {},
    initConnectivity<Ring>());
model.addSynapsePopulation<
  WeightUpdateModels::StaticPulse,
  PostsynapticModels::ExpCond>(
    "StimPop1", SynapseMatrixType::SPARSE_GLOBALG,
  NO_DELAY,
"Stim", "Pop1",
    {}, s_ini,
    ps_p, {},
initConnectivity<FirstToFirst>());
```

and tenHHRingSimulation.cc' should look like this:

```
// Standard C++ includes
#include <fstream>
// tenHHRing simulation code
#include "tenHHRing_CODE/definitions.h"
int main()
    allocateMem();
    initialize();
    initializeSparse();
    std::ofstream os("tenHHRing_output.V.dat");
    while(t < 200.0f) {
    if(iT == 0) {
             glbSpkStim[0] = 0;
glbSpkCntStim[0] = 1;
             pushStimCurrentSpikesToDevice();
        stepTimeU();
         pullVPop1FromDevice();
         os << t << " ";
         for (int j= 0; j < 10; j++) {
    os << VPop1[j] << " ";
         os << std::endl;
    os.close();
    return 0;
```

Finally if we build, make and run this model; and plot the first 200 ms of the ten neurons' membrane voltages - they now looks like this:



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12 Best practices guide

GeNN generates code according to the network model defined by the user, and allows users to include the generated code in their programs as they want. Here we provide a guideline to setup GeNN and use generated functions. We recommend users to also have a look at the Examples, and to follow the tutorials Tutorial 1 and Tutorial 2.

12.1 Creating and simulating a network model

The user is first expected to create an object of class ModelSpec by creating the function modelDefinition() which includes calls to following methods:

- · ModelSpec::setDT();
- ModelSpec::setName();

Then add neuron populations by:

ModelSpec::addNeuronPopulation();

for each neuron population. Add synapse populations by:

ModelSpec::addSynapsePopulation();

for each synapse population.

Other optional functions are explained in ModelSpec class reference. At the end the function should look like this:

```
void modelDefinition(ModelSpec &model) {
  model.setDT(0.5);
  model.setName("YourModelName");
  model.addNeuronPopulation(...);
  ...
  model.addSynapsePopulation(...);
  ...
}
```

modelSpec.h should be included in the file where this function is defined.

This function will be called by generateALL.cc to create corresponding CPU and GPU simulation codes under the <YourModelName> CODE directory.

These functions can then be used in a .cc file which runs the simulation. This file should include <YourModel

Name>_CODE/definitions.h. Generated code differ from one model to the other, but core functions are the same
and they should be called in correct order. First, the following variables should be defined and initialized:

- ModelSpec model // initialized by calling modelDefinition(model)
- · Array containing current input (if any)

Any variables marked as unintialised using the uninitialisedVar() function or sparse connectivity not initialised using a snippet must be initialised by the user between calls to initialize() and initialize \Leftrightarrow Sparse(). Core functions generated by GeNN to be included in the user code include:

```
allocateMem()
initialize()
initializeSparse()
get<neuron name>CurrentSpikes()
get<neuron name>CurrentSpikeCount()
stepTime()
freeMem()
```

In order to correctly access neuron state and spikes for the current timestep, correctly accounting for delay buffering etc, you can use the getCurrent<var name><neuron name>(), get<neuron name>CurrentSpikeS() and 'get<neuron name>CurrentSpikeCount() functions. By setting GENN_PREFERENCES::automaticCopy, GeNN can be used in a simple mode where CUDA automatically transfers data between the GPU and CPU when required (see https://devblogs.nvidia.com/unified-memory-cuda-beginners/). However, copying elements between the GPU and the host memory is costly in terms of performance and the automatic copying operates on a fairly coarse grain (pages are approximately 4 bytes). Therefore, in order to maximise performance, we recommend you do not use automatic copying and instead manually call the following functions when required:

```
push<neuron or synapse name>StateToDevice()
pull<neuron or synapse name>StateFromDevice()
push<neuron name>SpikesToDevice()
pull<neuron name>SpikesFromDevice()
push<neuron name>SpikesEventsToDevice()
pull<neuron name>SpikesEventsFromDevice()
push<neuron name>SpikeTimesToDevice()
pull<neuron name>SpikeTimesFromDevice()
push<neuron name>CurrentSpikesToDevice()
pull<neuron name>CurrentSpikesFromDevice()
pull<neuron name>CurrentSpikesFromDevice()
push<neuron name>CurrentSpikesEventsToDevice()
```

pull<neuron name>CurrentSpikesEventsFromDevice()

- pull<synapse name>ConnectivityFromDevice()
- push<synapse name>ConnectivityToDevice()
- pull<var name><neuron or synapse name>FromDevice()
- push<var name><neuron or synapse name>ToDevice()
- pushCurrent<var name><neuron name>ToDevice()
- pullCurrent<var name><neuron name>FromDevice()
- getCurrent<var name><neuron name>()
- copyStateToDevice()
- copyStateFromDevice()
- copyCurrentSpikesFromDevice()
- copyCurrentSpikesEventsFromDevice()

You can use the <code>push<neuron</code> or <code>synapse</code> <code>name>StateToDevice()</code> to copy from the host to the GPU. At the end of your simulation, if you want to access the variables you need to copy them back from the device using the <code>pull<neuron</code> or <code>synapse</code> <code>name>StateFromDevice()</code> function or one of the more fine-grained functions listed above.

12.1.1 Extra Global Parameters

If extra global parameters have a "scalar" type such as float they can be set directly from simulation code. For example the extra global parameter "reward" of population "Pop" could be set with:

```
rewardPop = 5.0f;
```

However, if extra global parameters have a pointer type such as float*, GeNN generates additional functions to allocate, free and copy these variables between host and device:

- allocate<var name><neuron or synapse name>
- free<var name><neuron or synapse name>
- push<var name><neuron or synapse name>ToDevice
- pull<var name><neuron or synapse name>FromDevice These operate in much the same manner as the functions for interacting with standard variables described above but the allocate, push and pull functions all take a "count" parameter specifying how many entries the extra global parameter array should be.

12.2 Floating point precision

Double precision floating point numbers are supported by devices with compute capability 1.3 or higher. If you have an older GPU, you need to use single precision floating point in your models and simulation.

GPUs are designed to work better with single precision while double precision is the standard for CPUs. This difference should be kept in mind while comparing performance.

While setting up the network for GeNN, double precision floating point numbers are used as this part is done on the CPU. For the simulation, GeNN lets users choose between single or double precision. Overall, new variables in the generated code are defined with the precision specified by ModelSpec::setPrecision(unsigned int), providing GENN_FLOAT or GENN_DOUBLE as argument. GENN_FLOAT is the default value. The keyword scalar can be used in the user-defined model codes for a variable that could either be single or double precision. This keyword is

detected at code generation and substituted with "float" or "double" according to the precision set by ModelSpec :: setPrecision(unsigned int).

There may be ambiguities in arithmetic operations using explicit numbers. Standard C compilers presume that any number defined as "X" is an integer and any number defined as "X.Y" is a double. Make sure to use the same precision in your operations in order to avoid performance loss.

12.3 Working with variables in GeNN

12.3.1 Model variables

User-defined model variables originate from classes derived off the NeuronModels::Base, WeightUpdateModels ::Base or PostsynapticModels::Base classes. The name of model variable is defined in the model type, i.e. with a statement such as

```
SET_VARS({{"V", "scalar"}});
```

When a neuron or synapse population using this model is added to the model, the full GeNN name of the variable will be obtained by concatenating the variable name with the name of the population. For example if we add a population called Pop using a model which contains our V variable, a variable VPop of type scalar* will be available in the global namespace of the simulation program. GeNN will pre-allocate this C array to the correct size of elements corresponding to the size of the neuron population. GeNN will also free these variables when the provided function freeMem() is called. Users can otherwise manipulate these variable arrays as they wish. For convenience, GeNN provides functions to copy each state variable from the device into host memory and vice versa e.g. pullVPopFromDevice() and pushVPoptoDevice(). Alternatively, all state variables associated with a population can be copied using a single call E.g.

```
pullPopStateFromDevice();
```

These conventions also apply to the the variables of postsynaptic and weight update models.

Note

Be aware that the above naming conventions do assume that variables from the weightupdate models and the postSynModels that are used together in a synapse population are unique. If both the weightupdate model and the postSynModel have a variable of the same name, the behaviour is undefined.

12.3.2 Built-in Variables in GeNN

GeNN has no explicitly hard-coded synapse and neuron variables. Users are free to name the variable of their models as they want. However, there are some reserved variables that are used for intermediary calculations and communication between different parts of the generated code. They can be used in the user defined code but no other variables should be defined with these names.

- DT: Time step (typically in ms) for simulation; Neuron integration can be done in multiple sub-steps inside the neuron model for numerical stability (see Traub-Miles and Izhikevich neuron model variations in Neuron models).
- inSyn: This is an intermediary synapse variable which contains the summed input into a postsynaptic neuron (originating from the addtoinSyn variables of the incoming synapses).
- Isyn: This is a local variable which contains the (summed) input current to a neuron. It is typically the sum of any explicit current input and all synaptic inputs. The way its value is calculated during the update of the postsynaptic neuron is defined by the code provided in the postsynaptic model. For example, the standard PostsynapticModels::ExpCond postsynaptic model defines

which implements a conductance based synapse in which the postsynaptic current is given by $I_{\text{syn}} = g * s * (V_{\text{rev}} - V_{\text{post}})$.

The value resulting from the current converter code is assigned to Isyn and can then be used in neuron sim code like so:

```
(V) += (-(V) + (Isyn)) *DT
```

sT: This is a neuron variable containing the last spike time of each neuron and is automatically generated for
pre and postsynaptic neuron groups if they are connected using a synapse population with a weight update
model that has SET_NEEDS_PRE_SPIKE_TIME(true) or SET_NEEDS_POST_SPIKE_TIME(true) set.

In addition to these variables, neuron variables can be referred to in the synapse models by calling \$(<neuronVar~Name>_pre) for the presynaptic neuron population, and \$(<neuronVarName>_post) for the postsynaptic population. For example, \$(sT_pre), \$(sT_post), \$(V_pre), etc.

12.4 Debugging suggestions

In Linux, users can call <code>cuda-gdb</code> to debug on the GPU. Example projects in the <code>userproject</code> directory come with a flag to enable debugging (-debug). genn-buildmodel.sh has a debug flag (-d) to generate debugging data. If you are executing a project with debugging on, the code will be compiled with -g -G flags. In CPU mode the executable will be run in gdb, and in GPU mode it will be run in cuda-gdb in tui mode.

Note

Do not forget to switch debugging flags -g and -G off after debugging is complete as they may negatively affect performance.

On Mac, some versions of clang aren't supported by the CUDA toolkit. This is a recurring problem on Fedora as well, where CUDA doesn't keep up with GCC releases. You can either hack the CUDA header which checks compiler versions - cuda/include/host_config.h - or just use an older XCode version (6.4 works fine).

On Windows models can also be debugged and developed by opening the sln file used to build the model in Visual Studio. From here files can be added to the project, build settings can be adjusted and the full suite of Visual Studio debugging and profiling tools can be used.

Note

When opening the models in the userproject directory in Visual Studio, right-click on the project in the solution explorer, select 'Properties'. Then, making sure the desired configuration is selected, navigate to 'Debugging' under 'Configuration Properties', set the 'Working Directory' to '..' and the 'Command Arguments' to match those passed to genn-buildmodel e.g. 'outdir' to use an output directory called outdir.

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13 Credits

GeNN was created by Thomas Nowotny.

GeNN is currently maintained and developed by James Knight.

Current sources and PyGeNN were first implemented by Anton Komissarov.

Izhikevich model and sparse connectivity by Esin Yavuz.

Block size optimisations, delayed synapses and page-locked memory by James Turner.

User-defined synaptic and postsynaptic methods by Alex Cope and Esin Yavuz.

Example projects were provided by Alan Diamond, James Turner, Esin Yavuz and Thomas Nowotny. MPI support was largely developed by Mengchi Zhang.

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14 Preprocessor symbols for porting Random123 to different platforms.

The Random123 library is portable across C, C++, CUDA, OpenCL environments, and multiple operating systems (Linux, Windows 7, Mac OS X, FreeBSD, Solaris). This level of portability requires the abstraction of some features and idioms that are either not standardized (e.g., asm statments), or for which different vendors have their own standards (e.g., SSE intrinsics) or for which vendors simply refuse to conform to well-established standards (e.g., <inttypes.h>).

Random123/features/compilerfeatures.h conditionally includes a compiler-or-OS-specific Random123/featires/X XXfeatures.h file which defines appropriate values for the preprocessor symbols which can be used with a specific compiler or OS. Those symbols will then be used by other header files and source files in the Random123 library (and may be used by applications) to control what actually gets presented to the compiler.

Most of the symbols are boolean valued. In general, they will **always** be defined with value either 1 or 0, so do **NOT** use #ifdef. Use #if R123_USE_SOMETHING instead.

Library users can override any value by defining the pp-symbol with a compiler option, e.g.,

```
cc -DR123_USE_MULHILO64_C99
```

will use a strictly c99 version of the full-width 64x64->128-bit multiplication function, even if it would be disabled by default

All boolean-valued pre-processor symbols in Random123/features/compilerfeatures.h start with the prefix R123 $_{\leftarrow}$ USE $_{-}$

```
AES NI
AES_OPENSSL
SSE4_2
SSE4_1
SSE
STD_RANDOM
GNU_UINT128
ASM GNU
ASM_MSASM
CPUID_MSVC
CXX11_RANDOM
CXX11_TYPE_TRAITS
CXX11_STATIC_ASSERT
CXX11 CONSTEXPR
CXX11_UNRESTRICTED_UNIONS
CXX11_EXPLICIT_CONVERSIONS
CXX11_LONG_LONG
CXX11
X86INTRIN H
IA32INTRIN H
XMMINTRIN H
EMMINTRIN H
SMMINTRIN_H
WMMINTRIN H
INTRIN_H
MULHILO32_ASM
```

MULHILO64 ASM

```
MULHILO64_MSVC_INTRIN
MULHILO64_CUDA_INTRIN
MULHILO64_OPENCL_INTRIN
MULHILO64_C99
U01_DOUBLE
```

Most have obvious meanings. Some non-obvious ones:

AES NI and AES OPENSSL are not mutually exclusive. You can have one, both or neither.

GNU_UINT128 says that it's safe to use __uint128_t, but it does not require its use. In particular, it should be used in mulhilo<uint64_t> only if MULHILO64_ASM is unset.

If the XXXINTRIN H macros are true, then one should

```
#include <xxxintrin.h>
```

to gain accesss to compiler intrinsics.

The CXX11_SOME_FEATURE macros allow the code to use specific features of the C++11 language and library. The catchall In the absence of a specific CXX11_SOME_FEATURE, the feature is controlled by the catch-all R123← _USE_CXX11 macro.

U01_DOUBLE defaults on, and can be turned off (set to 0) if one does not want the utility functions that convert to double (i.e. u01_*_53()), e.g. on OpenCL without the cl_khr_fp64 extension.

There are a number of invariants that are always true. Application code may choose to rely on these:

- · ASM_GNU and ASM_MASM are mutually exclusive
- · The "higher" SSE values imply the lower ones.

There are also non-boolean valued symbols:

- R123_STATIC_INLINE According to both C99 and GNU99, the 'static inline' declaration allows the compiler to not emit code if the function is not used. Note that the semantics of 'inline', 'static' and 'extern' in gcc have changed over time and are subject to modification by command line options, e.g., -std=gnu89, -fgnu-inline. Nevertheless, it appears that the meaning of 'static inline' has not changed over time and (with a little luck) the use of 'static inline' here will be portable between versions of gcc and to other C99 compilers. See: http://gcc.gnu.org/onlinedocs/gcc/Inline.html http://www.greenend.org.cuk/rjk/2003/03/inline.html
- R123_FORCE_INLINE(decl) which expands to 'decl', adorned with the compiler-specific embellishments to strongly encourage that the declared function be inlined. If there is no such compiler-specific magic, it should expand to decl, unadorned.
- R123_CUDA_DEVICE which expands to **device** (or something else with sufficiently similar semantics) when CUDA is in use, and expands to nothing in other cases.
- R123_ASSERT(x) which expands to assert(x), or maybe to nothing at all if we're in an environment so feature-poor that you can't even call assert (I'm looking at you, CUDA and OpenCL), or even include assert.h safely (OpenCL).
- R123_STATIC_ASSERT(expr,msg) which expands to static_assert(expr,msg), or to an expression that will raise a compile-time exception if expr is not true.
- R123_ULONG_LONG which expands to a declaration of the longest available unsigned integer.
- R123_64BIT(x) expands to something equivalent to UINT64_C(x) from <stdint.h>, even in environments where <stdint.h> is not available, e.g., MSVC and OpenCL.
- R123_BUILTIN_EXPECT(expr,likely_value) expands to something with the semantics of gcc's __builtin_
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19 Namespace Documentation

19.1 CodeGenerator Namespace Reference

Helper class for generating code - automatically inserts brackets, indents etc.

Namespaces

- CUDA
- SingleThreadedCPU

Classes

- · class BackendBase
- class CodeStream
- · class GroupMerged
- class MemAlloc
- class MergedStructData
- · class MergedStructGenerator
- class ModelSpecMerged
- · class NeuronGroupMergedBase
- class NeuronInitGroupMerged
- · class NeuronSpikeQueueUpdateGroupMerged
- · class NeuronUpdateGroupMerged
- class PostsynapticUpdateGroupMerged
- struct PreferencesBase

Base class for backend preferences - can be accessed via a global in 'classic' C++ code generator.

- class PresynapticUpdateGroupMerged
- class Substitutions
- class SupportCodeMerged
- class SynapseConnectivityHostInitGroupMerged
- class SynapseConnectivityInitGroupMerged
- class SynapseDendriticDelayUpdateGroupMerged
- class SynapseDenseInitGroupMerged
- class SynapseDynamicsGroupMerged
- class SynapseGroupMergedBase
- class SynapseSparseInitGroupMerged
- class TeeBuf
- class TeeStream

Functions

void substitute (std::string &s, const std::string &trg, const std::string &rep)

Tool for substituting strings in the neuron code strings or other templates.

• bool regexVarSubstitute (std::string &s, const std::string &trg, const std::string &rep)

Tool for substituting variable names in the neuron code strings or other templates using regular expressions.

bool regexFuncSubstitute (std::string &s, const std::string &trg, const std::string &rep)

Tool for substituting function names in the neuron code strings or other templates using regular expressions.

This function substitutes function calls in the form:

size_t ceilDivide (size_t numerator, size_t denominator)

Divide two integers, rounding up i.e. effectively taking ceil.

size_t padSize (size_t size, size_t blockSize)

Pad an integer to a multiple of another.

- template<typename T >
 void genMergedGroupPush (CodeStream &os, const std::vector< T > &groups, const MergedStructData
 &mergedStructData, const std::string &suffix, const BackendBase &backend, BackendBase::MemorySpaces
 &memorySpaces)
- void genScalarEGPPush (CodeStream &os, const MergedStructData &mergedStructData, const std::string &suffix, const BackendBase &backend)
- std::string ensureFtype (const std::string &oldcode, const std::string &type)

This function implements a parser that converts any floating point constant in a code snippet to a floating point constant with an explicit precision (by appending "f" or removing it).

void checkUnreplacedVariables (const std::string &code, const std::string &codeName)

This function checks for unknown variable definitions and returns a gennError if any are found.

template<typename P, typename D>
 void neuronSubstitutionsInSynapticCode (CodeGenerator::Substitutions &substitutions, const Neuron←
 GroupInternal *archetypeNG, const std::string &offset, const std::string &delayOffset, const std::string &idx,
 const std::string &sourceSuffix, const std::string &destSuffix, const std::string &varPrefix, const std::string
 &varSuffix, P isParamHeterogeneousFn, D isDerivedParamHeterogeneousFn)

Function for performing the code and value substitutions necessary to insert neuron related variables, parameters, and extraGlobal parameters into synaptic code.

- GENN_EXPORT std::ostream & operator<< (std::ostream &s, const CodeStream::OB &ob)
- GENN_EXPORT std::ostream & operator<< (std::ostream &s, const CodeStream::CB &cb)
- GENN_EXPORT std::pair< std::vector< std::string >, MemAlloc > generateAll (const ModelSpecInternal &model, const BackendBase &backend, const filesystem::path &outputPath, bool standaloneModules=false)
- void GENN_EXPORT generateMakefile (std::ostream &os, const BackendBase &backend, const std::vector
 std::string > &moduleNames)
- void GENN_EXPORT generateMSBuild (std::ostream &os, const BackendBase &backend, const std::string &projectGUID, const std::vector< std::string > &moduleNames)
- void generateNeuronUpdate (CodeStream &os, const MergedStructData &mergedStructData, Backend← Base::MemorySpaces &memorySpaces, const ModelSpecMerged &modelMerged, const BackendBase &backend)
- MemAlloc generateRunner (CodeStream &definitions, CodeStream &definitionsInternal, CodeStream &runner, MergedStructData &mergedStructData, const ModelSpecMerged &modelMerged, const BackendBase &backend)
- void generateSupportCode (CodeStream &os, const ModelSpecMerged &modelMerged)
- void generateSynapseUpdate (CodeStream &os, const MergedStructData &mergedStructData, Backend
 Base::MemorySpaces &memorySpaces, const ModelSpecMerged &modelMerged, const BackendBase &backend)

19.1.1 Detailed Description

Helper class for generating code - automatically inserts brackets, indents etc.

Very thin wrapper around a number of groups which have been merged together.

Based heavily on: https://stackoverflow.com/questions/15053753/writing-a-manipulator-for-a-cus

19.1.2 Function Documentation

19.1.2.1 ceilDivide()

Divide two integers, rounding up i.e. effectively taking ceil.

19.1.2.2 checkUnreplacedVariables()

This function checks for unknown variable definitions and returns a gennError if any are found.

19.1.2.3 ensureFtype()

This function implements a parser that converts any floating point constant in a code snippet to a floating point constant with an explicit precision (by appending "f" or removing it).

19.1.2.4 functionSubstitute()

```
void CodeGenerator::functionSubstitute (
    std::string & code,
    const std::string & funcName,
    unsigned int numParams,
    const std::string & replaceFuncTemplate )
```

This function substitutes function calls in the form:

\$(functionName, parameter1, param2Function(0.12, "string"))

with replacement templates in the form:

```
actualFunction(CONSTANT, $(0), $(1))
```

19.1.2.5 generateAll()

19.1.2.6 generateInit()

```
19.1.2.7 generateMakefile()
```

```
void CodeGenerator::generateMakefile (
             std::ostream & os,
             const BackendBase & backend,
             const std::vector< std::string > & moduleNames )
19.1.2.8 generateMSBuild()
void CodeGenerator::generateMSBuild (
            std::ostream & os,
             const BackendBase & backend,
             const std::string & projectGUID,
             const std::vector< std::string > & moduleNames )
19.1.2.9 generateNeuronUpdate()
void CodeGenerator::generateNeuronUpdate (
             CodeStream & os,
             const MergedStructData & mergedStructData,
             BackendBase::MemorySpaces & memorySpaces,
             const ModelSpecMerged & modelMerged,
             const BackendBase & backend )
19.1.2.10 generateRunner()
MemAlloc CodeGenerator::generateRunner (
             CodeStream & definitions,
             CodeStream & definitionsInternal,
             CodeStream & runner,
             MergedStructData & mergedStructData,
             const ModelSpecMerged & modelMerged,
             const BackendBase & backend )
19.1.2.11 generateSupportCode()
void CodeGenerator::generateSupportCode (
             CodeStream & os,
             const ModelSpecMerged & modelMerged )
19.1.2.12 generateSynapseUpdate()
void CodeGenerator::generateSynapseUpdate (
             CodeStream & os,
             const MergedStructData & mergedStructData,
             BackendBase::MemorySpaces & memorySpaces,
             const ModelSpecMerged & modelMerged,
             const BackendBase & backend )
```

19.1.2.13 genMergedGroupPush()

19.1.2.14 genScalarEGPPush()

19.1.2.15 neuronSubstitutionsInSynapticCode()

Function for performing the code and value substitutions necessary to insert neuron related variables, parameters, and extraGlobal parameters into synaptic code.

```
size_t size,
size_t blockSize ) [inline]
```

Pad an integer to a multiple of another.

19.1.2.19 regexFuncSubstitute()

```
bool CodeGenerator::regexFuncSubstitute (  std::string \ \& \ s,   const \ std::string \ \& \ trg,   const \ std::string \ \& \ rep \ )
```

Tool for substituting function names in the neuron code strings or other templates using regular expressions.

19.1.2.20 regexVarSubstitute()

```
bool CodeGenerator::regexVarSubstitute (
    std::string & s,
    const std::string & trg,
    const std::string & rep )
```

Tool for substituting variable names in the neuron code strings or other templates using regular expressions.

19.1.2.21 substitute()

```
void CodeGenerator::substitute (
    std::string & s,
    const std::string & trg,
    const std::string & rep )
```

Tool for substituting strings in the neuron code strings or other templates.

19.2 CodeGenerator::CUDA Namespace Reference

Namespaces

- Optimiser
- PresynapticUpdateStrategy

Classes

- · class Backend
- struct Preferences

Preferences for CUDA backend.

Typedefs

using KernelBlockSize = std::array < size_t, KernelMax >
 Array of block sizes for each kernel.

Enumerations

• enum DeviceSelect { DeviceSelect::OPTIMAL, DeviceSelect::MOST_MEMORY, DeviceSelect::MANUAL }

Methods for selecting CUDA device.

enum BlockSizeSelect { BlockSizeSelect::OCCUPANCY, BlockSizeSelect::MANUAL }

Methods for selecting CUDA kernel block size.

• enum Kernel {

KernelNeuronUpdate, KernelPresynapticUpdate, KernelPostsynapticUpdate, KernelSynapseDynamics⇔ Update,

KernelInitialize, KernelInitializeSparse, KernelPreNeuronReset, KernelPreSynapseReset, KernelMax }

Kernels generated by CUDA backend.

19.2.1 Typedef Documentation

19.2.1.1 KernelBlockSize

```
using CodeGenerator::CUDA::KernelBlockSize = typedef std::array<size_t, KernelMax>
```

Array of block sizes for each kernel.

19.2.2 Enumeration Type Documentation

19.2.2.1 BlockSizeSelect

```
enum CodeGenerator::CUDA::BlockSizeSelect [strong]
```

Methods for selecting CUDA kernel block size.

Enumerator

OCCUPANCY	Pick optimal blocksize for each kernel based on occupancy.
MANUAL	Use block sizes specified by user.

19.2.2.2 DeviceSelect

```
enum CodeGenerator::CUDA::DeviceSelect [strong]
```

Methods for selecting CUDA device.

Enumerator

OPTIMAL	Pick optimal device based on how well kernels can be simultaneously simulated and occupancy.
MOST_MEMORY	Pick device with most global memory.
MANUAL	Use device specified by user.

19.2.2.3 Kernel

enum CodeGenerator::CUDA::Kernel

Kernels generated by CUDA backend.

Enumerator

KernelNeuronUpdate	
Rememeuronopuate	
KernelPresynapticUpdate	
KernelPostsynapticUpdate	
KernelSynapseDynamicsUpdate	
KernelInitialize	
KernelInitializeSparse	
KernelPreNeuronReset	
KernelPreSynapseReset	
KernelMax	

19.3 CodeGenerator::CUDA::Optimiser Namespace Reference

Functions

 BACKEND_EXPORT Backend createBackend (const ModelSpecInternal &model, const filesystem::path &outputPath, plog::Severity backendLevel, plog::IAppender *backendAppender, const Preferences &preferences)

19.3.1 Function Documentation

19.3.1.1 createBackend()

19.4 CodeGenerator::CUDA::PresynapticUpdateStrategy Namespace Reference

Classes

- class Base
- class PostSpan

Postsynaptic parallelism.

class PostSpanBitmask

Postsynaptic parallelism.

class PreSpan

Presynaptic parallelism.

• class PreSpanProcedural

Presynaptic parallelism with procedural connectivity.

19.5 CodeGenerator::SingleThreadedCPU Namespace Reference

Namespaces

Optimiser

Classes

- · class Backend
- struct Preferences

19.6 CodeGenerator::SingleThreadedCPU::Optimiser Namespace Reference

Functions

 BACKEND_EXPORT Backend createBackend (const ModelSpecInternal &model, const filesystem::path &outputPath, plog::Severity backendLevel, plog::IAppender *backendAppender, const Preferences &preferences)

19.6.1 Function Documentation

19.6.1.1 createBackend()

19.7 CurrentSourceModels Namespace Reference

Classes

· class Base

Base class for all current source models.

class DC

DC source.

· class GaussianNoise

Noisy current source with noise drawn from normal distribution.

- class PoissonExp
- 19.8 filesystem Namespace Reference
- 19.9 InitSparseConnectivitySnippet Namespace Reference

Base class for all sparse connectivity initialisation snippets.

Classes

- · class Base
- · class FixedNumberPostWithReplacement

Initialises connectivity with a fixed number of random synapses per row.

- class FixedNumberTotalWithReplacement
- · class FixedProbability
- · class FixedProbabilityBase
- · class FixedProbabilityNoAutapse

- · class Init
- class OneToOne

Initialises connectivity to a 'one-to-one' diagonal matrix.

· class Uninitialised

Used to mark connectivity as uninitialised - no initialisation code will be run.

19.9.1 Detailed Description

Base class for all sparse connectivity initialisation snippets.

19.10 InitVarSnippet Namespace Reference

Base class for all value initialisation snippets.

Classes

- class Base
- · class Constant

Initialises variable to a constant value.

class Exponential

Initialises variable by sampling from the exponential distribution.

· class Gamma

Initialises variable by sampling from the exponential distribution.

· class Normal

Initialises variable by sampling from the normal distribution.

- · class NormalClipped
- · class NormalClippedDelay
- · class Uniform

Initialises variable by sampling from the uniform distribution.

· class Uninitialised

Used to mark variables as uninitialised - no initialisation code will be run.

19.10.1 Detailed Description

Base class for all value initialisation snippets.

19.11 Logging Namespace Reference

Enumerations

 enum Channel { CHANNEL_GENN = 0, CHANNEL_CODE_GEN = 1, CHANNEL_BACKEND = 2, CHANN← EL_MAX }

Functions

• GENN_EXPORT void init (plog::Severity gennLevel, plog::Severity codeGeneratorLevel, plog::IAppender *gennAppender, plog::IAppender *codeGeneratorAppender)

19.11.1 Enumeration Type Documentation

19.11.1.1 Channel

```
enum Logging::Channel
```

Enumerator

CHANNEL_GENN	
CHANNEL_CODE_GEN	
CHANNEL_BACKEND	
CHANNEL_MAX	

19.11.2 Function Documentation

19.11.2.1 init()

19.12 Models Namespace Reference

Classes

class Base

Base class for all models - in addition to the parameters snippets have, models can have state variables.

- class VarInit
- · class VarInitContainerBase
- class VarInitContainerBase< 0 >

19.12.1 Detailed Description

Class used to bind together everything required to initialise a variable:

- 1. A pointer to a variable initialisation snippet
- 2. The parameters required to control the variable initialisation snippet

19.13 NeuronModels Namespace Reference

Classes

• class Base

Base class for all neuron models.

· class Izhikevich

Izhikevich neuron with fixed parameters [1].

• class IzhikevichVariable

Izhikevich neuron with variable parameters [1].

- class LIF
- class Poisson

Poisson neurons.

class PoissonNew

Poisson neurons.

class RulkovMap

Rulkov Map neuron.

· class SpikeSource

Empty neuron which allows setting spikes from external sources.

class SpikeSourceArray

Spike source array.

class TraubMiles

Hodgkin-Huxley neurons with Traub & Miles algorithm.

· class TraubMilesAlt

Hodgkin-Huxley neurons with Traub & Miles algorithm.

· class TraubMilesFast

Hodgkin-Huxley neurons with Traub & Miles algorithm: Original fast implementation, using 25 inner iterations.

class TraubMilesNStep

Hodgkin-Huxley neurons with Traub & Miles algorithm.

19.14 plog Namespace Reference

19.15 PostsynapticModels Namespace Reference

Classes

· class Base

Base class for all postsynaptic models.

class DeltaCurr

Simple delta current synapse.

class ExpCond

Exponential decay with synaptic input treated as a conductance value.

class ExpCurr

Exponential decay with synaptic input treated as a current value.

19.16 pugi Namespace Reference

19.17 pygenn Namespace Reference

Namespaces

- genn_groups
- · genn model
- · model_preprocessor

19.18 pygenn.genn_groups Namespace Reference

Classes

· class CurrentSource

Class representing a current injection into a group of neurons.

class Group

Parent class of NeuronGroup, SynapseGroup and CurrentSource.

class NeuronGroup

Class representing a group of neurons.

class SynapseGroup

Class representing synaptic connection between two groups of neurons.

Variables

• xrange = range

GeNNGroups This module provides classes which automatize model checks and parameter convesions for GeNN Groups.

19.18.1 Variable Documentation

19.18.1.1 xrange

pygenn.genn_groups.xrange = range

GeNNGroups This module provides classes which automatize model checks and parameter convesions for GeNN Groups.

19.19 pygenn.genn_model Namespace Reference

Classes

class GeNNModel

GeNNModel class This class helps to define, build and run a GeNN model from python.

Functions

· def init var (init var snippet, param space)

This helper function creates a VarInit object to easily initialise a variable using a snippet.

def init_connectivity (init_sparse_connect_snippet, param_space)

This helper function creates a InitSparseConnectivitySnippet::Init object to easily initialise connectivity using a snippet.

def create_custom_neuron_class (class_name, param_names=None, var_name_types=None, derived_
 params=None, sim_code=None, threshold_condition_code=None, reset_code=None, support_code=None,
 extra_global_params=None, additional_input_vars=None, is_auto_refractory_required=None, custom_
 body=None)

This helper function creates a custom NeuronModel class.

def create_custom_postsynaptic_class (class_name, param_names=None, var_name_types=None, derived_params=None, decay_code=None, apply_input_code=None, support_code=None, custom_
 body=None)

This helper function creates a custom PostsynapticModel class.

def create_custom_weight_update_class (class_name, param_names=None, var_name_types=None, pre
 _var_name_types=None, post_var_name_types=None, derived_params=None, sim_code=None, event
 _code=None, learn_post_code=None, synapse_dynamics_code=None, event_threshold_condition_
 code=None, pre_spike_code=None, post_spike_code=None, sim_support_code=None, learn_post_
 support_code=None, synapse_dynamics_support_code=None, extra_global_params=None, is_pre_
 spike_time_required=None, is_post_spike_time_required=None, custom_body=None)

This helper function creates a custom WeightUpdateModel class.

def create_custom_current_source_class (class_name, param_names=None, var_name_types=None, derived_params=None, injection_code=None, extra_global_params=None, custom_body=None)

This helper function creates a custom NeuronModel class.

def create_custom_model_class (class_name, base, param_names, var_name_types, derived_params, custom_body)

This helper function completes a custom model class creation.

def create_dpf_class (dp_func)

Helper function to create derived parameter function class.

def create_cmlf_class (cml_func)

Helper function to create function class for calculating sizes of matrices initialised with sparse connectivity initialisation snippet.

def create_custom_init_var_snippet_class (class_name, param_names=None, derived_params=None, var
 __init_code=None, extra_global_params=None, custom_body=None)

This helper function creates a custom InitVarSnippet class.

def create_custom_sparse_connect_init_snippet_class (class_name, param_names=None, derived_
 params=None, row_build_code=None, row_build_state_vars=None, calc_max_row_len_func=None, calc_
 max col len func=None, extra global params=None, custom body=None)

This helper function creates a custom InitSparseConnectivitySnippet class.

Variables

- backend_modules = OrderedDict()
- m = import_module(".genn_wrapper." + b + "Backend", "pygenn")

19.19.1 Function Documentation

Helper function to create function class for calculating sizes of matrices initialised with sparse connectivity initialisation snippet.

Parameters

```
cml_func a function which computes the length and takes three args "num_pre" (unsigned int), "num_post" (unsigned int) and "pars" (vector of double)
```

19.19.1.2 create_custom_current_source_class()

This helper function creates a custom NeuronModel class.

See also

```
create_custom_neuron_class
create_custom_weight_update_class
create_custom_current_source_class
create_custom_init_var_snippet_class
create_custom_sparse_connect_init_snippet_class
```

Parameters

class_name	name of the new class
param_names	list of strings with param names of the model
var_name_types	list of pairs of strings with varible names and types of the model
derived_params	list of pairs, where the first member is string with name of the derived parameter and the second MUST be an instance of the class which inherits from pygenn.genn_wrapper.DerivedParamFunc
injection_code	string with the current injection code
extra_global_params	list of pairs of strings with names and types of additional parameters
custom_body	dictionary with additional attributes and methods of the new class

19.19.1.3 create_custom_init_var_snippet_class()

This helper function creates a custom InitVarSnippet class.

See also

```
create_custom_neuron_class
create_custom_weight_update_class
create_custom_postsynaptic_class
create_custom_current_source_class
create_custom_sparse_connect_init_snippet_class
```

Parameters

class_name	name of the new class
param_names	list of strings with param names of the model
derived_params	list of pairs, where the first member is string with name of the derived parameter and the second MUST be an instance of the pygenn.genn_wrapper.DerivedParamFunc' class
var_init_code	string with the variable initialization code
extra_global_params	list of pairs of strings with names and types of additional parameters
custom_body	dictionary with additional attributes and methods of the new class

19.19.1.4 create_custom_model_class()

This helper function completes a custom model class creation.

This part is common for all model classes and is nearly useless on its own unless you specify custom body.

See also

```
create_custom_neuron_class
create_custom_weight_update_class
create_custom_postsynaptic_class
create_custom_current_source_class
create_custom_init_var_snippet_class
create_custom_sparse_connect_init_snippet_class
```

Parameters

class_name	name of the new class	
base	base class	
param_names	list of strings with param names of the model	
var_name_types	list of pairs of strings with varible names and types of the model	
derived_params	params list of pairs, where the first member is string with name of the derived parameter and the second MUST be an instance of the class which inherits from the pygenn.genn_wrapper.DerivedParamFunc class	
custom_body	dictionary with attributes and methods of the new class	

19.19.1.5 create_custom_neuron_class()

This helper function creates a custom NeuronModel class.

See also

```
create_custom_postsynaptic_class
create_custom_weight_update_class
create_custom_current_source_class
```

```
create_custom_init_var_snippet_class
create_custom_sparse_connect_init_snippet_class
```

Parameters

class_name	name of the new class
param_names	list of strings with param names of the model
var_name_types	list of pairs of strings with varible names and types of the model
derived_params	list of pairs, where the first member is string with name of the derived parameter and the second MUST be an instance of a class which inherits from pygenn.genn_wrapper.Snippet.DerivedParamFunc
sim_code	string with the simulation code
threshold_condition_code	string with the threshold condition code
reset_code	string with the reset code
support_code	string with the support code
extra_global_params	list of pairs of strings with names and types of additional parameters
additional_input_vars	list of tuples with names and types as strings and initial values of additional local input variables
is_auto_refractory_required	does this model require auto-refractory logic to be generated?
custom_body	dictionary with additional attributes and methods of the new class

19.19.1.6 create_custom_postsynaptic_class()

This helper function creates a custom PostsynapticModel class.

See also

```
create_custom_neuron_class
create_custom_weight_update_class
create_custom_current_source_class
create_custom_init_var_snippet_class
create_custom_sparse_connect_init_snippet_class
```

Parameters

class_name	name of the new class	
param_names	list of strings with param names of the model	
var_name_types	list of pairs of strings with varible names and types of the model	
derived_params	list of pairs, where the first member is string with name of the derived parameter and the second MUST be an instance of a class which inherits from pygenn.genn_wrapper.DerivedParamFunc	
decay_code	string with the decay code	

Parameters

apply_input_code	_code string with the apply input code	
support_code	string with the support code	
custom_body dictionary with additional attributes and methods of the new class		

19.19.1.7 create_custom_sparse_connect_init_snippet_class()

This helper function creates a custom InitSparseConnectivitySnippet class.

See also

```
create_custom_neuron_class
create_custom_weight_update_class
create_custom_postsynaptic_class
create_custom_current_source_class
create_custom_init_var_snippet_class
```

Parameters

class_name	name of the new class
param_names	list of strings with param names of the model
derived_params	list of pairs, where the first member is string with name of the derived parameter and the second MUST be an instance of the class which inherits from pygenn.genn_wrapper.DerivedParamFunc
row_build_code	string with row building initialization code
row_build_state_vars	list of tuples of state variables, their types and their initial values to use across row building loop
calc_max_row_len_func	instance of class inheriting from CalcMaxLengthFunc used to calculate maximum row length of synaptic matrix
calc_max_col_len_func	instance of class inheriting from CalcMaxLengthFunc used to calculate maximum col length of synaptic matrix
extra_global_params	list of pairs of strings with names and types of additional parameters
custom_body	dictionary with additional attributes and methods of the new class

19.19.1.8 create_custom_weight_update_class()

```
var_name_types = None,
pre_var_name_types = None,
post_var_name_types = None,
derived_params = None,
sim_code = None,
event_code = None,
learn_post_code = None,
synapse_dynamics_code = None,
event_threshold_condition_code = None,
pre_spike_code = None,
post_spike_code = None,
sim_support_code = None,
learn_post_support_code = None,
synapse_dynamics_suppport_code = None,
extra_global_params = None,
is_pre_spike_time_required = None,
is_post_spike_time_required = None,
custom\_body = None)
```

This helper function creates a custom WeightUpdateModel class.

See also

```
create_custom_neuron_class
create_custom_postsynaptic_class
create_custom_current_source_class
create_custom_init_var_snippet_class
create_custom_sparse_connect_init_snippet_class
```

Parameters

class_name	name of the new class
param_names	list of strings with param names of the model
var_name_types	list of pairs of strings with variable names and types of the model
pre_var_name_types	list of pairs of strings with presynaptic variable names and types of the model
post_var_name_types	list of pairs of strings with postsynaptic variable names and types of the model
derived_params	list of pairs, where the first member is string with name of the derived
	parameter and the second MUST be an instance of a class which
	<pre>inherits from pygenn.genn_wrapper.DerivedParamFunc</pre>
sim_code	string with the simulation code
event_code	string with the event code
learn_post_code	string with the code to include in learn_synapse_post kernel/function
synapse_dynamics_code	string with the synapse dynamics code
event_threshold_condition_code	string with the event threshold condition code
pre_spike_code	string with the code run once per spiking presynaptic neuron
post_spike_code	string with the code run once per spiking postsynaptic neuron
sim_support_code	string with simulation support code
learn_post_support_code	string with support code for learn_synapse_post kernel/function
synapse_dynamics_suppport_code	string with synapse dynamics support code
extra_global_params	list of pairs of strings with names and types of additional parameters
is_pre_spike_time_required	boolean, is presynaptic spike time required in any weight update kernels?
is_post_spike_time_required	boolean, is postsynaptic spike time required in any weight update kernels?

Parameters

custom_body	dictionary with additional attributes and methods of the new class
-------------	--

19.19.1.9 create_dpf_class()

```
\label{lem:def_pygenn_model.create_dpf_class} \mbox{ (} \\ dp\_func \mbox{ )}
```

Helper function to create derived parameter function class.

Parameters

dp_func	a function which computes the derived parameter and takes two args "pars" (vector of double) and
	"dt" (double)

19.19.1.10 init_connectivity()

This helper function creates a InitSparseConnectivitySnippet::Init object to easily initialise connectivity using a snippet.

Parameters

init_sparse_connect_snippet	type of the InitSparseConnectivitySnippet class as string or instance of class derived from InitSparseConnectivitySnippet::Custom.
param_space	dict with param values for the InitSparseConnectivitySnippet class

19.19.1.11 init_var()

This helper function creates a VarInit object to easily initialise a variable using a snippet.

Parameters

init_var_snippet	type of the InitVarSnippet class as string or instance of class derived from InitVarSnippet::Custom class.	
param_space	dict with param values for the InitVarSnippet class	

19.19.2 Variable Documentation

19.19.2.1 backend_modules

```
pygenn.genn_model.backend_modules = OrderedDict()

19.19.2.2 m

pygenn.genn_model.m = import_module(".genn_wrapper." + b + "Backend", "pygenn")
```

19.20 pygenn.model_preprocessor Namespace Reference

Classes

· class ExtraGlobalVariable

Class holding information about GeNN extra global pointer variable.

· class Variable

Class holding information about GeNN variables.

Functions

def prepare_model (model, group, param_space, var_space, pre_var_space=None, post_var_space=None, model_family=None)

Prepare a model by checking its validity and extracting information about variables and parameters.

def prepare_snippet (snippet, param_space, snippet_family)

Prepare a snippet by checking its validity and extracting information about parameters.

def is_model_valid (model, model_family)

Check whether the model is valid, i.e is native or derived from model_family.Custom.

• def param_space_to_vals (model, param_space)

Convert a param_space dict to ParamValues.

• def param_space_to_val_vec (model, param_space)

Convert a param_space dict to a std::vector<double>

• def var_space_to_vals (model, var_space)

Convert a var_space dict to VarValues.

• def pre_var_space_to_vals (model, var_space)

Convert a var space dict to PreVarValues.

• def post_var_space_to_vals (model, var_space)

Convert a var_space dict to PostVarValues.

Variables

- GeNNType = namedtuple("GeNNType", ["np_dtype", "assign_ext_ptr_array", "assign_ext_ptr_single"])
- dictionary genn_types

19.20.1 Function Documentation

19.20.1.1 is_model_valid()

Check whether the model is valid, i.e is native or derived from model family. Custom.

Parameters

model	string or instance of model_family.Custom	
model_family	model family (NeuronModels, WeightUpdateModels or PostsynapticModels) to which model should belong to	

Returns

instance of the model and its type as string

Raises ValueError if model is not valid (i.e. is not custom and is not natively available)

```
19.20.1.2 param_space_to_val_vec()
```

Convert a param_space dict to a std::vector<double>

Parameters

model	instance of the model
param_space	dict with parameters

Returns

native vector of parameters

```
19.20.1.3 param_space_to_vals()
```

Convert a param_space dict to ParamValues.

Parameters

model	instance of the model
param_space	dict with parameters

Returns

native model's ParamValues

```
19.20.1.4 post_var_space_to_vals()
```

Convert a var_space dict to PostVarValues.

Parameters

model	instance of the weight update model
var_space	dict with Variables

Returns

native model's VarValues

```
19.20.1.5 pre_var_space_to_vals()
```

Convert a var_space dict to PreVarValues.

Parameters

model	instance of the weight update model
var_space	dict with Variables

Returns

native model's VarValues

19.20.1.6 prepare_model()

Prepare a model by checking its validity and extracting information about variables and parameters.

Parameters

model	string or instance of a class derived from
	pygenn.genn_wrapper.NeuronModels.Custom or
	pygenn.genn_wrapper.WeightUpdateModels.Custom or
	pygenn.genn_wrapper.CurrentSourceModels.Custom
group	group model will belong to
param_space	dict with model parameters
var_space	dict with model variables
pre_var_space	optional dict with (weight update) model presynaptic variables
post_var_space	optional dict with (weight update) model postsynaptic variables
model_family	pygenn.genn_wrapper.NeuronModels or
	pygenn.genn_wrapper.WeightUpdateModels or
	pygenn.genn_wrapper.CurrentSourceModels

Returns

tuple consisting of (model instance, model type, model parameter names, model parameters, list of variable names, dict mapping names of variables to instances of class Variable)

19.20.1.7 prepare_snippet()

Prepare a snippet by checking its validity and extracting information about parameters.

Parameters

snippet	string or instance of a class derived from
	<pre>pygenn.genn_wrapper.InitVarSnippet.Custom or</pre>
	<pre>pygenn.genn_wrapper.InitSparseConnectivitySnippet.Custom</pre>
param_space	dict with model parameters
snippet_family	pygenn.genn_wrapper.InitVarSnippet or
	pygenn.genn_wrapper.InitSparseConnectivitySnippet

Returns

tuple consisting of (snippet instance, snippet type, snippet parameter names, snippet parameters)

19.20.1.8 var_space_to_vals()

Convert a var_space dict to VarValues.

Parameters

model	instance of the model
var_space	dict with Variables

Returns

native model's VarValues

19.20.2 Variable Documentation

19.20.2.1 genn_types

dictionary pygenn.model_preprocessor.genn_types

Initial value:

```
1 = {
```

```
2
      "scalar":
                           GeNNType(np.float32, slm.assign_external_pointer_array_f,
      slm.assign_external_pointer_single_f),
3
      "float":
                           GennType(np.float32, slm.assign_external_pointer_array_f,
      {\tt slm.assign\_external\_pointer\_single\_f)}\, ,
      "double":
4
                           GeNNType(np.float64, slm.assign_external_pointer_array_d,
      slm.assign_external_pointer_single_d),
      "int":
                           GennType(np.int32, slm.assign_external_pointer_array_i,
      slm.assign_external_pointer_single_i),
6
      "unsigned int":
                           GeNNType(np.uint32, slm.assign_external_pointer_array_ui,
      slm.assign_external_pointer_single_ui),
7
      "short":
                           GennType(np.int16, slm.assign_external_pointer_array_s,
      slm.assign_external_pointer_single_s),
8
      "unsigned short":
                           GennType (np.uint16, slm.assign external pointer array us,
      slm.assign_external_pointer_single_us),
9
                            GeNNType(np.int8, slm.assign_external_pointer_array_sc,
      {\tt slm.assign\_external\_pointer\_single\_sc)}\,,
       "unsigned char":
1.0
                            GeNNType(np.uint8, slm.assign_external_pointer_array_uc,
      slm.assign_external_pointer_single_uc),
       "uint64_t": GeNNType(np.uint64, None, None),
"int64_t": GeNNType(np.int64, None, None),
"uint32 +" GeNNType(np.uint32, slm.assign e
11
      "int64_t":
      "uint32_t":
                           GeNNType(np.uint32, slm.assign_external_pointer_array_ui,
      slm.assign_external_pointer_single_ui),
14
       "int32 t":
                            GeNNType(np.int32, slm.assign_external_pointer_array_i,
      slm.assign_external_pointer_single_i),
15
       "uint16_t":
                            GeNNType (np.uint16, slm.assign_external_pointer_array_us,
      slm.assign_external_pointer_single_us),
16
                            GeNNType(np.int16, slm.assign_external_pointer_array_s,
      slm.assign_external_pointer_single_s),
17
       "uint8 t":
                            GeNNType(np.uint8, slm.assign_external_pointer_array_uc,
      slm.assign_external_pointer_single_uc),
       "int8_t":
18
                            GeNNType(np.int8, slm.assign_external_pointer_array_sc,
      slm.assign_external_pointer_single_sc) }
```

19.20.2.2 GeNNType

```
pygenn.model\_preprocessor.GeNNType = namedtuple("GeNNType", ["np_dtype", "assign_ext_ptr_\leftrightarrow array", "assign_ext_ptr_single"])
```

19.21 r123 Namespace Reference

19.21.1 Detailed Description

Most of the Random123 C++ API is contained in the r123 namespace.

19.22 Snippet Namespace Reference

Classes

class Base

Base class for all code snippets.

- class Init
- · class ValueBase
- class ValueBase< 0 >

19.22.1 Detailed Description

Wrapper to ensure at compile time that correct number of values are used when specifying the values of a model's parameters and initial state.

19.23 SpineMLSimulator Namespace Reference

Namespaces

Connectors

- Input
- InputValue
- LogOutput
- ModelProperty

Classes

- class NetworkClient
- class Simulator
- · class StateVar
- · class Timer
- · class TimerAccumulate

Typedefs

· typedef float scalar

19.23.1 Typedef Documentation

```
19.23.1.1 scalar
```

```
typedef float SpineMLSimulator::scalar
```

19.24 SpineMLSimulator::Connectors Namespace Reference

Functions

• unsigned int create (const pugi::xml_node &node, double dt, unsigned int numPre, unsigned int numPost, unsigned int **rowLength, unsigned int **ind, uint8_t **delay, const unsigned int *maxRowLength, const filesystem::path &basePath, std::vector< unsigned int > &remapIndices)

19.24.1 Function Documentation

19.24.1.1 create()

19.25 SpineMLSimulator::Input Namespace Reference

Classes

- · class Analogue
- · class Base
- · class InterSpikeIntervalBase
- · class PoissonSpikeRate
- · class RegularSpikeRate
- class SpikeBase
- class SpikeTime

19.26 SpineMLSimulator::InputValue Namespace Reference

Classes

- · class Base
- · class Constant
- · class ConstantArray
- class External
- class ExternalNetwork
- · class ScalarBase
- class TimeVarying
- class TimeVaryingArray

Functions

std::unique_ptr< Base > create (double dt, unsigned int numNeurons, const pugi::xml_node &node, std
 ::map< std::string, InputValue::External *> &externalInputs)

19.26.1 Function Documentation

19.26.1.1 create()

19.27 SpineMLSimulator::LogOutput Namespace Reference

Classes

- class AnalogueBase
- · class AnalogueExternal
- · class AnalogueFile
- · class AnalogueNetwork
- · class Base
- · class Event

19.28 SpineMLSimulator::ModelProperty Namespace Reference

Classes

- · class Base
- · class ExponentialDistribution
- class Fixed
- · class NormalDistribution
- · class UniformDistribution
- · class ValueList

Functions

std::unique_ptr< Base > create (const pugi::xml_node &node, const StateVar< scalar > &stateVar, unsigned int size, bool skipGeNNInitialised, const filesystem::path &basePath, const std::string &value
 Namespace, const std::vector< unsigned int > *remapIndices)

19.28.1 Function Documentation

19.28.1.1 create()

19.29 Utils Namespace Reference

Functions

• GENN_EXPORT bool isRNGRequired (const std::string &code)

Does the code string contain any functions requiring random number generator.

GENN_EXPORT bool isRNGRequired (const std::vector< Models::VarInit > &varInitialisers)

Does the model with the vectors of variable initialisers and modes require an RNG for the specified init location i.e. host or device.

GENN_EXPORT bool isTypePointer (const std::string &type)

Function to determine whether a string containing a type is a pointer.

GENN_EXPORT bool isTypePointerToPointer (const std::string &type)

Function to determine whether a string containing a type is a pointer to a pointer.

GENN_EXPORT std::string getUnderlyingType (const std::string &type)

Assuming type is a string containing a pointer type, function to return the underlying type.

template < class T , typename std::enable_if < std::is_floating_point < T >::value >::type * = nullptr > void write Precise String (std::ostream &os, T value)

This function writes a floating point value to a stream -setting the precision so no digits are lost.

template < class T , typename std::enable_if < std::is_floating_point < T >::value >::type * = nullptr > std::string writePreciseString (T value)

This function writes a floating point value to a string - setting the precision so no digits are lost.

19.29.1 Function Documentation

19.29.1.1 getUnderlyingType()

Assuming type is a string containing a pointer type, function to return the underlying type.

Does the code string contain any functions requiring random number generator.

Does the model with the vectors of variable initialisers and modes require an RNG for the specified init location i.e. host or device.

19.29.1.4 isTypePointer()

Function to determine whether a string containing a type is a pointer.

19.29.1.5 isTypePointerToPointer()

Function to determine whether a string containing a type is a pointer to a pointer.

19.29.1.6 writePreciseString() [1/2]

This function writes a floating point value to a stream -setting the precision so no digits are lost.

19.29.1.7 writePreciseString() [2/2]

```
template<class T , typename std::enable_if< std::is_floating_point< T >::value >::type * =
nullptr>
```

```
std::string Utils::writePreciseString ( T \ value )
```

This function writes a floating point value to a string - setting the precision so no digits are lost.

19.30 WeightUpdateModels Namespace Reference

Classes

· class Base

Base class for all weight update models.

class PiecewiseSTDP

This is a simple STDP rule including a time delay for the finite transmission speed of the synapse.

· class StaticGraded

Graded-potential, static synapse.

· class StaticPulse

Pulse-coupled, static synapse.

class StaticPulseDendriticDelay

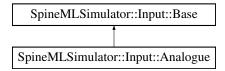
Pulse-coupled, static synapse with heterogenous dendritic delays.

20 Class Documentation

20.1 SpineMLSimulator::Input::Analogue Class Reference

```
#include <input.h>
```

Inheritance diagram for SpineMLSimulator::Input::Analogue:



Public Member Functions

- · virtual void apply (double dt, unsigned long long timestep) override

Additional Inherited Members

20.1.1 Constructor & Destructor Documentation

20.1.1.1 Analogue()

20.1.2 Member Function Documentation

20.1.2.1 apply()

Implements SpineMLSimulator::Input::Base.

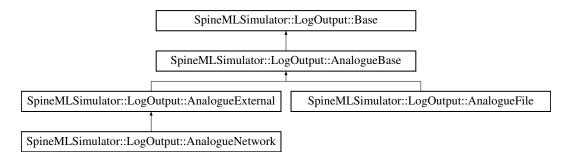
The documentation for this class was generated from the following files:

- · input.h
- · input.cc

20.2 SpineMLSimulator::LogOutput::AnalogueBase Class Reference

```
#include <logOutput.h>
```

Inheritance diagram for SpineMLSimulator::LogOutput::AnalogueBase:



Public Member Functions

- AnalogueBase (const pugi::xml_node &node, double dt, const ModelProperty::Base *modelProperty)
- const scalar * getStateVarBegin () const
- const scalar * getStateVarEnd () const
- unsigned int getModelPropertySize () const
- const std::vector< unsigned int > & getIndices () const

Protected Member Functions

· void pullModelPropertyFromDevice () const

20.2.1 Constructor & Destructor Documentation

20.2.1.1 AnalogueBase()

20.2.2 Member Function Documentation

20.2.2.1 getIndices()

 $\verb|const| std::vector<unsigned int>& SpineMLSimulator::LogOutput::AnalogueBase::getIndices () \\ |const| [inline] \\$

20.2.2.2 getModelPropertySize()

unsigned int SpineMLSimulator::LogOutput::AnalogueBase::getModelPropertySize () const [inline]

20.2.2.3 getStateVarBegin()

const scalar* SpineMLSimulator::LogOutput::AnalogueBase::getStateVarBegin () const [inline]

20.2.2.4 getStateVarEnd()

const scalar* SpineMLSimulator::LogOutput::AnalogueBase::getStateVarEnd () const [inline]

20.2.2.5 pullModelPropertyFromDevice()

void SpineMLSimulator::LogOutput::AnalogueBase::pullModelPropertyFromDevice () const [inline],
[protected]

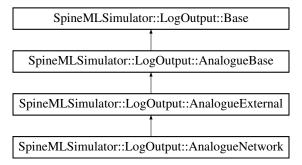
The documentation for this class was generated from the following files:

- · logOutput.h
- logOutput.cc

20.3 SpineMLSimulator::LogOutput::AnalogueExternal Class Reference

#include <logOutput.h>

 $Inheritance\ diagram\ for\ Spine MLS imulator:: Log Output:: Analogue External:$



Public Member Functions

- AnalogueExternal (const pugi::xml_node &node, double dt, const std::string &port, unsigned int popSize, const filesystem::path &logPath, const ModelProperty::Base *modelProperty)
- · virtual void record (double dt, unsigned long long timestep) final

Protected Member Functions

virtual void recordInternal ()

20.3.1 Constructor & Destructor Documentation

20.3.1.1 AnalogueExternal()

20.3.2 Member Function Documentation

20.3.2.1 record()

```
void SpineMLSimulator::LogOutput::AnalogueExternal::record ( \label{eq:double} \ dt, \label{eq:double} \ unsigned \ long \ long \ timestep \ ) \quad \mbox{[final], [virtual]}
```

Implements SpineMLSimulator::LogOutput::Base.

20.3.2.2 recordinternal()

```
virtual void SpineMLSimulator::LogOutput::AnalogueExternal::recordInternal ( ) [inline],
[protected], [virtual]
```

Reimplemented in SpineMLSimulator::LogOutput::AnalogueNetwork.

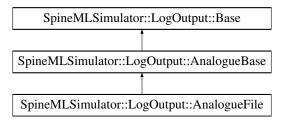
The documentation for this class was generated from the following files:

- logOutput.h
- · logOutput.cc

20.4 SpineMLSimulator::LogOutput::AnalogueFile Class Reference

```
#include <logOutput.h>
```

Inheritance diagram for SpineMLSimulator::LogOutput::AnalogueFile:



Public Member Functions

- AnalogueFile (const pugi::xml_node &node, double dt, unsigned long long numTimeSteps, const std::string &port, unsigned int popSize, const filesystem::path &logPath, const ModelProperty::Base *modelProperty)
- · virtual void record (double dt, unsigned long long timestep) override

Additional Inherited Members

20.4.1 Constructor & Destructor Documentation

20.4.1.1 AnalogueFile()

20.4.2 Member Function Documentation

20.4.2.1 record()

```
void SpineMLSimulator::LogOutput::AnalogueFile::record ( \mbox{double $dt$,} \mbox{unsigned long long $timestep$ ) [override], [virtual]}
```

Implements SpineMLSimulator::LogOutput::Base.

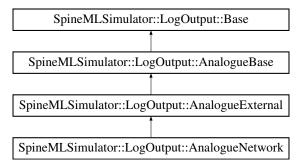
The documentation for this class was generated from the following files:

- logOutput.h
- logOutput.cc

20.5 SpineMLSimulator::LogOutput::AnalogueNetwork Class Reference

```
#include <logOutput.h>
```

Inheritance diagram for SpineMLSimulator::LogOutput::AnalogueNetwork:



Public Member Functions

 AnalogueNetwork (const pugi::xml_node &node, double dt, const std::string &port, unsigned int popSize, const filesystem::path &logPath, const ModelProperty::Base *modelProperty)

Protected Member Functions

· virtual void recordInternal () override

20.5.1 Constructor & Destructor Documentation

20.5.1.1 AnalogueNetwork()

20.5.2 Member Function Documentation

20.5.2.1 recordInternal()

```
void SpineMLSimulator::LogOutput::AnalogueNetwork::recordInternal ( ) [override], [protected],
[virtual]
```

Reimplemented from SpineMLSimulator::LogOutput::AnalogueExternal.

The documentation for this class was generated from the following files:

- logOutput.h
- logOutput.cc

20.6 AnalogueRecorder < T > Class Template Reference

```
#include <analogueRecorder.h>
```

Public Member Functions

- AnalogueRecorder (const std::string &filename, std::initializer_list< T *> variables, unsigned int popSize, const std::string &delimiter="")
- AnalogueRecorder (const std::string &filename, T *variable, unsigned int popSize, const std::string &delimiter="")
- void record (double t)

20.6.1 Constructor & Destructor Documentation

20.6.1.1 AnalogueRecorder() [1/2]

20.6.1.2 AnalogueRecorder() [2/2]

20.6.2 Member Function Documentation

20.6.2.1 record()

```
\label{template} \begin{tabular}{ll} template < typename T > & t
```

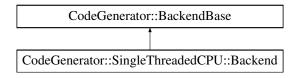
The documentation for this class was generated from the following file:

· analogueRecorder.h

20.7 CodeGenerator::SingleThreadedCPU::Backend Class Reference

```
#include <backend.h>
```

Inheritance diagram for CodeGenerator::SingleThreadedCPU::Backend:



Public Member Functions

- Backend (const std::string &scalarType, const Preferences &preferences)
- virtual void genNeuronUpdate (CodeStream &os, const ModelSpecMerged &modelMerged, NeuronGroup
 — SimHandler simHandler, NeuronUpdateGroupMergedHandler wuVarUpdateHandler, HostHandler pushEG
 — PHandler) const override

Generate platform-specific function to update the state of all neurons.

virtual void genSynapseUpdate (CodeStream &os, const ModelSpecMerged &modelMerged, Presynaptic
 UpdateGroupMergedHandler wumThreshHandler, PresynapticUpdateGroupMergedHandler wumSim
 Handler, PresynapticUpdateGroupMergedHandler wumEventHandler, PresynapticUpdateGroupMerged
 Handler wumProceduralConnectHandler, PostsynapticUpdateGroupMergedHandler postLearnHandler,

SynapseDynamicsGroupMergedHandler synapseDynamicsHandler, HostHandler pushEGPHandler) const override

Generate platform-specific function to update the state of all synapses.

- virtual void genInit (CodeStream &os, const ModelSpecMerged &modelMerged, NeuronInitGroup
 MergedHandler localNGHandler, SynapseDenseInitGroupMergedHandler sgDenseInitHandler, Synapse
 ConnectivityInitMergedGroupHandler sgSparseConnectHandler, SynapseSparseInitGroupMergedHandler
 sgSparseInitHandler, HostHandler initPushEGPHandler, HostHandler initSparsePushEGPHandler) const
 override
- virtual size_t getSynapticMatrixRowStride (const SynapseGroupInternal &sg) const override

Gets the stride used to access synaptic matrix rows, taking into account sparse data structure, padding etc.

 virtual void genDefinitionsPreamble (CodeStream &os, const ModelSpecMerged &modelMerged) const override

Definitions is the usercode-facing header file for the generated code. This function generates a 'preamble' to this header file.

virtual void genDefinitionsInternalPreamble (CodeStream &os, const ModelSpecMerged &modelMerged)
 const override

Definitions internal is the internal header file for the generated code. This function generates a 'preamble' to this header file.

- virtual void genRunnerPreamble (CodeStream &os, const ModelSpecMerged &modelMerged) const override
- virtual void genAllocateMemPreamble (CodeStream &os, const ModelSpecMerged &modelMerged) const override
- virtual void genStepTimeFinalisePreamble (CodeStream &os, const ModelSpecMerged &modelMerged) const override

After all timestep logic is complete.

- virtual void genVariableImplementation (CodeStream &os, const std::string &type, const std::string &name,
 VarLocation loc) const override
- virtual MemAlloc genVariableAllocation (CodeStream &os, const std::string &type, const std::string &name, VarLocation loc, size_t count) const override
- virtual void genVariableFree (CodeStream &os, const std::string &name, VarLocation loc) const override
- virtual void genExtraGlobalParamDefinition (CodeStream &definitions, const std::string &type, const std
 ::string &name, VarLocation loc) const override
- virtual void genExtraGlobalParamImplementation (CodeStream &os, const std::string &type, const std::string &name, VarLocation loc) const override
- virtual void genExtraGlobalParamAllocation (CodeStream &os, const std::string &type, const std::string &name, VarLocation loc, const std::string &countVarName="count", const std::string &prefix="") const override
- virtual void genExtraGlobalParamPush (CodeStream &os, const std::string &type, const std::string &name, VarLocation loc, const std::string &countVarName="count", const std::string &prefix="") const override
- virtual void genExtraGlobalParamPull (CodeStream &os, const std::string &type, const std::string &name, VarLocation loc, const std::string &countVarName="count", const std::string &prefix="") const override

Generate code for declaring merged group data to the 'device'.

virtual void genMergedGroupPush (CodeStream &os, const std::string &suffix, size_t idx, size_t numGroups)
 const override

Generate code for pushing merged group data to the 'device'.

virtual void genMergedExtraGlobalParamPush (CodeStream &os, const std::string &suffix, size_t merged
 Groupldx, const std::string &groupldx, const std::string &fieldName, const std::string &egpName) const override

! Generate code for pushing an updated EGP value into the merged group structure on 'device'

 virtual void genPopVariableInit (CodeStream &os, const Substitutions &kernelSubs, Handler handler) const override

- virtual void genVariableInit (CodeStream &os, const std::string &count, const std::string &indexVarName, const Substitutions &kernelSubs, Handler handler) const override
- virtual void genSynapseVariableRowInit (CodeStream &os, const SynapseGroupMergedBase &sg, const Substitutions &kernelSubs, Handler handler) const override
- virtual void genVariablePush (CodeStream &os, const std::string &type, const std::string &name, VarLocation loc, bool autoInitialized, size t count) const override

Generate code for pushing a variable to the 'device'.

• virtual void genVariablePull (CodeStream &os, const std::string &type, const std::string &name, VarLocation loc, size_t count) const override

Generate code for pulling a variable from the 'device'.

 virtual void genCurrentVariablePush (CodeStream &os, const NeuronGroupInternal &ng, const std::string &type, const std::string &name, VarLocation loc) const override

Generate code for pushing a variable's value in the current timestep to the 'device'.

• virtual void genCurrentVariablePull (CodeStream &os, const NeuronGroupInternal &ng, const std::string &type, const std::string &name, VarLocation loc) const override

Generate code for pulling a variable's value in the current timestep from the 'device'.

- virtual void genCurrentTrueSpikePush (CodeStream &os, const NeuronGroupInternal &ng) const override Generate code for pushing true spikes emitted by a neuron group in the current timestep to the 'device'.
- virtual void genCurrentTrueSpikePull (CodeStream &os, const NeuronGroupInternal &ng) const override Generate code for pulling true spikes emitted by a neuron group in the current timestep from the 'device'.
- virtual void genCurrentSpikeLikeEventPush (CodeStream &os, const NeuronGroupInternal &ng) const override

Generate code for pushing spike-like events emitted by a neuron group in the current timestep to the 'device'.

- virtual void genCurrentSpikeLikeEventPull (CodeStream &os, const NeuronGroupInternal &ng) const override Generate code for pulling spike-like events emitted by a neuron group in the current timestep from the 'device'.
- virtual MemAlloc genGlobalDeviceRNG (CodeStream &definitions, CodeStream &definitionsInternal, Code
 Stream &runner, CodeStream &allocations, CodeStream &free) const override

Generate a single RNG instance.

Generate an RNG with a state per population member.

- virtual void genTimer (CodeStream &definitions, CodeStream &definitionsInternal, CodeStream &runner, CodeStream &allocations, CodeStream &free, CodeStream &stepTimeFinalise, const std::string &name, bool updateInStepTime) const override
- virtual void genReturnFreeDeviceMemoryBytes (CodeStream &os) const override

Generate code to return amount of free 'device' memory in bytes.

• virtual void genMakefilePreamble (std::ostream &os) const override

This function can be used to generate a preamble for the GNU makefile used to build.

- virtual void genMakefileLinkRule (std::ostream &os) const override
- virtual void genMakefileCompileRule (std::ostream &os) const override
- virtual void genMSBuildConfigProperties (std::ostream &os) const override
- virtual void genMSBuildImportProps (std::ostream &os) const override
- virtual void genMSBuildItemDefinitions (std::ostream &os) const override
- virtual void genMSBuildCompileModule (const std::string &moduleName, std::ostream &os) const override
- virtual void genMSBuildImportTarget (std::ostream &os) const override
- virtual std::string getArrayPrefix () const override
- virtual std::string getScalarPrefix () const override
- virtual bool isGlobalHostRNGRequired (const ModelSpecMerged &modelMerged) const override

Different backends use different RNGs for different things. Does this one require a global host RNG for the specified model?

virtual bool isGlobalDeviceRNGRequired (const ModelSpecMerged &modelMerged) const override

Different backends use different RNGs for different things. Does this one require a global device RNG for the specified model?

· virtual bool isPopulationRNGRequired () const override

Different backends use different RNGs for different things. Does this one require population RNGs?

virtual bool isSynRemapRequired () const override

Different backends may implement synapse dynamics differently. Does this one require a synapse remapping data structure?

virtual bool isPostsynapticRemapRequired () const override

Different backends may implement synaptic plasticity differently. Does this one require a postsynaptic remapping data structure?

virtual bool isAutomaticCopyEnabled () const override

Is automatic copy mode enabled in the preferences?

virtual bool shouldGenerateEmptyStatePushPull () const override

Should GeNN generate empty state push and pull functions?

• virtual bool shouldGenerateExtraGlobalParamPull () const override

Should GeNN generate pull functions for extra global parameters? These are very rarely used.

virtual size_t getDeviceMemoryBytes () const override

How many bytes of memory does 'device' have.

 virtual MemorySpaces getMergedGroupMemorySpaces (const ModelSpecMerged &modelMerged) const override

Additional Inherited Members

20.7.1 Constructor & Destructor Documentation

20.7.1.1 Backend()

20.7.2 Member Function Documentation

20.7.2.1 genAllocateMemPreamble()

Allocate memory is the first function in GeNN generated code called by usercode and it should only ever be called once. Therefore it's a good place for any global initialisation. This function generates a 'preamble' to this function.

Implements CodeGenerator::BackendBase.

20.7.2.2 genCurrentSpikeLikeEventPull()

Generate code for pulling spike-like events emitted by a neuron group in the current timestep from the 'device'.

20.7.2.3 genCurrentSpikeLikeEventPush()

Generate code for pushing spike-like events emitted by a neuron group in the current timestep to the 'device'.

Implements CodeGenerator::BackendBase.

20.7.2.4 genCurrentTrueSpikePull()

Generate code for pulling true spikes emitted by a neuron group in the current timestep from the 'device'.

Implements CodeGenerator::BackendBase.

20.7.2.5 genCurrentTrueSpikePush()

Generate code for pushing true spikes emitted by a neuron group in the current timestep to the 'device'.

Implements CodeGenerator::BackendBase.

20.7.2.6 genCurrentVariablePull()

Generate code for pulling a variable's value in the current timestep from the 'device'.

Implements CodeGenerator::BackendBase.

20.7.2.7 genCurrentVariablePush()

Generate code for pushing a variable's value in the current timestep to the 'device'.

20.7.2.8 genDefinitionsInternalPreamble()

Definitions internal is the internal header file for the generated code. This function generates a 'preamble' to this header file.

This will only be included by the platform-specific compiler used to build this backend so can include platform-specific types or headers

Implements CodeGenerator::BackendBase.

20.7.2.9 genDefinitionsPreamble()

Definitions is the usercode-facing header file for the generated code. This function generates a 'preamble' to this header file.

This will be included from a standard C++ compiler so shouldn't include any platform-specific types or headers Implements CodeGenerator::BackendBase.

20.7.2.10 genExtraGlobalParamAllocation()

Implements CodeGenerator::BackendBase.

20.7.2.11 genExtraGlobalParamDefinition()

Implements CodeGenerator::BackendBase.

20.7.2.12 genExtraGlobalParamImplementation()

20.7.2.13 genExtraGlobalParamPull()

Implements CodeGenerator::BackendBase.

20.7.2.14 genExtraGlobalParamPush()

Implements CodeGenerator::BackendBase.

20.7.2.15 genGlobalDeviceRNG()

Generate a single RNG instance.

On single-threaded platforms this can be a standard RNG like M.T. but, on parallel platforms, it is likely to be a counter-based RNG

 $Implements\ Code Generator :: Backend Base.$

20.7.2.16 genInit()

20.7.2.17 genMakefileCompileRule()

The GNU make build system uses 'pattern rules' (https://www.gnu.org/software/make/manual/html-_node/Pattern-Intro.html) to build backend modules into objects. This function should generate a GNU make pattern rule capable of building each module (i.e. compiling .cc file \$< into .o file \$@).

Implements CodeGenerator::BackendBase.

20.7.2.18 genMakefileLinkRule()

The GNU make build system will populate a variable called with a list of objects to link. This function should generate a GNU make rule to build these objects into a shared library.

Implements CodeGenerator::BackendBase.

20.7.2.19 genMakefilePreamble()

This function can be used to generate a preamble for the GNU makefile used to build.

Implements CodeGenerator::BackendBase.

20.7.2.20 genMergedExtraGlobalParamPush()

! Generate code for pushing an updated EGP value into the merged group structure on 'device'

Implements CodeGenerator::BackendBase.

20.7.2.21 genMergedGroupImplementation()

Generate code for declaring merged group data to the 'device'.

20.7.2.22 genMergedGroupPush()

Generate code for pushing merged group data to the 'device'.

Implements CodeGenerator::BackendBase.

20.7.2.23 genMSBuildCompileModule()

Implements CodeGenerator::BackendBase.

20.7.2.24 genMSBuildConfigProperties()

In MSBuild, 'properties' are used to configure global project settings e.g. whether the MSBuild project builds a static or dynamic library This function can be used to add additional XML properties to this section.

 $\textbf{see} \quad \texttt{https://docs.microsoft.com/en-us/visualstudio/msbuild/msbuild-properties} \\ \textbf{for more information.}$

Implements CodeGenerator::BackendBase.

20.7.2.25 genMSBuildImportProps()

Implements CodeGenerator::BackendBase.

20.7.2.26 genMSBuildImportTarget()

Implements CodeGenerator::BackendBase.

20.7.2.27 genMSBuildItemDefinitions()

In MSBuild, the 'item definitions' are used to override the default properties of 'items' such as <ClCompile> or <Link>. This function should generate XML to correctly configure the 'items' required to build the generated code, taking into account etc.

see https://docs.microsoft.com/en-us/visualstudio/msbuild/msbuild-items#item-definitions
for more information.

Implements CodeGenerator::BackendBase.

20.7.2.28 genNeuronUpdate()

Generate platform-specific function to update the state of all neurons.

Parameters

os	CodeStream to write function to
model	merged model to generate code for
simHandler	callback to write platform-independent code to update an individual NeuronGroup
wuVarUpdateHandler	callback to write platform-independent code to update pre and postsynaptic weight update model variables when neuron spikes

Implements CodeGenerator::BackendBase.

20.7.2.29 genPopulationRNG()

Generate an RNG with a state per population member.

Implements CodeGenerator::BackendBase.

20.7.2.30 genPopVariableInit()

Implements CodeGenerator::BackendBase.

20.7.2.31 genReturnFreeDeviceMemoryBytes()

Generate code to return amount of free 'device' memory in bytes.

20.7.2.32 genRunnerPreamble()

Implements CodeGenerator::BackendBase.

20.7.2.33 genStepTimeFinalisePreamble()

After all timestep logic is complete.

Implements CodeGenerator::BackendBase.

20.7.2.34 genSynapseUpdate()

Generate platform-specific function to update the state of all synapses.

Parameters

os	CodeStream to write function to
model	model to generate code for
wumThreshHandler	callback to write platform-independent code to update an individual NeuronGroup
wumSimHandler	callback to write platform-independent code to process presynaptic spikes. "id_pre", "id_post" and "id_syn" variables; and either "addToInSynDelay" or "addToInSyn" function will be provided to callback via Substitutions.
wumEventHandler	callback to write platform-independent code to process presynaptic spike-like events. "id_pre", "id_post" and "id_syn" variables; and either "addToInSynDelay" or "addToInSyn" function will be provided to callback via Substitutions.
wumProceduralConnectHandler	callback to write platform-indepent code to procedurally generate connectivity "id_pre" variable and "addSynapse" function will be provided to callback via Substitutions. callback needs to implement loop over synapses in row, providing "synAddress" variable if INDIVIDUALG
postLearnHandler	callback to write platform-independent code to process postsynaptic spikes. "id_pre", "id_post" and "id_syn" variables will be provided to callback via Substitutions.

Parameters

synapseDynamicsHandler	callback to write platform-independent code to update time-driven synapse	Ī
	dynamics. "id_pre", "id_post" and "id_syn" variables; and either	
	"addToInSynDelay" or "addToInSyn" function will be provided to callback via	
	Substitutions.	

Implements CodeGenerator::BackendBase.

20.7.2.35 genSynapseVariableRowInit()

Implements CodeGenerator::BackendBase.

20.7.2.36 genTimer()

Implements CodeGenerator::BackendBase.

20.7.2.37 genVariableAllocation()

Implements CodeGenerator::BackendBase.

20.7.2.38 genVariableDefinition()

20.7.2.39 genVariableFree()

Implements CodeGenerator::BackendBase.

20.7.2.40 genVariableImplementation()

Implements CodeGenerator::BackendBase.

20.7.2.41 genVariableInit()

Implements CodeGenerator::BackendBase.

20.7.2.42 genVariablePull()

Generate code for pulling a variable from the 'device'.

Implements CodeGenerator::BackendBase.

20.7.2.43 genVariablePush()

Generate code for pushing a variable to the 'device'.

20.7.2.44 getArrayPrefix()

```
virtual std::string CodeGenerator::SingleThreadedCPU::Backend::getArrayPrefix ( ) const [inline],
[override], [virtual]
```

When backends require separate 'device' and 'host' versions of variables, they are identified with a prefix. This function returns this prefix so it can be used in otherwise platform-independent code.

Reimplemented from CodeGenerator::BackendBase.

20.7.2.45 getDeviceMemoryBytes()

```
virtual size_t CodeGenerator::SingleThreadedCPU::Backend::getDeviceMemoryBytes ( ) const [inline],
[override], [virtual]
```

How many bytes of memory does 'device' have.

Implements CodeGenerator::BackendBase.

20.7.2.46 getMergedGroupMemorySpaces()

Some backends will have additional small, fast, memory spaces for read-only data which might Be well-suited to storing merged group structs. This method returns the prefix required to Place arrays in these and their size in preferential order

Implements CodeGenerator::BackendBase.

20.7.2.47 getScalarPrefix()

```
virtual std::string CodeGenerator::SingleThreadedCPU::Backend::getScalarPrefix ( ) const [inline],
[override], [virtual]
```

Reimplemented from CodeGenerator::BackendBase.

20.7.2.48 getSynapticMatrixRowStride()

Gets the stride used to access synaptic matrix rows, taking into account sparse data structure, padding etc.

Implements CodeGenerator::BackendBase.

20.7.2.49 isAutomaticCopyEnabled()

```
virtual bool CodeGenerator::SingleThreadedCPU::Backend::isAutomaticCopyEnabled ( ) const [inline],
[override], [virtual]
```

Is automatic copy mode enabled in the preferences?

20.7.2.50 isGlobalDeviceRNGRequired()

Different backends use different RNGs for different things. Does this one require a global device RNG for the specified model?

Implements CodeGenerator::BackendBase.

20.7.2.51 isGlobalHostRNGRequired()

Different backends use different RNGs for different things. Does this one require a global host RNG for the specified model?

Implements CodeGenerator::BackendBase.

20.7.2.52 isPopulationRNGRequired()

```
virtual bool CodeGenerator::SingleThreadedCPU::Backend::isPopulationRNGRequired ( ) const
[inline], [override], [virtual]
```

Different backends use different RNGs for different things. Does this one require population RNGs?

Implements CodeGenerator::BackendBase.

20.7.2.53 isPostsynapticRemapRequired()

```
virtual bool CodeGenerator::SingleThreadedCPU::Backend::isPostsynapticRemapRequired ( ) const
[inline], [override], [virtual]
```

Different backends may implement synaptic plasticity differently. Does this one require a postsynaptic remapping data structure?

Implements CodeGenerator::BackendBase.

20.7.2.54 isSynRemapRequired()

```
virtual bool CodeGenerator::SingleThreadedCPU::Backend::isSynRemapRequired ( ) const [inline],
[override], [virtual]
```

Different backends may implement synapse dynamics differently. Does this one require a synapse remapping data structure?

 $Implements\ Code Generator :: Backend Base.$

20.7.2.55 shouldGenerateEmptyStatePushPull()

```
virtual bool CodeGenerator::SingleThreadedCPU::Backend::shouldGenerateEmptyStatePushPull ( )
const [inline], [override], [virtual]
```

Should GeNN generate empty state push and pull functions?

20.7.2.56 shouldGenerateExtraGlobalParamPull()

```
virtual bool CodeGenerator::SingleThreadedCPU::Backend::shouldGenerateExtraGlobalParamPull ( )
const [inline], [override], [virtual]
```

Should GeNN generate pull functions for extra global parameters? These are very rarely used.

Implements CodeGenerator::BackendBase.

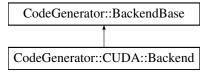
The documentation for this class was generated from the following files:

- single_threaded_cpu/backend.h
- single_threaded_cpu/backend.cc

20.8 CodeGenerator::CUDA::Backend Class Reference

#include <backend.h>

Inheritance diagram for CodeGenerator::CUDA::Backend:



Public Member Functions

- Backend (const KernelBlockSize &kernelBlockSizes, const Preferences &preferences, const std::string &scalarType, int device)
- virtual void genNeuronUpdate (CodeStream &os, const ModelSpecMerged &modelMerged, NeuronGroup
 — SimHandler simHandler, NeuronUpdateGroupMergedHandler wuVarUpdateHandler, HostHandler pushEG
 — PHandler) const override

Generate platform-specific function to update the state of all neurons.

virtual void genSynapseUpdate (CodeStream &os, const ModelSpecMerged &modelMerged, Presynaptic
 UpdateGroupMergedHandler wumThreshHandler, PresynapticUpdateGroupMergedHandler wumSim
 Handler, PresynapticUpdateGroupMergedHandler wumEventHandler, PresynapticUpdateGroupMerged
 Handler wumProceduralConnectHandler, PostsynapticUpdateGroupMergedHandler postLearnHandler,
 SynapseDynamicsGroupMergedHandler synapseDynamicsHandler, HostHandler pushEGPHandler) const
 override

Generate platform-specific function to update the state of all synapses.

- virtual void genInit (CodeStream &os, const ModelSpecMerged &modelMerged, NeuronInitGroup
 MergedHandler localNGHandler, SynapseDenseInitGroupMergedHandler sgDenseInitHandler, Synapse
 ConnectivityInitMergedGroupHandler sgSparseConnectHandler, SynapseSparseInitGroupMergedHandler
 sgSparseInitHandler, HostHandler initPushEGPHandler, HostHandler initSparsePushEGPHandler) const
 override
- virtual size_t getSynapticMatrixRowStride (const SynapseGroupInternal &sg) const override
 Gets the stride used to access synaptic matrix rows, taking into account sparse data structure, padding etc.
- virtual void genDefinitionsPreamble (CodeStream &os, const ModelSpecMerged &modelMerged) const override

Definitions is the usercode-facing header file for the generated code. This function generates a 'preamble' to this header file

virtual void genDefinitionsInternalPreamble (CodeStream &os, const ModelSpecMerged &modelMerged)
 const override

Definitions internal is the internal header file for the generated code. This function generates a 'preamble' to this header file.

virtual void genRunnerPreamble (CodeStream &os, const ModelSpecMerged &modelMerged) const override

- virtual void genAllocateMemPreamble (CodeStream &os, const ModelSpecMerged &modelMerged) const override
- virtual void genStepTimeFinalisePreamble (CodeStream &os, const ModelSpecMerged &modelMerged) const override

After all timestep logic is complete.

- virtual void genVariableImplementation (CodeStream &os, const std::string &type, const std::string &name, VarLocation loc) const override
- virtual MemAlloc genVariableAllocation (CodeStream &os, const std::string &type, const std::string &name, VarLocation loc, size_t count) const override
- virtual void genVariableFree (CodeStream &os, const std::string &name, VarLocation loc) const override
- virtual void genExtraGlobalParamImplementation (CodeStream &os, const std::string &type, const std::string &name, VarLocation loc) const override
- virtual void genExtraGlobalParamAllocation (CodeStream &os, const std::string &type, const std::string &name, VarLocation loc, const std::string &countVarName="count", const std::string &prefix="") const override
- virtual void genExtraGlobalParamPush (CodeStream &os, const std::string &type, const std::string &name,
 VarLocation loc, const std::string &countVarName="count", const std::string &prefix=""") const override
- virtual void genExtraGlobalParamPull (CodeStream &os, const std::string &type, const std::string &name, VarLocation loc, const std::string &countVarName="count", const std::string &prefix="") const override

Generate code for declaring merged group data to the 'device'.

virtual void genMergedGroupPush (CodeStream &os, const std::string &suffix, size_t idx, size_t numGroups)
 const override

Generate code for pushing merged group data to the 'device'.

virtual void genMergedExtraGlobalParamPush (CodeStream &os, const std::string &suffix, size_t merged
 GroupIdx, const std::string &groupIdx, const std::string &fieldName, const std::string &egpName) const override

Generate code for pushing an updated EGP value into the merged group structure on 'device'.

- virtual void genPopVariableInit (CodeStream &os, const Substitutions &kernelSubs, Handler handler) const override
- virtual void genVariableInit (CodeStream &os, const std::string &count, const std::string &indexVarName, const Substitutions &kernelSubs, Handler handler) const override
- virtual void genSynapseVariableRowInit (CodeStream &os, const SynapseGroupMergedBase &sg, const Substitutions &kernelSubs, Handler handler) const override
- virtual void genVariablePush (CodeStream &os, const std::string &type, const std::string &name, VarLocation loc, bool autoInitialized, size_t count) const override

Generate code for pushing a variable to the 'device'.

• virtual void genVariablePull (CodeStream &os, const std::string &type, const std::string &name, VarLocation loc, size_t count) const override

Generate code for pulling a variable from the 'device'.

 virtual void genCurrentVariablePush (CodeStream &os, const NeuronGroupInternal &ng, const std::string &type, const std::string &name, VarLocation loc) const override

Generate code for pushing a variable's value in the current timestep to the 'device'.

• virtual void genCurrentVariablePull (CodeStream &os, const NeuronGroupInternal &ng, const std::string &type, const std::string &name, VarLocation loc) const override

Generate code for pulling a variable's value in the current timestep from the 'device'.

- virtual void genCurrentTrueSpikePush (CodeStream &os, const NeuronGroupInternal &ng) const override

 Generate code for pushing true spikes emitted by a neuron group in the current timestep to the 'device'.
- · virtual void genCurrentTrueSpikePull (CodeStream &os, const NeuronGroupInternal &ng) const override

Generate code for pulling true spikes emitted by a neuron group in the current timestep from the 'device'.

 virtual void genCurrentSpikeLikeEventPush (CodeStream &os, const NeuronGroupInternal &ng) const override

Generate code for pushing spike-like events emitted by a neuron group in the current timestep to the 'device'.

- virtual void genCurrentSpikeLikeEventPull (CodeStream &os, const NeuronGroupInternal &ng) const override
 - Generate code for pulling spike-like events emitted by a neuron group in the current timestep from the 'device'.

Generate a single RNG instance.

virtual MemAlloc genPopulationRNG (CodeStream &definitions, CodeStream &definitionsInternal, Code
 Stream &runner, CodeStream &allocations, CodeStream &free, const std::string &name, size_t count) const override

Generate an RNG with a state per population member.

- virtual void genTimer (CodeStream &definitions, CodeStream &definitionsInternal, CodeStream &runner, CodeStream &allocations, CodeStream &free, CodeStream &stepTimeFinalise, const std::string &name, bool updateInStepTime) const override
- virtual void genReturnFreeDeviceMemoryBytes (CodeStream &os) const override

Generate code to return amount of free 'device' memory in bytes.

• virtual void genMakefilePreamble (std::ostream &os) const override

This function can be used to generate a preamble for the GNU makefile used to build.

- · virtual void genMakefileLinkRule (std::ostream &os) const override
- · virtual void genMakefileCompileRule (std::ostream &os) const override
- virtual void genMSBuildConfigProperties (std::ostream &os) const override
- · virtual void genMSBuildImportProps (std::ostream &os) const override
- · virtual void genMSBuildItemDefinitions (std::ostream &os) const override
- virtual void genMSBuildCompileModule (const std::string &moduleName, std::ostream &os) const override
- virtual void genMSBuildImportTarget (std::ostream &os) const override
- virtual std::string getArrayPrefix () const override
- virtual std::string getScalarPrefix () const override
- virtual bool isGlobalHostRNGRequired (const ModelSpecMerged &modelMerged) const override

Different backends use different RNGs for different things. Does this one require a global host RNG for the specified model?

virtual bool isGlobalDeviceRNGRequired (const ModelSpecMerged &modelMerged) const override

Different backends use different RNGs for different things. Does this one require a global device RNG for the specified model?

virtual bool isPopulationRNGRequired () const override

Different backends use different RNGs for different things. Does this one require population RNGs?

virtual bool isSynRemapRequired () const override

Different backends may implement synapse dynamics differently. Does this one require a synapse remapping data structure?

virtual bool isPostsynapticRemapRequired () const override

Different backends may implement synaptic plasticity differently. Does this one require a postsynaptic remapping data structure?

virtual bool isAutomaticCopyEnabled () const override

Is automatic copy mode enabled in the preferences?

· virtual bool shouldGenerateEmptyStatePushPull () const override

Should GeNN generate empty state push and pull functions?

virtual bool shouldGenerateExtraGlobalParamPull () const override

Should GeNN generate pull functions for extra global parameters? These are very rarely used.

• virtual size_t getDeviceMemoryBytes () const override

How many bytes of memory does 'device' have.

 virtual MemorySpaces getMergedGroupMemorySpaces (const ModelSpecMerged &modelMerged) const override

- const cudaDeviceProp & getChosenCUDADevice () const
- int getChosenDeviceID () const
- int getRuntimeVersion () const
- std::string getNVCCFlags () const
- std::string getFloatAtomicAdd (const std::string &ftype) const
- size_t getNumInitialisationRNGStreams (const ModelSpecMerged &modelMerged) const

Get total number of RNG streams potentially used to initialise model.

• size_t getKernelBlockSize (Kernel kernel) const

Static Public Member Functions

- static size_t getNumPresynapticUpdateThreads (const SynapseGroupInternal &sg, const cudaDeviceProp &deviceProps, const Preferences &preferences)
- static size t getNumPostsynapticUpdateThreads (const SynapseGroupInternal &sg)
- static size t getNumSynapseDynamicsThreads (const SynapseGroupInternal &sg)
- static void addPresynapticUpdateStrategy (PresynapticUpdateStrategy::Base *strategy)

Register a new presynaptic update strategy.

Static Public Attributes

static const char * KernelNames [KernelMax]

Additional Inherited Members

20.8.1 Constructor & Destructor Documentation

20.8.1.1 Backend()

20.8.2 Member Function Documentation

20.8.2.1 addPresynapticUpdateStrategy()

Register a new presynaptic update strategy.

This function should be called with strategies in ascending order of preference

20.8.2.2 genAllocateMemPreamble()

Allocate memory is the first function in GeNN generated code called by usercode and it should only ever be called once. Therefore it's a good place for any global initialisation. This function generates a 'preamble' to this function.

Implements CodeGenerator::BackendBase.

20.8.2.3 genCurrentSpikeLikeEventPull()

Generate code for pulling spike-like events emitted by a neuron group in the current timestep from the 'device'. Implements CodeGenerator::BackendBase.

20.8.2.4 genCurrentSpikeLikeEventPush()

Generate code for pushing spike-like events emitted by a neuron group in the current timestep to the 'device'. Implements CodeGenerator::BackendBase.

20.8.2.5 genCurrentTrueSpikePull()

Generate code for pulling true spikes emitted by a neuron group in the current timestep from the 'device'.

Implements CodeGenerator::BackendBase.

20.8.2.6 genCurrentTrueSpikePush()

Generate code for pushing true spikes emitted by a neuron group in the current timestep to the 'device'.

Implements CodeGenerator::BackendBase.

20.8.2.7 genCurrentVariablePull()

Generate code for pulling a variable's value in the current timestep from the 'device'.

20.8.2.8 genCurrentVariablePush()

Generate code for pushing a variable's value in the current timestep to the 'device'.

Implements CodeGenerator::BackendBase.

20.8.2.9 genDefinitionsInternalPreamble()

Definitions internal is the internal header file for the generated code. This function generates a 'preamble' to this header file.

This will only be included by the platform-specific compiler used to build this backend so can include platform-specific types or headers

Implements CodeGenerator::BackendBase.

20.8.2.10 genDefinitionsPreamble()

Definitions is the usercode-facing header file for the generated code. This function generates a 'preamble' to this header file.

This will be included from a standard C++ compiler so shouldn't include any platform-specific types or headers Implements CodeGenerator::BackendBase.

20.8.2.11 genExtraGlobalParamAllocation()

Implements CodeGenerator::BackendBase.

20.8.2.12 genExtraGlobalParamDefinition()

```
VarLocation loc ) const [override], [virtual]
```

Implements CodeGenerator::BackendBase.

20.8.2.13 genExtraGlobalParamImplementation()

Implements CodeGenerator::BackendBase.

20.8.2.14 genExtraGlobalParamPull()

Implements CodeGenerator::BackendBase.

20.8.2.15 genExtraGlobalParamPush()

Implements CodeGenerator::BackendBase.

20.8.2.16 genGlobalDeviceRNG()

Generate a single RNG instance.

On single-threaded platforms this can be a standard RNG like M.T. but, on parallel platforms, it is likely to be a counter-based RNG

20.8.2.17 genInit()

Implements CodeGenerator::BackendBase.

20.8.2.18 genMakefileCompileRule()

The GNU make build system uses 'pattern rules' (https://www.gnu.org/software/make/manual/html-_node/Pattern-Intro.html) to build backend modules into objects. This function should generate a GNU make pattern rule capable of building each module (i.e. compiling .cc file \$< into .o file \$@).

Implements CodeGenerator::BackendBase.

20.8.2.19 genMakefileLinkRule()

The GNU make build system will populate a variable called with a list of objects to link. This function should generate a GNU make rule to build these objects into a shared library.

Implements CodeGenerator::BackendBase.

20.8.2.20 genMakefilePreamble()

This function can be used to generate a preamble for the GNU makefile used to build.

Implements CodeGenerator::BackendBase.

20.8.2.21 genMergedExtraGlobalParamPush()

Generate code for pushing an updated EGP value into the merged group structure on 'device'.

20.8.2.22 genMergedGroupImplementation()

Generate code for declaring merged group data to the 'device'.

Implements CodeGenerator::BackendBase.

20.8.2.23 genMergedGroupPush()

Generate code for pushing merged group data to the 'device'.

Implements CodeGenerator::BackendBase.

20.8.2.24 genMSBuildCompileModule()

Implements CodeGenerator::BackendBase.

20.8.2.25 genMSBuildConfigProperties()

In MSBuild, 'properties' are used to configure global project settings e.g. whether the MSBuild project builds a static or dynamic library This function can be used to add additional XML properties to this section.

 $\textbf{see} \quad \texttt{https://docs.microsoft.com/en-us/visualstudio/msbuild/msbuild-properties} \\ \textbf{for more information.} \\$

Implements CodeGenerator::BackendBase.

20.8.2.26 genMSBuildImportProps()

Implements CodeGenerator::BackendBase.

20.8.2.27 genMSBuildImportTarget()

Implements CodeGenerator::BackendBase.

20.8.2.28 genMSBuildItemDefinitions()

In MSBuild, the 'item definitions' are used to override the default properties of 'items' such as <ClCompile> or <Link>. This function should generate XML to correctly configure the 'items' required to build the generated code, taking into account etc.

 $\textbf{see} \ \texttt{https://docs.microsoft.com/en-us/visualstudio/msbuild/msbuild-items\#item-definitions} \\ \textbf{for more information.} \\$

Implements CodeGenerator::BackendBase.

20.8.2.29 genNeuronUpdate()

Generate platform-specific function to update the state of all neurons.

Parameters

os	CodeStream to write function to
model	merged model to generate code for
simHandler	callback to write platform-independent code to update an individual NeuronGroup
wuVarUpdateHandler	callback to write platform-independent code to update pre and postsynaptic weight update model variables when neuron spikes

Implements CodeGenerator::BackendBase.

20.8.2.30 genPopulationRNG()

Generate an RNG with a state per population member.

Implements CodeGenerator::BackendBase.

20.8.2.31 genPopVariableInit()

```
\verb"void CodeGenerator":: \verb"CUDA":: \verb"Backend":: genPopVariableInit" (
```

```
CodeStream & os,
const Substitutions & kernelSubs,
Handler handler ) const [override], [virtual]
```

Implements CodeGenerator::BackendBase.

20.8.2.32 genReturnFreeDeviceMemoryBytes()

Generate code to return amount of free 'device' memory in bytes.

Implements CodeGenerator::BackendBase.

20.8.2.33 genRunnerPreamble()

Implements CodeGenerator::BackendBase.

20.8.2.34 genStepTimeFinalisePreamble()

After all timestep logic is complete.

Implements CodeGenerator::BackendBase.

20.8.2.35 genSynapseUpdate()

Generate platform-specific function to update the state of all synapses.

Parameters

OS	CodeStream to write function to
model	model to generate code for
wumThreshHandler	callback to write platform-independent code to update an individual NeuronGroup
wumSimHandler	callback to write platform-independent code to process presynaptic spikes. "id_pre", "id_post" and "id_syn" variables; and either "addToInSynDelay" or "addToInSyn" function will be provided to callback via Substitutions.

Parameters

wumEventHandler	callback to write platform-independent code to process presynaptic spike-like events. "id_pre", "id_post" and "id_syn" variables; and either "addToInSynDelay" or "addToInSyn" function will be provided to callback via Substitutions.
wumProceduralConnectHandler	callback to write platform-indepent code to procedurally generate connectivity "id_pre" variable and "addSynapse" function will be provided to callback via Substitutions. callback needs to implement loop over synapses in row, providing "synAddress" variable if INDIVIDUALG
postLearnHandler	callback to write platform-independent code to process postsynaptic spikes. "id_pre", "id_post" and "id_syn" variables will be provided to callback via Substitutions.
synapseDynamicsHandler	callback to write platform-independent code to update time-driven synapse dynamics. "id_pre", "id_post" and "id_syn" variables; and either "addToInSynDelay" or "addToInSyn" function will be provided to callback via Substitutions.

Implements CodeGenerator::BackendBase.

20.8.2.36 genSynapseVariableRowInit()

Implements CodeGenerator::BackendBase.

20.8.2.37 genTimer()

Implements CodeGenerator::BackendBase.

20.8.2.38 genVariableAllocation()

20.8.2.39 genVariableDefinition()

Implements CodeGenerator::BackendBase.

20.8.2.40 genVariableFree()

Implements CodeGenerator::BackendBase.

20.8.2.41 genVariableImplementation()

Implements CodeGenerator::BackendBase.

20.8.2.42 genVariableInit()

Implements CodeGenerator::BackendBase.

20.8.2.43 genVariablePull()

Generate code for pulling a variable from the 'device'.

20.8.2.44 genVariablePush()

Generate code for pushing a variable to the 'device'.

Implements CodeGenerator::BackendBase.

20.8.2.45 getArrayPrefix()

```
virtual std::string CodeGenerator::CUDA::Backend::getArrayPrefix ( ) const [inline], [override],
[virtual]
```

When backends require separate 'device' and 'host' versions of variables, they are identified with a prefix. This function returns this prefix so it can be used in otherwise platform-independent code.

Reimplemented from CodeGenerator::BackendBase.

20.8.2.46 getChosenCUDADevice()

```
const cudaDeviceProp& CodeGenerator::CUDA::Backend::getChosenCUDADevice ( ) const [inline]
```

20.8.2.47 getChosenDeviceID()

```
int CodeGenerator::CUDA::Backend::getChosenDeviceID ( ) const [inline]
```

20.8.2.48 getDeviceMemoryBytes()

```
virtual size_t CodeGenerator::CUDA::Backend::getDeviceMemoryBytes ( ) const [inline], [override],
[virtual]
```

How many bytes of memory does 'device' have.

Implements CodeGenerator::BackendBase.

20.8.2.49 getFloatAtomicAdd()

20.8.2.50 getKernelBlockSize()

20.8.2.51 getMergedGroupMemorySpaces()

Some backends will have additional small, fast, memory spaces for read-only data which might Be well-suited to storing merged group structs. This method returns the prefix required to Place arrays in these and their size in preferential order

Implements CodeGenerator::BackendBase.

```
20.8.2.52 getNumInitialisationRNGStreams()
```

Get total number of RNG streams potentially used to initialise model.

NOTE because RNG supports 2⁶4 streams, we are overly conservative

20.8.2.53 getNumPostsynapticUpdateThreads()

```
\label{lem:size_tode} size\_t \ \mbox{CodeGenerator::CUDA::Backend::getNumPostsynapticUpdateThreads (} \\ const \ \mbox{SynapseGroupInternal \& $sg$ ) [static] \\
```

20.8.2.54 getNumPresynapticUpdateThreads()

20.8.2.55 getNumSynapseDynamicsThreads()

```
\label{lem:size_todeGenerator::CUDA::Backend::getNumSynapseDynamicsThreads ( \\ const SynapseGroupInternal & sg ) [static] \\
```

20.8.2.56 getNVCCFlags()

```
std::string CodeGenerator::CUDA::Backend::getNVCCFlags ( ) const
```

20.8.2.57 getRuntimeVersion()

```
int CodeGenerator::CUDA::Backend::getRuntimeVersion ( ) const [inline]
```

20.8.2.58 getScalarPrefix()

```
virtual std::string CodeGenerator::CUDA::Backend::getScalarPrefix ( ) const [inline], [override],
[virtual]
```

Reimplemented from CodeGenerator::BackendBase.

20.8.2.59 getSynapticMatrixRowStride()

Gets the stride used to access synaptic matrix rows, taking into account sparse data structure, padding etc.

Implements CodeGenerator::BackendBase.

20.8.2.60 isAutomaticCopyEnabled()

```
virtual bool CodeGenerator::CUDA::Backend::isAutomaticCopyEnabled ( ) const [inline], [override],
[virtual]
```

Is automatic copy mode enabled in the preferences?

Implements CodeGenerator::BackendBase.

20.8.2.61 isGlobalDeviceRNGRequired()

Different backends use different RNGs for different things. Does this one require a global device RNG for the specified model?

Implements CodeGenerator::BackendBase.

20.8.2.62 isGlobalHostRNGRequired()

Different backends use different RNGs for different things. Does this one require a global host RNG for the specified model?

Implements CodeGenerator::BackendBase.

20.8.2.63 isPopulationRNGRequired()

```
virtual bool CodeGenerator::CUDA::Backend::isPopulationRNGRequired ( ) const [inline], [override],
```

Different backends use different RNGs for different things. Does this one require population RNGs?

Implements CodeGenerator::BackendBase.

20.8.2.64 isPostsynapticRemapRequired()

```
virtual bool CodeGenerator::CUDA::Backend::isPostsynapticRemapRequired ( ) const [inline],
[override], [virtual]
```

Different backends may implement synaptic plasticity differently. Does this one require a postsynaptic remapping data structure?

20.8.2.65 isSynRemapRequired()

```
virtual bool CodeGenerator::CUDA::Backend::isSynRemapRequired ( ) const [inline], [override],
[virtual]
```

Different backends may implement synapse dynamics differently. Does this one require a synapse remapping data structure?

Implements CodeGenerator::BackendBase.

20.8.2.66 shouldGenerateEmptyStatePushPull()

```
virtual bool CodeGenerator::CUDA::Backend::shouldGenerateEmptyStatePushPull ( ) const [inline],
[override], [virtual]
```

Should GeNN generate empty state push and pull functions?

Implements CodeGenerator::BackendBase.

20.8.2.67 shouldGenerateExtraGlobalParamPull()

```
virtual bool CodeGenerator::CUDA::Backend::shouldGenerateExtraGlobalParamPull ( ) const [inline],
[override], [virtual]
```

Should GeNN generate pull functions for extra global parameters? These are very rarely used.

Implements CodeGenerator::BackendBase.

20.8.3 Member Data Documentation

20.8.3.1 KernelNames

```
const char * CodeGenerator::CUDA::Backend::KernelNames [static]
```

Initial value:

```
"updateNeuronsKernel",
"updatePresynapticKernel",
"updatePostsynapticKernel",
"updateSynapseDynamicsKernel",
"initializeKernel",
"initializeSparseKernel",
"preNeuronResetKernel",
"preSynapseResetKernel"}
```

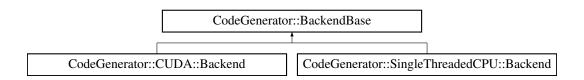
The documentation for this class was generated from the following files:

- · cuda/backend.h
- · cuda/backend.cc

20.9 CodeGenerator::BackendBase Class Reference

```
#include <backendBase.h>
```

Inheritance diagram for CodeGenerator::BackendBase:



Public Types

- typedef std::function < void(CodeStream &) > HostHandler
- typedef std::function< void(CodeStream &, Substitutions &)> Handler
- template<typename T >
 using GroupHandler = std::function< void(CodeStream &, const T &, Substitutions &)>
- typedef GroupHandler< NeuronInitGroupMerged > NeuronInitGroupMergedHandler

Standard callback type which provides a CodeStream to write platform-independent code for the specified Synapse—Group to

- typedef GroupHandler < NeuronUpdateGroupMerged > NeuronUpdateGroupMergedHandler
- typedef GroupHandler < PresynapticUpdateGroupMerged > PresynapticUpdateGroupMergedHandler
- typedef GroupHandler < PostsynapticUpdateGroupMerged > PostsynapticUpdateGroupMergedHandler
- typedef GroupHandler < SynapseDynamicsGroupMerged > SynapseDynamicsGroupMergedHandler
- typedef GroupHandler< SynapseConnectivityInitGroupMerged > SynapseConnectivityInitMergedGroup← Handler
- typedef GroupHandler < SynapseDenseInitGroupMerged > SynapseDenseInitGroupMergedHandler
- typedef GroupHandler < SynapseSparseInitGroupMerged > SynapseSparseInitGroupMergedHandler
- typedef std::function < void(CodeStream &, const NeuronUpdateGroupMerged &, Substitutions &, Neuron
 —
 UpdateGroupMergedHandler, NeuronUpdateGroupMergedHandler) > NeuronGroupSimHandler

Callback function type for generation neuron group simulation code.

typedef std::vector< std::pair< std::string, size_t >> MemorySpaces

Vector of prefixes required to allocate in memory space and size of memory space.

Public Member Functions

- BackendBase (const std::string &scalarType)
- virtual ∼BackendBase ()
- virtual void genNeuronUpdate (CodeStream &os, const ModelSpecMerged &modelMerged, NeuronGroup
 — SimHandler simHandler, NeuronUpdateGroupMergedHandler wuVarUpdateHandler, HostHandler pushEG
 — PHandler) const =0

Generate platform-specific function to update the state of all neurons.

virtual void genSynapseUpdate (CodeStream &os, const ModelSpecMerged &modelMerged, Presynaptic
 UpdateGroupMergedHandler wumThreshHandler, PresynapticUpdateGroupMergedHandler wumSim
 Handler, PresynapticUpdateGroupMergedHandler wumEventHandler, PresynapticUpdateGroupMerged
 Handler wumProceduralConnectHandler, PostsynapticUpdateGroupMergedHandler postLearnHandler,
 SynapseDynamicsGroupMergedHandler synapseDynamicsHandler, HostHandler pushEGPHandler) const
 =0

Generate platform-specific function to update the state of all synapses.

- virtual void genInit (CodeStream &os, const ModelSpecMerged &modelMerged, NeuronInitGroup
 MergedHandler localNGHandler, SynapseDenseInitGroupMergedHandler sgDenseInitHandler, Synapse
 ConnectivityInitMergedGroupHandler sgSparseConnectHandler, SynapseSparseInitGroupMergedHandler
 sgSparseInitHandler, HostHandler initPushEGPHandler, HostHandler initSparsePushEGPHandler) const =0
- virtual size_t getSynapticMatrixRowStride (const SynapseGroupInternal &sg) const =0

Gets the stride used to access synaptic matrix rows, taking into account sparse data structure, padding etc.

virtual void genDefinitionsPreamble (CodeStream &os, const ModelSpecMerged &modelMerged) const =0
 Definitions is the usercode-facing header file for the generated code. This function generates a 'preamble' to this header file.

virtual void genDefinitionsInternalPreamble (CodeStream &os, const ModelSpecMerged &modelMerged)
 const =0

Definitions internal is the internal header file for the generated code. This function generates a 'preamble' to this header file.

- virtual void genRunnerPreamble (CodeStream &os, const ModelSpecMerged &modelMerged) const =0
- virtual void genAllocateMemPreamble (CodeStream &os, const ModelSpecMerged &modelMerged) const =0
- virtual void genStepTimeFinalisePreamble (CodeStream &os, const ModelSpecMerged &modelMerged) const =0

After all timestep logic is complete.

- virtual void genVariableImplementation (CodeStream &os, const std::string &type, const std::string &name, VarLocation loc) const =0
- virtual MemAlloc genVariableAllocation (CodeStream &os, const std::string &type, const std::string &name, VarLocation loc, size t count) const =0
- virtual void genVariableFree (CodeStream &os, const std::string &name, VarLocation loc) const =0
- virtual void genExtraGlobalParamDefinition (CodeStream &definitions, const std::string &type, const std
 ::string &name, VarLocation loc) const =0
- virtual void genExtraGlobalParamImplementation (CodeStream &os, const std::string &type, const std::string &name, VarLocation loc) const =0
- virtual void genExtraGlobalParamAllocation (CodeStream &os, const std::string &type, const std::string &name, VarLocation loc, const std::string &countVarName="count", const std::string &prefix="") const =0
- virtual void genExtraGlobalParamPush (CodeStream &os, const std::string &type, const std::string &name, VarLocation loc, const std::string &countVarName="count", const std::string &prefix="") const =0
- virtual void genExtraGlobalParamPull (CodeStream &os, const std::string &type, const std::string &name, VarLocation loc, const std::string &countVarName="count", const std::string &prefix="") const =0
- virtual void genMergedGroupImplementation (CodeStream &os, const std::string &memorySpace, const std
 ::string &suffix, size_t idx, size_t numGroups) const =0

Generate code for declaring merged group data to the 'device'.

virtual void genMergedGroupPush (CodeStream &os, const std::string &suffix, size_t idx, size_t numGroups)
 const =0

Generate code for pushing merged group data to the 'device'.

- virtual void genMergedExtraGlobalParamPush (CodeStream &os, const std::string &suffix, size_t merged
 GroupIdx, const std::string &groupIdx, const std::string &fieldName, const std::string &egpName) const =0

 Generate code for pushing an updated EGP value into the merged group structure on 'device'.
- virtual void genPopVariableInit (CodeStream &os, const Substitutions &kernelSubs, Handler handler) const =0
- virtual void genVariableInit (CodeStream &os, const std::string &count, const std::string &indexVarName, const Substitutions &kernelSubs, Handler handler) const =0
- virtual void genSynapseVariableRowInit (CodeStream &os, const SynapseGroupMergedBase &sg, const Substitutions &kernelSubs, Handler handler) const =0
- virtual void genVariablePush (CodeStream &os, const std::string &type, const std::string &name, VarLocation loc, bool autoInitialized, size_t count) const =0

Generate code for pushing a variable to the 'device'.

• virtual void genVariablePull (CodeStream &os, const std::string &type, const std::string &name, VarLocation loc, size_t count) const =0

Generate code for pulling a variable from the 'device'.

 virtual void genCurrentVariablePush (CodeStream &os, const NeuronGroupInternal &ng, const std::string &type, const std::string &name, VarLocation loc) const =0

Generate code for pushing a variable's value in the current timestep to the 'device'.

• virtual void genCurrentVariablePull (CodeStream &os, const NeuronGroupInternal &ng, const std::string &type, const std::string &name, VarLocation loc) const =0

Generate code for pulling a variable's value in the current timestep from the 'device'.

• virtual void genCurrentTrueSpikePush (CodeStream &os, const NeuronGroupInternal &ng) const =0

Generate code for pushing true spikes emitted by a neuron group in the current timestep to the 'device'.

virtual void genCurrentTrueSpikePull (CodeStream &os, const NeuronGroupInternal &ng) const =0

Generate code for pulling true spikes emitted by a neuron group in the current timestep from the 'device'.

- virtual void genCurrentSpikeLikeEventPush (CodeStream &os, const NeuronGroupInternal &ng) const =0
 - Generate code for pushing spike-like events emitted by a neuron group in the current timestep to the 'device'.
- virtual void genCurrentSpikeLikeEventPull (CodeStream &os, const NeuronGroupInternal &ng) const =0
 - Generate code for pulling spike-like events emitted by a neuron group in the current timestep from the 'device'.

Generate a single RNG instance.

Generate an RNG with a state per population member.

- virtual void genTimer (CodeStream &definitions, CodeStream &definitionsInternal, CodeStream &runner, CodeStream &allocations, CodeStream &free, CodeStream &stepTimeFinalise, const std::string &name, bool updateInStepTime) const =0
- virtual void genReturnFreeDeviceMemoryBytes (CodeStream &os) const =0

Generate code to return amount of free 'device' memory in bytes.

virtual void genMakefilePreamble (std::ostream &os) const =0

This function can be used to generate a preamble for the GNU makefile used to build.

- virtual void genMakefileLinkRule (std::ostream &os) const =0
- virtual void genMakefileCompileRule (std::ostream &os) const =0
- virtual void genMSBuildConfigProperties (std::ostream &os) const =0
- virtual void genMSBuildImportProps (std::ostream &os) const =0
- virtual void genMSBuildItemDefinitions (std::ostream &os) const =0
- virtual void genMSBuildCompileModule (const std::string &moduleName, std::ostream &os) const =0
- virtual void genMSBuildImportTarget (std::ostream &os) const =0
- · virtual std::string getArrayPrefix () const
- · virtual std::string getScalarPrefix () const
- virtual bool isGlobalHostRNGRequired (const ModelSpecMerged &modelMerged) const =0

Different backends use different RNGs for different things. Does this one require a global host RNG for the specified model?

virtual bool isGlobalDeviceRNGRequired (const ModelSpecMerged &modelMerged) const =0

Different backends use different RNGs for different things. Does this one require a global device RNG for the specified model?

• virtual bool isPopulationRNGRequired () const =0

Different backends use different RNGs for different things. Does this one require population RNGs?

virtual bool isSynRemapRequired () const =0

Different backends may implement synapse dynamics differently. Does this one require a synapse remapping data structure?

virtual bool isPostsynapticRemapRequired () const =0

Different backends may implement synaptic plasticity differently. Does this one require a postsynaptic remapping data structure?

virtual bool isAutomaticCopyEnabled () const =0

Is automatic copy mode enabled in the preferences?

virtual bool shouldGenerateEmptyStatePushPull () const =0

Should GeNN generate empty state push and pull functions?

virtual bool shouldGenerateExtraGlobalParamPull () const =0

Should GeNN generate pull functions for extra global parameters? These are very rarely used.

virtual size t getDeviceMemoryBytes () const =0

How many bytes of memory does 'device' have.

• virtual MemorySpaces getMergedGroupMemorySpaces (const ModelSpecMerged &modelMerged) const =0

void genVariablePushPull (CodeStream &push, CodeStream &pull, const std::string &type, const std::string &name, VarLocation loc, bool autoInitialized, size_t count) const

Helper function to generate matching push and pull functions for a variable.

 void genCurrentVariablePushPull (CodeStream &push, CodeStream &pull, const NeuronGroupInternal &ng, const std::string &type, const std::string &name, VarLocation loc) const

Helper function to generate matching push and pull functions for the current state of a variable.

 MemAlloc genArray (CodeStream &definitions, CodeStream &definitionsInternal, CodeStream &runner, CodeStream &allocations, CodeStream &free, const std::string &type, const std::string &name, VarLocation loc, size_t count) const

Helper function to generate matching definition, declaration, allocation and free code for an array.

• void genScalar (CodeStream &definitions, CodeStream &definitionsInternal, CodeStream &runner, const std::string &type, const std::string &name, VarLocation loc) const

Helper function to generate matching definition and declaration code for a scalar variable.

size_t getSize (const std::string &type) const

Get the size of the type.

Protected Member Functions

void addType (const std::string &type, size_t size)

20.9.1 Member Typedef Documentation

20.9.1.1 GroupHandler

```
template<typename T >
using CodeGenerator::BackendBase::GroupHandler = std::function <void(CodeStream &, const T &,
Substitutions&)>
```

20.9.1.2 Handler

typedef std:: function < void (CodeStream &, Substitutions&) > CodeGenerator:: BackendBase:: Handler

20.9.1.3 HostHandler

typedef std::function<void(CodeStream &)> CodeGenerator::BackendBase::HostHandler

20.9.1.4 MemorySpaces

```
typedef std::vector<std::pair<std::string, size_t> > CodeGenerator::BackendBase::MemorySpaces
```

Vector of prefixes required to allocate in memory space and size of memory space.

20.9.1.5 NeuronGroupSimHandler

typedef std::function<void(CodeStream &, const NeuronUpdateGroupMerged &, Substitutions&, NeuronUpdateGroupMergedHandler, NeuronUpdateGroupMergedHandler)> CodeGenerator::BackendBase↔::NeuronGroupSimHandler

Callback function type for generation neuron group simulation code.

Provides additional callbacks to insert code to emit spikes

20.9.1.6 NeuronInitGroupMergedHandler

typedef GroupHandler<NeuronInitGroupMerged> CodeGenerator::BackendBase::NeuronInitGroup↔ MergedHandler

Standard callback type which provides a CodeStream to write platform-independent code for the specified SynapseGroup to.

20.9.1.7 NeuronUpdateGroupMergedHandler

typedef GroupHandler<NeuronUpdateGroupMerged> CodeGenerator::BackendBase::NeuronUpdateGroup↔ MergedHandler

20.9.1.8 PostsynapticUpdateGroupMergedHandler

 $\label{typedef} \begin{tabular}{ll} typedef GroupHandler < Postsynaptic Update GroupMerged > Code Generator:: Backend Base:: Postsynaptic \leftarrow Update GroupMerged Handler \\ \end{tabular}$

20.9.1.9 PresynapticUpdateGroupMergedHandler

 $\label{typedef} \begin{tabular}{ll} typedef GroupHandler <PresynapticUpdateGroupMerged > CodeGenerator::BackendBase::Presynaptic \leftarrow UpdateGroupMergedHandler \\ \end{tabular}$

20.9.1.10 SynapseConnectivityInitMergedGroupHandler

 $\label{typedef} \begin{tabular}{ll} typedef GroupHandler < Synapse ConnectivityInitGroupMerged > CodeGenerator:: BackendBase:: Synapse \leftarrow ConnectivityInitMergedGroupHandler \\ \end{tabular}$

20.9.1.11 SynapseDenselnitGroupMergedHandler

 $\label{typedef} typedef GroupHandler < SynapseDenseInitGroupMerged > CodeGenerator:: BackendBase:: SynapseDense \leftarrow InitGroupMergedHandler$

20.9.1.12 SynapseDynamicsGroupMergedHandler

 $\label{typedef} \begin{tabular}{ll} typedef GroupHandler < SynapseDynamicsGroupMerged > CodeGenerator:: BackendBase:: SynapseDynamics \leftarrow GroupMergedHandler \\ \end{tabular}$

20.9.1.13 SynapseSparseInitGroupMergedHandler

 $\label{typedef} typedef GroupHandler < SynapseSparseInitGroupMerged > CodeGenerator:: BackendBase:: SynapseSparse \leftarrow InitGroupMergedHandler$

20.9.2 Constructor & Destructor Documentation

20.9.2.1 BackendBase()

20.9.3.1 addType()

20.9.3.2 genAllocateMemPreamble()

Allocate memory is the first function in GeNN generated code called by usercode and it should only ever be called once. Therefore it's a good place for any global initialisation. This function generates a 'preamble' to this function.

Implemented in CodeGenerator::CUDA::Backend, and CodeGenerator::SingleThreadedCPU::Backend.

20.9.3.3 genArray()

Helper function to generate matching definition, declaration, allocation and free code for an array.

20.9.3.4 genCurrentSpikeLikeEventPull()

Generate code for pulling spike-like events emitted by a neuron group in the current timestep from the 'device'.

Implemented in CodeGenerator::CUDA::Backend, and CodeGenerator::SingleThreadedCPU::Backend.

20.9.3.5 genCurrentSpikeLikeEventPush()

Generate code for pushing spike-like events emitted by a neuron group in the current timestep to the 'device'.

Implemented in CodeGenerator::CUDA::Backend, and CodeGenerator::SingleThreadedCPU::Backend.

20.9.3.6 genCurrentTrueSpikePull()

Generate code for pulling true spikes emitted by a neuron group in the current timestep from the 'device'.

Implemented in CodeGenerator::CUDA::Backend, and CodeGenerator::SingleThreadedCPU::Backend.

20.9.3.7 genCurrentTrueSpikePush()

Generate code for pushing true spikes emitted by a neuron group in the current timestep to the 'device'.

Implemented in CodeGenerator::CUDA::Backend, and CodeGenerator::SingleThreadedCPU::Backend.

20.9.3.8 genCurrentVariablePull()

Generate code for pulling a variable's value in the current timestep from the 'device'.

Implemented in CodeGenerator::CUDA::Backend, and CodeGenerator::SingleThreadedCPU::Backend.

20.9.3.9 genCurrentVariablePush()

Generate code for pushing a variable's value in the current timestep to the 'device'.

Implemented in CodeGenerator::CUDA::Backend, and CodeGenerator::SingleThreadedCPU::Backend.

20.9.3.10 genCurrentVariablePushPull()

Helper function to generate matching push and pull functions for the current state of a variable.

20.9.3.11 genDefinitionsInternalPreamble()

Definitions internal is the internal header file for the generated code. This function generates a 'preamble' to this header file.

This will only be included by the platform-specific compiler used to build this backend so can include platform-specific types or headers

Implemented in CodeGenerator::CUDA::Backend, and CodeGenerator::SingleThreadedCPU::Backend.

20.9.3.12 genDefinitionsPreamble()

Definitions is the usercode-facing header file for the generated code. This function generates a 'preamble' to this header file.

This will be included from a standard C++ compiler so shouldn't include any platform-specific types or headers Implemented in CodeGenerator::CUDA::Backend, and CodeGenerator::SingleThreadedCPU::Backend.

20.9.3.13 genExtraGlobalParamAllocation()

Implemented in CodeGenerator::CUDA::Backend, and CodeGenerator::SingleThreadedCPU::Backend.

20.9.3.14 genExtraGlobalParamDefinition()

Implemented in CodeGenerator::CUDA::Backend, and CodeGenerator::SingleThreadedCPU::Backend.

20.9.3.15 genExtraGlobalParamImplementation()

Implemented in CodeGenerator::CUDA::Backend, and CodeGenerator::SingleThreadedCPU::Backend.

20.9.3.16 genExtraGlobalParamPull()

Implemented in CodeGenerator::CUDA::Backend, and CodeGenerator::SingleThreadedCPU::Backend.

20.9.3.17 genExtraGlobalParamPush()

Implemented in CodeGenerator::CUDA::Backend, and CodeGenerator::SingleThreadedCPU::Backend.

20.9.3.18 genGlobalDeviceRNG()

Generate a single RNG instance.

On single-threaded platforms this can be a standard RNG like M.T. but, on parallel platforms, it is likely to be a counter-based RNG

Implemented in CodeGenerator::CUDA::Backend, and CodeGenerator::SingleThreadedCPU::Backend.

20.9.3.19 genInit()

```
virtual void CodeGenerator::BackendBase::genInit (
```

```
CodeStream & os,
const ModelSpecMerged & modelMerged,
NeuronInitGroupMergedHandler localNGHandler,
SynapseDenseInitGroupMergedHandler sgDenseInitHandler,
SynapseConnectivityInitMergedGroupHandler sgSparseConnectHandler,
SynapseSparseInitGroupMergedHandler sgSparseInitHandler,
HostHandler initPushEGPHandler,
HostHandler initSparsePushEGPHandler) const [pure virtual]
```

Implemented in CodeGenerator::CUDA::Backend, and CodeGenerator::SingleThreadedCPU::Backend.

20.9.3.20 genMakefileCompileRule()

The GNU make build system uses 'pattern rules' (https://www.gnu.org/software/make/manual/html-_node/Pattern-Intro.html) to build backend modules into objects. This function should generate a GNU make pattern rule capable of building each module (i.e. compiling .cc file \$< into .o file \$@).

Implemented in CodeGenerator::CUDA::Backend, and CodeGenerator::SingleThreadedCPU::Backend.

20.9.3.21 genMakefileLinkRule()

The GNU make build system will populate a variable called with a list of objects to link. This function should generate a GNU make rule to build these objects into a shared library.

Implemented in CodeGenerator::CUDA::Backend, and CodeGenerator::SingleThreadedCPU::Backend.

20.9.3.22 genMakefilePreamble()

This function can be used to generate a preamble for the GNU makefile used to build.

Implemented in CodeGenerator::CUDA::Backend, and CodeGenerator::SingleThreadedCPU::Backend.

20.9.3.23 genMergedExtraGlobalParamPush()

Generate code for pushing an updated EGP value into the merged group structure on 'device'.

Implemented in CodeGenerator::CUDA::Backend, and CodeGenerator::SingleThreadedCPU::Backend.

20.9.3.24 genMergedGroupImplementation()

Generate code for declaring merged group data to the 'device'.

Implemented in CodeGenerator::CUDA::Backend, and CodeGenerator::SingleThreadedCPU::Backend.

20.9.3.25 genMergedGroupPush()

Generate code for pushing merged group data to the 'device'.

Implemented in CodeGenerator::CUDA::Backend, and CodeGenerator::SingleThreadedCPU::Backend.

20.9.3.26 genMSBuildCompileModule()

Implemented in CodeGenerator::CUDA::Backend, and CodeGenerator::SingleThreadedCPU::Backend.

20.9.3.27 genMSBuildConfigProperties()

In MSBuild, 'properties' are used to configure global project settings e.g. whether the MSBuild project builds a static or dynamic library This function can be used to add additional XML properties to this section.

 $\textbf{see} \quad \texttt{https://docs.microsoft.com/en-us/visualstudio/msbuild/msbuild-properties} \\ \textbf{for more information.} \\$

Implemented in CodeGenerator::CUDA::Backend, and CodeGenerator::SingleThreadedCPU::Backend.

20.9.3.28 genMSBuildImportProps()

Implemented in CodeGenerator::CUDA::Backend, and CodeGenerator::SingleThreadedCPU::Backend.

20.9.3.29 genMSBuildImportTarget()

```
\label{lem:codeGenerator::BackendBase::genMSBuildImportTarget ( \\ std::ostream \& os ) const [pure virtual]
```

Implemented in CodeGenerator::CUDA::Backend, and CodeGenerator::SingleThreadedCPU::Backend.

20.9.3.30 genMSBuildItemDefinitions()

In MSBuild, the 'item definitions' are used to override the default properties of 'items' such as <ClCompile> or <Link>. This function should generate XML to correctly configure the 'items' required to build the generated code, taking into account etc.

 $\textbf{see} \ \texttt{https://docs.microsoft.com/en-us/visualstudio/msbuild/msbuild-items\#item-definitions} \\ \textbf{for more information.} \\$

Implemented in CodeGenerator::CUDA::Backend, and CodeGenerator::SingleThreadedCPU::Backend.

20.9.3.31 genNeuronUpdate()

Generate platform-specific function to update the state of all neurons.

Parameters

os	CodeStream to write function to
model	merged model to generate code for
simHandler	callback to write platform-independent code to update an individual NeuronGroup
wuVarUpdateHandler	callback to write platform-independent code to update pre and postsynaptic weight update model variables when neuron spikes

Implemented in CodeGenerator::CUDA::Backend, and CodeGenerator::SingleThreadedCPU::Backend.

20.9.3.32 genPopulationRNG()

Generate an RNG with a state per population member.

Implemented in CodeGenerator::CUDA::Backend, and CodeGenerator::SingleThreadedCPU::Backend.

20.9.3.33 genPopVariableInit()

```
virtual void CodeGenerator::BackendBase::genPopVariableInit (
```

```
CodeStream & os,
const Substitutions & kernelSubs,
Handler handler ) const [pure virtual]
```

Implemented in CodeGenerator::CUDA::Backend, and CodeGenerator::SingleThreadedCPU::Backend.

20.9.3.34 genReturnFreeDeviceMemoryBytes()

Generate code to return amount of free 'device' memory in bytes.

Implemented in CodeGenerator::CUDA::Backend, and CodeGenerator::SingleThreadedCPU::Backend.

20.9.3.35 genRunnerPreamble()

Implemented in CodeGenerator::CUDA::Backend, and CodeGenerator::SingleThreadedCPU::Backend.

20.9.3.36 genScalar()

Helper function to generate matching definition and declaration code for a scalar variable.

20.9.3.37 genStepTimeFinalisePreamble()

After all timestep logic is complete.

Implemented in CodeGenerator::CUDA::Backend, and CodeGenerator::SingleThreadedCPU::Backend.

20.9.3.38 genSynapseUpdate()

```
HostHandler pushEGPHandler ) const [pure virtual]
```

Generate platform-specific function to update the state of all synapses.

Parameters

os	CodeStream to write function to
model	model to generate code for
wumThreshHandler	callback to write platform-independent code to update an individual NeuronGroup
wumSimHandler	callback to write platform-independent code to process presynaptic spikes. "id_pre", "id_post" and "id_syn" variables; and either "addToInSynDelay" or "addToInSyn" function will be provided to callback via Substitutions.
wumEventHandler	callback to write platform-independent code to process presynaptic spike-like events. "id_pre", "id_post" and "id_syn" variables; and either "addToInSynDelay" or "addToInSyn" function will be provided to callback via Substitutions.
wumProceduralConnectHandler	callback to write platform-indepent code to procedurally generate connectivity "id_pre" variable and "addSynapse" function will be provided to callback via Substitutions. callback needs to implement loop over synapses in row, providing "synAddress" variable if INDIVIDUALG
postLearnHandler	callback to write platform-independent code to process postsynaptic spikes. "id_pre", "id_post" and "id_syn" variables will be provided to callback via Substitutions.
synapseDynamicsHandler	callback to write platform-independent code to update time-driven synapse dynamics. "id_pre", "id_post" and "id_syn" variables; and either "addToInSynDelay" or "addToInSyn" function will be provided to callback via Substitutions.

Implemented in CodeGenerator::CUDA::Backend, and CodeGenerator::SingleThreadedCPU::Backend.

20.9.3.39 genSynapseVariableRowInit()

Implemented in CodeGenerator::CUDA::Backend, and CodeGenerator::SingleThreadedCPU::Backend.

20.9.3.40 genTimer()

Implemented in CodeGenerator::CUDA::Backend, and CodeGenerator::SingleThreadedCPU::Backend.

20.9.3.41 genVariableAllocation()

Implemented in CodeGenerator::CUDA::Backend, and CodeGenerator::SingleThreadedCPU::Backend.

20.9.3.42 genVariableDefinition()

Implemented in CodeGenerator::CUDA::Backend, and CodeGenerator::SingleThreadedCPU::Backend.

20.9.3.43 genVariableFree()

Implemented in CodeGenerator::CUDA::Backend, and CodeGenerator::SingleThreadedCPU::Backend.

20.9.3.44 genVariableImplementation()

Implemented in CodeGenerator::CUDA::Backend, and CodeGenerator::SingleThreadedCPU::Backend.

20.9.3.45 genVariableInit()

Implemented in CodeGenerator::CUDA::Backend, and CodeGenerator::SingleThreadedCPU::Backend.

20.9.3.46 genVariablePull()

```
virtual void CodeGenerator::BackendBase::genVariablePull (
```

```
CodeStream & os,
const std::string & type,
const std::string & name,
VarLocation loc,
size_t count ) const [pure virtual]
```

Generate code for pulling a variable from the 'device'.

Implemented in CodeGenerator::CUDA::Backend, and CodeGenerator::SingleThreadedCPU::Backend.

20.9.3.47 genVariablePush()

Generate code for pushing a variable to the 'device'.

Implemented in CodeGenerator::CUDA::Backend, and CodeGenerator::SingleThreadedCPU::Backend.

20.9.3.48 genVariablePushPull()

Helper function to generate matching push and pull functions for a variable.

20.9.3.49 getArrayPrefix()

```
virtual std::string CodeGenerator::BackendBase::getArrayPrefix ( ) const [inline], [virtual]
```

When backends require separate 'device' and 'host' versions of variables, they are identified with a prefix. This function returns this prefix so it can be used in otherwise platform-independent code.

Reimplemented in CodeGenerator::CUDA::Backend, and CodeGenerator::SingleThreadedCPU::Backend.

20.9.3.50 getDeviceMemoryBytes()

```
virtual size_t CodeGenerator::BackendBase::getDeviceMemoryBytes ( ) const [pure virtual]
```

How many bytes of memory does 'device' have.

Implemented in CodeGenerator::CUDA::Backend, and CodeGenerator::SingleThreadedCPU::Backend.

20.9.3.51 getMergedGroupMemorySpaces()

```
virtual MemorySpaces CodeGenerator::BackendBase::getMergedGroupMemorySpaces (
```

```
const ModelSpecMerged & modelMerged ) const [pure virtual]
```

Some backends will have additional small, fast, memory spaces for read-only data which might Be well-suited to storing merged group structs. This method returns the prefix required to Place arrays in these and their size in preferential order

Implemented in CodeGenerator::CUDA::Backend, and CodeGenerator::SingleThreadedCPU::Backend.

20.9.3.52 getScalarPrefix()

```
virtual std::string CodeGenerator::BackendBase::getScalarPrefix ( ) const [inline], [virtual]
```

 $Reimplemented\ in\ Code Generator :: CUDA :: Backend,\ and\ Code Generator :: Single Threaded CPU :: Backend.$

20.9.3.53 getSize()

Get the size of the type.

20.9.3.54 getSynapticMatrixRowStride()

Gets the stride used to access synaptic matrix rows, taking into account sparse data structure, padding etc.

Implemented in CodeGenerator::CUDA::Backend, and CodeGenerator::SingleThreadedCPU::Backend.

20.9.3.55 isAutomaticCopyEnabled()

```
virtual bool CodeGenerator::BackendBase::isAutomaticCopyEnabled ( ) const [pure virtual]
```

Is automatic copy mode enabled in the preferences?

Implemented in CodeGenerator::CUDA::Backend, and CodeGenerator::SingleThreadedCPU::Backend.

20.9.3.56 isGlobalDeviceRNGRequired()

Different backends use different RNGs for different things. Does this one require a global device RNG for the specified model?

Implemented in CodeGenerator::CUDA::Backend, and CodeGenerator::SingleThreadedCPU::Backend.

20.9.3.57 isGlobalHostRNGRequired()

Different backends use different RNGs for different things. Does this one require a global host RNG for the specified model?

Implemented in CodeGenerator::CUDA::Backend, and CodeGenerator::SingleThreadedCPU::Backend.

20.9.3.58 isPopulationRNGRequired()

virtual bool CodeGenerator::BackendBase::isPopulationRNGRequired () const [pure virtual]

Different backends use different RNGs for different things. Does this one require population RNGs?

Implemented in CodeGenerator::CUDA::Backend, and CodeGenerator::SingleThreadedCPU::Backend.

20.9.3.59 isPostsynapticRemapRequired()

virtual bool CodeGenerator::BackendBase::isPostsynapticRemapRequired () const [pure virtual]

Different backends may implement synaptic plasticity differently. Does this one require a postsynaptic remapping data structure?

Implemented in CodeGenerator::CUDA::Backend, and CodeGenerator::SingleThreadedCPU::Backend.

20.9.3.60 isSynRemapRequired()

virtual bool CodeGenerator::BackendBase::isSynRemapRequired () const [pure virtual]

Different backends may implement synapse dynamics differently. Does this one require a synapse remapping data structure?

Implemented in CodeGenerator::CUDA::Backend, and CodeGenerator::SingleThreadedCPU::Backend.

20.9.3.61 shouldGenerateEmptyStatePushPull()

virtual bool CodeGenerator::BackendBase::shouldGenerateEmptyStatePushPull () const [pure virtual]

Should GeNN generate empty state push and pull functions?

Implemented in CodeGenerator::CUDA::Backend, and CodeGenerator::SingleThreadedCPU::Backend.

20.9.3.62 shouldGenerateExtraGlobalParamPull()

virtual bool CodeGenerator::BackendBase::shouldGenerateExtraGlobalParamPull () const [pure virtual]

Should GeNN generate pull functions for extra global parameters? These are very rarely used.

Implemented in CodeGenerator::CUDA::Backend, and CodeGenerator::SingleThreadedCPU::Backend.

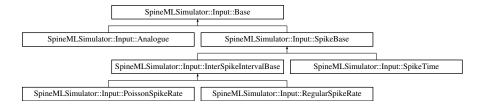
The documentation for this class was generated from the following files:

- · backendBase.h
- · backendBase.cc

20.10 SpineMLSimulator::Input::Base Class Reference

#include <input.h>

Inheritance diagram for SpineMLSimulator::Input::Base:



Public Member Functions

- virtual ∼Base ()
- virtual void apply (double dt, unsigned long long timestep)=0

Protected Member Functions

- Base (double dt, const pugi::xml_node &node, std::unique_ptr< InputValue::Base > value)
- bool shouldApply (unsigned long long timestep) const
- void updateValues (double dt, unsigned long long timestep, std::function< void(unsigned int, double)>
 applyValueFunc) const

20.10.1 Constructor & Destructor Documentation

```
20.10.1.1 \simBase()
```

```
SpineMLSimulator::Input::Base::~Base ( ) [virtual]
```

20.10.1.2 Base()

20.10.2 Member Function Documentation

20.10.2.1 apply()

```
virtual void SpineMLSimulator::Input::Base::apply ( \mbox{double } dt, \mbox{unsigned long long } timestep \mbox{)} \mbox{ [pure virtual]}
```

Implemented in SpineMLSimulator::Input::Analogue, SpineMLSimulator::Input::SpikeTime, and SpineML \leftarrow Simulator::Input::InterSpikeIntervalBase.

20.10.2.2 shouldApply()

20.10.2.3 updateValues()

The documentation for this class was generated from the following files:

- input.h
- · input.cc

20.11 CodeGenerator::CUDA::PresynapticUpdateStrategy::Base Class Reference

```
#include ynapticUpdateStrategy.h>
```

Inheritance diagram for CodeGenerator::CUDA::PresynapticUpdateStrategy::Base:



Public Member Functions

virtual size t getNumThreads (const SynapseGroupInternal &sg) const =0

Get the number of threads that presynaptic updates should be parallelised across.

virtual size_t getSynapticMatrixRowStride (const SynapseGroupInternal &sg) const =0

Gets the stride used to access synaptic matrix rows, taking into account sparse data structure, padding etc.

• virtual bool isCompatible (const SynapseGroupInternal &sg, const cudaDeviceProp &deviceProps, const Preferences &preferences) const =0

Is this presynaptic update strategy compatible with a given synapse group?

 virtual size_t getSharedMemoryPerThread (const PresynapticUpdateGroupMerged &sg, const Backend &backend) const =0

How many neurons does each thread accumulate the outputs of into shared memory.

- virtual void genPreamble (CodeStream &os, const ModelSpecMerged &modelMerged, const Presynaptic
 UpdateGroupMerged &sg, const Substitutions &popSubs, const Backend &backend, size_t idStart) const
 =0
- virtual void genUpdate (CodeStream &os, const ModelSpecMerged &modelMerged, const Presynaptic
 UpdateGroupMerged &sg, const Substitutions &popSubs, const Backend &backend, bool trueSpike,
 size_t idStart, BackendBase::PresynapticUpdateGroupMergedHandler wumThreshHandler, Backend
 Base::PresynapticUpdateGroupMergedHandler wumSimHandler, BackendBase::PresynapticUpdateGroup
 MergedHandler wumProceduralConnectHandler) const =0

Generate presynaptic update code.

virtual void genPostamble (CodeStream &os, const ModelSpecMerged &modelMerged, const Presynaptic
 UpdateGroupMerged &sg, const Substitutions &popSubs, const Backend &backend, size_t idStart) const
 =0

20.11.1 Member Function Documentation

20.11.1.1 genPostamble()

```
const PresynapticUpdateGroupMerged & sg,
const Substitutions & popSubs,
const Backend & backend,
size_t idStart ) const [pure virtual]
```

Implemented in CodeGenerator::CUDA::PresynapticUpdateStrategy::PreSpanProcedural, CodeGenerator::CUDA::PresynapticUpdateStrategy::PostSpanBitmask, CodeGenerator::CUDA::PresynapticUpdateStrategy::PostSpan, and CodeGenerator::CUDA::PresynapticUpdateStrategy::PreSpan.

20.11.1.2 genPreamble()

Implemented in CodeGenerator::CUDA::PresynapticUpdateStrategy::PreSpanProcedural, CodeGenerator::CUDA::PresynapticUpdateStrategy::PostSpanBitmask, CodeGenerator::CUDA::PresynapticUpdateStrategy::PostSpan, and CodeGenerator::CUDA::PresynapticUpdateStrategy::PostSpan.

20.11.1.3 genUpdate()

Generate presynaptic update code.

Implemented in CodeGenerator::CUDA::PresynapticUpdateStrategy::PreSpanProcedural, CodeGenerator::CUDA::PresynapticUpdateStrategy::PostSpanBitmask, CodeGenerator::CUDA::PresynapticUpdateStrategy::PostSpan, and CodeGenerator::CUDA::PresynapticUpdateStrategy::PostSpan.

20.11.1.4 getNumThreads()

Get the number of threads that presynaptic updates should be parallelised across.

Implemented in CodeGenerator::CUDA::PresynapticUpdateStrategy::PreSpanProcedural, CodeGenerator::CUDA::PresynapticUpdateStrategy::PostSpanBitmask, CodeGenerator::CUDA::PresynapticUpdateStrategy::PostSpan, and CodeGenerator::CUDA::PresynapticUpdateStrategy::PreSpan.

20.11.1.5 getSharedMemoryPerThread()

How many neurons does each thread accumulate the outputs of into shared memory.

Implemented in CodeGenerator::CUDA::PresynapticUpdateStrategy::PreSpanProcedural, CodeGenerator::CUDA::PresynapticUpdateStrategy::PostSpanBitmask, CodeGenerator::CUDA::PresynapticUpdateStrategy::PostSpan, and CodeGenerator::CUDA::PresynapticUpdateStrategy::PostSpan.

20.11.1.6 getSynapticMatrixRowStride()

```
\label{lem:const_synaptic} $$ virtual \ size_t \ CodeGenerator::CUDA::PresynapticUpdateStrategy::Base::getSynapticMatrixRow $$ Stride ( $$ const \ SynapseGroupInternal & $sg$ ) const [pure virtual] $$ $$
```

Gets the stride used to access synaptic matrix rows, taking into account sparse data structure, padding etc.

Implemented in CodeGenerator::CUDA::PresynapticUpdateStrategy::PreSpanProcedural, CodeGenerator::CUDA::PresynapticUpdateStrategy::PostSpanBitmask, CodeGenerator::CUDA::PresynapticUpdateStrategy::PostSpan, and CodeGenerator::CUDA::PresynapticUpdateStrategy::PreSpan.

20.11.1.7 isCompatible()

Is this presynaptic update strategy compatible with a given synapse group?

Implemented in CodeGenerator::CUDA::PresynapticUpdateStrategy::PreSpanProcedural, CodeGenerator::CUDA::PresynapticUpdateStrategy::PostSpanBitmask, CodeGenerator::CUDA::PresynapticUpdateStrategy::PostSpan, and CodeGenerator::CUDA::PresynapticUpdateStrategy::PostSpan.

The documentation for this class was generated from the following file:

· presynapticUpdateStrategy.h

20.12 SpineMLSimulator::InputValue::Base Class Reference

```
#include <inputValue.h>
```

Inheritance diagram for SpineMLSimulator::InputValue::Base:



Public Member Functions

- virtual ∼Base ()
- virtual void update (double dt, unsigned long long timestep, std::function< void(unsigned int, double)>
 applyValueFunc)=0

Protected Member Functions

- Base (unsigned int numNeurons, const pugi::xml_node &node)
- const std::vector< unsigned int > & getTargetIndices () const
- unsigned int getNumNeurons () const

20.12.1 Constructor & Destructor Documentation

```
20.12.1.1 ∼Base()
virtual SpineMLSimulator::InputValue::Base::~Base ( ) [inline], [virtual]
20.12.1.2 Base()
SpineMLSimulator::InputValue::Base::Base (
            unsigned int numNeurons,
             const pugi::xml_node & node ) [protected]
20.12.2 Member Function Documentation
20.12.2.1 getNumNeurons()
unsigned int SpineMLSimulator::InputValue::Base::getNumNeurons ( ) const [inline], [protected]
20.12.2.2 getTargetIndices()
const std::vector<unsigned int>& SpineMLSimulator::InputValue::Base::getTargetIndices ( )
const [inline], [protected]
20.12.2.3 update()
virtual void SpineMLSimulator::InputValue::Base::update (
             double dt,
             unsigned long long timestep,
             std::function< void(unsigned int, double)> applyValueFunc ) [pure virtual]
```

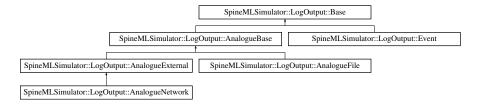
The documentation for this class was generated from the following files:

- inputValue.h
- inputValue.cc

20.13 SpineMLSimulator::LogOutput::Base Class Reference

```
#include <logOutput.h>
```

Inheritance diagram for SpineMLSimulator::LogOutput::Base:



Public Member Functions

- Base (const pugi::xml_node &node, double dt)
- virtual ∼Base ()
- virtual void record (double dt, unsigned long long timestep)=0

Protected Member Functions

- bool shouldRecord (unsigned long long timestep) const
- unsigned long long getEndTimestep () const

20.13.1 Constructor & Destructor Documentation

```
20.13.1.1 Base()
```

20.13.1.2 \sim Base()

```
virtual SpineMLSimulator::LogOutput::Base::~Base ( ) [inline], [virtual]
```

20.13.2 Member Function Documentation

20.13.2.1 getEndTimestep()

```
unsigned long long SpineMLSimulator::LogOutput::Base::getEndTimestep ( ) const [inline],
[protected]
```

20.13.2.2 record()

```
virtual void SpineMLSimulator::LogOutput::Base::record ( double dt, unsigned long long timestep) [pure virtual]
```

Implemented in SpineMLSimulator::LogOutput::Event, SpineMLSimulator::LogOutput::AnalogueExternal, and SpineMLSimulator::LogOutput::AnalogueFile.

20.13.2.3 shouldRecord()

```
bool SpineMLSimulator::LogOutput::Base::shouldRecord ( unsigned\ long\ long\ timestep\ )\ const\ [inline]\text{, [protected]}
```

The documentation for this class was generated from the following files:

- logOutput.h
- · logOutput.cc

20.14 SpineMLSimulator::ModelProperty::Base Class Reference

```
#include <modelProperty.h>
```

Inheritance diagram for SpineMLSimulator::ModelProperty::Base:

```
SpiteAll Simulator: Model Property: Base

SpiteAll Simulator: Model Property: Especial Simulator: Model Property: Fised

SpiteAll Simulator: Model Property: Curiem Distribution

SpiteAll Simulator: Model Property: Valed List

SpiteAll Simulator: Model Property: Valed Li
```

Public Member Functions

- Base (const StateVar< scalar > &stateVar, unsigned int size)
- virtual ∼Base ()
- scalar * getHostStateVar ()
- const scalar * getHostStateVar () const
- void pushToDevice () const
- · void pullFromDevice () const
- unsigned int getSize () const

20.14.1 Constructor & Destructor Documentation

20.14.1.1 Base()

20.14.1.2 ∼Base()

```
virtual SpineMLSimulator::ModelProperty::Base::~Base ( ) [inline], [virtual]
```

20.14.2 Member Function Documentation

```
20.14.2.1 getHostStateVar() [1/2]
```

```
scalar* SpineMLSimulator::ModelProperty::Base::getHostStateVar ( ) [inline]
```

20.14.2.2 getHostStateVar() [2/2] const scalar* SpineMLSimulator::ModelProperty::Base::getHostStateVar () const [inline] 20.14.2.3 getSize() unsigned int SpineMLSimulator::ModelProperty::Base::getSize () const [inline] 20.14.2.4 pullFromDevice() void SpineMLSimulator::ModelProperty::Base::pullFromDevice () const [inline] 20.14.2.5 pushToDevice()

The documentation for this class was generated from the following file:

void SpineMLSimulator::ModelProperty::Base::pushToDevice () const [inline]

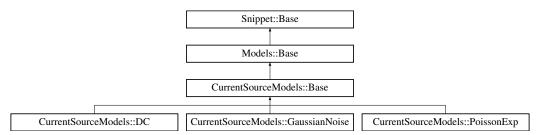
· modelProperty.h

20.15 CurrentSourceModels::Base Class Reference

Base class for all current source models.

#include <currentSourceModels.h>

Inheritance diagram for CurrentSourceModels::Base:



Public Member Functions

- virtual std::string getInjectionCode () const
 Gets the code that defines current injected each timestep.
- bool canBeMerged (const Base *other) const

Can this neuron model be merged with other? i.e. can they be simulated using same generated code.

Additional Inherited Members

20.15.1 Detailed Description

Base class for all current source models.

20.15.2 Member Function Documentation

20.15.2.1 canBeMerged()

Can this neuron model be merged with other? i.e. can they be simulated using same generated code.

20.15.2.2 getInjectionCode()

```
virtual std::string CurrentSourceModels::Base::getInjectionCode ( ) const [inline], [virtual]
```

Gets the code that defines current injected each timestep.

The documentation for this class was generated from the following files:

- · currentSourceModels.h
- currentSourceModels.cc

20.16 InitSparseConnectivitySnippet::Base Class Reference

#include <initSparseConnectivitySnippet.h>

Inheritance diagram for InitSparseConnectivitySnippet::Base:



Public Types

typedef std::function< unsigned int(unsigned int, unsigned int, const std::vector< double > &)> CalcMax←
 LengthFunc

Public Member Functions

- · virtual std::string getRowBuildCode () const
- virtual ParamValVec getRowBuildStateVars () const
- virtual std::string getHostInitCode () const
- virtual CalcMaxLengthFunc getCalcMaxRowLengthFunc () const

Get function to calculate the maximum row length of this connector based on the parameters and the size of the pre and postsynaptic population.

virtual CalcMaxLengthFunc getCalcMaxColLengthFunc () const

Get function to calculate the maximum column length of this connector based on the parameters and the size of the pre and postsynaptic population.

• bool canBeMerged (const Base *other) const

Can this neuron model be merged with other? i.e. can they be simulated using same generated code.

Additional Inherited Members

20.16.1 Member Typedef Documentation

20.16.1.1 CalcMaxLengthFunc

typedef std::function<unsigned int(unsigned int, unsigned int, const std::vector<double> &)>
InitSparseConnectivitySnippet::Base::CalcMaxLengthFunc

20.16.2 Member Function Documentation

20.16.2.1 canBeMerged()

Can this neuron model be merged with other? i.e. can they be simulated using same generated code.

20.16.2.2 getCalcMaxColLengthFunc()

```
virtual CalcMaxLengthFunc InitSparseConnectivitySnippet::Base::getCalcMaxColLengthFunc ( )
const [inline], [virtual]
```

Get function to calculate the maximum column length of this connector based on the parameters and the size of the pre and postsynaptic population.

20.16.2.3 getCalcMaxRowLengthFunc()

```
virtual CalcMaxLengthFunc InitSparseConnectivitySnippet::Base::getCalcMaxRowLengthFunc ( )
const [inline], [virtual]
```

Get function to calculate the maximum row length of this connector based on the parameters and the size of the pre and postsynaptic population.

20.16.2.4 getHostInitCode()

```
virtual std::string InitSparseConnectivitySnippet::Base::getHostInitCode ( ) const [inline],
[virtual]
```

20.16.2.5 getRowBuildCode()

```
virtual std::string InitSparseConnectivitySnippet::Base::getRowBuildCode ( ) const [inline],
[virtual]
```

Reimplemented in InitSparseConnectivitySnippet::FixedProbabilityBase.

20.16.2.6 getRowBuildStateVars()

```
virtual ParamValVec InitSparseConnectivitySnippet::Base::getRowBuildStateVars ( ) const [inline],
[virtual]
```

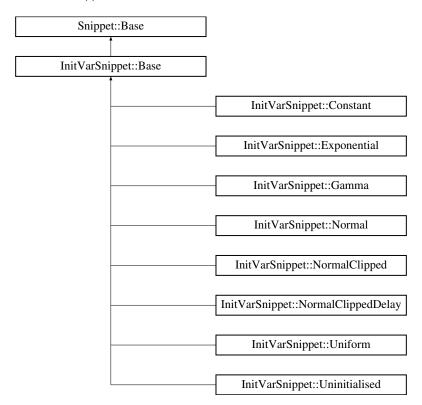
The documentation for this class was generated from the following files:

- initSparseConnectivitySnippet.h
- initSparseConnectivitySnippet.cc

20.17 InitVarSnippet::Base Class Reference

```
#include <initVarSnippet.h>
```

Inheritance diagram for InitVarSnippet::Base:



Public Member Functions

- virtual std::string getCode () const
- bool canBeMerged (const Base *other) const

Can this neuron model be merged with other? i.e. can they be simulated using same generated code.

Additional Inherited Members

20.17.1 Member Function Documentation

20.17.1.1 canBeMerged()

Can this neuron model be merged with other? i.e. can they be simulated using same generated code.

20.17.1.2 getCode()

```
virtual std::string InitVarSnippet::Base::getCode ( ) const [inline], [virtual]
```

The documentation for this class was generated from the following files:

initVarSnippet.h

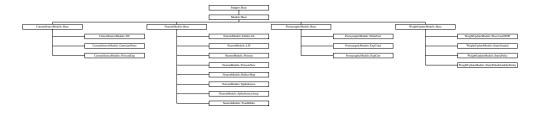
initVarSnippet.cc

20.18 Models::Base Class Reference

Base class for all models - in addition to the parameters snippets have, models can have state variables.

```
#include <models.h>
```

Inheritance diagram for Models::Base:



Classes

struct Var

A variable has a name, a type and an access type.

Public Types

typedef std::vector< Var > VarVec

Public Member Functions

• virtual VarVec getVars () const

Gets names and types (as strings) of model variables.

size_t getVarIndex (const std::string &varName) const

Find the index of a named variable.

Protected Member Functions

• bool canBeMerged (const Base *other) const

Additional Inherited Members

20.18.1 Detailed Description

Base class for all models - in addition to the parameters snippets have, models can have state variables.

20.18.2 Member Typedef Documentation

20.18.2.1 VarVec

typedef std::vector<Var> Models::Base::VarVec

20.18.3 Member Function Documentation

```
20.18.3.1 canBeMerged()
```

Find the index of a named variable.

```
20.18.3.3 getVars()
```

```
virtual VarVec Models::Base::getVars ( ) const [inline], [virtual]
```

Gets names and types (as strings) of model variables.

Reimplemented in NeuronModels::TraubMiles, NeuronModels::PoissonNew, NeuronModels::Poisson, Neuron Models::SpikeSourceArray, WeightUpdateModels::PiecewiseSTDP, NeuronModels::LIF, WeightUpdateModels::StaticGraded, NeuronModels::IzhikevichVariable, WeightUpdateModels::StaticPulseDendriticDelay, Neuron Models::Izhikevich, WeightUpdateModels::StaticPulse, NeuronModels::RulkovMap, and CurrentSourceModels::PoissonExp.

The documentation for this class was generated from the following file:

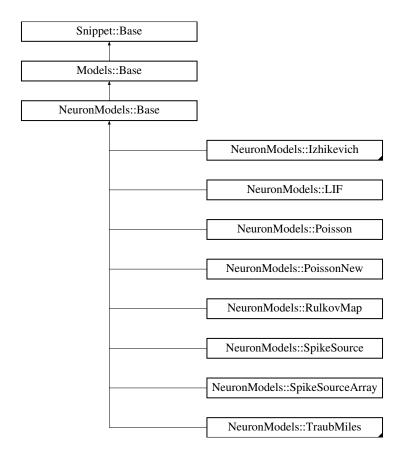
· models.h

20.19 NeuronModels::Base Class Reference

Base class for all neuron models.

```
#include <neuronModels.h>
```

Inheritance diagram for NeuronModels::Base:



Public Member Functions

· virtual std::string getSimCode () const

Gets the code that defines the execution of one timestep of integration of the neuron model.

- virtual std::string getThresholdConditionCode () const
 - Gets code which defines the condition for a true spike in the described neuron model.
- virtual std::string getResetCode () const
 - Gets code that defines the reset action taken after a spike occurred. This can be empty.
- virtual std::string getSupportCode () const
 - Gets support code to be made available within the neuron kernel/funcion.
- virtual Models::Base::ParamValVec getAdditionalInputVars () const
- · virtual bool isAutoRefractoryRequired () const
 - Does this model require auto-refractory logic?
- bool canBeMerged (const Base *other) const

Can this neuron model be merged with other? i.e. can they be simulated using same generated code.

Additional Inherited Members

20.19.1 Detailed Description

Base class for all neuron models.

20.19.2 Member Function Documentation

20.19.2.1 canBeMerged()

Can this neuron model be merged with other? i.e. can they be simulated using same generated code.

20.19.2.2 getAdditionalInputVars()

```
virtual Models::Base::ParamValVec NeuronModels::Base::getAdditionalInputVars ( ) const [inline],
[virtual]
```

Gets names, types (as strings) and initial values of local variables into which the 'apply input code' of (potentially) multiple postsynaptic input models can apply input

20.19.2.3 getResetCode()

```
virtual std::string NeuronModels::Base::getResetCode ( ) const [inline], [virtual]
```

Gets code that defines the reset action taken after a spike occurred. This can be empty.

Reimplemented in NeuronModels::SpikeSourceArray, and NeuronModels::LIF.

20.19.2.4 getSimCode()

```
virtual std::string NeuronModels::Base::getSimCode ( ) const [inline], [virtual]
```

Gets the code that defines the execution of one timestep of integration of the neuron model.

The code will refer to for the value of the variable with name "NN". It needs to refer to the predefined variable "ISYN", i.e. contain, if it is to receive input.

Reimplemented in NeuronModels::TraubMilesNStep, NeuronModels::TraubMilesAlt, NeuronModels::TraubMiles, NeuronModels::PoissonNew, NeuronModels::Poisson, NeuronModels::Spike SourceArray, NeuronModels::LIF, NeuronModels::lzhikevich, and NeuronModels::RulkovMap.

20.19.2.5 getSupportCode()

```
virtual std::string NeuronModels::Base::getSupportCode ( ) const [inline], [virtual]
```

Gets support code to be made available within the neuron kernel/funcion.

This is intended to contain user defined device functions that are used in the neuron codes. Preprocessor defines are also allowed if appropriately safeguarded against multiple definition by using ifndef; functions should be declared as "host device "to be available for both GPU and CPU versions.

20.19.2.6 getThresholdConditionCode()

```
virtual std::string NeuronModels::Base::getThresholdConditionCode ( ) const [inline], [virtual]
```

Gets code which defines the condition for a true spike in the described neuron model.

This evaluates to a bool (e.g. "V > 20").

Reimplemented in NeuronModels::TraubMiles, NeuronModels::PoissonNew, NeuronModels::Poisson, Neuron Models::SpikeSourceArray, NeuronModels::SpikeSource, NeuronModels::LIF, NeuronModels::Izhikevich, and NeuronModels::RulkovMap.

20.19.2.7 isAutoRefractoryRequired()

```
virtual bool NeuronModels::Base::isAutoRefractoryRequired ( ) const [inline], [virtual]
```

Does this model require auto-refractory logic?

The documentation for this class was generated from the following files:

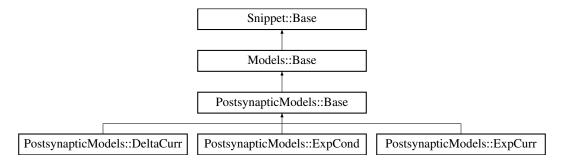
- · neuronModels.h
- neuronModels.cc

20.20 PostsynapticModels::Base Class Reference

Base class for all postsynaptic models.

```
#include <postsynapticModels.h>
```

Inheritance diagram for PostsynapticModels::Base:



Public Member Functions

- · virtual std::string getDecayCode () const
- · virtual std::string getApplyInputCode () const
- virtual std::string getSupportCode () const
- bool canBeMerged (const Base *other) const

Can this neuron model be merged with other? i.e. can they be simulated using same generated code.

Additional Inherited Members

20.20.1 Detailed Description

Base class for all postsynaptic models.

20.20.2 Member Function Documentation

20.20.2.1 canBeMerged()

Can this neuron model be merged with other? i.e. can they be simulated using same generated code.

20.20.2.2 getApplyInputCode()

virtual std::string PostsynapticModels::Base::getApplyInputCode () const [inline], [virtual]

Reimplemented in PostsynapticModels::DeltaCurr, PostsynapticModels::ExpCond, and PostsynapticModels::Exp← Curr.

20.20.2.3 getDecayCode()

virtual std::string PostsynapticModels::Base::getDecayCode () const [inline], [virtual]

Reimplemented in PostsynapticModels::ExpCond, and PostsynapticModels::ExpCurr.

20.20.2.4 getSupportCode()

virtual std::string PostsynapticModels::Base::getSupportCode () const [inline], [virtual]

The documentation for this class was generated from the following files:

- · postsynapticModels.h
- postsynapticModels.cc

20.21 Snippet::Base Class Reference

Base class for all code snippets.

#include <snippet.h>

Inheritance diagram for Snippet::Base:



Classes

• struct DerivedParam

A derived parameter has a name and a function for obtaining its value.

struct EGP

An extra global parameter has a name and a type.

struct ParamVal

Additional input variables, row state variables and other things have a name, a type and an initial value.

Public Types

- typedef std::vector< std::string > StringVec
- typedef std::vector< EGP > EGPVec
- typedef std::vector< ParamVal > ParamValVec
- $\bullet \ \ typedef \ std:: vector < {\color{red} Corived Param} > {\color{red} Derived Param} Vec$

Public Member Functions

- virtual ~Base ()
- virtual StringVec getParamNames () const

Gets names of of (independent) model parameters.

- virtual DerivedParamVec getDerivedParams () const
- virtual EGPVec getExtraGlobalParams () const
- size_t getExtraGlobalParamIndex (const std::string ¶mName) const

Find the index of a named extra global parameter.

Protected Member Functions

• bool canBeMerged (const Base *other) const

Static Protected Member Functions

template<typename T >
 static size_t getNamedVecIndex (const std::string &name, const std::vector< T > &vec)

20.21.1 Detailed Description

Base class for all code snippets.

20.21.2 Member Typedef Documentation

20.21.2.1 DerivedParamVec

typedef std::vector<DerivedParam> Snippet::Base::DerivedParamVec

20.21.2.2 EGPVec

typedef std::vector<EGP> Snippet::Base::EGPVec

20.21.2.3 ParamValVec

typedef std::vector<ParamVal> Snippet::Base::ParamValVec

20.21.2.4 StringVec

typedef std::vector<std::string> Snippet::Base::StringVec

20.21.3 Constructor & Destructor Documentation

20.21.3.1 \sim Base()

virtual Snippet::Base::~Base () [inline], [virtual]

20.21.4 Member Function Documentation

20.21.4.1 canBeMerged()

virtual DerivedParamVec Snippet::Base::getDerivedParams () const [inline], [virtual]

Gets names of derived model parameters and the function objects to call to Calculate their value from a vector of model parameter values

Reimplemented in NeuronModels::PoissonNew, WeightUpdateModels::PiecewiseSTDP, NeuronModels::LIF, Init VarSnippet::NormalClippedDelay, NeuronModels::RulkovMap, InitSparseConnectivitySnippet::FixedProbability Base, CurrentSourceModels::PoissonExp, PostsynapticModels::ExpCond, and PostsynapticModels::ExpCurr.

20.21.4.3 getExtraGlobalParamIndex()

Find the index of a named extra global parameter.

20.21.4.4 getExtraGlobalParams()

```
virtual EGPVec Snippet::Base::getExtraGlobalParams ( ) const [inline], [virtual]
```

Gets names and types (as strings) of additional per-population parameters for the snippet

Reimplemented in NeuronModels::SpikeSourceArray, and InitSparseConnectivity \leftarrow Snippet::FixedNumberTotalWithReplacement.

20.21.4.5 getNamedVecIndex()

20.21.4.6 getParamNames()

```
virtual StringVec Snippet::Base::getParamNames ( ) const [inline], [virtual]
```

Gets names of of (independent) model parameters.

Reimplemented in NeuronModels::TraubMilesNStep, NeuronModels::TraubMiles, NeuronModels::PoissonNew, NeuronModels::Poisson, WeightUpdateModels::PiecewiseSTDP, InitSparseConnectivitySnippet::FixedNumber TotalWithReplacement, NeuronModels::LIF, InitSparseConnectivitySnippet::FixedNumberPostWithReplacement, WeightUpdateModels::StaticGraded, NeuronModels::IzhikevichVariable, InitVarSnippet::Gamma, InitVarSnippet::Exponential, NeuronModels::Izhikevich, InitVarSnippet::NormalClippedDelay, InitVarSnippet::NormalClipped, NeuronModels::RulkovMap, InitSparseConnectivitySnippet::FixedProbabilityBase, CurrentSourceModels::&PoissonExp, InitVarSnippet::Normal, PostsynapticModels::ExpCond, InitVarSnippet::Uniform, CurrentSource

Models::GaussianNoise, InitVarSnippet::Constant, CurrentSourceModels::DC, and PostsynapticModels::ExpCurr.

The documentation for this class was generated from the following file:

· snippet.h

20.22 WeightUpdateModels::Base Class Reference

Base class for all weight update models.

#include <weightUpdateModels.h>

Inheritance diagram for WeightUpdateModels::Base:



Public Member Functions

virtual std::string getSimCode () const

Gets simulation code run when 'true' spikes are received.

virtual std::string getEventCode () const

Gets code run when events (all the instances where event threshold condition is met) are received.

virtual std::string getLearnPostCode () const

Gets code to include in the learnSynapsesPost kernel/function.

virtual std::string getSynapseDynamicsCode () const

Gets code for synapse dynamics which are independent of spike detection.

virtual std::string getEventThresholdConditionCode () const

Gets codes to test for events.

virtual std::string getSimSupportCode () const

Gets support code to be made available within the synapse kernel/function.

virtual std::string getLearnPostSupportCode () const

Gets support code to be made available within learnSynapsesPost kernel/function.

virtual std::string getSynapseDynamicsSuppportCode () const

Gets support code to be made available within the synapse dynamics kernel/function.

- virtual std::string getPreSpikeCode () const
- virtual std::string getPostSpikeCode () const
- virtual VarVec getPreVars () const
- virtual VarVec getPostVars () const
- virtual bool isPreSpikeTimeRequired () const

Whether presynaptic spike times are needed or not.

virtual bool isPostSpikeTimeRequired () const

Whether postsynaptic spike times are needed or not.

• size t getPreVarIndex (const std::string &varName) const

Find the index of a named presynaptic variable.

size_t getPostVarIndex (const std::string &varName) const

Find the index of a named postsynaptic variable.

bool canBeMerged (const Base *other) const

Can this weight update model be merged with other? i.e. can they be simulated using same generated code.

Additional Inherited Members

20.22.1 Detailed Description

Base class for all weight update models.

20.22.2 Member Function Documentation

20.22.2.1 canBeMerged()

Can this weight update model be merged with other? i.e. can they be simulated using same generated code.

20.22.2.2 getEventCode()

```
virtual std::string WeightUpdateModels::Base::getEventCode ( ) const [inline], [virtual]
```

Gets code run when events (all the instances where event threshold condition is met) are received.

Reimplemented in WeightUpdateModels::StaticGraded.

20.22.2.3 getEventThresholdConditionCode()

virtual std::string WeightUpdateModels::Base::getEventThresholdConditionCode () const [inline],
[virtual]

Gets codes to test for events.

Reimplemented in WeightUpdateModels::StaticGraded.

20.22.2.4 getLearnPostCode()

```
virtual std::string WeightUpdateModels::Base::getLearnPostCode ( ) const [inline], [virtual]
```

Gets code to include in the learnSynapsesPost kernel/function.

For examples when modelling STDP, this is where the effect of postsynaptic spikes which occur *after* presynaptic spikes are applied.

Reimplemented in WeightUpdateModels::PiecewiseSTDP.

20.22.2.5 getLearnPostSupportCode()

```
virtual std::string WeightUpdateModels::Base::getLearnPostSupportCode ( ) const [inline],
[virtual]
```

Gets support code to be made available within learnSynapsesPost kernel/function.

Preprocessor defines are also allowed if appropriately safeguarded against multiple definition by using ifndef; functions should be declared as "__host__ __device__" to be available for both GPU and CPU versions.

20.22.2.6 getPostSpikeCode()

virtual std::string WeightUpdateModels::Base::getPostSpikeCode () const [inline], [virtual]

Gets code to be run once per spiking postsynaptic neuron before learn post code is run on synapses

This is typically for the code to update postsynaptic variables. Presynaptic and synapse variables are not accesible from within this code

20.22.2.7 getPostVarIndex()

Find the index of a named postsynaptic variable.

20.22.2.8 getPostVars()

```
virtual VarVec WeightUpdateModels::Base::getPostVars ( ) const [inline], [virtual]
```

Gets names and types (as strings) of state variables that are common across all synapses going to the same postsynaptic neuron

20.22.2.9 getPreSpikeCode()

```
virtual std::string WeightUpdateModels::Base::getPreSpikeCode ( ) const [inline], [virtual]
```

Gets code to be run once per spiking presynaptic neuron before sim code is run on synapses

This is typically for the code to update presynaptic variables. Postsynaptic and synapse variables are not accesible from within this code

20.22.2.10 getPreVarIndex()

Find the index of a named presynaptic variable.

20.22.2.11 getPreVars()

```
virtual VarVec WeightUpdateModels::Base::getPreVars ( ) const [inline], [virtual]
```

Gets names and types (as strings) of state variables that are common across all synapses coming from the same presynaptic neuron

20.22.2.12 getSimCode()

```
virtual std::string WeightUpdateModels::Base::getSimCode ( ) const [inline], [virtual]
```

Gets simulation code run when 'true' spikes are received.

 $Reimplemented \ in \ WeightUpdateModels:: Piecewise STDP, \ WeightUpdateModels:: StaticPulseDendriticDelay, \ and \ WeightUpdateModels:: StaticPulse.$

20.22.2.13 getSimSupportCode()

```
virtual std::string WeightUpdateModels::Base::getSimSupportCode ( ) const [inline], [virtual]
```

Gets support code to be made available within the synapse kernel/function.

This is intended to contain user defined device functions that are used in the weight update code. Preprocessor defines are also allowed if appropriately safeguarded against multiple definition by using ifndef; functions should be declared as "__host__ __device__" to be available for both GPU and CPU versions; note that this support code is available to sim, event threshold and event code

20.22.2.14 getSynapseDynamicsCode()

```
virtual std::string WeightUpdateModels::Base::getSynapseDynamicsCode ( ) const [inline],
[virtual]
```

Gets code for synapse dynamics which are independent of spike detection.

20.22.2.15 getSynapseDynamicsSuppportCode()

```
virtual std::string WeightUpdateModels::Base::getSynapseDynamicsSuppportCode ( ) const [inline],
[virtual]
```

Gets support code to be made available within the synapse dynamics kernel/function.

Preprocessor defines are also allowed if appropriately safeguarded against multiple definition by using ifndef; functions should be declared as "__host__ __device__" to be available for both GPU and CPU versions.

20.22.2.16 isPostSpikeTimeRequired()

```
virtual bool WeightUpdateModels::Base::isPostSpikeTimeRequired ( ) const [inline], [virtual]
```

Whether postsynaptic spike times are needed or not.

Reimplemented in WeightUpdateModels::PiecewiseSTDP.

20.22.2.17 isPreSpikeTimeRequired()

```
virtual bool WeightUpdateModels::Base::isPreSpikeTimeRequired ( ) const [inline], [virtual]
```

Whether presynaptic spike times are needed or not.

Reimplemented in WeightUpdateModels::PiecewiseSTDP.

The documentation for this class was generated from the following files:

- weightUpdateModels.h
- weightUpdateModels.cc

20.23 CodeGenerator::CodeStream::CB Struct Reference

A close bracket marker.

```
#include <codeStream.h>
```

Public Member Functions

• CB (unsigned int level)

Public Attributes

· const unsigned int Level

20.23.1 Detailed Description

A close bracket marker.

Write to code stream os using:

```
os << CB(16);
```

20.23.2 Constructor & Destructor Documentation

```
20.23.2.1 CB()
CodeGenerator::CodeStream::CB::CB (
              unsigned int level ) [inline]
20.23.3 Member Data Documentation
20.23.3.1 Level
const unsigned int CodeGenerator::CodeStream::CB::Level
The documentation for this struct was generated from the following file:
    · codeStream.h
20.24 clrngLfsr113Stream_ Struct Reference
#include <lfsr113.h>
Public Attributes
    • union {
       struct {
          clrngLfsr113StreamState states [3]
       }
       struct {
          clrngLfsr113StreamState current
          clrngLfsr113StreamState initial
          clrngLfsr113StreamState substream
       }
     };
20.24.1 Member Data Documentation
20.24.1.1 "@1
union { ... }
20.24.1.2 current
clrngLfsr113StreamState clrngLfsr113Stream_::current
```

clrngLfsr113StreamState clrngLfsr113Stream_::initial

20.24.1.3 initial

20.24.1.4 states

clrngLfsr113StreamState clrngLfsr113Stream_::states[3]

20.24.1.5 substream

clrngLfsr113StreamState clrngLfsr113Stream_::substream

The documentation for this struct was generated from the following file:

• Ifsr113.h

20.25 clrngLfsr113StreamCreator_ Struct Reference

Public Attributes

- clrngLfsr113StreamState initialState
- clrngLfsr113StreamState nextState

20.25.1 Member Data Documentation

20.25.1.1 initialState

 $\verb|clrngLfsr113StreamState| clrngLfsr113StreamCreator_::initialState|$

20.25.1.2 nextState

clrngLfsr113StreamState clrngLfsr113StreamCreator_::nextState

The documentation for this struct was generated from the following file:

• lfsr113.c

20.26 clrngLfsr113StreamState Struct Reference

```
#include <lfsr113.h>
```

Public Attributes

cl_uint g [4]

Seed for the first LFSR component.

20.26.1 Member Data Documentation

20.26.1.1 g

cl_uint clrngLfsr113StreamState::g[4]

Seed for the first LFSR component.

The documentation for this struct was generated from the following file:

• lfsr113.h

20.27 clrngPhilox432Counter_ Struct Reference

```
#include <philox432.h>
```

Public Attributes

- · clrngPhilox432SB H
- clrngPhilox432SB L

20.27.1 Member Data Documentation

20.27.1.1 H

```
clrngPhilox432SB clrngPhilox432Counter_::H
```

20.27.1.2 L

```
clrngPhilox432SB clrngPhilox432Counter_::L
```

The documentation for this struct was generated from the following file:

• philox432.h

20.28 clrngPhilox432SB_ Struct Reference

```
#include <philox432.h>
```

Public Attributes

- · cl uint msb
- cl_uint Isb

20.28.1 Member Data Documentation

20.28.1.1 lsb

```
cl_uint clrngPhilox432SB_::lsb
```

• philox432.h

```
20.28.1.2 msb
cl_uint clrngPhilox432SB_::msb
The documentation for this struct was generated from the following file:
    • philox432.h
20.29 clrngPhilox432Stream_ Struct Reference
#include <philox432.h>
Public Attributes
    • union {
        struct {
          clrngPhilox432StreamState states [3]
       }
       struct {
          clrngPhilox432StreamState current
          clrngPhilox432StreamState initial
          clrngPhilox432StreamState substream
     };
20.29.1 Member Data Documentation
20.29.1.1 "@7
union { ... }
20.29.1.2 current
clrngPhilox432StreamState clrngPhilox432Stream_::current
20.29.1.3 initial
clrngPhilox432StreamState clrngPhilox432Stream_::initial
20.29.1.4 states
clrngPhilox432StreamState clrngPhilox432Stream_::states[3]
20.29.1.5 substream
clrngPhilox432StreamState clrngPhilox432Stream_::substream
The documentation for this struct was generated from the following file:
```

20.30 clrngPhilox432StreamCreator_ Struct Reference

Public Attributes

- clrngPhilox432StreamState initialState
- clrngPhilox432StreamState nextState
- clrngPhilox432Counter JumpDistance

20.30.1 Member Data Documentation

20.30.1.1 initialState

 $\verb|clrngPhilox432StreamState| clrngPhilox432StreamCreator| :: initialState|$

20.30.1.2 JumpDistance

clrngPhilox432Counter clrngPhilox432StreamCreator_::JumpDistance

20.30.1.3 nextState

clrngPhilox432StreamState clrngPhilox432StreamCreator_::nextState

The documentation for this struct was generated from the following file:

• philox432.c

20.31 clrngPhilox432StreamState Struct Reference

```
#include <philox432.h>
```

Public Attributes

- clrngPhilox432Counter ctr
- cl_uint deck [4]
- cl_uint deckIndex

20.31.1 Member Data Documentation

20.31.1.1 ctr

clrngPhilox432Counter clrngPhilox432StreamState::ctr

20.31.1.2 deck

cl_uint clrngPhilox432StreamState::deck[4]

20.31.1.3 deckIndex

cl_uint clrngPhilox432StreamState::deckIndex

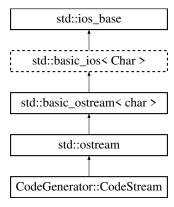
The documentation for this struct was generated from the following file:

• philox432.h

20.32 CodeGenerator::CodeStream Class Reference

#include <codeStream.h>

Inheritance diagram for CodeGenerator::CodeStream:



Classes

• struct CB

A close bracket marker.

struct OB

An open bracket marker.

class Scope

Public Member Functions

- CodeStream ()
- CodeStream (std::ostream &stream)
- void setSink (std::ostream &stream)

Friends

- GENN_EXPORT friend std::ostream & operator<< (std::ostream &s, const OB &ob)
- GENN_EXPORT friend std::ostream & operator<< (std::ostream &s, const CB &cb)

20.32.1 Constructor & Destructor Documentation

20.32.1.1 CodeStream() [1/2]

CodeGenerator::CodeStream::CodeStream () [inline]

The documentation for this class was generated from the following file:

const CB & cb) [friend]

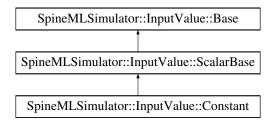
• codeStream.h

20.32.3.2 operator << [2/2]

20.33 SpineMLSimulator::InputValue::Constant Class Reference

```
#include <inputValue.h>
```

Inheritance diagram for SpineMLSimulator::InputValue::Constant:



Public Member Functions

- Constant (double dt, unsigned int numNeurons, const pugi::xml_node &node)
- virtual void update (double dt, unsigned long long timestep, std::function< void(unsigned int, double)> applyValueFunc) override

Additional Inherited Members

20.33.1 Constructor & Destructor Documentation

20.33.1.1 Constant()

20.33.2 Member Function Documentation

20.33.2.1 update()

Implements SpineMLSimulator::InputValue::Base.

The documentation for this class was generated from the following files:

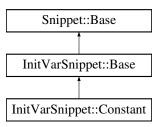
- · inputValue.h
- inputValue.cc

20.34 InitVarSnippet::Constant Class Reference

Initialises variable to a constant value.

```
#include <initVarSnippet.h>
```

Inheritance diagram for InitVarSnippet::Constant:



Public Member Functions

- DECLARE SNIPPET (InitVarSnippet::Constant, 1)
- SET_CODE ("\$(value) = \$(constant);")
- virtual StringVec getParamNames () const override

Gets names of of (independent) model parameters.

Additional Inherited Members

20.34.1 Detailed Description

Initialises variable to a constant value.

This snippet takes 1 parameter:

• value - The value to intialise the variable to

Note

This snippet type is seldom used directly - Models::VarInit has an implicit constructor that, internally, creates one of these snippets

20.34.2 Member Function Documentation

20.34.2.1 DECLARE_SNIPPET()

20.34.2.2 getParamNames()

```
virtual StringVec InitVarSnippet::Constant::getParamNames ( ) const [inline], [override],
[virtual]
```

Gets names of of (independent) model parameters.

Reimplemented from Snippet::Base.

```
20.34.2.3 SET_CODE()
```

```
InitVarSnippet::Constant::SET_CODE ( )
```

The documentation for this class was generated from the following file:

· initVarSnippet.h

20.35 SpineMLSimulator::InputValue::ConstantArray Class Reference

```
#include <inputValue.h>
```

Inheritance diagram for SpineMLSimulator::InputValue::ConstantArray:

```
SpineMLSimulator::InputValue::Base

SpineMLSimulator::InputValue::ConstantArray
```

Public Member Functions

- ConstantArray (double dt, unsigned int numNeurons, const pugi::xml_node &node)
- virtual void update (double dt, unsigned long long timestep, std::function< void(unsigned int, double)> applyValueFunc) override

Additional Inherited Members

20.35.1 Constructor & Destructor Documentation

20.35.1.1 ConstantArray()

20.35.2 Member Function Documentation

20.35.2.1 update()

Implements SpineMLSimulator::InputValue::Base.

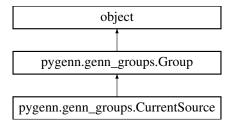
The documentation for this class was generated from the following files:

- inputValue.h
- inputValue.cc

20.36 pygenn.genn_groups.CurrentSource Class Reference

Class representing a current injection into a group of neurons.

Inheritance diagram for pygenn.genn_groups.CurrentSource:



Public Member Functions

```
    def __init__ (self, name, model)
        Init CurrentSource.
```

def size (self)

Number of neuron in the injected population.

- def size (self, _)
- def set_current_source_model (self, model, param_space, var_space)

Set curront source model, its parameters and initial variables.

def add_to (self, nn_model, pop)

Inject this CurrentSource into population and add it to the GeNN NNmodel.

def add_extra_global_param (self, param_name, param_values)

Set extra global parameter.

• def set_extra_global_param (self, param_name, param_values)

Set extra global parameter.

- def load (self)
- def load_init_egps (self)
- def reinitialise (self)

Reinitialise current source.

Public Attributes

- current_source_model
- · target pop
- pop

20.36.1 Detailed Description

Class representing a current injection into a group of neurons.

20.36.2 Constructor & Destructor Documentation

Init CurrentSource.

Parameters

```
name string name of the current source
```

20.36.3 Member Function Documentation

20.36.3.1 add_extra_global_param()

Set extra global parameter.

Parameters

param_name	string with the name of the extra global parameter
param_values	iterable or a single value

20.36.3.2 add_to()

```
def pygenn.genn_groups.CurrentSource.add_to ( self, \\ nn\_model, \\ pop )
```

Inject this CurrentSource into population and add it to the GeNN NNmodel.

Parameters

рор	instance of NeuronGroup into which this CurrentSource should be injected
nn_model	GeNN NNmodel

20.36.3.3 load()

```
\label{local_control} $\operatorname{def pygenn.genn\_groups.CurrentSource.load} \ ($\operatorname{\it self}$ )
```

20.36.3.4 load_init_egps()

```
\label{local_def} \mbox{def pygenn.genn\_groups.CurrentSource.load\_init\_egps (} \\ self \mbox{)}
```

20.36.3.5 reinitialise()

```
def pygenn.genn_groups.CurrentSource.reinitialise ( self \ )
```

Reinitialise current source.

20.36.3.6 set_current_source_model()

Set curront source model, its parameters and initial variables.

Parameters

model	type as string of intance of the model
param_space	dict with model parameters
var_space	dict with model variables

20.36.3.7 set_extra_global_param()

Set extra global parameter.

Parameters

param_name	string with the name of the extra global parameter
param_values	iterable or a single value

```
20.36.3.8 size() [1/2]
def pygenn.genn_groups.CurrentSource.size (
```

Number of neuron in the injected population.

20.36.4 Member Data Documentation

20.36.4.1 current_source_model

pygenn.genn_groups.CurrentSource.current_source_model

20.36.4.2 pop

pygenn.genn_groups.CurrentSource.pop

20.36.4.3 target_pop

pygenn.genn_groups.CurrentSource.target_pop

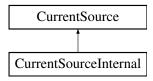
The documentation for this class was generated from the following file:

genn_groups.py

20.37 CurrentSource Class Reference

#include <currentSource.h>

Inheritance diagram for CurrentSource:



Public Member Functions

- CurrentSource (const CurrentSource &)=delete
- CurrentSource ()=delete
- void setVarLocation (const std::string &varName, VarLocation loc)

Set location of current source state variable.

void setExtraGlobalParamLocation (const std::string ¶mName, VarLocation loc)

Set location of extra global parameter.

- const std::string & getName () const
- const CurrentSourceModels::Base * getCurrentSourceModel () const

Gets the current source model used by this group.

- const std::vector< double > & getParams () const
- const std::vector< Models::VarInit > & getVarInitialisers () const
- VarLocation getVarLocation (const std::string &varName) const

Get variable location for current source model state variable.

VarLocation getVarLocation (size t index) const

Get variable location for current source model state variable.

VarLocation getExtraGlobalParamLocation (const std::string ¶mName) const

Get location of neuron model extra global parameter by name.

• VarLocation getExtraGlobalParamLocation (size t index) const

Get location of neuron model extra global parameter by omdex.

Protected Member Functions

- CurrentSource (const std::string &name, const CurrentSourceModels::Base *currentSourceModel, const std::vector< double > ¶ms, const std::vector< Models::VarInit > &varInitialisers, VarLocation default← VarLocation, VarLocation defaultExtraGlobalParamLocation)
- void initDerivedParams (double dt)
- const std::vector< double > & getDerivedParams () const
- · bool isSimRNGRequired () const

Does this current source require an RNG to simulate.

bool isInitRNGRequired () const

Does this current source group require an RNG for it's init code.

bool canBeMerged (const CurrentSource & other) const

Can this current source be merged with other? i.e. can they be simulated using same generated code.

• bool canInitBeMerged (const CurrentSource &other) const

Can the initialisation of these current sources be merged together? i.e. can they be initialised using same generated code.

20.37.1 Constructor & Destructor Documentation

```
20.37.1.1 CurrentSource() [1/3]
CurrentSource::CurrentSource (
             const CurrentSource & ) [delete]
20.37.1.2 CurrentSource() [2/3]
CurrentSource::CurrentSource ( ) [delete]
20.37.1.3 CurrentSource() [3/3]
CurrentSource::CurrentSource (
             const std::string & name,
             const CurrentSourceModels::Base * currentSourceModel,
             const std::vector< double > & params,
              const std::vector< Models::VarInit > & varInitialisers,
              VarLocation defaultVarLocation,
              VarLocation defaultExtraGlobalParamLocation ) [inline], [protected]
20.37.2 Member Function Documentation
20.37.2.1 canBeMerged()
bool CurrentSource::canBeMerged (
              const CurrentSource & other ) const [protected]
Can this current source be merged with other? i.e. can they be simulated using same generated code.
NOTE: this can only be called after model is finalized
20.37.2.2 canInitBeMerged()
bool CurrentSource::canInitBeMerged (
             const CurrentSource & other ) const [protected]
Can the initialisation of these current sources be merged together? i.e. can they be initialised using same generated
NOTE: this can only be called after model is finalized
20.37.2.3 getCurrentSourceModel()
const CurrentSourceModels::Base* CurrentSource::getCurrentSourceModel ( ) const [inline]
Gets the current source model used by this group.
20.37.2.4 getDerivedParams()
const std::vector<double>& CurrentSource::getDerivedParams ( ) const [inline], [protected]
20.37.2.5 getExtraGlobalParamLocation() [1/2]
VarLocation CurrentSource::getExtraGlobalParamLocation (
```

const std::string & paramName) const

```
Get location of neuron model extra global parameter by name.
```

This is only used by extra global parameters which are pointers

```
20.37.2.6 getExtraGlobalParamLocation() [2/2]
VarLocation CurrentSource::getExtraGlobalParamLocation (
              size_t index ) const [inline]
Get location of neuron model extra global parameter by omdex.
This is only used by extra global parameters which are pointers
20.37.2.7 getName()
const std::string& CurrentSource::getName ( ) const [inline]
20.37.2.8 getParams()
const std::vector<double>& CurrentSource::getParams ( ) const [inline]
20.37.2.9 getVarInitialisers()
const std::vector<Models::VarInit>& CurrentSource::getVarInitialisers ( ) const [inline]
20.37.2.10 getVarLocation() [1/2]
VarLocation CurrentSource::getVarLocation (
              const std::string & varName ) const
Get variable location for current source model state variable.
20.37.2.11 getVarLocation() [2/2]
VarLocation CurrentSource::getVarLocation (
              size_t index ) const [inline]
Get variable location for current source model state variable.
20.37.2.12 initDerivedParams()
void CurrentSource::initDerivedParams (
              double dt ) [protected]
20.37.2.13 isInitRNGRequired()
bool CurrentSource::isInitRNGRequired ( ) const [protected]
Does this current source group require an RNG for it's init code.
```

20.37.2.14 isSimRNGRequired()

bool CurrentSource::isSimRNGRequired () const [protected]

Does this current source require an RNG to simulate.

20.37.2.15 setExtraGlobalParamLocation()

Set location of extra global parameter.

This is ignored for simulations on hardware with a single memory space and only applies to extra global parameters which are pointers.

20.37.2.16 setVarLocation()

Set location of current source state variable.

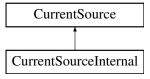
The documentation for this class was generated from the following files:

- · currentSource.h
- · currentSource.cc

20.38 CurrentSourceInternal Class Reference

```
#include <currentSourceInternal.h>
```

Inheritance diagram for CurrentSourceInternal:



Public Member Functions

• CurrentSourceInternal (const std::string &name, const CurrentSourceModels::Base *currentSourceModel, const std::vector< double > ¶ms, const std::vector< Models::VarInit > &varInitialisers, VarLocation defaultVarLocation, VarLocation defaultExtraGlobalParamLocation)

Additional Inherited Members

20.38.1 Constructor & Destructor Documentation

20.38.1.1 CurrentSourceInternal()

```
VarLocation defaultVarLocation,
VarLocation defaultExtraGlobalParamLocation ) [inline]
```

The documentation for this class was generated from the following file:

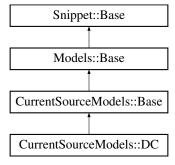
· currentSourceInternal.h

20.39 CurrentSourceModels::DC Class Reference

DC source.

#include <currentSourceModels.h>

Inheritance diagram for CurrentSourceModels::DC:



Public Types

- typedef Snippet::ValueBase< 1 > ParamValues
- typedef Models::VarInitContainerBase< 0 > VarValues
- typedef Models::VarInitContainerBase< 0 > PreVarValues
- $\bullet \ \ typedef \ Models:: VarInitContainer Base < 0 > PostVarValues \\$

Public Member Functions

- SET_INJECTION_CODE ("\$(injectCurrent, \$(amp));\)
- virtual StringVec getParamNames () const override

Gets names of of (independent) model parameters.

Static Public Member Functions

static const DC * getInstance ()

Additional Inherited Members

20.39.1 Detailed Description

DC source.

It has a single parameter:

• amp - amplitude of the current [nA]

20.39.2 Member Typedef Documentation

20.39.2.1 ParamValues typedef Snippet::ValueBase< 1 > CurrentSourceModels::DC::ParamValues 20.39.2.2 PostVarValues typedef Models::VarInitContainerBase<0> CurrentSourceModels::DC::PostVarValues 20.39.2.3 PreVarValues typedef Models::VarInitContainerBase<0> CurrentSourceModels::DC::PreVarValues 20.39.2.4 VarValues typedef Models::VarInitContainerBase< 0 > CurrentSourceModels::DC::VarValues 20.39.3 Member Function Documentation 20.39.3.1 getInstance() static const DC* CurrentSourceModels::DC::getInstance () [inline], [static] 20.39.3.2 getParamNames() virtual StringVec CurrentSourceModels::DC::getParamNames () const [inline], [override], Gets names of of (independent) model parameters. Reimplemented from Snippet::Base. 20.39.3.3 SET_INJECTION_CODE()

The documentation for this class was generated from the following file:

"\$(injectCurrent, \$(amp));\)

currentSourceModels.h

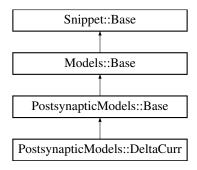
20.40 PostsynapticModels::DeltaCurr Class Reference

CurrentSourceModels::DC::SET_INJECTION_CODE (

Simple delta current synapse.

```
#include <postsynapticModels.h>
```

Inheritance diagram for PostsynapticModels::DeltaCurr:



Public Types

- typedef Snippet::ValueBase< 0 > ParamValues
- typedef Models::VarInitContainerBase< 0 > VarValues
- typedef Models::VarInitContainerBase< 0 > PreVarValues
- typedef Models::VarInitContainerBase< 0 > PostVarValues

Public Member Functions

• virtual std::string getApplyInputCode () const override

Static Public Member Functions

• static const DeltaCurr * getInstance ()

Additional Inherited Members

20.40.1 Detailed Description

Simple delta current synapse.

Synaptic input provides a direct inject of instantaneous current

20.40.2 Member Typedef Documentation

20.40.2.1 ParamValues

 ${\tt typedef~Snippet::ValueBase<~0~>~PostsynapticModels::DeltaCurr::ParamValues}$

20.40.2.2 PostVarValues

typedef Models::VarInitContainerBase<0> PostsynapticModels::DeltaCurr::PostVarValues

20.40.2.3 PreVarValues

typedef Models::VarInitContainerBase<0> PostsynapticModels::DeltaCurr::PreVarValues

20.40.2.4 VarValues

typedef Models::VarInitContainerBase< 0 > PostsynapticModels::DeltaCurr::VarValues

20.40.3 Member Function Documentation

20.40.3.1 getApplyInputCode()

```
virtual std::string PostsynapticModels::DeltaCurr::getApplyInputCode ( ) const [inline],
[override], [virtual]
```

Reimplemented from PostsynapticModels::Base.

20.40.3.2 getInstance()

```
static const DeltaCurr* PostsynapticModels::DeltaCurr::getInstance ( ) [inline], [static]
```

The documentation for this class was generated from the following file:

· postsynapticModels.h

20.41 Snippet::Base::DerivedParam Struct Reference

A derived parameter has a name and a function for obtaining its value.

```
#include <snippet.h>
```

Public Member Functions

bool operator== (const DerivedParam &other) const

Public Attributes

- std::string name
- std::function< double(const std::vector< double > &, double)> func

20.41.1 Detailed Description

A derived parameter has a name and a function for obtaining its value.

20.41.2 Member Function Documentation

20.41.2.1 operator==()

20.41.3 Member Data Documentation

20.41.3.1 func

```
std::function<double(const std::vector<double> &, double)> Snippet::Base::DerivedParam::func
```

20.41.3.2 name

```
std::string Snippet::Base::DerivedParam::name
```

The documentation for this struct was generated from the following file:

· snippet.h

20.42 Snippet::Base::EGP Struct Reference

An extra global parameter has a name and a type.

```
#include <snippet.h>
```

Public Member Functions

• bool operator== (const EGP &other) const

Public Attributes

- · std::string name
- std::string type

20.42.1 Detailed Description

An extra global parameter has a name and a type.

20.42.2 Member Function Documentation

20.42.2.1 operator==()

20.42.3 Member Data Documentation

20.42.3.1 name

```
std::string Snippet::Base::EGP::name
```

20.42.3.2 type

```
std::string Snippet::Base::EGP::type
```

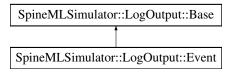
The documentation for this struct was generated from the following file:

· snippet.h

20.43 SpineMLSimulator::LogOutput::Event Class Reference

```
#include <logOutput.h>
```

Inheritance diagram for SpineMLSimulator::LogOutput::Event:



Public Member Functions

- Event (const pugi::xml_node &node, double dt, unsigned long long numTimeSteps, const std::string &port, unsigned int popSize, const filesystem::path &logPath, unsigned int *spikeQueuePtr, unsigned int *host
 SpikeCount, unsigned int *hostSpikes, void(*pullCurrentSpikesFunc)(void))
- · virtual void record (double dt, unsigned long long timestep) override

Additional Inherited Members

20.43.1 Constructor & Destructor Documentation

20.43.1.1 Event()

20.43.2 Member Function Documentation

20.43.2.1 record()

Implements SpineMLSimulator::LogOutput::Base.

The documentation for this class was generated from the following files:

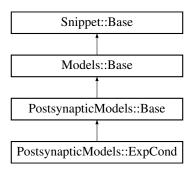
- logOutput.h
- logOutput.cc

20.44 PostsynapticModels::ExpCond Class Reference

Exponential decay with synaptic input treated as a conductance value.

#include <postsynapticModels.h>

Inheritance diagram for PostsynapticModels::ExpCond:



Public Types

- typedef Snippet::ValueBase< 2 > ParamValues
- typedef Models::VarInitContainerBase< 0 > VarValues
- typedef Models::VarInitContainerBase< 0 > PreVarValues
- $\bullet \ \ type def \ Models:: VarInit Container Base < 0 > Post Var Values \\$

Public Member Functions

- virtual std::string getDecayCode () const override
- virtual std::string getApplyInputCode () const override
- virtual StringVec getParamNames () const override
 Gets names of of (independent) model parameters.
- virtual DerivedParamVec getDerivedParams () const override

Static Public Member Functions

static const ExpCond * getInstance ()

Additional Inherited Members

20.44.1 Detailed Description

Exponential decay with synaptic input treated as a conductance value.

This model has no variables and two parameters:

- · tau: Decay time constant
- E : Reversal potential

tau is used by the derived parameter expdecay which returns expf(-dt/tau).

20.44.2 Member Typedef Documentation

20.44.2.1 ParamValues

typedef Snippet::ValueBase< 2 > PostsynapticModels::ExpCond::ParamValues

20.44.2.2 PostVarValues

typedef Models::VarInitContainerBase<0> PostsynapticModels::ExpCond::PostVarValues

20.44.2.3 PreVarValues

typedef Models::VarInitContainerBase<0> PostsynapticModels::ExpCond::PreVarValues

20.44.2.4 VarValues

typedef Models::VarInitContainerBase< 0 > PostsynapticModels::ExpCond::VarValues

20.44.3 Member Function Documentation

20.44.3.1 getApplyInputCode()

virtual std::string PostsynapticModels::ExpCond::getApplyInputCode () const [inline], [override],
[virtual]

Reimplemented from PostsynapticModels::Base.

20.44.3.2 getDecayCode()

virtual std::string PostsynapticModels::ExpCond::getDecayCode () const [inline], [override],
[virtual]

Reimplemented from PostsynapticModels::Base.

20.44.3.3 getDerivedParams()

```
virtual DerivedParamVec PostsynapticModels::ExpCond::getDerivedParams ( ) const [inline],
[override], [virtual]
```

Gets names of derived model parameters and the function objects to call to Calculate their value from a vector of model parameter values

Reimplemented from Snippet::Base.

20.44.3.4 getInstance()

static const ExpCond* PostsynapticModels::ExpCond::getInstance () [inline], [static]

20.44.3.5 getParamNames()

virtual StringVec PostsynapticModels::ExpCond::getParamNames () const [inline], [override],
[virtual]

Gets names of of (independent) model parameters.

Reimplemented from Snippet::Base.

The documentation for this class was generated from the following file:

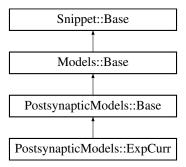
· postsynapticModels.h

20.45 PostsynapticModels::ExpCurr Class Reference

Exponential decay with synaptic input treated as a current value.

#include <postsynapticModels.h>

Inheritance diagram for PostsynapticModels::ExpCurr:



Public Types

- typedef Snippet::ValueBase< 1 > ParamValues
- typedef Models::VarInitContainerBase< 0 > VarValues
- typedef Models::VarInitContainerBase< 0 > PreVarValues
- typedef Models::VarInitContainerBase< 0 > PostVarValues

Public Member Functions

- virtual std::string getDecayCode () const override
- virtual std::string getApplyInputCode () const override
- virtual StringVec getParamNames () const override
 Gets names of of (independent) model parameters.
- · virtual DerivedParamVec getDerivedParams () const override

Static Public Member Functions

• static const ExpCurr * getInstance ()

Additional Inherited Members

20.45.1 Detailed Description

Exponential decay with synaptic input treated as a current value.

This model has no variables and a single parameter:

• tau: Decay time constant

20.45.2 Member Typedef Documentation

20.45.2.1 ParamValues

typedef Snippet::ValueBase< 1 > PostsynapticModels::ExpCurr::ParamValues

20.45.2.2 PostVarValues

typedef Models::VarInitContainerBase<0> PostsynapticModels::ExpCurr::PostVarValues

20.45.2.3 PreVarValues

typedef Models::VarInitContainerBase<0> PostsynapticModels::ExpCurr::PreVarValues

20.45.2.4 VarValues

typedef Models::VarInitContainerBase< 0 > PostsynapticModels::ExpCurr::VarValues

20.45.3 Member Function Documentation

20.45.3.1 getApplyInputCode()

virtual std::string PostsynapticModels::ExpCurr::getApplyInputCode () const [inline], [override],
[virtual]

Reimplemented from PostsynapticModels::Base.

20.45.3.2 getDecayCode()

virtual std::string PostsynapticModels::ExpCurr::getDecayCode () const [inline], [override],
[virtual]

Reimplemented from PostsynapticModels::Base.

20.45.3.3 getDerivedParams()

```
virtual DerivedParamVec PostsynapticModels::ExpCurr::getDerivedParams ( ) const [inline],
[override], [virtual]
```

Gets names of derived model parameters and the function objects to call to Calculate their value from a vector of model parameter values

Reimplemented from Snippet::Base.

20.45.3.4 getInstance()

static const ExpCurr* PostsynapticModels::ExpCurr::getInstance () [inline], [static]

20.45.3.5 getParamNames()

```
virtual StringVec PostsynapticModels::ExpCurr::getParamNames ( ) const [inline], [override],
[virtual]
```

Gets names of of (independent) model parameters.

Reimplemented from Snippet::Base.

The documentation for this class was generated from the following file:

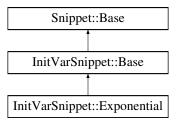
· postsynapticModels.h

20.46 InitVarSnippet::Exponential Class Reference

Initialises variable by sampling from the exponential distribution.

```
#include <initVarSnippet.h>
```

Inheritance diagram for InitVarSnippet::Exponential:



Public Member Functions

- DECLARE_SNIPPET (InitVarSnippet::Exponential, 1)
- SET_CODE ("\$(value) = \$(lambda) * \$(gennrand_exponential);")
- virtual StringVec getParamNames () const override

Gets names of of (independent) model parameters.

Additional Inherited Members

20.46.1 Detailed Description

Initialises variable by sampling from the exponential distribution.

This snippet takes 1 parameter:

• lambda - mean event rate (events per unit time/distance)

20.46.2 Member Function Documentation

20.46.2.1 DECLARE_SNIPPET()

20.46.2.2 getParamNames()

```
virtual StringVec InitVarSnippet::Exponential::getParamNames ( ) const [inline], [override],
[virtual]
```

Gets names of of (independent) model parameters.

Reimplemented from Snippet::Base.

20.46.2.3 SET_CODE()

```
InitVarSnippet::Exponential::SET_CODE ( )
```

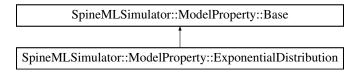
The documentation for this class was generated from the following file:

· initVarSnippet.h

20.47 SpineMLSimulator::ModelProperty::ExponentialDistribution Class Reference

```
#include <modelProperty.h>
```

Inheritance diagram for SpineMLSimulator::ModelProperty::ExponentialDistribution:



Public Member Functions

- ExponentialDistribution (const pugi::xml_node &node, const StateVar < scalar > &stateVar, unsigned int size)
- void setValue (scalar lambda)

20.47.1 Constructor & Destructor Documentation

20.47.1.1 ExponentialDistribution()

20.47.2 Member Function Documentation

20.47.2.1 setValue()

The documentation for this class was generated from the following files:

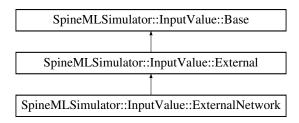
modelProperty.h

· modelProperty.cc

20.48 SpineMLSimulator::InputValue::External Class Reference

```
#include <inputValue.h>
```

Inheritance diagram for SpineMLSimulator::InputValue::External:



Public Member Functions

- External (double dt, unsigned int numNeurons, const pugi::xml_node &node)
- virtual void update (double dt, unsigned long long timestep, std::function< void(unsigned int, double)>
 applyValueFunc) final
- std::vector< double >::iterator getBufferBegin ()
- std::vector< double >::iterator getBufferEnd ()

Protected Member Functions

- virtual void updateInternal ()
- unsigned int getSize () const
- std::vector< double > & getBuffer ()

20.48.1 Constructor & Destructor Documentation

20.48.1.1 External()

20.48.2 Member Function Documentation

20.48.2.1 getBuffer()

20.48.2.2 getBufferBegin()

std::vector<double>::iterator SpineMLSimulator::InputValue::External::getBufferBegin () [inline]

20.48.2.3 getBufferEnd()

```
std::vector<double>::iterator SpineMLSimulator::InputValue::External::getBufferEnd ( ) [inline]
```

20.48.2.4 getSize()

```
unsigned int SpineMLSimulator::InputValue::External::getSize ( ) const [inline], [protected]
```

20.48.2.5 update()

Implements SpineMLSimulator::InputValue::Base.

20.48.2.6 updateInternal()

```
virtual void SpineMLSimulator::InputValue::External::updateInternal ( ) [inline], [protected],
[virtual]
```

Reimplemented in SpineMLSimulator::InputValue::ExternalNetwork.

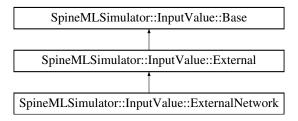
The documentation for this class was generated from the following files:

- · inputValue.h
- inputValue.cc

20.49 SpineMLSimulator::InputValue::ExternalNetwork Class Reference

```
#include <inputValue.h>
```

Inheritance diagram for SpineMLSimulator::InputValue::ExternalNetwork:



Public Member Functions

• ExternalNetwork (double dt, unsigned int numNeurons, const pugi::xml node &node)

Protected Member Functions

· virtual void updateInternal () override

20.49.1 Constructor & Destructor Documentation

20.49.1.1 ExternalNetwork()

20.49.2 Member Function Documentation

20.49.2.1 updateInternal()

```
void SpineMLSimulator::InputValue::ExternalNetwork::updateInternal ( ) [override], [protected],
[virtual]
```

Reimplemented from SpineMLSimulator::InputValue::External.

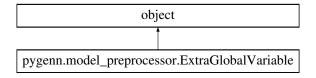
The documentation for this class was generated from the following files:

- inputValue.h
- · inputValue.cc

20.50 pygenn.model_preprocessor.ExtraGlobalVariable Class Reference

Class holding information about GeNN extra global pointer variable.

 $Inheritance\ diagram\ for\ pygenn.model_preprocessor. Extra Global Variable:$



Public Member Functions

- def __init__ (self, variable_name, variable_type, values=None)
 Init Variable.
- def set_values (self, values)

Set Variable's values.

Public Attributes

- · is_scalar
- type
- name
- view
- values

20.50.1 Detailed Description

Class holding information about GeNN extra global pointer variable.

20.50.2 Constructor & Destructor Documentation

Init Variable.

Parameters

variable_name	string name of the variable
variable_type	string type of the variable
values	iterable

20.50.3 Member Function Documentation

20.50.3.1 set_values()

```
def pygenn.model_preprocessor.ExtraGlobalVariable.set_values ( self, \\ values \ )
```

Set Variable's values.

Parameters

values	iterable or single value

20.50.4 Member Data Documentation

20.50.4.1 is_scalar

pygenn.model_preprocessor.ExtraGlobalVariable.is_scalar

20.50.4.2 name

 $\verb|pygenn.model_preprocessor.ExtraGlobalVariable.name|\\$

20.50.4.3 type

```
pygenn.model_preprocessor.ExtraGlobalVariable.type
```

20.50.4.4 values

```
pygenn.model_preprocessor.ExtraGlobalVariable.values
```

20.50.4.5 view

```
pygenn.model_preprocessor.ExtraGlobalVariable.view
```

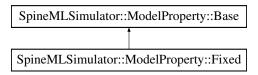
The documentation for this class was generated from the following file:

· model preprocessor.py

20.51 SpineMLSimulator::ModelProperty::Fixed Class Reference

```
#include <modelProperty.h>
```

Inheritance diagram for SpineMLSimulator::ModelProperty::Fixed:



Public Member Functions

- Fixed (const pugi::xml_node &node, const StateVar< scalar > &stateVar, unsigned int size)
- Fixed (double value, const StateVar< scalar > &stateVar, unsigned int size)
- void setValue (scalar value)

20.51.1 Constructor & Destructor Documentation

20.51.2 Member Function Documentation

20.51.2.1 setValue()

The documentation for this class was generated from the following files:

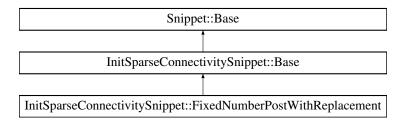
- modelProperty.h
- modelProperty.cc

20.52 InitSparseConnectivitySnippet::FixedNumberPostWithReplacement Class Reference

Initialises connectivity with a fixed number of random synapses per row.

```
#include <initSparseConnectivitySnippet.h>
```

Inheritance diagram for InitSparseConnectivitySnippet::FixedNumberPostWithReplacement:



Public Member Functions

- DECLARE_SNIPPET (InitSparseConnectivitySnippet::FixedNumberPostWithReplacement, 1)
- SET_ROW_BUILD_CODE ("if(c == 0) {\ " \$(endRow);\" "}\" "const scalar u=\$(gennrand_uniform);\\" "x+=(1.0 x) *(1.0 pow(u, 1.0/(scalar) c));\\" "unsigned int postIdx=(unsigned int)(x *\$(num_post));\\" "postIdx=(postIdx< \$(num_post)) ? postIdx :(\$(num_post) 1);\\" "\$(addSynapse, postIdx+\$(id_post_begin));\\" "c--;\\")
- SET ROW BUILD STATE VARS ({{"x", "scalar", 0.0},{"c", "unsigned int", "\$(rowLength)"}})
- · virtual StringVec getParamNames () const override

Gets names of of (independent) model parameters.

- SET_CALC_MAX_ROW_LENGTH_FUNC ([](unsigned int, unsigned int, const std::vector< double > &pars) { return(unsigned int) pars[0];})
- SET_CALC_MAX_COL_LENGTH_FUNC ([](unsigned int numPre, unsigned int numPost, const std::vector < double > &pars) { const double quantile=pow(0.9999, 1.0/(double) numPost);return binomialInverseC DF(quantile,(unsigned int) pars[0] *numPre, 1.0/(double) numPost);})

Additional Inherited Members

20.52.1 Detailed Description

Initialises connectivity with a fixed number of random synapses per row.

The postsynaptic targets of the synapses can be initialised in parallel by sampling from the discrete uniform distribution. However, to sample connections in ascending order, we sample from the 1st order statistic of the uniform distribution – Beta[1, Npost] – essentially the next smallest value. In this special case this is equivalent to the exponential distribution which can be sampled in constant time using the inversion method.

20.52.2 Member Function Documentation

20.52.2.2 getParamNames()

```
virtual StringVec InitSparseConnectivitySnippet::FixedNumberPostWithReplacement::getParamNames
() const [inline], [override], [virtual]
```

Gets names of of (independent) model parameters.

Reimplemented from Snippet::Base.

20.52.2.3 SET_CALC_MAX_COL_LENGTH_FUNC()

20.52.2.4 SET_CALC_MAX_ROW_LENGTH_FUNC()

20.52.2.5 SET_ROW_BUILD_CODE()

```
InitSparseConnectivitySnippet::FixedNumberPostWithReplacement::SET_ROW_BUILD_CODE ( )
```

20.52.2.6 SET_ROW_BUILD_STATE_VARS()

```
\label{limit_sparse} In it Sparse Connectivity Snippet:: Fixed Number Post With Replacement:: SET_ROW_BUILD_STATE_VARS \ ( \\ \{ "x", "scalar", 0.0 \}, \{ "c", "unsigned int", " \$ (row Length) " \} \} \ )
```

The documentation for this class was generated from the following file:

· initSparseConnectivitySnippet.h

20.53 InitSparseConnectivitySnippet::FixedNumberTotalWithReplacement Class Reference

#include <initSparseConnectivitySnippet.h>

 $Inheritance\ diagram\ for\ Init Sparse Connectivity Snippet :: Fixed Number Total With Replacement:$

Public Member Functions

- DECLARE_SNIPPET (InitSparseConnectivitySnippet::FixedNumberTotalWithReplacement, 1)
- SET_ROW_BUILD_CODE ("if(c == 0) {\ " \$(endRow);\" "}\" "const scalar u=\$(gennrand_uniform);\" "x+=(1.0 x) *(1.0 pow(u, 1.0/(scalar) c));\" "unsigned int postldx=(unsigned int)(x *\$(num_post));\" "postldx=(postldx< \$(num_post)) ? postldx :(\$(num_post) 1);\" "\$(addSynapse, postldx+\$(id_post_begin));\" "c--;\")
- SET_ROW_BUILD_STATE_VARS ({{"x", "scalar", 0.0},{"c", "unsigned int", "\$(preCalcRowLength)[(\$(id_pre) * \$(num_threads)) + \$(id_thread)]"}})
- virtual StringVec getParamNames () const override

Gets names of of (independent) model parameters.

- virtual EGPVec getExtraGlobalParams () const override
- SET_HOST_INIT_CODE ("// Allocate pre-calculated row length array\ "\$(allocatepreCalcRowLength, \$(num_pre) *\$(num_threads));\" "//Calculate row lengths\" "const size_t numPostPerThread=(\$(num_←) post)+\$(num_threads) - 1)/\$(num_threads);\" "const size_t leftOverNeurons=\$(num_post) % numPostPer ← Thread;\" "size_t remainingConnections=\$(total);\" "size_t matrixSize=(size_t)\$(num_pre) *(size_t)\$(num_c post);\" "uint16_t *subRowLengths=\$(preCalcRowLength);\" "//Loop through rows\" "for(size_t i=0;i< \$(num ← pre);i++) {\" " const bool lastPre=(i==(\$(num pre) - 1));\" "/Loop through subrows\" " for(size t j=0;j< \$(num_threads);j++) {\" " const bool lastSubRow=(j==(\$(num_threads) - 1));\" "//If this isn 't the last sub-row of the matrix\" "if(!lastPre||! lastSubRow) {\" "//Get length of this subrow\" " const unsigned int numSub↔ RowNeurons=(leftOverNeurons !=0 &&lastSubRow) ? leftOverNeurons :numPostPerThread;\" "//Calculate probability\" " const double probability=(double) numSubRowNeurons/(double) matrixSize;\" "//Create distribution to sample row length\" " std::binomial distribution < size t > rowLengthDist(remainingConnections, probability);\" "//Sample row length;\" " const size_t subRowLength=rowLengthDist(\$(rng));\" "//Update counters\" " remainingConnections -=subRowLength;\" " matrixSize -=numSubRowNeurons;\" "//Add row length to array\" " assert(subRowLength< std::numeric_limits< uint16_t >::max());\" " *subRowLengths++=(uint16_t) subRowLength;\" " }\" " }\" " "/Insert remaining connections into last sub-row\" " *subRowLengths=(uint16↔ _t) remainingConnections;\" "//Push populated row length array\" "\$(pushpreCalcRowLength, \$(num_pre) *\$(num_threads));\")
- SET_CALC_MAX_ROW_LENGTH_FUNC ([](unsigned int numPre, unsigned int numPost, const std ::vector < double > &pars) { const double quantile=pow(0.9999, 1.0/(double) numPre);return binomial ← InverseCDF(quantile,(unsigned int) pars[0],(double) numPost/((double) numPre *(double) numPost));})
- SET_CALC_MAX_COL_LENGTH_FUNC ([](unsigned int numPre, unsigned int numPost, const std::vector < double > &pars) { const double quantile=pow(0.9999, 1.0/(double) numPost);return binomialInverseC DF(quantile,(unsigned int) pars[0],(double) numPre/((double) numPre *(double) numPost));})

Additional Inherited Members

20.53.1 Detailed Description

Initialises connectivity with a total number of random synapses. The first stage in using this connectivity is to determine how many of the total synapses end up in each row. This can be determined by sampling from the multinomial distribution. However, this operation cannot be efficiently parallelised so must be performed on the host and the result passed as an extra global parameter array.

Once the length of each row is determined, the postsynaptic targets of the synapses can be initialised in parallel by sampling from the discrete uniform distribution. However, to sample connections in ascending order, we sample from the 1st order statistic of the uniform distribution – Beta[1, Npost] – essentially the next smallest value. In

this special case this is equivalent to the exponential distribution which can be sampled in constant time using the inversion method.

20.53.2 Member Function Documentation

20.53.2.1 DECLARE_SNIPPET()

```
\label{limit-sparse-connectivity-sparse-connectivity-sparse-connectivity-sparse-connectivity-sparse-connectivity-sparse-connectivity-sparse-connectivity-sparse-connectivity-sparse-connectivity-sparse-connectivity-sparse-connectivity-sparse-connectivity-sparse-connectivity-sparse-connectivity-sparse-connectivity-sparse-connectivity-sparse-connectivity-sparse-connectivity-sparse-connectivity-sparse-connectivity-sparse-connectivity-sparse-connectivity-sparse-connectivity-sparse-connectivity-sparse-connectivity-sparse-connectivity-sparse-connectivity-sparse-connectivity-sparse-connectivity-sparse-connectivity-sparse-connectivity-sparse-connectivity-sparse-connectivity-sparse-connectivity-sparse-connectivity-sparse-connectivity-sparse-connectivity-sparse-connectivity-sparse-connectivity-sparse-connectivity-sparse-connectivity-sparse-connectivity-sparse-connectivity-sparse-connectivity-sparse-connectivity-sparse-connectivity-sparse-connectivity-sparse-connectivity-sparse-connectivity-sparse-connectivity-sparse-connectivity-sparse-connectivity-sparse-connectivity-sparse-connectivity-sparse-connectivity-sparse-connectivity-sparse-connectivity-sparse-connectivity-sparse-connectivity-sparse-connectivity-sparse-connectivity-sparse-connectivity-sparse-connectivity-sparse-connectivity-sparse-connectivity-sparse-connectivity-sparse-connectivity-sparse-connectivity-sparse-connectivity-sparse-connectivity-sparse-connectivity-sparse-connectivity-sparse-connectivity-sparse-connectivity-sparse-connectivity-sparse-connectivity-sparse-connectivity-sparse-connectivity-sparse-connectivity-sparse-connectivity-sparse-connectivity-sparse-connectivity-sparse-connectivity-sparse-connectivity-sparse-connectivity-sparse-connectivity-sparse-connectivity-sparse-connectivity-sparse-connectivity-sparse-connectivity-sparse-connectivity-sparse-connectivity-sparse-connectivity-sparse-connectivity-sparse-connectivity-sparse-connectivity-sparse-connectivity-sparse-connectivity-sparse-connectivity-sparse-connectivity-sparse-connectivity-sparse-conn
```

20.53.2.2 getExtraGlobalParams()

```
virtual EGPVec InitSparseConnectivitySnippet::FixedNumberTotalWithReplacement::getExtraGlobal↔ Params () const [inline], [override], [virtual]
```

Gets names and types (as strings) of additional per-population parameters for the snippet

Reimplemented from Snippet::Base.

20.53.2.3 getParamNames()

```
virtual StringVec InitSparseConnectivitySnippet::FixedNumberTotalWithReplacement::getParam
Names ( ) const [inline], [override], [virtual]
```

Gets names of of (independent) model parameters.

Reimplemented from Snippet::Base.

20.53.2.4 SET_CALC_MAX_COL_LENGTH_FUNC()

20.53.2.5 SET_CALC_MAX_ROW_LENGTH_FUNC()

20.53.2.6 SET_HOST_INIT_CODE()

```
In it Sparse Connectivity Snippet:: Fixed Number Total With Replacement:: SET\_HOST\_INIT\_CODE \ (\ )
```

20.53.2.7 SET_ROW_BUILD_CODE()

 $In it Sparse Connectivity Snippet:: Fixed Number Total With Replacement:: SET_ROW_BUILD_CODE \ (\)$

20.53.2.8 SET_ROW_BUILD_STATE_VARS()

```
InitSparseConnectivitySnippet::FixedNumberTotalWithReplacement::SET_ROW_BUILD_STATE_VARS \ ( \{ "x", "scalar", 0.0 \}, \{ "c", "unsigned int", " $ (preCalcRowLength) [ ($ (id_pre) * $ (num_threads)) + $ (id_thread) ] " \} \} )
```

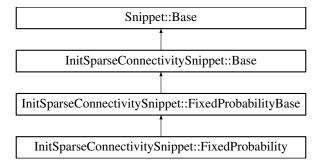
The documentation for this class was generated from the following file:

initSparseConnectivitySnippet.h

20.54 InitSparseConnectivitySnippet::FixedProbability Class Reference

#include <initSparseConnectivitySnippet.h>

Inheritance diagram for InitSparseConnectivitySnippet::FixedProbability:



Public Member Functions

- DECLARE_SNIPPET (InitSparseConnectivitySnippet::FixedProbability, 1)
- SET_ROW_BUILD_CODE ("const scalar u = \$(gennrand_uniform);\ "prevJ+=(1+(int)(log(u) *\$(probLog←Recip)));\" "if(prevJ< \$(num_post)) {\" " \$(addSynapse, prevJ+\$(id_post_begin));\" "}\" "else {\" " \$(endRow);\" "}\")

Additional Inherited Members

20.54.1 Detailed Description

Initialises connectivity with a fixed probability of a synapse existing between a pair of pre and postsynaptic neurons.

Whether a synapse exists between a pair of pre and a postsynaptic neurons can be modelled using a Bernoulli distribution. While this COULD br sampling directly by repeatedly drawing from the uniform distribution, this is innefficient. Instead we sample from the gemetric distribution which describes "the probability distribution of the number of Bernoulli trials needed to get one success" – essentially the distribution of the 'gaps' between synapses. We do this using the "inversion method" described by Devroye (1986) – essentially inverting the CDF of the equivalent continuous distribution (in this case the exponential distribution)

20.54.2 Member Function Documentation

20.54.2.1 DECLARE_SNIPPET()

1)

20.54.2.2 SET_ROW_BUILD_CODE()

InitSparseConnectivitySnippet::FixedProbability::SET_ROW_BUILD_CODE ()

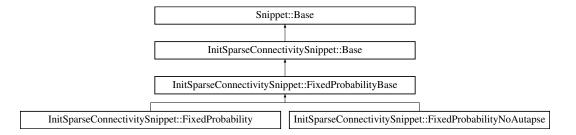
The documentation for this class was generated from the following file:

· initSparseConnectivitySnippet.h

20.55 InitSparseConnectivitySnippet::FixedProbabilityBase Class Reference

#include <initSparseConnectivitySnippet.h>

 $Inheritance\ diagram\ for\ InitSparseConnectivitySnippet::FixedProbabilityBase:$



Public Member Functions

- virtual std::string getRowBuildCode () const override=0
- SET_ROW_BUILD_STATE_VARS ({{"prevJ", "int", -1}})
- · virtual StringVec getParamNames () const override

Gets names of of (independent) model parameters.

- virtual DerivedParamVec getDerivedParams () const override
- SET_CALC_MAX_ROW_LENGTH_FUNC ([](unsigned int numPre, unsigned int numPost, const std ::vector < double > &pars) { const double quantile=pow(0.9999, 1.0/(double) numPre);return binomial ← InverseCDF(quantile, numPost, pars[0]);})
- SET_CALC_MAX_COL_LENGTH_FUNC ([](unsigned int numPre, unsigned int numPost, const std::vector < double > &pars) { const double quantile=pow(0.9999, 1.0/(double) numPost);return binomialInverseC← DF(quantile, numPre, pars[0]);})

Additional Inherited Members

20.55.1 Detailed Description

Base class for snippets which initialise connectivity with a fixed probability of a synapse existing between a pair of pre and postsynaptic neurons.

20.55.2 Member Function Documentation

20.55.2.1 getDerivedParams()

```
virtual DerivedParamVec InitSparseConnectivitySnippet::FixedProbabilityBase::getDerivedParams
( ) const [inline], [override], [virtual]
```

Gets names of derived model parameters and the function objects to call to Calculate their value from a vector of model parameter values

Reimplemented from Snippet::Base.

20.55.2.2 getParamNames()

```
virtual StringVec InitSparseConnectivitySnippet::FixedProbabilityBase::getParamNames ( ) const
[inline], [override], [virtual]
```

Gets names of of (independent) model parameters.

Reimplemented from Snippet::Base.

20.55.2.3 getRowBuildCode()

```
virtual std::string InitSparseConnectivitySnippet::FixedProbabilityBase::getRowBuildCode ( )
const [override], [pure virtual]
```

Reimplemented from InitSparseConnectivitySnippet::Base.

20.55.2.4 SET_CALC_MAX_COL_LENGTH_FUNC()

20.55.2.5 SET_CALC_MAX_ROW_LENGTH_FUNC()

20.55.2.6 SET_ROW_BUILD_STATE_VARS()

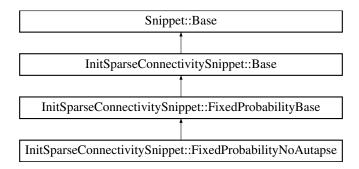
The documentation for this class was generated from the following file:

initSparseConnectivitySnippet.h

20.56 InitSparseConnectivitySnippet::FixedProbabilityNoAutapse Class Reference

#include <initSparseConnectivitySnippet.h>

Inheritance diagram for InitSparseConnectivitySnippet::FixedProbabilityNoAutapse:



Public Member Functions

- DECLARE_SNIPPET (InitSparseConnectivitySnippet::FixedProbabilityNoAutapse, 1)
- SET_ROW_BUILD_CODE ("int nextJ;\"do {\" " const scalar u=\$(gennrand_uniform);\" " nextJ=prevJ+(1+(int)(log(u) *\$(probLogRecip)));\" "} while(nextJ==\$(id_pre));\" "prevJ=nextJ;\" "if(prevJ< \$(num_post)) {\" " \$(add\compost_begin));\" "}\" "else {\" " \$(endRow);\" "}\")

Additional Inherited Members

20.56.1 Detailed Description

Initialises connectivity with a fixed probability of a synapse existing between a pair of pre and postsynaptic neurons. This version ensures there are no autapses - connections between neurons with the same id so should be used for recurrent connections.

Whether a synapse exists between a pair of pre and a postsynaptic neurons can be modelled using a Bernoulli distribution. While this COULD br sampling directly by repeatedly drawing from the uniform distribution, this is innefficient. Instead we sample from the gemetric distribution which describes "the probability distribution of the number of Bernoulli trials needed to get one success" – essentially the distribution of the 'gaps' between synapses. We do this using the "inversion method" described by Devroye (1986) – essentially inverting the CDF of the equivalent continuous distribution (in this case the exponential distribution)

20.56.2 Member Function Documentation

20.56.2.1 DECLARE_SNIPPET()

20.56.2.2 SET_ROW_BUILD_CODE()

```
\label{limitsparseConnectivitySnippet::FixedProbabilityNoAutapse::SET\_ROW\_BUILD\_CODE \ (\ )
```

The documentation for this class was generated from the following file:

• initSparseConnectivitySnippet.h

20.57 CodeGenerator::Substitutions::FunctionTemplate Struct Reference

```
#include <substitutions.h>
```

Public Member Functions

FunctionTemplate operator= (const FunctionTemplate &o)

Public Attributes

• const std::string genericName

Generic name used to refer to function in user code.

const unsigned int numArguments

Number of function arguments.

const std::string doublePrecisionTemplate

The function template (for use with functionSubstitute) used when model uses double precision.

• const std::string singlePrecisionTemplate

The function template (for use with functionSubstitute) used when model uses single precision.

20.57.1 Detailed Description

Immutable structure for specifying how to implement a generic function e.g. gennrand_uniform

NOTE for the sake of easy initialisation first two parameters of GenericFunction are repeated (C++17 fixes)

20.57.2 Member Function Documentation

20.57.2.1 operator=()

20.57.3 Member Data Documentation

20.57.3.1 doublePrecisionTemplate

```
const std::string CodeGenerator::Substitutions::FunctionTemplate::doublePrecisionTemplate
```

The function template (for use with functionSubstitute) used when model uses double precision.

20.57.3.2 genericName

```
\verb|const| std::string CodeGenerator::Substitutions::FunctionTemplate::genericName| \\
```

Generic name used to refer to function in user code.

20.57.3.3 numArguments

```
\verb|const| unsigned| int CodeGenerator:: Substitutions:: FunctionTemplate:: num Arguments | Substitutions | Su
```

Number of function arguments.

20.57.3.4 singlePrecisionTemplate

```
const std::string CodeGenerator::Substitutions::FunctionTemplate::singlePrecisionTemplate
```

The function template (for use with functionSubstitute) used when model uses single precision.

The documentation for this struct was generated from the following file:

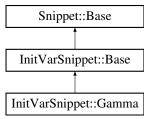
· substitutions.h

20.58 InitVarSnippet::Gamma Class Reference

Initialises variable by sampling from the exponential distribution.

```
#include <initVarSnippet.h>
```

Inheritance diagram for InitVarSnippet::Gamma:



Public Member Functions

- DECLARE_SNIPPET (InitVarSnippet::Gamma, 2)
- SET_CODE ("\$(value) = \$(b) * \$(gennrand_gamma, \$(a));")
- virtual StringVec getParamNames () const override

Gets names of of (independent) model parameters.

Additional Inherited Members

20.58.1 Detailed Description

Initialises variable by sampling from the exponential distribution.

This snippet takes 1 parameter:

• lambda - mean event rate (events per unit time/distance)

20.58.2 Member Function Documentation

20.58.2.1 DECLARE_SNIPPET()

20.58.2.2 getParamNames()

virtual StringVec InitVarSnippet::Gamma::getParamNames () const [inline], [override], [virtual]

Gets names of of (independent) model parameters.

Reimplemented from Snippet::Base.

20.58.2.3 SET_CODE()

```
InitVarSnippet::Gamma::SET_CODE ( )
```

The documentation for this class was generated from the following file:

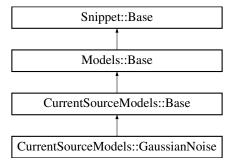
initVarSnippet.h

20.59 CurrentSourceModels::GaussianNoise Class Reference

Noisy current source with noise drawn from normal distribution.

#include <currentSourceModels.h>

Inheritance diagram for CurrentSourceModels::GaussianNoise:



Public Types

- typedef Snippet::ValueBase< 2 > ParamValues
- typedef Models::VarInitContainerBase< 0 > VarValues
- typedef Models::VarInitContainerBase< 0 > PreVarValues
- typedef Models::VarInitContainerBase< 0 > PostVarValues

Public Member Functions

- SET_INJECTION_CODE ("\$(injectCurrent, \$(mean) + \$(gennrand_normal) * \$(sd));\)
- · virtual StringVec getParamNames () const override

Gets names of of (independent) model parameters.

Static Public Member Functions

• static const GaussianNoise * getInstance ()

Additional Inherited Members

20.59.1 Detailed Description

Noisy current source with noise drawn from normal distribution.

It has 2 parameters:

- mean mean of the normal distribution [nA]
- sd standard deviation of the normal distribution [nA]

20.59.2 Member Typedef Documentation

```
20.59.2.1 ParamValues

typedef Snippet::ValueBase< 2 > CurrentSourceModels::GaussianNoise::ParamValues

20.59.2.2 PostVarValues

typedef Models::VarInitContainerBase<0> CurrentSourceModels::GaussianNoise::PostVarValues

20.59.2.3 PreVarValues

typedef Models::VarInitContainerBase<0> CurrentSourceModels::GaussianNoise::PreVarValues

20.59.2.4 VarValues

typedef Models::VarInitContainerBase< 0 > CurrentSourceModels::GaussianNoise::VarValues

20.59.3 Member Function Documentation
```

20.59.3.1 getInstance()

```
static const GaussianNoise* CurrentSourceModels::GaussianNoise::getInstance ( ) [inline],
[static]
```

20.59.3.2 getParamNames()

```
virtual StringVec CurrentSourceModels::GaussianNoise::getParamNames ( ) const [inline], [override],
[virtual]
```

Gets names of of (independent) model parameters.

Reimplemented from Snippet::Base.

20.59.3.3 SET_INJECTION_CODE()

 ${\tt CurrentSourceModels::GaussianNoise::SET_INJECTION_CODE} \ \ ($

```
"$(injectCurrent, $(mean) + $(gennrand_normal) * $(sd));\ )
```

The documentation for this class was generated from the following file:

· currentSourceModels.h

20.60 GenerateRunBase Class Reference

```
#include <generateRun.h>
```

Public Member Functions

- GenerateRunBase (const std::string &projectName)
- virtual void writeSizes (std::ofstream &sizes) const
- int getExitCode (const CLI::ParseError &e)
- void parseCommandLine (int argc, char **argv)
- int buildAndRun (std::initializer_list< std::string > runParams={}) const

Protected Member Functions

- virtual int runTools () const
- CLI::App & getApp ()
- std::string getOutDir () const
- const std::string & getExperimentName () const

20.60.1 Constructor & Destructor Documentation

20.60.1.1 GenerateRunBase()

20.60.2 Member Function Documentation

20.60.2.1 buildAndRun()

20.60.2.2 getApp()

```
CLI::App& GenerateRunBase::getApp ( ) [inline], [protected]
```

20.60.2.3 getExitCode()

20.60.2.4 getExperimentName()

The documentation for this class was generated from the following file:

· generateRun.h

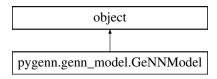
20.61 pygenn.genn_model.GeNNModel Class Reference

virtual void GenerateRunBase::writeSizes (

GeNNModel class This class helps to define, build and run a GeNN model from python.

std::ofstream & sizes) const [inline], [virtual]

Inheritance diagram for pygenn.genn_model.GeNNModel:



Public Member Functions

def __init__ (self, precision=None, model_name="GeNNModel", backend=None, genn_log_level=genn—wrapper.warning, backend_log_level=genn_wrapper.warning, preference_kwargs)

Init GeNNModel.

- def use backend (self)
- def use_backend (self, backend)
- def timing_enabled (self)
- def timing_enabled (self, timing)
- def default_var_location (self)

Default variable location - defines where state variables are initialised.

- def default_var_location (self, location)
- · def default_sparse_connectivity_location (location)

Default sparse connectivity mode - where connectivity is initialised.

- def default_sparse_connectivity_location (self, location)
- def model name (self)

Name of the model.

- def model name (self, model name)
- · def t (self)

Simulation time in ms.

- · def t (self, t)
- def timestep (self)

Simulation time step.

- def timestep (self, timestep)
- def free_device_mem_bytes (self)
- def dT (self)

Step size.

- def dT (self, dt)
- def neuron_update_time (self)
- · def init time (self)
- def presynaptic_update_time (self)
- def postsynaptic_update_time (self)
- def synapse_dynamics_time (self)
- · def init sparse time (self)
- def add_neuron_population (self, pop_name, num_neurons, neuron, param_space, var_space)

Add a neuron population to the GeNN model.

 def add_synapse_population (self, pop_name, matrix_type, delay_steps, source, target, w_update_model, wu_param_space, wu_var_space, wu_pre_var_space, wu_post_var_space, postsyn_model, ps_param_ space, ps_var_space, connectivity_initialiser=None)

Add a synapse population to the GeNN model.

def add_slave_synapse_population (self, pop_name, master_pop, delay_steps, source, target, postsyn_
 model, ps param space, ps var space)

Add a 'slave' population to the GeNN model which shares weights and connectivity with a 'master' population.

def add_current_source (self, cs_name, current_source_model, pop, param_space, var_space)

Add a current source to the GeNN model.

def build (self, path_to_model="./")

Finalize and build a GeNN model.

def load (self, path_to_model="./")

import the model as shared library and initialize it

· def reinitialise (self)

reinitialise model to its original state without re-loading

- def step_time (self)
- def pull_state_from_device (self, pop_name)

Pull state from the device for a given population.

def pull_spikes_from_device (self, pop_name)

Pull spikes from the device for a given population.

• def pull current spikes from device (self, pop name)

Pull spikes from the device for a given population.

def pull_connectivity_from_device (self, pop_name)

Pull connectivity from the device for a given population.

• def pull var from device (self, pop name, var name)

Pull variable from the device for a given population.

def pull_extra_global_param_from_device (self, pop_name, egp_name, size=1)

Pull extra global parameter from the device for a given population.

def push_state_to_device (self, pop_name)

Push state to the device for a given population.

• def push_spikes_to_device (self, pop_name)

Push spikes to the device for a given population.

• def push_current_spikes_to_device (self, pop_name)

Push current spikes to the device for a given population.

• def push_connectivity_to_device (self, pop_name)

Push connectivity to the device for a given population.

def push_var_to_device (self, pop_name, var_name)

Push variable to the device for a given population.

• def push_extra_global_param_to_device (self, pop_name, egp_name, size=1)

Push extra global parameter to the device for a given population.

• def end (self)

Free memory.

Public Attributes

- · use backend
- · backend_log_level
- · default_var_location
- · model name
- · neuron_populations
- synapse_populations
- · current sources
- dT

20.61.1 Detailed Description

GeNNModel class This class helps to define, build and run a GeNN model from python.

20.61.2 Constructor & Destructor Documentation

Init GeNNModel.

Parameters

precision	string precision as string ("float", "double" or "long double"). defaults to float.	
model_name	string name of the model. Defaults to "GeNNModel".	
backend	string specifying name of backend module to use Defaults to None to pick 'best' backend for your system	

20.61.3 Member Function Documentation

20.61.3.1 add_current_source()

Add a current source to the GeNN model.

Parameters

cs_name	name of the new current source
current_source_model	type of the CurrentSourceModels class as string or instance of CurrentSourceModels class derived from
	<pre>pygenn.genn_wrapper.CurrentSourceModels.Custom (see also pygenn.genn_model.create_custom_current_source_class)</pre>
рор	population into which the current source should be injected (either name or NeuronGroup object)
param_space	dict with param values for the CurrentSourceModels class
var_space	dict with initial variable values for the CurrentSourceModels class

20.61.3.2 add_neuron_population()

Add a neuron population to the GeNN model.

Parameters

pop_name	name of the new population
num_neurons	number of neurons in the new population
neuron	type of the NeuronModels class as string or instance of neuron class derived from
	pygenn.genn_wrapper.NeuronModels.Custom (see also
	pygenn.genn_model.create_custom_neuron_class)
param_space	dict with param values for the NeuronModels class
var_space	dict with initial variable values for the NeuronModels class

20.61.3.3 add_slave_synapse_population()

```
{\tt def pygenn\_model.GeNNModel.add\_slave\_synapse\_population} \ \ (
```

```
self,
pop_name,
master_pop,
delay_steps,
source,
target,
postsyn_model,
ps_param_space,
ps_var_space )
```

Add a 'slave' population to the GeNN model which shares weights and connectivity with a 'master' population.

Parameters

pop_name	name of the new population
master_pop	master synapse group to share weights with (either name or SynapseGroup object)
delay_steps	delay in number of steps
source	source neuron group (either name or NeuronGroup object)
target	target neuron group (either name or NeuronGroup object)
postsyn_model	type of the PostsynapticModels class as string or instance of postsynaptic model class derived from pygenn.genn_wrapper.PostsynapticModels.Custom (see also pygenn.genn_model.create_custom_postsynaptic_class)
ps_param_space	dict with param values for the PostsynapticModels class
ps_var_space	dict with initial variable values for the PostsynapticModels class

20.61.3.4 add_synapse_population()

```
def pygenn.genn_model.GeNNModel.add_synapse_population (
             self,
             pop_name,
             matrix_type,
              delay_steps,
              source,
              target,
              w_update_model,
              wu_param_space,
              wu_var_space,
              wu_pre_var_space,
              wu_post_var_space,
              postsyn_model,
              ps_param_space,
              ps_var_space,
              connectivity_initialiser = None )
```

Add a synapse population to the GeNN model.

Parameters

pop_name	name of the new population
matrix_type	type of the matrix as string
delay_steps	delay in number of steps
source	source neuron group (either name or NeuronGroup object)
target	target neuron group (either name or NeuronGroup object)

Parameters

w_update_model	type of the WeightUpdateModels class as string or instance of weight update model class derived from
	pygenn.genn_wrapper.WeightUpdateModels.Custom(see also pygenn.genn_model.create_custom_weight_update_class)
wu_param_space	dict with param values for the WeightUpdateModels class
wu_var_space	dict with initial values for WeightUpdateModels state variables
wu_pre_var_space	dict with initial values for WeightUpdateModels presynaptic variables
wu_post_var_space	dict with initial values for WeightUpdateModels postsynaptic variables
postsyn_model	type of the PostsynapticModels class as string or instance of postsynaptic model class derived from pygenn.genn_wrapper.PostsynapticModels.Custom (see also pygenn.genn_model.create_custom_postsynaptic_class)
ps_param_space	dict with param values for the PostsynapticModels class
ps_var_space	dict with initial variable values for the PostsynapticModels class
connectivity_initialiser	InitSparseConnectivitySnippet::Init for connectivity

20.61.3.5 build()

Finalize and build a GeNN model.

Parameters

```
path_to_model path where to place the generated model code. Defaults to the local directory.
```

Default sparse connectivity mode - where connectivity is initialised.

Default variable location - defines where state variables are initialised.

```
20.61.3.9 default_var_location() [2/2]
def pygenn.genn_model.GeNNModel.default_var_location (
               self,
               location )
20.61.3.10 dT() [1/2]
{\tt def pygenn.genn\_model.GeNNModel.dT} (
              self )
Step size.
20.61.3.11 dT() [2/2]
def pygenn.genn_model.GeNNModel.dT (
               self,
               dt )
20.61.3.12 end()
def pygenn.genn_model.GeNNModel.end (
               self )
Free memory.
20.61.3.13 free_device_mem_bytes()
def pygenn.genn_model.GeNNModel.free_device_mem_bytes (
               self )
20.61.3.14 init_sparse_time()
def pygenn.genn_model.GeNNModel.init_sparse_time (
               self )
20.61.3.15 init_time()
def pygenn.genn_model.GeNNModel.init_time (
               self )
20.61.3.16 load()
def pygenn.genn_model.GeNNModel.load (
               self,
               path\_to\_model = "./")
```

import the model as shared library and initialize it

20.61.3.17 model_name() [1/2]

```
def pygenn.genn_model.GeNNModel.model_name (
               self )
Name of the model.
20.61.3.18 model_name() [2/2]
def pygenn.genn_model.GeNNModel.model_name (
              self,
               model_name )
20.61.3.19 neuron_update_time()
def pygenn.genn_model.GeNNModel.neuron_update_time (
               self )
20.61.3.20 postsynaptic_update_time()
def pygenn.genn_model.GeNNModel.postsynaptic_update_time (
               self )
20.61.3.21 presynaptic_update_time()
def pygenn.genn_model.GeNNModel.presynaptic_update_time (
               self )
20.61.3.22 pull_connectivity_from_device()
def pygenn.genn_model.GeNNModel.pull_connectivity_from_device (
               self,
               pop_name )
Pull connectivity from the device for a given population.
20.61.3.23 pull_current_spikes_from_device()
{\tt def pygenn.genn\_model.GeNNModel.pull\_current\_spikes\_from\_device} \ \ (
               pop_name )
Pull spikes from the device for a given population.
20.61.3.24 pull_extra_global_param_from_device()
def pygenn.genn_model.GeNNModel.pull_extra_global_param_from_device (
               self,
               pop_name,
               egp_name,
               size = 1 )
```

Pull extra global parameter from the device for a given population.

```
20.61.3.25 pull_spikes_from_device()
```

Pull spikes from the device for a given population.

20.61.3.26 pull_state_from_device()

Pull state from the device for a given population.

20.61.3.27 pull_var_from_device()

Pull variable from the device for a given population.

20.61.3.28 push_connectivity_to_device()

Push connectivity to the device for a given population.

20.61.3.29 push_current_spikes_to_device()

```
def pygenn.genn_model.GeNNModel.push_current_spikes_to_device ( self, \\ pop_name \ )
```

Push current spikes to the device for a given population.

20.61.3.30 push_extra_global_param_to_device()

Push extra global parameter to the device for a given population.

```
20.61.3.31 push_spikes_to_device()
def pygenn.genn_model.GeNNModel.push_spikes_to_device (
               self,
               pop_name )
Push spikes to the device for a given population.
20.61.3.32 push_state_to_device()
def pygenn.genn_model.GeNNModel.push_state_to_device (
               self,
               pop_name )
Push state to the device for a given population.
20.61.3.33 push_var_to_device()
def pygenn.genn_model.GeNNModel.push_var_to_device (
               self,
               pop_name,
               var_name )
Push variable to the device for a given population.
20.61.3.34 reinitialise()
def pygenn.genn_model.GeNNModel.reinitialise (
               self )
reinitialise model to its original state without re-loading
20.61.3.35 step_time()
def pygenn.genn_model.GeNNModel.step_time (
               self )
20.61.3.36 synapse_dynamics_time()
def pygenn.genn_model.GeNNModel.synapse_dynamics_time (
              self )
20.61.3.37 t() [1/2]
def pygenn.genn_model.GeNNModel.t (
               self )
Simulation time in ms.
20.61.3.38 t() [2/2]
def pygenn.genn_model.GeNNModel.t (
```

self,

t) 20.61.3.39 timestep() [1/2] def pygenn.genn_model.GeNNModel.timestep (self) Simulation time step. **20.61.3.40** timestep() [2/2] def pygenn.genn_model.GeNNModel.timestep (self, timestep) 20.61.3.41 timing_enabled() [1/2] ${\tt def pygenn.genn_model.GeNNModel.timing_enabled} \ \ ($ self) 20.61.3.42 timing_enabled() [2/2] def pygenn.genn_model.GeNNModel.timing_enabled (self, timing) 20.61.3.43 use_backend() [1/2] def pygenn.genn_model.GeNNModel.use_backend (self) 20.61.3.44 use_backend() [2/2] def pygenn.genn_model.GeNNModel.use_backend (self, backend) 20.61.4 Member Data Documentation 20.61.4.1 backend_log_level pygenn.genn_model.GeNNModel.backend_log_level

20.61.4.2 current_sources

pygenn.genn_model.GeNNModel.current_sources

20.61.4.3 default_var_location

pygenn.genn_model.GeNNModel.default_var_location

20.61.4.4 dT

pygenn.genn_model.GeNNModel.dT

20.61.4.5 model_name

pygenn.genn_model.GeNNModel.model_name

20.61.4.6 neuron_populations

 $\verb"pygenn.genn_model.GeNNModel.neuron_populations"$

20.61.4.7 synapse_populations

 $\verb|pygenn.genn_model.GeNNModel.synapse_populations|\\$

20.61.4.8 use_backend

pygenn.genn_model.GeNNModel.use_backend

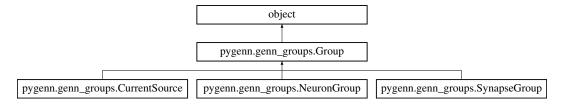
The documentation for this class was generated from the following file:

· genn model.py

20.62 pygenn.genn_groups.Group Class Reference

Parent class of NeuronGroup, SynapseGroup and CurrentSource.

Inheritance diagram for pygenn.genn_groups.Group:



Public Member Functions

- def __init__ (self, name, model)
 - Init Group.
- def set_var (self, var_name, values)

Set values for a Variable.

def pull_state_from_device (self)

Wrapper around GeNNModel.pull_state_from_device.

• def pull_var_from_device (self, var_name)

Wrapper around GeNNModel.pull_var_from_device.

• def pull_extra_global_param_from_device (self, egp_name, size=1)

Wrapper around GeNNModel.pull_extra_global_param_from_device.

• def push_state_to_device (self)

Wrapper around GeNNModel.push_state_to_device.

• def push_var_to_device (self, var_name)

Wrapper around GeNNModel.push_var_to_device.

• def push_extra_global_param_to_device (self, egp_name, size=1)

Wrapper around GeNNModel.push_extra_global_param_to_device.

Public Attributes

- name
- vars
- · extra_global_params

20.62.1 Detailed Description

Parent class of NeuronGroup, SynapseGroup and CurrentSource.

20.62.2 Constructor & Destructor Documentation

Init Group.

Parameters

name string name of the Group

20.62.3 Member Function Documentation

20.62.3.1 pull_extra_global_param_from_device()

Wrapper around GeNNModel.pull_extra_global_param_from_device.

Parameters

var_name	string with the name of the variable
size	number of entries in EGP array

```
20.62.3.2 pull_state_from_device()
```

```
def pygenn.genn_groups.Group.pull_state_from_device ( self \ )
```

Wrapper around GeNNModel.pull_state_from_device.

20.62.3.3 pull_var_from_device()

Wrapper around GeNNModel.pull_var_from_device.

Parameters

var_name	string with the name of the variable
----------	--------------------------------------

20.62.3.4 push_extra_global_param_to_device()

Wrapper around GeNNModel.push_extra_global_param_to_device.

Parameters

var_name	string with the name of the variable
size	number of entries in EGP array

20.62.3.5 push_state_to_device()

```
\label{lem:condition} $\operatorname{def pygenn.genn\_groups.Group.push\_state\_to\_device} \ ( \\ self )
```

Wrapper around GeNNModel.push_state_to_device.

20.62.3.6 push_var_to_device()

Wrapper around GeNNModel.push_var_to_device.

Parameters

var_name	string with the name of the variable

20.62.3.7 set_var()

Set values for a Variable.

Parameters

var_name	string with the name of the variable
values	iterable or a single value

20.62.4 Member Data Documentation

20.62.4.1 extra_global_params

```
pygenn.genn_groups.Group.extra_global_params
```

20.62.4.2 name

pygenn.genn_groups.Group.name

20.62.4.3 vars

pygenn.genn_groups.Group.vars

The documentation for this class was generated from the following file:

genn_groups.py

20.63 CodeGenerator::GroupMerged < G > Class Template Reference

```
#include <groupMerged.h>
```

Public Types

• typedef G GroupInternal

Public Member Functions

- GroupMerged (size_t index, const std::vector< std::reference_wrapper< const GroupInternal >> groups)
- size_t getIndex () const
- const GroupInternal & getArchetype () const

Get 'archetype' neuron group - it's properties represent those of all other merged neuron groups.

• const std::vector< std::reference_wrapper< const GroupInternal > > & getGroups () const

Gets access to underlying vector of neuron groups which have been merged.

Protected Member Functions

template < typename P >
bool isParamValueHeterogeneous (size_t index, P getParamValuesFn) const

Helper to test whether parameter values are heterogeneous within merged group.

template<typename P >
 bool isParamValueHeterogeneous (const std::vector< std::string > &codeStrings, const std::string ¶m
 Name, size_t index, P getParamValuesFn) const

Helper to test whether parameter values are heterogeneous within merged group.

20.63.1 Member Typedef Documentation

20.63.1.1 GroupInternal

```
template<typename G>
typedef G CodeGenerator::GroupMerged< G >::GroupInternal
```

20.63.2 Constructor & Destructor Documentation

20.63.2.1 GroupMerged()

20.63.3 Member Function Documentation

20.63.3.1 getArchetype()

```
template<typename G>
const GroupInternal& CodeGenerator::GroupMerged< G >::getArchetype ( ) const [inline]
```

Get 'archetype' neuron group - it's properties represent those of all other merged neuron groups.

20.63.3.2 getGroups()

```
template<typename G>
const std::vector<std::reference_wrapper<const GroupInternal> >& CodeGenerator::GroupMerged<
G >::getGroups ( ) const [inline]
```

Gets access to underlying vector of neuron groups which have been merged.

20.63.3.3 getIndex()

```
template<typename G>
size_t CodeGenerator::GroupMerged< G >::getIndex ( ) const [inline]
```

20.63.3.4 isParamValueHeterogeneous() [1/2]

Helper to test whether parameter values are heterogeneous within merged group.

20.63.3.5 isParamValueHeterogeneous() [2/2]

Helper to test whether parameter values are heterogeneous within merged group.

The documentation for this class was generated from the following file:

· groupMerged.h

20.64 InitSparseConnectivitySnippet::Init Class Reference

```
#include <initSparseConnectivitySnippet.h>
```

Inheritance diagram for InitSparseConnectivitySnippet::Init:

```
Snippet::Init< InitSparseConnectivitySnippet::Base >

InitSparseConnectivitySnippet::Init
```

Public Member Functions

Init (const Base *snippet, const std::vector< double > ¶ms)

20.64.1 Constructor & Destructor Documentation

```
20.64.1.1 Init()
```

The documentation for this class was generated from the following file:

initSparseConnectivitySnippet.h

20.65 Snippet::Init < SnippetBase > Class Template Reference

```
#include <snippet.h>
```

Public Member Functions

- Init (const SnippetBase *snippet, const std::vector< double > ¶ms)
- const SnippetBase * getSnippet () const
- const std::vector< double > & getParams () const
- const std::vector< double > & getDerivedParams () const
- · void initDerivedParams (double dt)
- bool canBeMerged (const Init< SnippetBase > &other) const

20.65.1 Detailed Description

```
template<typename SnippetBase> class Snippet::Init< SnippetBase>
```

Class used to bind together everything required to utilize a snippet

- 1. A pointer to a variable initialisation snippet
- 2. The parameters required to control the variable initialisation snippet

20.65.2 Constructor & Destructor Documentation

20.65.2.1 Init()

20.65.3 Member Function Documentation

20.65.3.1 canBeMerged()

20.65.3.2 getDerivedParams()

```
template<typename SnippetBase>
const std::vector<double>& Snippet::Init< SnippetBase >::getDerivedParams ( ) const [inline]
```

20.65.3.3 getParams()

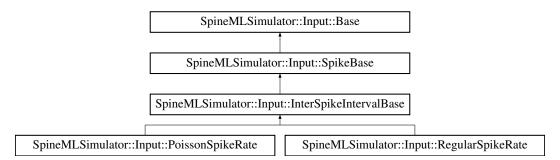
The documentation for this class was generated from the following file:

· snippet.h

20.66 SpineMLSimulator::Input::InterSpikeIntervalBase Class Reference

```
#include <input.h>
```

Inheritance diagram for SpineMLSimulator::Input::InterSpikeIntervalBase:



Public Member Functions

· virtual void apply (double dt, unsigned long long timestep) override

Protected Member Functions

- InterSpikeIntervalBase (double dt, const pugi::xml_node &node, std::unique_ptr< InputValue::Base > value, unsigned int popSize, unsigned int *spikeQueuePtr, unsigned int *hostSpikeCount, unsigned int *hostSpikes, PushCurrentSpikesFunc pushCurrentSpikes)
- virtual double getTimeToSpike (double isiMs)=0

Additional Inherited Members

20.66.1 Constructor & Destructor Documentation

20.66.1.1 InterSpikeIntervalBase()

20.66.2 Member Function Documentation

20.66.2.1 apply()

```
void SpineMLSimulator::Input::InterSpikeIntervalBase::apply ( \label{eq:condition} \mbox{double $dt$,} unsigned long long timestep ) [override], [virtual]
```

Implements SpineMLSimulator::Input::Base.

20.66.2.2 getTimeToSpike()

```
\label{lem:continuous} \begin{tabular}{ll} virtual double SpineMLSimulator::Input::InterSpikeIntervalBase::getTimeToSpike ( \\ double $isiMs$ ) [protected], [pure virtual] \end{tabular}
```

Implemented in SpineMLSimulator::Input::PoissonSpikeRate, and SpineMLSimulator::Input::RegularSpikeRate.

The documentation for this class was generated from the following files:

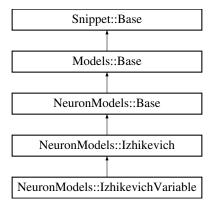
- input.h
- · input.cc

20.67 NeuronModels::Izhikevich Class Reference

Izhikevich neuron with fixed parameters [1].

```
#include <neuronModels.h>
```

Inheritance diagram for NeuronModels::Izhikevich:



Public Types

- typedef Snippet::ValueBase< 4 > ParamValues
- typedef Models::VarInitContainerBase< 2 > VarValues
- typedef Models::VarInitContainerBase< 0 > PreVarValues
- typedef Models::VarInitContainerBase< 0 > PostVarValues

Public Member Functions

• virtual std::string getSimCode () const override

Gets the code that defines the execution of one timestep of integration of the neuron model.

• virtual std::string getThresholdConditionCode () const override

Gets code which defines the condition for a true spike in the described neuron model.

• virtual StringVec getParamNames () const override

Gets names of of (independent) model parameters.

virtual VarVec getVars () const override

Gets names and types (as strings) of model variables.

Static Public Member Functions

static const NeuronModels::Izhikevich * getInstance ()

Additional Inherited Members

20.67.1 Detailed Description

Izhikevich neuron with fixed parameters [1].

It is usually described as

$$\begin{array}{lcl} \frac{dV}{dt} & = & 0.04V^2 + 5V + 140 - U + I, \\ \frac{dU}{dt} & = & a(bV - U), \end{array}$$

I is an external input current and the voltage V is reset to parameter c and U incremented by parameter d, whenever V >= 30 mV. This is paired with a particular integration procedure of two 0.5 ms Euler time steps for the V equation followed by one 1 ms time step of the U equation. Because of its popularity we provide this model in this form here event though due to the details of the usual implementation it is strictly speaking inconsistent with the displayed equations.

Variables are:

- ∨ Membrane potential
- U Membrane recovery variable

Parameters are:

- · a time scale of U
- b sensitivity of U
- $\bullet \ \, _{\rm C}$ after-spike reset value of V
- d after-spike reset value of U

20.67.2 Member Typedef Documentation

20.67.2.1 ParamValues

typedef Snippet::ValueBase< 4 > NeuronModels::Izhikevich::ParamValues

20.67.2.2 PostVarValues

typedef Models::VarInitContainerBase<0> NeuronModels::Izhikevich::PostVarValues

20.67.2.3 PreVarValues

typedef Models::VarInitContainerBase<0> NeuronModels::Izhikevich::PreVarValues

20.67.2.4 VarValues

typedef Models::VarInitContainerBase< 2 > NeuronModels::Izhikevich::VarValues

20.67.3 Member Function Documentation

20.67.3.1 getInstance()

static const NeuronModels::Izhikevich* NeuronModels::Izhikevich::getInstance () [inline],
[static]

20.67.3.2 getParamNames()

virtual StringVec NeuronModels::Izhikevich::getParamNames () const [inline], [override],
[virtual]

Gets names of of (independent) model parameters.

Reimplemented from Snippet::Base.

Reimplemented in NeuronModels::IzhikevichVariable.

20.67.3.3 getSimCode()

virtual std::string NeuronModels::Izhikevich::getSimCode () const [inline], [override],
[virtual]

Gets the code that defines the execution of one timestep of integration of the neuron model.

The code will refer to for the value of the variable with name "NN". It needs to refer to the predefined variable "ISYN", i.e. contain, if it is to receive input.

Reimplemented from NeuronModels::Base.

20.67.3.4 getThresholdConditionCode()

```
virtual std::string NeuronModels::Izhikevich::getThresholdConditionCode ( ) const [inline],
[override], [virtual]
```

Gets code which defines the condition for a true spike in the described neuron model.

This evaluates to a bool (e.g. "V > 20").

Reimplemented from NeuronModels::Base.

20.67.3.5 getVars()

```
virtual VarVec NeuronModels::Izhikevich::getVars ( ) const [inline], [override], [virtual]
```

Gets names and types (as strings) of model variables.

Reimplemented from Models::Base.

Reimplemented in NeuronModels::IzhikevichVariable.

The documentation for this class was generated from the following file:

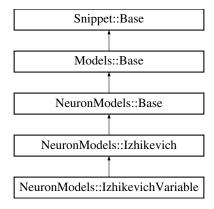
neuronModels.h

20.68 NeuronModels::IzhikevichVariable Class Reference

Izhikevich neuron with variable parameters [1].

```
#include <neuronModels.h>
```

Inheritance diagram for NeuronModels::IzhikevichVariable:



Public Types

- typedef Snippet::ValueBase< 0 > ParamValues
- typedef Models::VarInitContainerBase< 6 > VarValues
- typedef Models::VarInitContainerBase< 0 > PreVarValues
- typedef Models::VarInitContainerBase< 0 > PostVarValues

Public Member Functions

• virtual StringVec getParamNames () const override

Gets names of of (independent) model parameters.

· virtual VarVec getVars () const override

Gets names and types (as strings) of model variables.

Static Public Member Functions

static const NeuronModels::IzhikevichVariable * getInstance ()

Additional Inherited Members

20.68.1 Detailed Description

Izhikevich neuron with variable parameters [1].

This is the same model as Izhikevich but parameters are defined as "variables" in order to allow users to provide individual values for each individual neuron instead of fixed values for all neurons across the population.

Accordingly, the model has the Variables:

- V Membrane potential
- U Membrane recovery variable
- · a time scale of U
- b sensitivity of U
- c after-spike reset value of V
- d after-spike reset value of U

and no parameters.

20.68.2 Member Typedef Documentation

```
20.68.2.1 ParamValues
```

typedef Snippet::ValueBase< 0 > NeuronModels::IzhikevichVariable::ParamValues

20.68.2.2 PostVarValues

typedef Models::VarInitContainerBase<0> NeuronModels::IzhikevichVariable::PostVarValues

20.68.2.3 PreVarValues

typedef Models::VarInitContainerBase<0> NeuronModels::IzhikevichVariable::PreVarValues

20.68.2.4 VarValues

typedef Models::VarInitContainerBase< 6 > NeuronModels::IzhikevichVariable::VarValues

20.68.3 Member Function Documentation

20.68.3.1 getInstance()

```
static const NeuronModels::IzhikevichVariable* NeuronModels::IzhikevichVariable::getInstance (
) [inline], [static]
```

20.68.3.2 getParamNames()

virtual StringVec NeuronModels::IzhikevichVariable::getParamNames () const [inline], [override],
[virtual]

Gets names of of (independent) model parameters.

Reimplemented from NeuronModels::Izhikevich.

20.68.3.3 getVars()

```
virtual VarVec NeuronModels::IzhikevichVariable::getVars ( ) const [inline], [override],
[virtual]
```

Gets names and types (as strings) of model variables.

Reimplemented from NeuronModels::Izhikevich.

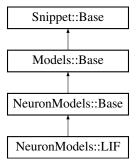
The documentation for this class was generated from the following file:

neuronModels.h

20.69 NeuronModels::LIF Class Reference

```
#include <neuronModels.h>
```

Inheritance diagram for NeuronModels::LIF:



Public Types

- typedef Snippet::ValueBase< 7 > ParamValues
- typedef Models::VarInitContainerBase< 2 > VarValues
- typedef Models::VarInitContainerBase< 0 > PreVarValues
- typedef Models::VarInitContainerBase< 0 > PostVarValues

Public Member Functions

- virtual std::string getSimCode () const override
 Gets the code that defines the execution of one timestep of integration of the neuron model.
- virtual std::string getThresholdConditionCode () const override

Gets code which defines the condition for a true spike in the described neuron model.

• virtual std::string getResetCode () const override

Gets code that defines the reset action taken after a spike occurred. This can be empty.

virtual StringVec getParamNames () const override

Gets names of of (independent) model parameters.

- · virtual DerivedParamVec getDerivedParams () const override
- · virtual VarVec getVars () const override

Gets names and types (as strings) of model variables.

• SET_NEEDS_AUTO_REFRACTORY (false)

Static Public Member Functions

static const LIF * getInstance ()

Additional Inherited Members

20.69.1 Member Typedef Documentation

20.69.1.1 ParamValues

```
typedef Snippet::ValueBase< 7 > NeuronModels::LIF::ParamValues
```

20.69.1.2 PostVarValues

```
typedef Models::VarInitContainerBase<0> NeuronModels::LIF::PostVarValues
```

20.69.1.3 PreVarValues

```
typedef Models::VarInitContainerBase<0> NeuronModels::LIF::PreVarValues
```

20.69.1.4 VarValues

```
typedef Models::VarInitContainerBase< 2 > NeuronModels::LIF::VarValues
```

20.69.2 Member Function Documentation

20.69.2.1 getDerivedParams()

```
virtual DerivedParamVec NeuronModels::LIF::getDerivedParams ( ) const [inline], [override],
[virtual]
```

Gets names of derived model parameters and the function objects to call to Calculate their value from a vector of model parameter values

Reimplemented from Snippet::Base.

20.69.2.2 getInstance()

```
static const LIF* NeuronModels::LIF::getInstance ( ) [inline], [static]
```

20.69.2.3 getParamNames()

```
virtual StringVec NeuronModels::LIF::getParamNames ( ) const [inline], [override], [virtual]
```

Gets names of of (independent) model parameters.

Reimplemented from Snippet::Base.

20.69.2.4 getResetCode()

```
virtual std::string NeuronModels::LIF::getResetCode ( ) const [inline], [override], [virtual]
```

Gets code that defines the reset action taken after a spike occurred. This can be empty.

Reimplemented from NeuronModels::Base.

20.69.2.5 getSimCode()

```
virtual std::string NeuronModels::LIF::getSimCode ( ) const [inline], [override], [virtual]
```

Gets the code that defines the execution of one timestep of integration of the neuron model.

The code will refer to for the value of the variable with name "NN". It needs to refer to the predefined variable "ISYN", i.e. contain, if it is to receive input.

Reimplemented from NeuronModels::Base.

20.69.2.6 getThresholdConditionCode()

```
virtual std::string NeuronModels::LIF::getThresholdConditionCode ( ) const [inline], [override],
[virtual]
```

Gets code which defines the condition for a true spike in the described neuron model.

This evaluates to a bool (e.g. "V > 20").

Reimplemented from NeuronModels::Base.

20.69.2.7 getVars()

```
virtual VarVec NeuronModels::LIF::getVars ( ) const [inline], [override], [virtual]
```

Gets names and types (as strings) of model variables.

Reimplemented from Models::Base.

20.69.2.8 SET_NEEDS_AUTO_REFRACTORY()

The documentation for this class was generated from the following file:

• neuronModels.h

20.70 CodeGenerator::MemAlloc Class Reference

```
#include <backendBase.h>
```

Public Member Functions

- size_t getHostBytes () const
- size_t getDeviceBytes () const
- size_t getZeroCopyBytes () const
- size_t getHostMBytes () const
- size_t getDeviceMBytes () const
- size_t getZeroCopyMBytes () const
- MemAlloc & operator+= (const MemAlloc &rhs)

Static Public Member Functions

- static MemAlloc zero ()
- static MemAlloc host (size_t hostBytes)
- static MemAlloc device (size t deviceBytes)
- static MemAlloc zeroCopy (size_t zeroCopyBytes)

20.70.1 Member Function Documentation

The documentation for this class was generated from the following file:

· backendBase.h

20.71 CodeGenerator::MergedStructData::MergedEGP Struct Reference

Immutable structure for tracking where an extra global variable ends up after merging.

```
#include <codeGenUtils.h>
```

Public Member Functions

MergedEGP (size_t m, size_t g, const std::string &t, const std::string &f)

Public Attributes

- const size_t mergedGroupIndex
- const size_t groupIndex
- · const std::string type
- const std::string fieldName

20.71.1 Detailed Description

Immutable structure for tracking where an extra global variable ends up after merging.

20.71.2 Constructor & Destructor Documentation

20.71.2.1 MergedEGP()

20.71.3 Member Data Documentation

20.71.3.1 fieldName

const std::string CodeGenerator::MergedStructData::MergedEGP::fieldName

20.71.3.2 groupIndex

```
const size_t CodeGenerator::MergedStructData::MergedEGP::groupIndex
```

20.71.3.3 mergedGroupIndex

```
const size_t CodeGenerator::MergedStructData::MergedEGP::mergedGroupIndex
```

20.71.3.4 type

```
\verb|const| std::string| CodeGenerator::MergedStructData::MergedEGP::type| \\
```

The documentation for this struct was generated from the following file:

· codeGenUtils.h

20.72 CodeGenerator::MergedStructData Class Reference

```
#include <codeGenUtils.h>
```

Classes

struct MergedEGP

Immutable structure for tracking where an extra global variable ends up after merging.

Public Types

typedef std::map< std::string, std::unordered_multimap< std::string, MergedEGP >> MergedEGPMap
 Map of original extra global param names to their locations within merged structures.

Public Member Functions

- const MergedEGPMap & getMergedEGPs () const
- void addMergedEGP (const std::string &variableName, const std::string &mergedGroupType, size_

 t mergedGroupIndex, size_t groupIndex, const std::string &type, const std::string &fieldName)

- size_t getMergedGroupSize (const std::string &mergedGroupType, size_t mergedGroupIndex) const
- void addMergedGroupSize (const std::string &mergedGroupType, size_t mergedGroupIndex, size_t size
 — Bytes)

20.72.1 Detailed Description

Class for storing data generated when writing merged structures in runner and required in later code generation

20.72.2 Member Typedef Documentation

20.72.2.1 MergedEGPMap

```
typedef std::map<std::string, std::unordered_multimap<std::string, MergedEGP> > CodeGenerator
::MergedStructData::MergedEGPMap
```

Map of original extra global param names to their locations within merged structures.

20.72.3 Member Function Documentation

20.72.3.1 addMergedEGP()

20.72.3.2 addMergedGroupSize()

20.72.3.3 getMergedEGPs()

```
\verb|const MergedEGPMap& CodeGenerator::MergedStructData::getMergedEGPs ( ) const [inline]|\\
```

20.72.3.4 getMergedGroupSize()

The documentation for this class was generated from the following file:

· codeGenUtils.h

20.73 CodeGenerator::MergedStructGenerator < T > Class Template Reference

#include <mergedStructGenerator.h>

Public Types

- enum FieldType { FieldType::Standard, FieldType::ScalarEGP, FieldType::PointerEGP }
- typedef std::function< std::string(const typename T::GroupInternal &, size_t)> GetFieldValueFunc

Public Member Functions

- MergedStructGenerator (const T &mergedGroup, const std::string &precision)
- void addField (const std::string &type, const std::string &name, GetFieldValueFunc getFieldValue, FieldType fieldType=FieldType::Standard)
- void addScalarField (const std::string &name, GetFieldValueFunc getFieldValue, FieldType=Field
 — Type::Standard)
- void addPointerField (const std::string &type, const std::string &name, const std::string &prefix)
- void addVars (const Models::Base::VarVec &vars, const std::string &arrayPrefix)
- void addEGPs (const Snippet::Base::EGPVec &egps, const std::string &arrayPrefix, const std::string &var
 — Name="")
- template<typename G, typename H >
 void addHeterogeneousParams (const Snippet::Base::StringVec ¶mNames, const std::string &suffix, G getParamValues, H isHeterogeneous)
- template<typename G, typename H >
 void addHeterogeneousDerivedParams (const Snippet::Base::DerivedParamVec &derivedParams, const std::string &suffix, G getDerivedParamValues, H isHeterogeneous)
- template<typename V, typename H >
 void addHeterogeneousVarInitParams (const Models::Base::VarVec &vars, V getVarInitialisers, H is
 Heterogeneous)
- template<typename V, typename H >
 void addHeterogeneousVarInitDerivedParams (const Models::Base::VarVec &vars, V getVarInitialisers, H is
 Heterogeneous)
- void generate (const BackendBase &backend, CodeStream &definitionsInternal, CodeStream &definitions
 — InternalFunc, CodeStream &definitionsInternalVar, CodeStream &runnerVarDecl, CodeStream &runner
 — MergedStructAlloc, MergedStructData &mergedStructData, const std::string &name, bool host=false) const

Protected Member Functions

• const T & getMergedGroup () const

20.73.1 Member Typedef Documentation

20.73.1.1 GetFieldValueFunc

```
template<typename T>
typedef std::function<std::string(const typename T::GroupInternal &, size_t)> CodeGenerator
::MergedStructGenerator< T >::GetFieldValueFunc
```

20.73.2 Member Enumeration Documentation

20.73.2.1 FieldType

```
template<typename T>
enum CodeGenerator::MergedStructGenerator::FieldType [strong]
```

Enumerator

Standard	
Stanuaru	
ScalarEGP	
PointerEGP	

20.73.3 Constructor & Destructor Documentation

20.73.3.1 MergedStructGenerator()

20.73.4 Member Function Documentation

20.73.4.1 addEGPs()

20.73.4.2 addField()

20.73.4.3 addHeterogeneousDerivedParams()

20.73.4.4 addHeterogeneousParams()

20.73.4.5 addHeterogeneousVarInitDerivedParams()

20.73.4.6 addHeterogeneousVarInitParams()

20.73.4.7 addPointerField()

20.73.4.8 addScalarField()

20.73.4.9 addVars()

20.73.4.10 generate()

20.73.4.11 getMergedGroup()

```
template<typename T>
const T& CodeGenerator::MergedStructGenerator< T >::getMergedGroup ( ) const [inline], [protected]
```

The documentation for this class was generated from the following file:

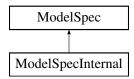
· mergedStructGenerator.h

20.74 ModelSpec Class Reference

Object used for specifying a neuronal network model.

```
#include <modelSpec.h>
```

Inheritance diagram for ModelSpec:



Public Types

- typedef std::map< std::string, NeuronGroupInternal >::value_type NeuronGroupValueType
- typedef std::map< std::string, SynapseGroupInternal >::value_type SynapseGroupValueType

Public Member Functions

- ModelSpec ()
- ModelSpec (const ModelSpec &)=delete
- ModelSpec & operator= (const ModelSpec &)=delete
- ∼ModelSpec ()
- void setName (const std::string &name)

Method to set the neuronal network model name.

void setPrecision (FloatType)

Set numerical precision for floating point.

• void setTimePrecision (TimePrecision timePrecision)

Set numerical precision for time.

void setDT (double dt)

Set the integration step size of the model.

void setTiming (bool timingEnabled)

Set whether timers and timing commands are to be included.

void setSeed (unsigned int rngSeed)

Set the random seed (disables automatic seeding if argument not 0).

void setDefaultVarLocation (VarLocation loc)

What is the default location for model state variables?

void setDefaultExtraGlobalParamLocation (VarLocation loc)

What is the default location for model extra global parameters?

void setDefaultSparseConnectivityLocation (VarLocation loc)

What is the default location for sparse synaptic connectivity?

void setDefaultNarrowSparseIndEnabled (bool enabled)

Sets default for whether narrow i.e. less than 32-bit types are used for sparse matrix indices.

void setMergePostsynapticModels (bool merge)

Should compatible postsynaptic models and dendritic delay buffers be merged?

const std::string & getName () const

Gets the name of the neuronal network model.

const std::string & getPrecision () const

Gets the floating point numerical precision.

• std::string getTimePrecision () const

Gets the floating point numerical precision used to represent time.

double getDT () const

Gets the model integration step size.

• unsigned int getSeed () const

Get the random seed.

• bool isTimingEnabled () const

Are timers and timing commands enabled.

· unsigned int getNumNeurons () const

How many neurons make up the entire model.

NeuronGroup * findNeuronGroup (const std::string &name)

Find a neuron group by name.

• template<typename NeuronModel >

NeuronGroup * addNeuronPopulation (const std::string &name, unsigned int size, const NeuronModel *model, const typename NeuronModel::ParamValues ¶mValues, const typename NeuronModel::Var← Values &varInitialisers)

Adds a new neuron group to the model using a neuron model managed by the user.

• template<typename NeuronModel >

NeuronGroup * addNeuronPopulation (const std::string &name, unsigned int size, const typename Neuron← Model::ParamValues ¶mValues, const typename NeuronModel::VarValues &varInitialisers)

Adds a new neuron group to the model using a singleton neuron model created using standard DECLARE_MODEL and IMPLEMENT_MODEL macros.

SynapseGroup * findSynapseGroup (const std::string &name)

Find a synapse group by name.

 $\bullet \ \ template {<} typename \ Weight Update Model \ , typename \ Postsynaptic Model \ > \\$

SynapseGroup * addSynapsePopulation (const std::string &name, SynapseMatrixType mtype, unsigned int delaySteps, const std::string &src, const std::string &trg, const WeightUpdateModel *wum, const typename WeightUpdateModel::ParamValues &weightParamValues, const typename WeightUpdate &weight PreVarValues &weightVarInitialisers, const typename WeightUpdateModel::PreVarValues &weightPostVarInitialisers, const PostsynapticModel *psm, const typename PostsynapticModel::ParamValues &postsynapticParamValues, const typename PostsynapticModel::VarValues &postsynapticVarInitialisers, const InitSparseConnectivity Snippet::Init &connectivityInitialiser=uninitialisedConnectivity())

Adds a synapse population to the model using weight update and postsynaptic models managed by the user.

template<typename WeightUpdateModel , typename PostsynapticModel >
 SynapseGroup * addSynapsePopulation (const std::string &name, SynapseMatrixType mtype, unsigned int delaySteps, const std::string &src, const std::string &trg, const typename WeightUpdateModel::Param
 Values &weightParamValues, const typename WeightUpdateModel::VarValues &weightVarInitialisers, const typename PostsynapticModel::ParamValues &postsynapticParamValues, const typename Postsynaptic
 Model::VarValues &postsynapticVarInitialisers, const InitSparseConnectivitySnippet::Init &connectivity
 Initialiser=uninitialisedConnectivity())

Adds a synapse population to the model using singleton weight update and postsynaptic models created using standard DECLARE_MODEL and IMPLEMENT_MODEL macros.

• template<typename WeightUpdateModel, typename PostsynapticModel > SynapseGroup * addSynapsePopulation (const std::string &name, SynapseMatrixType mtype, unsigned int delaySteps, const std::string &src, const std::string &trg, const typename WeightUpdateModel::

ParamValues &weightParamValues, const typename WeightUpdateModel::VarValues &weightVarInitialisers, const typename WeightUpdateModel::PreVarValues &weightPreVarInitialisers, const typename Weight

UpdateModel::PostVarValues &weightPostVarInitialisers, const typename PostsynapticModel::ParamValues &postsynapticParamValues, const typename PostsynapticModel::VarValues &postsynapticVarInitialisers,

const InitSparseConnectivitySnippet::Init &connectivityInitialiser=uninitialisedConnectivity())

Adds a synapse population to the model using singleton weight update and postsynaptic models created using standard DECLARE_MODEL and IMPLEMENT_MODEL macros.

template<typename PostsynapticModel >

SynapseGroup * addSlaveSynapsePopulation (const std::string &name, const std::string &weightSharing
MasterName, unsigned int delaySteps, const std::string &src, const std::string &trg, const Postsynaptic

Model *psm, const typename PostsynapticModel::ParamValues &postsynapticParamValues, const typename
PostsynapticModel::VarValues &postsynapticVarInitialisers)

Adds a synapse population to the model using shared per-synapse variables and a postsynaptic model managed by the user.

• template<typename PostsynapticModel >

SynapseGroup * addSlaveSynapsePopulation (const std::string &name, const std::string &weightSharing
MasterName, unsigned int delaySteps, const std::string &src, const std::string &trg, const typename
PostsynapticModel::ParamValues &postsynapticParamValues, const typename PostsynapticModel::Var
Values &postsynapticVarInitialisers)

Adds a synapse population to the model using shared per-synapse variables and a singleton postsynaptic model created using standard DECLARE_MODEL and IMPLEMENT_MODEL macros.

CurrentSource * findCurrentSource (const std::string &name)

Find a current source by name.

 $\bullet \ \ template{<} typename \ CurrentSourceModel>$

CurrentSource * addCurrentSource (const std::string ¤tSourceName, const CurrentSourceModel *model, const std::string &targetNeuronGroupName, const typename CurrentSourceModel::ParamValues ¶mValues, const typename CurrentSourceModel::VarValues &varInitialisers)

Adds a new current source to the model using a current source model managed by the user.

• template<typename CurrentSourceModel >

CurrentSource * addCurrentSource (const std::string ¤tSourceName, const std::string &target ← NeuronGroupName, const typename CurrentSourceModel::ParamValues ¶mValues, const typename CurrentSourceModel::VarValues &varInitialisers)

Adds a new current source to the model using a singleton current source model created using standard DECLARE← _MODEL and IMPLEMENT_MODEL macros.

Protected Member Functions

· void finalize ()

Finalise model.

• std::string scalarExpr (double) const

Get the string literal that should be used to represent a value in the model's floating-point type.

bool zeroCopyInUse () const

Are any variables in any populations in this model using zero-copy memory?

const std::map< std::string, NeuronGroupInternal > & getNeuronGroups () const

Get std::map containing local named NeuronGroup objects in model.

- const std::map< std::string, SynapseGroupInternal > & getSynapseGroups () const
 - Get std::map containing local named SynapseGroup objects in model.
- const std::map< std::string, CurrentSourceInternal > & getLocalCurrentSources () const

Get std::map containing local named CurrentSource objects in model.

20.74.1 Detailed Description

Object used for specifying a neuronal network model.

20.74.2 Member Typedef Documentation

20.74.2.1 NeuronGroupValueType

```
typedef std::map<std::string, NeuronGroupInternal>::value_type ModelSpec::NeuronGroupValueType
```

20.74.2.2 SynapseGroupValueType

```
typedef std::map<std::string, SynapseGroupInternal>::value_type ModelSpec::SynapseGroupValue← Type
```

20.74.3 Constructor & Destructor Documentation

```
20.74.3.1 ModelSpec() [1/2]
```

```
ModelSpec::ModelSpec ( )
```

20.74.3.2 ModelSpec() [2/2]

20.74.3.3 \sim ModelSpec()

```
ModelSpec::~ModelSpec ( )
```

20.74.4 Member Function Documentation

20.74.4.1 addCurrentSource() [1/2]

```
const CurrentSourceModel * model,
const std::string & targetNeuronGroupName,
const typename CurrentSourceModel::ParamValues & paramValues,
const typename CurrentSourceModel::VarValues & varInitialisers ) [inline]
```

Adds a new current source to the model using a current source model managed by the user.

Template Parameters

CurrentSourceModel	type of current source model (derived from CurrentSourceModels::Base).
--------------------	--

Parameters

name	string containing unique name of current source.
model	current source model to use for current source.
targetNeuronGroupName	string name of the target neuron group
paramValues	parameters for model wrapped in CurrentSourceModel::ParamValues object.
varInitialisers	state variable initialiser snippets and parameters wrapped in
	CurrentSource::VarValues object.

Returns

pointer to newly created CurrentSource

20.74.4.2 addCurrentSource() [2/2]

Adds a new current source to the model using a singleton current source model created using standard DECLAR← E_MODEL and IMPLEMENT_MODEL macros.

Template Parameters

CurrentSourceModel	type of neuron model (derived from CurrentSourceModel::Base).
--------------------	---

Parameters

name	string containing unique name of current source.
targetNeuronGroupName	string name of the target neuron group
paramValues	parameters for model wrapped in CurrentSourceModel::ParamValues object.
varInitialisers	state variable initialiser snippets and parameters wrapped in CurrentSourceModel::VarValues object.

Returns

pointer to newly created CurrentSource

20.74.4.3 addNeuronPopulation() [1/2]

Adds a new neuron group to the model using a neuron model managed by the user.

Template Parameters

NeuronModel	type of neuron model (derived from NeuronModels::Base).
-------------	---

Parameters

name	string containing unique name of neuron population.
size	integer specifying how many neurons are in the population.
model	neuron model to use for neuron group.
paramValues	parameters for model wrapped in NeuronModel::ParamValues object.
varlnitialisers	state variable initialiser snippets and parameters wrapped in NeuronModel::VarValues object.

Returns

pointer to newly created NeuronGroup

20.74.4.4 addNeuronPopulation() [2/2]

Adds a new neuron group to the model using a singleton neuron model created using standard DECLARE_MODEL and IMPLEMENT_MODEL macros.

Template Parameters

NeuronModel	type of neuron model (derived from NeuronModels::Base).

Parameters

name	string containing unique name of neuron population.
size	integer specifying how many neurons are in the population.
paramValues	parameters for model wrapped in NeuronModel::ParamValues object.
varInitialisers	state variable initialiser snippets and parameters wrapped in NeuronModel::VarValues object.

Returns

pointer to newly created NeuronGroup

20.74.4.5 addSlaveSynapsePopulation() [1/2]

Adds a synapse population to the model using shared per-synapse variables and a postsynaptic model managed by the user.

Template Parameters

PostsynapticModel	type of postsynaptic model (derived from PostsynapticModels::Base).
-------------------	---

Parameters

name	string containing unique name of synapse population.
weightSharingMasterName	string containing name of 'master' synapse population
delaySteps	integer specifying number of timesteps delay this synaptic connection should incur (or NO_DELAY for none)
src	string specifying name of presynaptic (source) population
trg	string specifying name of postsynaptic (target) population
psm	postsynaptic model to use for synapse group.
postsynapticParamValues	parameters for postsynaptic model wrapped in PostsynapticModel::ParamValues object.
postsynapticVarInitialisers	postsynaptic model state variable initialiser snippets and parameters wrapped in NeuronModel::VarValues object.

Returns

pointer to newly created SynapseGroup

20.74.4.6 addSlaveSynapsePopulation() [2/2]

Adds a synapse population to the model using shared per-synapse variables and a singleton postsynaptic model created using standard DECLARE_MODEL and IMPLEMENT_MODEL macros.

Template Parameters

	PostsynapticModel	type of postsynaptic model (derived from PostsynapticModels::Base).
--	-------------------	---

Parameters

name	string containing unique name of synapse population.		
weightSharingMasterName	string containing name of 'master' synapse population		
delaySteps	integer specifying number of timesteps delay this synaptic connection should incur (or NO_DELAY for none)		
src	string specifying name of presynaptic (source) population		
trg	string specifying name of postsynaptic (target) population		
postsynapticParamValues	parameters for postsynaptic model wrapped in PostsynapticModel::ParamValues object.		
postsynapticVarInitialisers	postsynaptic model state variable initialiser snippets and parameters wrapped in NeuronModel::VarValues object.		

Returns

pointer to newly created SynapseGroup

20.74.4.7 addSynapsePopulation() [1/3]

```
template<typename WeightUpdateModel , typename PostsynapticModel >
SynapseGroup* ModelSpec::addSynapsePopulation (
           const std::string & name,
           SynapseMatrixType mtype,
           unsigned int delaySteps,
           const std::string & src,
           const std::string & trg,
           const WeightUpdateModel * wum,
           const typename WeightUpdateModel::ParamValues & weightParamValues,
           const typename WeightUpdateModel::VarValues & weightVarInitialisers,
           \verb|const typename WeightUpdateModel::PreVarValues & weightPreVarInitialisers|, \\
           const typename WeightUpdateModel::PostVarValues & weightPostVarInitialisers,
           const PostsynapticModel * psm,
           const typename PostsynapticModel::ParamValues & postsynapticParamValues,
           const typename PostsynapticModel:: VarValues & postsynapticVarInitialisers,
           Connectivity() ) [inline]
```

Adds a synapse population to the model using weight update and postsynaptic models managed by the user.

Template Parameters

WeightUpdateModel	$type\ of\ weight\ update\ model\ (derived\ from\ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ $	
PostsynapticModel	type of postsynaptic model (derived from PostsynapticModels::Base).	

Parameters

name	string containing unique name of synapse population.	
mtype	how the synaptic matrix associated with this synapse population should be represented.	
delaySteps	integer specifying number of timesteps delay this synaptic connection should incur (or NO_DELAY for none)	
src	string specifying name of presynaptic (source) population	
trg	string specifying name of postsynaptic (target) population	
wum	weight update model to use for synapse group.	
weightParamValues	parameters for weight update model wrapped in WeightUpdateModel::ParamValues object.	
weightVarInitialisers	weight update model state variable initialiser snippets and parameters wrapped in WeightUpdateModel::VarValues object.	
weightPreVarInitialisers	weight update model presynaptic state variable initialiser snippets and parameters wrapped in WeightUpdateModel::VarValues object.	
weightPostVarInitialisers	weight update model postsynaptic state variable initialiser snippets and parameters wrapped in WeightUpdateModel::VarValues object.	
psm	postsynaptic model to use for synapse group.	
postsynapticParamValues	parameters for postsynaptic model wrapped in PostsynapticModel::ParamValues object.	
postsynapticVarInitialisers	postsynaptic model state variable initialiser snippets and parameters wrapped in NeuronModel::VarValues object.	
connectivityInitialiser	sparse connectivity initialisation snippet used to initialise connectivity for SynapseMatrixConnectivity::SPARSE or SynapseMatrixConnectivity::BITMASK. Typically wrapped with it's parameters using initConnectivity function	

Returns

pointer to newly created SynapseGroup

20.74.4.8 addSynapsePopulation() [2/3]

Adds a synapse population to the model using singleton weight update and postsynaptic models created using standard DECLARE_MODEL and IMPLEMENT_MODEL macros.

Template Parameters

WeightUpdateModel	type of weight update model (derived from WeightUpdateModels::Base).
PostsynapticModel	type of postsynaptic model (derived from PostsynapticModels::Base).

Parameters

name	string containing unique name of synapse population.
mtype	how the synaptic matrix associated with this synapse population should be represented.
delaySteps	integer specifying number of timesteps delay this synaptic connection should incur (or NO_DELAY for none)
src	string specifying name of presynaptic (source) population
trg	string specifying name of postsynaptic (target) population
weightParamValues	parameters for weight update model wrapped in WeightUpdateModel::ParamValues object.
weightVarInitialisers	weight update model state variable initialiser snippets and parameters wrapped in WeightUpdateModel::VarValues object.
postsynapticParamValues	parameters for postsynaptic model wrapped in PostsynapticModel::ParamValues object.
postsynapticVarInitialisers	postsynaptic model state variable initialiser snippets and parameters wrapped in NeuronModel::VarValues object.
connectivityInitialiser	sparse connectivity initialisation snippet used to initialise connectivity for SynapseMatrixConnectivity::SPARSE or SynapseMatrixConnectivity::BITMASK. Typically wrapped with it's parameters using initConnectivity function

Returns

pointer to newly created SynapseGroup

20.74.4.9 addSynapsePopulation() [3/3]

Adds a synapse population to the model using singleton weight update and postsynaptic models created using standard DECLARE_MODEL and IMPLEMENT_MODEL macros.

Template Parameters

WeightUpdateModel	type of weight update model (derived from WeightUpdateModels::Base).
PostsynapticModel	type of postsynaptic model (derived from PostsynapticModels::Base).

Parameters

name	string containing unique name of synapse population.	
mtype	how the synaptic matrix associated with this synapse population should be represented.	
delaySteps	integer specifying number of timesteps delay this synaptic connection should incur (or NO_DELAY for none)	
src	string specifying name of presynaptic (source) population	
trg	string specifying name of postsynaptic (target) population	
weightParamValues	parameters for weight update model wrapped in WeightUpdateModel::ParamValues object.	
weightVarInitialisers	weight update model per-synapse state variable initialiser snippets and parameters wrapped in WeightUpdateModel::VarValues object.	
weightPreVarInitialisers	weight update model presynaptic state variable initialiser snippets and parameters wrapped in WeightUpdateModel::VarValues object.	
weightPostVarInitialisers	weight update model postsynaptic state variable initialiser snippets and parameters wrapped in WeightUpdateModel::VarValues object.	
postsynapticParamValues	parameters for postsynaptic model wrapped in PostsynapticModel::ParamValues object.	
postsynapticVarInitialisers	postsynaptic model state variable initialiser snippets and parameters wrapped in NeuronModel::VarValues object.	
connectivityInitialiser	sparse connectivity initialisation snippet used to initialise connectivity for SynapseMatrixConnectivity::SPARSE or SynapseMatrixConnectivity::BITMASK. Typically wrapped with it's parameters using initConnectivity function	

Returns

pointer to newly created SynapseGroup

20.74.4.10 finalize()

```
void ModelSpec::finalize ( ) [protected]
```

Finalise model.

20.74.4.11 findCurrentSource()

Find a current source by name.

This function attempts to find an existing current source.

20.74.4.12 findNeuronGroup()

Find a neuron group by name.

20.74.4.13 findSynapseGroup()

```
{\tt SynapseGroup} \ * \ {\tt ModelSpec::findSynapseGroup} \ (
```

```
const std::string & name )
```

Find a synapse group by name.

20.74.4.14 getDT()

```
double ModelSpec::getDT ( ) const [inline]
```

Gets the model integration step size.

20.74.4.15 getLocalCurrentSources()

```
const std::map<std::string, CurrentSourceInternal>& ModelSpec::getLocalCurrentSources ( )
const [inline], [protected]
```

Get std::map containing local named CurrentSource objects in model.

20.74.4.16 getName()

```
const std::string& ModelSpec::getName ( ) const [inline]
```

Gets the name of the neuronal network model.

20.74.4.17 getNeuronGroups()

```
const std::map<std::string, NeuronGroupInternal>& ModelSpec::getNeuronGroups ( ) const [inline],
[protected]
```

Get std::map containing local named NeuronGroup objects in model.

20.74.4.18 getNumNeurons()

```
\verb"unsigned" int ModelSpec::getNumNeurons" ( ) const
```

How many neurons make up the entire model.

20.74.4.19 getPrecision()

```
const std::string& ModelSpec::getPrecision ( ) const [inline]
```

Gets the floating point numerical precision.

20.74.4.20 getSeed()

```
unsigned int ModelSpec::getSeed ( ) const [inline]
```

Get the random seed.

20.74.4.21 getSynapseGroups()

```
const std::map<std::string, SynapseGroupInternal>& ModelSpec::getSynapseGroups ( ) const [inline],
[protected]
```

Get std::map containing local named SynapseGroup objects in model.

```
20.74.4.22 getTimePrecision()
```

```
std::string ModelSpec::getTimePrecision ( ) const
```

Gets the floating point numerical precision used to represent time.

20.74.4.23 isTimingEnabled()

```
bool ModelSpec::isTimingEnabled ( ) const [inline]
```

Are timers and timing commands enabled.

20.74.4.24 operator=()

20.74.4.25 scalarExpr()

Get the string literal that should be used to represent a value in the model's floating-point type.

20.74.4.26 setDefaultExtraGlobalParamLocation()

What is the default location for model extra global parameters?

Historically, this was just left up to the user to handle

20.74.4.27 setDefaultNarrowSparseIndEnabled()

Sets default for whether narrow i.e. less than 32-bit types are used for sparse matrix indices.

20.74.4.28 setDefaultSparseConnectivityLocation()

What is the default location for sparse synaptic connectivity?

Historically, everything was allocated on both the host AND device

20.74.4.29 setDefaultVarLocation()

What is the default location for model state variables?

Historically, everything was allocated on both the host AND device

```
20.74.4.30 setDT()
```

Set the integration step size of the model.

20.74.4.31 setMergePostsynapticModels()

```
void ModelSpec::setMergePostsynapticModels (
          bool merge ) [inline]
```

Should compatible postsynaptic models and dendritic delay buffers be merged?

This can significantly reduce the cost of updating neuron population but means that per-synapse group inSyn arrays can not be retrieved

20.74.4.32 setName()

Method to set the neuronal network model name.

20.74.4.33 setPrecision()

Set numerical precision for floating point.

This function sets the numerical precision of floating type variables. By default, it is GENN_GENN_FLOAT.

20.74.4.34 setSeed()

```
void ModelSpec::setSeed (
          unsigned int rngSeed ) [inline]
```

Set the random seed (disables automatic seeding if argument not 0).

20.74.4.35 setTimePrecision()

Set numerical precision for time.

20.74.4.36 setTiming()

```
void ModelSpec::setTiming (
          bool timingEnabled ) [inline]
```

Set whether timers and timing commands are to be included.

20.74.4.37 zeroCopylnUse()

```
bool ModelSpec::zeroCopyInUse ( ) const [protected]
```

Are any variables in any populations in this model using zero-copy memory?

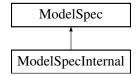
The documentation for this class was generated from the following files:

- · modelSpec.h
- · modelSpec.cc

20.75 ModelSpecInternal Class Reference

#include <modelSpecInternal.h>

Inheritance diagram for ModelSpecInternal:



Additional Inherited Members

The documentation for this class was generated from the following file:

· modelSpecInternal.h

20.76 CodeGenerator::ModelSpecMerged Class Reference

#include <modelSpecMerged.h>

Public Member Functions

- ModelSpecMerged (const ModelSpecInternal &model, const BackendBase &backend)
- const ModelSpecInternal & getModel () const

Get underlying, unmerged model.

- const std::vector< NeuronUpdateGroupMerged > & getMergedNeuronUpdateGroups () const Get merged neuron groups which require updating.
- const std::vector< PresynapticUpdateGroupMerged > & getMergedPresynapticUpdateGroups () const Get merged synapse groups which require presynaptic updates.
- const std::vector< PostsynapticUpdateGroupMerged > & getMergedPostsynapticUpdateGroups () const Get merged synapse groups which require postsynaptic updates.
- const std::vector < SynapseDynamicsGroupMerged > & getMergedSynapseDynamicsGroups () const
 Get merged synapse groups which require synapse dynamics.
- const std::vector< NeuronInitGroupMerged > & getMergedNeuronInitGroups () const Get merged neuron groups which require initialisation.
- const std::vector< SynapseDenseInitGroupMerged > & getMergedSynapseDenseInitGroups () const Get merged synapse groups with dense connectivity which require initialisation.
- const std::vector< SynapseConnectivityInitGroupMerged > & getMergedSynapseConnectivityInitGroups () const

Get merged synapse groups which require connectivity initialisation.

- const std::vector< SynapseSparseInitGroupMerged > & getMergedSynapseSparseInitGroups () const Get merged synapse groups with sparse connectivity which require initialisation.
- const std::vector< NeuronSpikeQueueUpdateGroupMerged > & getMergedNeuronSpikeQueueUpdate
 Groups () const

Get merged neuron groups which require their spike queues updating.

const std::vector< SynapseDendriticDelayUpdateGroupMerged > & getMergedSynapseDendriticDelay
 UpdateGroups () const

Get merged synapse groups which require their dendritic delay updating.

const std::vector < SynapseConnectivityHostInitGroupMerged > & getMergedSynapseConnectivityHostInit
 Groups () const

Merged synapse groups which require host code to initialise their synaptic connectivity.

- void genNeuronUpdateGroupSupportCode (CodeStream &os) const
- void genPostsynapticDynamicsSupportCode (CodeStream &os) const
- void genPresynapticUpdateSupportCode (CodeStream &os) const
- void genPostsynapticUpdateSupportCode (CodeStream &os) const
- void genSynapseDynamicsSupportCode (CodeStream &os) const
- const std::string & getNeuronUpdateSupportCodeNamespace (const std::string &code) const
- const std::string & getPostsynapticDynamicsSupportCodeNamespace (const std::string &code) const
- const std::string & getPresynapticUpdateSupportCodeNamespace (const std::string &code) const
- const std::string & getPostsynapticUpdateSupportCodeNamespace (const std::string &code) const
- const std::string & getSynapseDynamicsSupportCodeNamespace (const std::string &code) const

20.76.1 Constructor & Destructor Documentation

20.76.1.1 ModelSpecMerged()

20.76.2 Member Function Documentation

20.76.2.1 genNeuronUpdateGroupSupportCode()

20.76.2.2 genPostsynapticDynamicsSupportCode()

20.76.2.3 genPostsynapticUpdateSupportCode()

20.76.2.4 genPresynapticUpdateSupportCode()

20.76.2.5 genSynapseDynamicsSupportCode()

20.76.2.6 getMergedNeuronInitGroups()

const std::vector<NeuronInitGroupMerged>& CodeGenerator::ModelSpecMerged::getMergedNeuron←
InitGroups () const [inline]

Get merged neuron groups which require initialisation.

20.76.2.7 getMergedNeuronSpikeQueueUpdateGroups()

 $\verb|const| std::vector<|NeuronSpikeQueueUpdateGroupMerged>& CodeGenerator::ModelSpecMerged::get \leftarrow |MergedNeuronSpikeQueueUpdateGroups||(inline)||$

Get merged neuron groups which require their spike queues updating.

20.76.2.8 getMergedNeuronUpdateGroups()

Get merged neuron groups which require updating.

20.76.2.9 getMergedPostsynapticUpdateGroups()

 $\verb|const| std::vector<| PostsynapticUpdateGroupMerged>& CodeGenerator::ModelSpecMerged::getMerged \\ \\ PostsynapticUpdateGroups () const [inline] \\$

Get merged synapse groups which require postsynaptic updates.

20.76.2.10 getMergedPresynapticUpdateGroups()

const std::vector<PresynapticUpdateGroupMerged>& CodeGenerator::ModelSpecMerged::getMerged↔
PresynapticUpdateGroups () const [inline]

Get merged synapse groups which require presynaptic updates.

20.76.2.11 getMergedSynapseConnectivityHostInitGroups()

 $\verb|const| std::vector<SynapseConnectivityHostInitGroupMerged> \& CodeGenerator::ModelSpecMerged \\ & ::getMergedSynapseConnectivityHostInitGroups () const [inline] \\$

Merged synapse groups which require host code to initialise their synaptic connectivity.

20.76.2.12 getMergedSynapseConnectivityInitGroups()

const std::vector<SynapseConnectivityInitGroupMerged>& CodeGenerator::ModelSpecMerged::get
MergedSynapseConnectivityInitGroups () const [inline]

Get merged synapse groups which require connectivity initialisation.

20.76.2.13 getMergedSynapseDendriticDelayUpdateGroups()

 $\verb|const| std::vector<SynapseDendriticDelayUpdateGroupMerged>& CodeGenerator::ModelSpecMerged$\\ &::getMergedSynapseDendriticDelayUpdateGroups () const [inline]$

Get merged synapse groups which require their dendritic delay updating.

20.76.2.14 getMergedSynapseDenseInitGroups()

 $\verb|const| std::vector<SynapseDenseInitGroupMerged>& CodeGenerator::ModelSpecMerged::getMerged \leftarrow SynapseDenseInitGroups () const [inline]$

Get merged synapse groups with dense connectivity which require initialisation.

20.76.2.15 getMergedSynapseDynamicsGroups()

 $\verb|const| std::vector<SynapseDynamicsGroupMerged>& CodeGenerator::ModelSpecMerged::getMerged \\ \\ SynapseDynamicsGroups () const [inline]$

Get merged synapse groups which require synapse dynamics.

20.76.2.16 getMergedSynapseSparseInitGroups()

const std::vector<SynapseSparseInitGroupMerged>& CodeGenerator::ModelSpecMerged::getMerged←
SynapseSparseInitGroups () const [inline]

Get merged synapse groups with sparse connectivity which require initialisation.

20.76.2.17 getModel()

const ModelSpecInternal& CodeGenerator::ModelSpecMerged::getModel () const [inline]

Get underlying, unmerged model.

20.76.2.18 getNeuronUpdateSupportCodeNamespace()

20.76.2.19 getPostsynapticDynamicsSupportCodeNamespace()

20.76.2.20 getPostsynapticUpdateSupportCodeNamespace()

20.76.2.21 getPresynapticUpdateSupportCodeNamespace()

20.76.2.22 getSynapseDynamicsSupportCodeNamespace()

The documentation for this class was generated from the following files:

- modelSpecMerged.h
- modelSpecMerged.cc

20.77 SpineMLSimulator::NetworkClient Class Reference

```
#include <networkClient.h>
```

Public Types

- enum Mode: char { Mode::Source = 45, Mode::Target = 46 }
- enum DataType: char { DataType::Analogue = 31, DataType::Events = 32, DataType::Impulses = 33 }

Public Member Functions

- NetworkClient ()
- NetworkClient (const std::string &hostname, unsigned int port, unsigned int size, DataType dataType, Mode mode, const std::string &connectionName)
- ∼NetworkClient ()
- bool connect (const std::string &hostname, unsigned int port, unsigned int size, DataType dataType, Mode mode, const std::string &connectionName)
- bool receive (std::vector< double > &buffer)
- bool send (const std::vector< double > &buffer)

20.77.1 Member Enumeration Documentation

20.77.1.1 DataType

```
enum SpineMLSimulator::NetworkClient::DataType : char [strong]
```

Enumerator

Analogue	
Events	
Impulses	

20.77.1.2 Mode

```
enum SpineMLSimulator::NetworkClient::Mode : char [strong]
```

Enumerator

```
Source
Target
```

20.77.2 Constructor & Destructor Documentation

20.77.3.2 receive()

20.77.3.3 send()

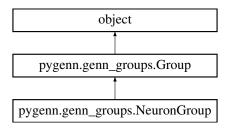
The documentation for this class was generated from the following files:

- networkClient.h
- networkClient.cc

20.78 pygenn.genn_groups.NeuronGroup Class Reference

Class representing a group of neurons.

Inheritance diagram for pygenn.genn_groups.NeuronGroup:



Public Member Functions

• def __init__ (self, name, model)

Init NeuronGroup.

• def current_spikes (self)

Current spikes from GeNN.

def delay_slots (self)

Maximum delay steps needed for this group.

- · def size (self)
- def set_neuron (self, model, param_space, var_space)

Set neuron, its parameters and initial variables.

def add_to (self, model_spec, num_neurons)

Add this NeuronGroup to a model.

• def add extra global param (self, param name, param values)

Set extra global parameter.

• def set_extra_global_param (self, param_name, param_values)

Set extra global parameter.

• def pull_spikes_from_device (self)

Wrapper around GeNNModel.pull_spikes_from_device.

def pull_current_spikes_from_device (self)

Wrapper around GeNNModel.pull_current_spikes_from_device.

def push_spikes_to_device (self)

Wrapper around GeNNModel.push_spikes_to_device.

def push_current_spikes_to_device (self)

 $Wrapper\ around\ GeNNModel.push_current_spikes_to_device.$

· def load (self)

Loads neuron group.

- · def load init egps (self)
- def reinitialise (self)

Reinitialise neuron group.

Public Attributes

- neuron
- spikes
- spike_count
- spike_que_ptr
- is_spike_source_array
- type
- pop

20.78.1 Detailed Description

Class representing a group of neurons.

20.78.2 Constructor & Destructor Documentation

Init NeuronGroup.

Parameters

```
name string name of the group
```

20.78.3 Member Function Documentation

20.78.3.1 add_extra_global_param()

Set extra global parameter.

Parameters

	param_name	string with the name of the extra global parameter
ſ	param_values	iterable or a single value

20.78.3.2 add_to()

```
\label{lem:condition} \mbox{def pygenn.genn\_groups.NeuronGroup.add\_to (} \\ self,
```

```
model_spec,
num_neurons )
```

Add this NeuronGroup to a model.

Parameters

model_spec	pygenn.genn_model.GeNNModel to add to
num_neurons	int number of neurons

```
20.78.3.3 current_spikes()
{\tt def pygenn.genn\_groups.NeuronGroup.current\_spikes} \ (
                self )
Current spikes from GeNN.
20.78.3.4 delay_slots()
def pygenn.genn_groups.NeuronGroup.delay_slots (
                self )
Maximum delay steps needed for this group.
20.78.3.5 load()
{\tt def pygenn.genn\_groups.NeuronGroup.load} \ (
                self )
Loads neuron group.
20.78.3.6 load_init_egps()
{\tt def pygenn.genn\_groups.NeuronGroup.load\_init\_egps} \ \ (
                self )
20.78.3.7 pull_current_spikes_from_device()
def pygenn.genn_groups.NeuronGroup.pull_current_spikes_from_device (
Wrapper around GeNNModel.pull_current_spikes_from_device.
20.78.3.8 pull_spikes_from_device()
{\tt def pygenn.genn\_groups.NeuronGroup.pull\_spikes\_from\_device} \ \ (
                self )
```

Wrapper around GeNNModel.pull_spikes_from_device.

```
20.78.3.9 push_current_spikes_to_device()
```

```
def pygenn.genn_groups.NeuronGroup.push_current_spikes_to_device ( self \ )
```

Wrapper around GeNNModel.push_current_spikes_to_device.

20.78.3.10 push_spikes_to_device()

```
\label{lem:condition} $\operatorname{def pygenn.genn\_groups.NeuronGroup.push\_spikes\_to\_device} \ ( \\ self )
```

Wrapper around GeNNModel.push_spikes_to_device.

20.78.3.11 reinitialise()

```
\label{lem:condition} \mbox{def pygenn.genn\_groups.NeuronGroup.reinitialise (} \\ self \mbox{)}
```

Reinitialise neuron group.

20.78.3.12 set_extra_global_param()

Set extra global parameter.

Parameters

param_name	string with the name of the extra global parameter
param_values	iterable or a single value

20.78.3.13 set_neuron()

Set neuron, its parameters and initial variables.

Parameters

model	type as string of intance of the model
param_space	dict with model parameters
var_space	dict with model variables

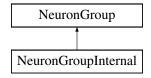
```
20.78.3.14 size()
def pygenn.genn_groups.NeuronGroup.size (
              self )
20.78.4 Member Data Documentation
20.78.4.1 is_spike_source_array
pygenn.genn_groups.NeuronGroup.is_spike_source_array
20.78.4.2 neuron
pygenn.genn_groups.NeuronGroup.neuron
20.78.4.3 pop
pygenn.genn_groups.NeuronGroup.pop
20.78.4.4 spike_count
pygenn.genn_groups.NeuronGroup.spike_count
20.78.4.5 spike_que_ptr
pygenn.genn_groups.NeuronGroup.spike_que_ptr
20.78.4.6 spikes
pygenn.genn_groups.NeuronGroup.spikes
20.78.4.7 type
pygenn.genn_groups.NeuronGroup.type
The documentation for this class was generated from the following file:
```

• genn_groups.py

20.79 NeuronGroup Class Reference

#include <neuronGroup.h>

Inheritance diagram for NeuronGroup:



Classes

struct SpikeEventThreshold

Structure used for storing spike event data.

Public Member Functions

- NeuronGroup (const NeuronGroup &)=delete
- NeuronGroup ()=delete
- void setSpikeLocation (VarLocation loc)

Set location of this neuron group's output spikes.

void setSpikeEventLocation (VarLocation loc)

Set location of this neuron group's output spike events.

void setSpikeTimeLocation (VarLocation loc)

Set location of this neuron group's output spike times.

void setVarLocation (const std::string &varName, VarLocation loc)

Set variable location of neuron model state variable.

• void setExtraGlobalParamLocation (const std::string ¶mName, VarLocation loc)

Set location of neuron model extra global parameter.

- const std::string & getName () const
- unsigned int getNumNeurons () const

Gets number of neurons in group.

const NeuronModels::Base * getNeuronModel () const

Gets the neuron model used by this group.

- const std::vector< double > & getParams () const
- const std::vector< Models::VarInit > & getVarInitialisers () const
- bool isSpikeTimeRequired () const
- bool isTrueSpikeRequired () const
- bool isSpikeEventRequired () const
- unsigned int getNumDelaySlots () const
- bool isDelayRequired () const
- bool isZeroCopyEnabled () const
- VarLocation getSpikeLocation () const

Get location of this neuron group's output spikes.

VarLocation getSpikeEventLocation () const

Get location of this neuron group's output spike events.

VarLocation getSpikeTimeLocation () const

Get location of this neuron group's output spike times.

VarLocation getVarLocation (const std::string &varName) const

Get location of neuron model state variable by name.

VarLocation getVarLocation (size_t index) const

Get location of neuron model state variable by index.

VarLocation getExtraGlobalParamLocation (const std::string ¶mName) const

Get location of neuron model extra global parameter by name.

VarLocation getExtraGlobalParamLocation (size_t index) const

Get location of neuron model extra global parameter by omdex.

• bool isSimRNGRequired () const

Does this neuron group require an RNG to simulate?

bool isInitRNGRequired () const

Does this neuron group require an RNG for it's init code?

Protected Member Functions

- NeuronGroup (const std::string &name, int numNeurons, const NeuronModels::Base *neuronModel, const std::vector< double > ¶ms, const std::vector< Models::VarInit > &varInitialisers, VarLocation default VarLocation, VarLocation defaultExtraGlobalParamLocation)
- · void checkNumDelaySlots (unsigned int requiredDelay)

Checks delay slots currently provided by the neuron group against a required delay and extends if required.

void updatePreVarQueues (const std::string &code)

Update which presynaptic variables require queues based on piece of code.

void updatePostVarQueues (const std::string &code)

Update which postsynaptic variables require queues based on piece of code.

- void addSpkEventCondition (const std::string &code, SynapseGroupInternal *synapseGroup)
- void addlnSyn (SynapseGroupInternal *synapseGroup)
- void addOutSyn (SynapseGroupInternal *synapseGroup)
- void initDerivedParams (double dt)
- void mergeIncomingPSM (bool merge)

Merge incoming postsynaptic models.

void injectCurrent (CurrentSourceInternal *source)

add input current source

const std::vector< SynapseGroupInternal * > & getInSyn () const

Gets pointers to all synapse groups which provide input to this neuron group.

- const std::vector< std::pair< SynapseGroupInternal *, std::vector< SynapseGroupInternal *>>> & get
 MergedInSyn () const
- const std::vector< SynapseGroupInternal * > & getOutSyn () const

Gets pointers to all synapse groups emanating from this neuron group.

const std::vector< CurrentSourceInternal * > & getCurrentSources () const

Gets pointers to all current sources which provide input to this neuron group.

- const std::vector< double > & getDerivedParams () const
- const std::set< SpikeEventThreshold > & getSpikeEventCondition () const
- std::vector< SynapseGroupInternal * > getInSynWithPostCode () const

Helper to get vector of incoming synapse groups which have postsynaptic update code.

std::vector< SynapseGroupInternal * > getOutSynWithPreCode () const

Helper to get vector of outgoing synapse groups which have presynaptic update code.

std::vector< SynapseGroupInternal * > getInSynWithPostVars () const

Helper to get vector of incoming synapse groups which have postsynaptic variables.

std::vector< SynapseGroupInternal * > getOutSynWithPreVars () const

Helper to get vector of outgoing synapse groups which have presynaptic variables.

- bool isVarQueueRequired (const std::string &var) const
- bool isVarQueueRequired (size t index) const
- bool canBeMerged (const NeuronGroup &other) const

Can this neuron group be merged with other? i.e. can they be simulated using same generated code.

bool canInitBeMerged (const NeuronGroup & other) const

Can the initialisation of these neuron groups be merged together? i.e. can they be initialised using same generated code.

20.79.1 Constructor & Destructor Documentation

```
20.79.1.1 NeuronGroup() [1/3]
NeuronGroup::NeuronGroup (
             const NeuronGroup & ) [delete]
20.79.1.2 NeuronGroup() [2/3]
NeuronGroup::NeuronGroup ( ) [delete]
20.79.1.3 NeuronGroup() [3/3]
NeuronGroup::NeuronGroup (
             const std::string & name,
             int numNeurons.
             const NeuronModels::Base * neuronModel,
             const std::vector< double > & params,
             const std::vector< Models::VarInit > & varInitialisers,
             VarLocation defaultVarLocation,
             VarLocation defaultExtraGlobalParamLocation ) [inline], [protected]
20.79.2 Member Function Documentation
20.79.2.1 addlnSyn()
void NeuronGroup::addInSyn (
             SynapseGroupInternal * synapseGroup ) [inline], [protected]
20.79.2.2 addOutSyn()
void NeuronGroup::addOutSyn (
             SynapseGroupInternal * synapseGroup ) [inline], [protected]
20.79.2.3 addSpkEventCondition()
void NeuronGroup::addSpkEventCondition (
             const std::string & code,
             {\tt SynapseGroupInternal} \ * \ synapseGroup \ ) \quad [{\tt protected}]
20.79.2.4 canBeMerged()
bool NeuronGroup::canBeMerged (
             const NeuronGroup & other ) const [protected]
```

Can this neuron group be merged with other? i.e. can they be simulated using same generated code.

NOTE: this can only be called after model is finalized

20.79.2.5 canInitBeMerged()

Can the initialisation of these neuron groups be merged together? i.e. can they be initialised using same generated code.

NOTE: this can only be called after model is finalized

20.79.2.6 checkNumDelaySlots()

Checks delay slots currently provided by the neuron group against a required delay and extends if required.

20.79.2.7 getCurrentSources()

```
const std::vector<CurrentSourceInternal*>& NeuronGroup::getCurrentSources ( ) const [inline],
[protected]
```

Gets pointers to all current sources which provide input to this neuron group.

20.79.2.8 getDerivedParams()

```
const std::vector<double>& NeuronGroup::getDerivedParams ( ) const [inline], [protected]
```

20.79.2.9 getExtraGlobalParamLocation() [1/2]

Get location of neuron model extra global parameter by name.

This is only used by extra global parameters which are pointers

```
20.79.2.10 getExtraGlobalParamLocation() [2/2]
```

Get location of neuron model extra global parameter by omdex.

This is only used by extra global parameters which are pointers

```
20.79.2.11 getInSyn()
```

```
const std::vector<SynapseGroupInternal*>& NeuronGroup::getInSyn ( ) const [inline], [protected]
```

Gets pointers to all synapse groups which provide input to this neuron group.

20.79.2.12 getInSynWithPostCode()

```
std::vector< SynapseGroupInternal * > NeuronGroup::getInSynWithPostCode ( ) const [protected]
```

Helper to get vector of incoming synapse groups which have postsynaptic update code.

```
20.79.2.13 getInSynWithPostVars()
std::vector< SynapseGroupInternal * > NeuronGroup::getInSynWithPostVars ( ) const [protected]
Helper to get vector of incoming synapse groups which have postsynaptic variables.
20.79.2.14 getMergedInSyn()
\verb|const| std::vector < std::pair < Synapse Group Internal*, std::vector < Synapse Group Internal*> > > \& Const| Synapse Group Internal*> > > & Const| Synapse Group Internal*> > > & Const| Synapse Group Internal*> & Const| Synapse Gr
NeuronGroup::getMergedInSyn ( ) const [inline], [protected]
20.79.2.15 getName()
const std::string& NeuronGroup::getName ( ) const [inline]
20.79.2.16 getNeuronModel()
const NeuronModels::Base* NeuronGroup::getNeuronModel ( ) const [inline]
Gets the neuron model used by this group.
20.79.2.17 getNumDelaySlots()
unsigned int NeuronGroup::getNumDelaySlots ( ) const [inline]
20.79.2.18 getNumNeurons()
unsigned int NeuronGroup::getNumNeurons ( ) const [inline]
Gets number of neurons in group.
20.79.2.19 getOutSyn()
\verb|const| std::vector<|SynapseGroupInternal*| > \& NeuronGroup::getOutSyn () const [inline], [protected]|
Gets pointers to all synapse groups emanating from this neuron group.
20.79.2.20 getOutSynWithPreCode()
std::vector< SynapseGroupInternal * > NeuronGroup::getOutSynWithPreCode ( ) const [protected]
Helper to get vector of outgoing synapse groups which have presynaptic update code.
20.79.2.21 getOutSynWithPreVars()
std::vector< SynapseGroupInternal * > NeuronGroup::getOutSynWithPreVars ( ) const [protected]
```

Helper to get vector of outgoing synapse groups which have presynaptic variables.

```
20.79.2.22 getParams()
const std::vector<double>& NeuronGroup::getParams ( ) const [inline]
20.79.2.23 getSpikeEventCondition()
const std::set<SpikeEventThreshold>& NeuronGroup::getSpikeEventCondition ( ) const [inline],
[protected]
20.79.2.24 getSpikeEventLocation()
VarLocation NeuronGroup::getSpikeEventLocation ( ) const [inline]
Get location of this neuron group's output spike events.
20.79.2.25 getSpikeLocation()
VarLocation NeuronGroup::getSpikeLocation ( ) const [inline]
Get location of this neuron group's output spikes.
20.79.2.26 getSpikeTimeLocation()
VarLocation NeuronGroup::getSpikeTimeLocation ( ) const [inline]
Get location of this neuron group's output spike times.
20.79.2.27 getVarInitialisers()
const std::vector<Models::VarInit>& NeuronGroup::getVarInitialisers ( ) const [inline]
20.79.2.28 getVarLocation() [1/2]
VarLocation NeuronGroup::getVarLocation (
              const std::string & varName ) const
Get location of neuron model state variable by name.
20.79.2.29 getVarLocation() [2/2]
VarLocation NeuronGroup::getVarLocation (
              size_t index ) const [inline]
Get location of neuron model state variable by index.
20.79.2.30 initDerivedParams()
void NeuronGroup::initDerivedParams (
              double dt ) [protected]
```

```
20.79.2.31 injectCurrent()
void NeuronGroup::injectCurrent (
              CurrentSourceInternal * source ) [protected]
add input current source
20.79.2.32 isDelayRequired()
bool NeuronGroup::isDelayRequired ( ) const [inline]
20.79.2.33 isInitRNGRequired()
bool NeuronGroup::isInitRNGRequired ( ) const
Does this neuron group require an RNG for it's init code?
20.79.2.34 isSimRNGRequired()
bool NeuronGroup::isSimRNGRequired ( ) const
Does this neuron group require an RNG to simulate?
20.79.2.35 isSpikeEventRequired()
bool NeuronGroup::isSpikeEventRequired ( ) const
20.79.2.36 isSpikeTimeRequired()
bool NeuronGroup::isSpikeTimeRequired ( ) const
20.79.2.37 isTrueSpikeRequired()
bool NeuronGroup::isTrueSpikeRequired ( ) const
20.79.2.38 isVarQueueRequired() [1/2]
bool NeuronGroup::isVarQueueRequired (
             const std::string & var ) const [protected]
20.79.2.39 isVarQueueRequired() [2/2]
bool NeuronGroup::isVarQueueRequired (
              size_t index ) const [inline], [protected]
20.79.2.40 isZeroCopyEnabled()
bool NeuronGroup::isZeroCopyEnabled ( ) const
```

20.79.2.41 mergeIncomingPSM()

Merge incoming postsynaptic models.

20.79.2.42 setExtraGlobalParamLocation()

Set location of neuron model extra global parameter.

This is ignored for simulations on hardware with a single memory space and only applies to extra global parameters which are pointers.

20.79.2.43 setSpikeEventLocation()

Set location of this neuron group's output spike events.

This is ignored for simulations on hardware with a single memory space

20.79.2.44 setSpikeLocation()

Set location of this neuron group's output spikes.

This is ignored for simulations on hardware with a single memory space

20.79.2.45 setSpikeTimeLocation()

Set location of this neuron group's output spike times.

This is ignored for simulations on hardware with a single memory space

20.79.2.46 setVarLocation()

Set variable location of neuron model state variable.

This is ignored for simulations on hardware with a single memory space

20.79.2.47 updatePostVarQueues()

Update which postsynaptic variables require queues based on piece of code.

20.79.2.48 updatePreVarQueues()

Update which presynaptic variables require queues based on piece of code.

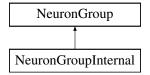
The documentation for this class was generated from the following files:

- · neuronGroup.h
- · neuronGroup.cc

20.80 NeuronGroupInternal Class Reference

```
#include <neuronGroupInternal.h>
```

Inheritance diagram for NeuronGroupInternal:



Public Member Functions

NeuronGroupInternal (const std::string &name, int numNeurons, const NeuronModels::Base *neuronModel, const std::vector< double > ¶ms, const std::vector< Models::VarInit > &varInitialisers, VarLocation defaultVarLocation, VarLocation defaultExtraGlobalParamLocation)

Additional Inherited Members

20.80.1 Constructor & Destructor Documentation

20.80.1.1 NeuronGroupInternal()

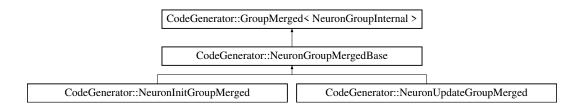
The documentation for this class was generated from the following file:

· neuronGroupInternal.h

20.81 CodeGenerator::NeuronGroupMergedBase Class Reference

```
#include <groupMerged.h>
```

 $Inheritance\ diagram\ for\ CodeGenerator:: NeuronGroupMergedBase:$



Public Member Functions

bool isParamHeterogeneous (size t index) const

Should the parameter be implemented heterogeneously?

• bool isDerivedParamHeterogeneous (size_t index) const

Should the derived parameter be implemented heterogeneously?

• bool isVarInitParamHeterogeneous (size_t varIndex, size_t paramIndex) const

Should the var init parameter be implemented heterogeneously?

bool isVarInitDerivedParamHeterogeneous (size t varIndex, size t paramIndex) const

Should the var init derived parameter be implemented heterogeneously?

bool isCurrentSourceParamHeterogeneous (size_t childIndex, size_t paramIndex) const

Should the current source parameter be implemented heterogeneously?

• bool isCurrentSourceDerivedParamHeterogeneous (size_t childIndex, size_t paramIndex) const

Should the current source derived parameter be implemented heterogeneously?

bool isCurrentSourceVarInitParamHeterogeneous (size_t childIndex, size_t varIndex, size_t paramIndex)
const

Should the current source var init parameter be implemented heterogeneously?

bool isCurrentSourceVarInitDerivedParamHeterogeneous (size_t childIndex, size_t varIndex, size_t param
 — Index) const

Should the current source var init derived parameter be implemented heterogeneously?

• bool isPSMParamHeterogeneous (size_t childIndex, size_t paramIndex) const

Should the postsynaptic model parameter be implemented heterogeneously?

• bool isPSMDerivedParamHeterogeneous (size_t childIndex, size_t varIndex) const

Should the postsynaptic model derived parameter be implemented heterogeneously?

• bool isPSMGlobalVarHeterogeneous (size_t childIndex, size_t paramIndex) const

Should the GLOBALG postsynaptic model variable be implemented heterogeneously?

bool isPSMVarInitParamHeterogeneous (size_t childIndex, size_t varIndex, size_t paramIndex) const

Should the postsynaptic model var init parameter be implemented heterogeneously?

 $\bullet \ \ bool\ is PSMV ar In it Derived Param Heterogeneous\ (size_t\ child Index,\ size_t\ var Index,\ size_t\ param Index)\ const$

Should the postsynaptic model var init derived parameter be implemented heterogeneously?

Protected Member Functions

- NeuronGroupMergedBase (size_t index, bool init, const std::vector< std::reference_wrapper< const NeuronGroupInternal >> &groups)
- void generate (MergedStructGenerator < NeuronGroupMergedBase > &gen, const BackendBase &backend, const std::string &precision, const std::string &timePrecision, bool init) const
- template<typename T, typename G, typename C>
 void orderNeuronGroupChildren (const std::vector< T > &archetypeChildren, std::vector< T >> &sortedGroupChildren, G getVectorFunc, C isCompatibleFunc) const
- template<typename T, typename G, typename C >
 void orderNeuronGroupChildren (std::vector< std::vector< T >> &sortedGroupChildren, G getVectorFunc,
 C isCompatibleFunc) const

- template<typename T = NeuronGroupMergedBase, typename H, typename V >
 void addHeterogeneousChildParams (MergedStructGenerator< NeuronGroupMergedBase > &gen, const
 Snippet::Base::StringVec ¶mNames, size_t childIndex, const std::string &prefix, H isChildParam
 HeterogeneousFn, V getValueFn) const
- template<typename T = NeuronGroupMergedBase, typename H, typename V > void addHeterogeneousChildDerivedParams (MergedStructGenerator< NeuronGroupMergedBase > &gen, const Snippet::Base::DerivedParamVec &derivedParams, size_t childIndex, const std::string &prefix, H is
 ChildDerivedParamHeterogeneousFn, V getValueFn) const
- template<typename T = NeuronGroupMergedBase, typename H, typename V > void addHeterogeneousChildVarInitParams (MergedStructGenerator< NeuronGroupMergedBase > &gen, const Snippet::Base::StringVec ¶mNames, size_t childIndex, size_t varIndex, const std::string &prefix, H isChildParamHeterogeneousFn, V getVarInitialiserFn) const
- template<typename T = NeuronGroupMergedBase, typename H, typename V > void addHeterogeneousChildVarInitDerivedParams (MergedStructGenerator< NeuronGroupMergedBase > &gen, const Snippet::Base::DerivedParamVec &derivedParams, size_t childIndex, size_t varIndex, const std::string &prefix, H isChildDerivedParamHeterogeneousFn, V getVarInitialiserFn) const
- template<typename S >
 void addChildEGPs (MergedStructGenerator< NeuronGroupMergedBase > &gen, const std::vector
 Snippet::Base::EGP > &egps, size_t childIndex, const std::string &arrayPrefix, const std::string &prefix, S getEGPSuffixFn) const

Additional Inherited Members

20.81.1 Constructor & Destructor Documentation

20.81.1.1 NeuronGroupMergedBase()

20.81.2 Member Function Documentation

20.81.2.1 addChildEGPs()

20.81.2.2 addHeterogeneousChildDerivedParams()

20.81.2.3 addHeterogeneousChildParams()

20.81.2.4 addHeterogeneousChildVarInitDerivedParams()

20.81.2.5 addHeterogeneousChildVarInitParams()

20.81.2.6 addMergedInSynPointerField()

20.81.2.7 generate()

20.81.2.8 isChildParamValueHeterogeneous()

20.81.2.9 isCurrentSourceDerivedParamHeterogeneous()

Should the current source derived parameter be implemented heterogeneously?

20.81.2.10 isCurrentSourceParamHeterogeneous()

Should the current source parameter be implemented heterogeneously?

20.81.2.11 isCurrentSourceVarInitDerivedParamHeterogeneous()

Should the current source var init derived parameter be implemented heterogeneously?

20.81.2.12 isCurrentSourceVarInitParamHeterogeneous()

Should the current source var init parameter be implemented heterogeneously?

20.81.2.13 isDerivedParamHeterogeneous()

```
bool CodeGenerator::NeuronGroupMergedBase::isDerivedParamHeterogeneous ( {\tt size\_t~index~)~const}
```

Should the derived parameter be implemented heterogeneously?

20.81.2.14 isParamHeterogeneous()

```
\label{lem:bool} \begin{tabular}{ll} bool & CodeGenerator:: NeuronGroupMergedBase:: is ParamHeterogeneous ( \\ & size\_t & index ) & const \\ \end{tabular}
```

Should the parameter be implemented heterogeneously?

20.81.2.15 isPSMDerivedParamHeterogeneous()

Should the postsynaptic model derived parameter be implemented heterogeneously?

20.81.2.16 isPSMGlobalVarHeterogeneous()

Should the GLOBALG postsynaptic model variable be implemented heterogeneously?

20.81.2.17 isPSMParamHeterogeneous()

Should the postsynaptic model parameter be implemented heterogeneously?

20.81.2.18 isPSMVarInitDerivedParamHeterogeneous()

Should the postsynaptic model var init derived parameter be implemented heterogeneously?

20.81.2.19 isPSMVarInitParamHeterogeneous()

```
size_t paramIndex ) const
```

Should the postsynaptic model var init parameter be implemented heterogeneously?

20.81.2.20 isVarInitDerivedParamHeterogeneous()

Should the var init derived parameter be implemented heterogeneously?

20.81.2.21 isVarInitParamHeterogeneous()

Should the var init parameter be implemented heterogeneously?

20.81.2.22 orderNeuronGroupChildren() [1/2]

20.81.2.23 orderNeuronGroupChildren() [2/2]

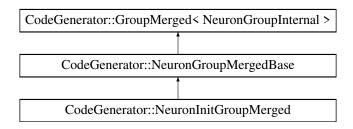
The documentation for this class was generated from the following files:

- · groupMerged.h
- groupMerged.cc

20.82 CodeGenerator::NeuronInitGroupMerged Class Reference

```
#include <groupMerged.h>
```

Inheritance diagram for CodeGenerator::NeuronInitGroupMerged:



Public Member Functions

- NeuronInitGroupMerged (size_t index, const std::vector< std::reference_wrapper< const NeuronGroup← Internal >> &groups)
- bool isInSynWUMVarInitParamHeterogeneous (size_t childIndex, size_t varIndex, size_t paramIndex) const Should the incoming synapse weight update model var init parameter be implemented heterogeneously?
- bool isInSynWUMVarInitDerivedParamHeterogeneous (size_t childIndex, size_t varIndex, size_t paramIndex)

Should the incoming synapse weight update model var init derived parameter be implemented heterogeneously?

- bool isOutSynWUMVarInitParamHeterogeneous (size_t childIndex, size_t varIndex, size_t paramIndex) const Should the outgoing synapse weight update model var init parameter be implemented heterogeneously?

Should the outgoing synapse weight update model var init derived parameter be implemented heterogeneously?

void generate (const BackendBase &backend, CodeStream &definitionsInternal, CodeStream &definitions
 — InternalFunc, CodeStream &definitionsInternalVar, CodeStream &runnerVarDecl, CodeStream &runner
 — MergedStructAlloc, MergedStructData &mergedStructData, const std::string &precision, const std::string &timePrecision) const

Additional Inherited Members

20.82.1 Constructor & Destructor Documentation

20.82.1.1 NeuronInitGroupMerged()

20.82.2 Member Function Documentation

20.82.2.1 generate()

```
const std::string & precision,
const std::string & timePrecision ) const
```

20.82.2.2 isInSynWUMVarInitDerivedParamHeterogeneous()

Should the incoming synapse weight update model var init derived parameter be implemented heterogeneously?

20.82.2.3 isInSynWUMVarInitParamHeterogeneous()

Should the incoming synapse weight update model var init parameter be implemented heterogeneously?

20.82.2.4 isOutSynWUMVarInitDerivedParamHeterogeneous()

Should the outgoing synapse weight update model var init derived parameter be implemented heterogeneously?

20.82.2.5 isOutSynWUMVarInitParamHeterogeneous()

Should the outgoing synapse weight update model var init parameter be implemented heterogeneously?

The documentation for this class was generated from the following files:

- groupMerged.h
- · groupMerged.cc

20.83 CodeGenerator::NeuronSpikeQueueUpdateGroupMerged Class Reference

```
#include <groupMerged.h>
```

Inheritance diagram for CodeGenerator::NeuronSpikeQueueUpdateGroupMerged:

```
CodeGenerator::GroupMerged < NeuronGroupInternal >

CodeGenerator::NeuronSpikeQueueUpdateGroupMerged
```

Public Member Functions

- NeuronSpikeQueueUpdateGroupMerged (size_t index, const std::vector< std::reference_wrapper< const NeuronGroupInternal >> &groups)
- void generate (const BackendBase &backend, CodeStream &definitionsInternal, CodeStream &definitions
 — InternalFunc, CodeStream &definitionsInternalVar, CodeStream &runnerVarDecl, CodeStream &runner
 — MergedStructAlloc, MergedStructData &mergedStructData, const std::string &precision) const
- void genMergedGroupSpikeCountReset (CodeStream &os) const

Additional Inherited Members

20.83.1 Constructor & Destructor Documentation

20.83.1.1 NeuronSpikeQueueUpdateGroupMerged()

20.83.2 Member Function Documentation

20.83.2.1 generate()

20.83.2.2 genMergedGroupSpikeCountReset()

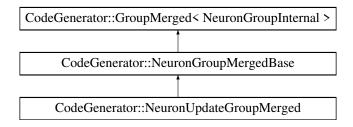
The documentation for this class was generated from the following files:

- · groupMerged.h
- groupMerged.cc

20.84 CodeGenerator::NeuronUpdateGroupMerged Class Reference

```
#include <groupMerged.h>
```

 $Inheritance\ diagram\ for\ CodeGenerator:: Neuron Update Group Merged:$



Public Member Functions

- NeuronUpdateGroupMerged (size_t index, const std::vector< std::reference_wrapper< const Neuron← GroupInternal >> &groups)
- std::string getCurrentQueueOffset () const

Get the expression to calculate the queue offset for accessing state of variables this timestep.

· std::string getPrevQueueOffset () const

Get the expression to calculate the queue offset for accessing state of variables in previous timestep.

• bool isInSynWUMParamHeterogeneous (size_t childIndex, size_t paramIndex) const

Should the incoming synapse weight update model parameter be implemented heterogeneously?

bool isInSynWUMDerivedParamHeterogeneous (size t childIndex, size t paramIndex) const

Should the incoming synapse weight update model derived parameter be implemented heterogeneously?

• bool isOutSynWUMParamHeterogeneous (size_t childIndex, size_t paramIndex) const

Should the outgoing synapse weight update model parameter be implemented heterogeneously?

bool isOutSynWUMDerivedParamHeterogeneous (size_t childIndex, size_t paramIndex) const

Should the outgoing synapse weight update model derived parameter be implemented heterogeneously?

void generate (const BackendBase &backend, CodeStream &definitionsInternal, CodeStream &definitions
 — InternalFunc, CodeStream &definitionsInternalVar, CodeStream &runnerVarDecl, CodeStream &runner
 — MergedStructAlloc, MergedStructData &mergedStructData, const std::string &precision, const std::string &timePrecision) const

Additional Inherited Members

20.84.1 Constructor & Destructor Documentation

20.84.1.1 NeuronUpdateGroupMerged()

20.84.2 Member Function Documentation

20.84.2.1 generate()

```
CodeStream & runnerMergedStructAlloc,
MergedStructData & mergedStructData,
const std::string & precision,
const std::string & timePrecision ) const
```

20.84.2.2 getCurrentQueueOffset()

```
\verb|std::string| CodeGenerator::NeuronUpdateGroupMerged::getCurrentQueueOffset () | constant of the constant o
```

Get the expression to calculate the queue offset for accessing state of variables this timestep.

20.84.2.3 getPrevQueueOffset()

```
\verb|std::string| CodeGenerator::NeuronUpdateGroupMerged::getPrevQueueOffset () constitutions and the string of the
```

Get the expression to calculate the queue offset for accessing state of variables in previous timestep.

20.84.2.4 isInSynWUMDerivedParamHeterogeneous()

Should the incoming synapse weight update model derived parameter be implemented heterogeneously?

20.84.2.5 isInSynWUMParamHeterogeneous()

Should the incoming synapse weight update model parameter be implemented heterogeneously?

20.84.2.6 isOutSynWUMDerivedParamHeterogeneous()

Should the outgoing synapse weight update model derived parameter be implemented heterogeneously?

20.84.2.7 isOutSynWUMParamHeterogeneous()

Should the outgoing synapse weight update model parameter be implemented heterogeneously?

The documentation for this class was generated from the following files:

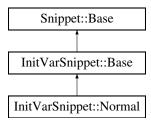
- groupMerged.h
- groupMerged.cc

20.85 InitVarSnippet::Normal Class Reference

Initialises variable by sampling from the normal distribution.

```
#include <initVarSnippet.h>
```

Inheritance diagram for InitVarSnippet::Normal:



Public Member Functions

- DECLARE SNIPPET (InitVarSnippet::Normal, 2)
- SET_CODE ("\$(value) = \$(mean) + (\$(gennrand_normal) * \$(sd));")
- virtual StringVec getParamNames () const override

Gets names of of (independent) model parameters.

Additional Inherited Members

20.85.1 Detailed Description

Initialises variable by sampling from the normal distribution.

This snippet takes 2 parameters:

- mean The mean
- sd The standard deviation

20.85.2 Member Function Documentation

20.85.2.1 DECLARE_SNIPPET()

20.85.2.2 getParamNames()

```
virtual StringVec InitVarSnippet::Normal::getParamNames ( ) const [inline], [override], [virtual]
```

Gets names of of (independent) model parameters.

Reimplemented from Snippet::Base.

20.85.2.3 SET_CODE()

```
InitVarSnippet::Normal::SET_CODE ( )
```

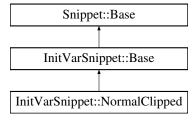
The documentation for this class was generated from the following file:

· initVarSnippet.h

20.86 InitVarSnippet::NormalClipped Class Reference

```
#include <initVarSnippet.h>
```

Inheritance diagram for InitVarSnippet::NormalClipped:



Public Member Functions

- DECLARE SNIPPET (InitVarSnippet::NormalClipped, 4)
- SET_CODE ("scalar normal;\ "do\" "{\" " normal=\$(mean)+(\$(gennrand_normal) *\$(sd));\" "} while(normal > \$(max)||normal < \$(min));\" "\$(value)=normal;\")
- virtual StringVec getParamNames () const override

Gets names of of (independent) model parameters.

Additional Inherited Members

20.86.1 Detailed Description

Initialises variable by sampling from the normal distribution, Resamples value if out of range specified my min and max

This snippet takes 2 parameters:

- mean The mean
- sd The standard deviation
- min The minimum value
- max The maximum value

20.86.2 Member Function Documentation

20.86.2.1 DECLARE_SNIPPET()

20.86.2.2 getParamNames()

virtual StringVec InitVarSnippet::NormalClipped::getParamNames () const [inline], [override],
[virtual]

Gets names of of (independent) model parameters.

Reimplemented from Snippet::Base.

20.86.2.3 SET_CODE()

```
InitVarSnippet::NormalClipped::SET_CODE ( )
```

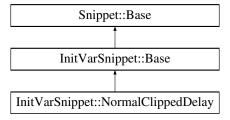
The documentation for this class was generated from the following file:

· initVarSnippet.h

20.87 InitVarSnippet::NormalClippedDelay Class Reference

```
#include <initVarSnippet.h>
```

Inheritance diagram for InitVarSnippet::NormalClippedDelay:



Public Member Functions

- DECLARE_SNIPPET (InitVarSnippet::NormalClippedDelay, 4)
- SET_CODE ("scalar normal;\ "do\" "{\" " normal=\$(meanTimestep)+(\$(gennrand_normal) *\$(sdTimestep));\" "} while(normal > \$(maxTimestep)||normal < \$(minTimestep));\" "\$(value)=rint(normal);\")
- virtual StringVec getParamNames () const override

Gets names of of (independent) model parameters.

virtual DerivedParamVec getDerivedParams () const override

Additional Inherited Members

20.87.1 Detailed Description

Initialises variable by sampling from the normal distribution, Resamples value of out of range specified my min and max. This snippet is intended for initializing (dendritic) delay parameters where parameters are specified in ms but converted to timesteps.

This snippet takes 2 parameters:

- mean The mean [ms]
- sd The standard deviation [ms]
- min The minimum value [ms]
- max The maximum value [ms]

20.87.2 Member Function Documentation

20.87.2.1 DECLARE_SNIPPET()

20.87.2.2 getDerivedParams()

```
virtual DerivedParamVec InitVarSnippet::NormalClippedDelay::getDerivedParams ( ) const [inline],
[override], [virtual]
```

Gets names of derived model parameters and the function objects to call to Calculate their value from a vector of model parameter values

Reimplemented from Snippet::Base.

20.87.2.3 getParamNames()

```
virtual StringVec InitVarSnippet::NormalClippedDelay::getParamNames ( ) const [inline], [override],
[virtual]
```

Gets names of of (independent) model parameters.

Reimplemented from Snippet::Base.

20.87.2.4 SET_CODE()

```
InitVarSnippet::NormalClippedDelay::SET_CODE ( )
```

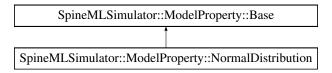
The documentation for this class was generated from the following file:

· initVarSnippet.h

20.88 SpineMLSimulator::ModelProperty::NormalDistribution Class Reference

```
#include <modelProperty.h>
```

Inheritance diagram for SpineMLSimulator::ModelProperty::NormalDistribution:



Public Member Functions

- NormalDistribution (const pugi::xml_node &node, const StateVar < scalar > &stateVar, unsigned int size)
- void setValue (scalar mean, scalar variance)

20.88.1 Constructor & Destructor Documentation

20.88.1.1 NormalDistribution()

20.88.2 Member Function Documentation

20.88.2.1 setValue()

The documentation for this class was generated from the following files:

- modelProperty.h
- modelProperty.cc

20.89 CodeGenerator::CodeStream::OB Struct Reference

An open bracket marker.

```
#include <codeStream.h>
```

Public Member Functions

• OB (unsigned int level)

Public Attributes

• const unsigned int Level

20.89.1 Detailed Description

An open bracket marker.

Write to code stream os using:

```
os << OB(16);
```

20.89.2 Constructor & Destructor Documentation

20.89.2.1 OB()

20.89.3 Member Data Documentation

20.89.3.1 Level

```
const unsigned int CodeGenerator::CodeStream::OB::Level
```

The documentation for this struct was generated from the following file:

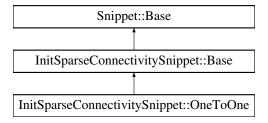
· codeStream.h

20.90 InitSparseConnectivitySnippet::OneToOne Class Reference

Initialises connectivity to a 'one-to-one' diagonal matrix.

```
#include <initSparseConnectivitySnippet.h>
```

Inheritance diagram for InitSparseConnectivitySnippet::OneToOne:



Public Member Functions

- DECLARE SNIPPET (InitSparseConnectivitySnippet::OneToOne, 0)
- SET_ROW_BUILD_CODE ("\$(addSynapse, \$(id_pre));\ "\$(endRow);\")
- SET_MAX_ROW_LENGTH (1)
- SET_MAX_COL_LENGTH (1)

Additional Inherited Members

20.90.1 Detailed Description

Initialises connectivity to a 'one-to-one' diagonal matrix.

20.90.2 Member Function Documentation

20.90.2.1 DECLARE_SNIPPET()

20.90.2.2 SET_MAX_COL_LENGTH()

20.90.2.3 SET_MAX_ROW_LENGTH()

20.90.2.4 SET_ROW_BUILD_CODE()

The documentation for this class was generated from the following file:

· initSparseConnectivitySnippet.h

20.91 Snippet::Base::ParamVal Struct Reference

Additional input variables, row state variables and other things have a name, a type and an initial value.

```
#include <snippet.h>
```

Public Member Functions

- ParamVal (const std::string &n, const std::string &t, const std::string &v)
- ParamVal (const std::string &n, const std::string &t, double v)
- ParamVal ()
- bool operator== (const ParamVal &other) const

Public Attributes

- std::string name
- · std::string type
- std::string value

20.91.1 Detailed Description

Additional input variables, row state variables and other things have a name, a type and an initial value.

20.91.2 Constructor & Destructor Documentation

20.91.2.1 ParamVal() [1/3]

```
20.91.2.2 ParamVal() [2/3]
Snippet::Base::ParamVal::ParamVal (
             const std::string & n,
             const std::string & t,
             double v ) [inline]
20.91.2.3 ParamVal() [3/3]
Snippet::Base::ParamVal::ParamVal ( ) [inline]
20.91.3 Member Function Documentation
20.91.3.1 operator==()
bool Snippet::Base::ParamVal::operator== (
             const ParamVal & other ) const [inline]
20.91.4 Member Data Documentation
20.91.4.1 name
std::string Snippet::Base::ParamVal::name
20.91.4.2 type
std::string Snippet::Base::ParamVal::type
20.91.4.3 value
std::string Snippet::Base::ParamVal::value
```

The documentation for this struct was generated from the following file:

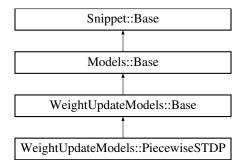
• snippet.h

20.92 WeightUpdateModels::PiecewiseSTDP Class Reference

This is a simple STDP rule including a time delay for the finite transmission speed of the synapse.

```
#include <weightUpdateModels.h>
```

 $Inheritance\ diagram\ for\ Weight Update Models:: Piecewise STDP:$



Public Member Functions

- DECLARE_WEIGHT_UPDATE_MODEL (PiecewiseSTDP, 10, 2, 0, 0)
- virtual StringVec getParamNames () const override

Gets names of of (independent) model parameters.

• virtual VarVec getVars () const override

Gets names and types (as strings) of model variables.

• virtual std::string getSimCode () const override

Gets simulation code run when 'true' spikes are received.

virtual std::string getLearnPostCode () const override

Gets code to include in the learnSynapsesPost kernel/function.

- virtual DerivedParamVec getDerivedParams () const override
- virtual bool isPreSpikeTimeRequired () const override

Whether presynaptic spike times are needed or not.

virtual bool isPostSpikeTimeRequired () const override

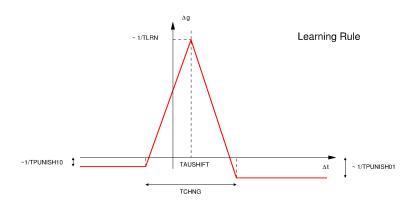
Whether postsynaptic spike times are needed or not.

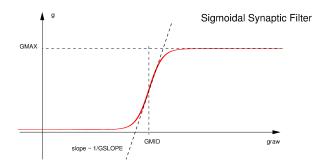
Additional Inherited Members

20.92.1 Detailed Description

This is a simple STDP rule including a time delay for the finite transmission speed of the synapse.

The STDP window is defined as a piecewise function:





The STDP curve is applied to the raw synaptic conductance gRaw, which is then filtered through the sugmoidal filter displayed above to obtain the value of g.

Note

The STDP curve implies that unpaired pre- and post-synaptic spikes incur a negative increment in gRaw (and hence in g).

The time of the last spike in each neuron, "sTXX", where XX is the name of a neuron population is (somewhat arbitrarily) initialised to -10.0 ms. If neurons never spike, these spike times are used.

It is the raw synaptic conductance gRaw that is subject to the STDP rule. The resulting synaptic conductance is a sigmoid filter of gRaw. This implies that g is initialised but not gRaw, the synapse will revert to the value that corresponds to gRaw.

An example how to use this synapse correctly is given in map_classol.cc (MBody1 userproject):

```
for (int i= 0; i < model.neuronN[1]*model.neuronN[3]; i++) {
    if (gKCDN[i] < 2.0*SCALAR_MIN) {
        cnt++;
        fprintf(stdout, "Too low conductance value %e detected and set to 2*SCALAR_MIN= %e, at index %d
    \n", gKCDN[i], 2*SCALAR_MIN, i);
        gRCDN[i] = 2.0*SCALAR_MIN; //to avoid log(0)/0 below
    }
    scalar tmp = gKCDN[i] / myKCDN_p[5]*2.0;
    gRawKCDN[i] = 0.5 * log( tmp / (2.0 - tmp)) /myKCDN_p[7] + myKCDN_p[6];
}
cerr << "Total number of low value corrections: " << cnt << endl;</pre>
```

Note

One cannot set values of g fully to 0, as this leads to gRaw= -infinity and this is not support. I.e., 'g' needs to be some nominal value > 0 (but can be extremely small so that it acts like it's 0).

The model has 2 variables:

- \bullet g: conductance of ${\tt scalar}$ type
- gRaw: raw conductance of scalar type

Parameters are (compare to the figure above):

- tLrn: Time scale of learning changes
- tChng: Width of learning window
- tDecay: Time scale of synaptic strength decay
- tPunish10: Time window of suppression in response to 1/0
- tPunish01: Time window of suppression in response to 0/1
- gMax: Maximal conductance achievable
- qMid: Midpoint of sigmoid g filter curve
- gSlope: Slope of sigmoid g filter curve
- tauShift: Shift of learning curve
- gSyn0: Value of syn conductance g decays to

20.92.2 Member Function Documentation

20.92.2.1 DECLARE_WEIGHT_UPDATE_MODEL()

20.92.2.2 getDerivedParams()

```
virtual DerivedParamVec WeightUpdateModels::PiecewiseSTDP::getDerivedParams ( ) const [inline],
[override], [virtual]
```

Gets names of derived model parameters and the function objects to call to Calculate their value from a vector of model parameter values

Reimplemented from Snippet::Base.

20.92.2.3 getLearnPostCode()

```
virtual std::string WeightUpdateModels::PiecewiseSTDP::getLearnPostCode ( ) const [inline],
[override], [virtual]
```

Gets code to include in the learnSynapsesPost kernel/function.

For examples when modelling STDP, this is where the effect of postsynaptic spikes which occur *after* presynaptic spikes are applied.

Reimplemented from WeightUpdateModels::Base.

20.92.2.4 getParamNames()

virtual StringVec WeightUpdateModels::PiecewiseSTDP::getParamNames () const [inline], [override],
[virtual]

Gets names of of (independent) model parameters.

Reimplemented from Snippet::Base.

20.92.2.5 getSimCode()

virtual std::string WeightUpdateModels::PiecewiseSTDP::getSimCode () const [inline], [override],
[virtual]

Gets simulation code run when 'true' spikes are received.

Reimplemented from WeightUpdateModels::Base.

20.92.2.6 getVars()

```
virtual VarVec WeightUpdateModels::PiecewiseSTDP::getVars ( ) const [inline], [override],
[virtual]
```

Gets names and types (as strings) of model variables.

Reimplemented from Models::Base.

20.92.2.7 isPostSpikeTimeRequired()

```
virtual bool WeightUpdateModels::PiecewiseSTDP::isPostSpikeTimeRequired ( ) const [inline],
[override], [virtual]
```

Whether postsynaptic spike times are needed or not.

Reimplemented from WeightUpdateModels::Base.

20.92.2.8 isPreSpikeTimeRequired()

```
virtual bool WeightUpdateModels::PiecewiseSTDP::isPreSpikeTimeRequired ( ) const [inline],
[override], [virtual]
```

Whether presynaptic spike times are needed or not.

Reimplemented from WeightUpdateModels::Base.

The documentation for this class was generated from the following file:

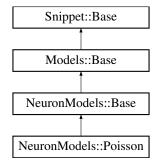
· weightUpdateModels.h

20.93 NeuronModels::Poisson Class Reference

Poisson neurons.

```
#include <neuronModels.h>
```

Inheritance diagram for NeuronModels::Poisson:



Public Types

- typedef Snippet::ValueBase< 4 > ParamValues
- typedef Models::VarInitContainerBase< 2 > VarValues
- typedef Models::VarInitContainerBase< 0 > PreVarValues
- typedef Models::VarInitContainerBase< 0 > PostVarValues

Public Member Functions

• virtual std::string getSimCode () const override

Gets the code that defines the execution of one timestep of integration of the neuron model.

virtual std::string getThresholdConditionCode () const override

Gets code which defines the condition for a true spike in the described neuron model.

· virtual StringVec getParamNames () const override

Gets names of of (independent) model parameters.

• virtual VarVec getVars () const override

Gets names and types (as strings) of model variables.

virtual EGPVec getExtraGlobalParams () const override

Static Public Member Functions

static const NeuronModels::Poisson * getInstance ()

Additional Inherited Members

20.93.1 Detailed Description

Poisson neurons.

Poisson neurons have constant membrane potential (Vrest) unless they are activated randomly to the Vspike value if (t- SpikeTime) > trefract.

It has 2 variables:

- V Membrane potential (mV)
- SpikeTime Time at which the neuron spiked for the last time (ms)

and 4 parameters:

- trefract Refractory period (ms)
- tspike duration of spike (ms)

- Vspike Membrane potential at spike (mV)
- Vrest Membrane potential at rest (mV)

Note

The initial values array for the Poisson type needs two entries for V, and SpikeTime and the parameter array needs four entries for therate, trefract, Vspike and Vrest, in that order.

This model uses a linear approximation for the probability of firing a spike in a given time step of size DT, i.e. the probability of firing is λ times DT: $p = \lambda \Delta t$. This approximation is usually very good, especially for typical, quite small time steps and moderate firing rates. However, it is worth noting that the approximation becomes poor for very high firing rates and large time steps.

20.93.2 Member Typedef Documentation

```
20.93.2.1 ParamValues
typedef Snippet::ValueBase< 4 > NeuronModels::Poisson::ParamValues
20.93.2.2 PostVarValues
typedef Models::VarInitContainerBase<0> NeuronModels::Poisson::PostVarValues
20.93.2.3 PreVarValues
typedef Models::VarInitContainerBase<0> NeuronModels::Poisson::PreVarValues
20.93.2.4 VarValues
typedef Models::VarInitContainerBase< 2 > NeuronModels::Poisson::VarValues
20.93.3 Member Function Documentation
20.93.3.1 getExtraGlobalParams()
virtual EGPVec NeuronModels::Poisson::getExtraGlobalParams ( ) const [inline], [override],
[virtual]
Gets names and types (as strings) of additional per-population parameters for the snippet
Reimplemented from Snippet::Base.
20.93.3.2 getInstance()
static const NeuronModels::Poisson* NeuronModels::Poisson::getInstance ( ) [inline], [static]
20.93.3.3 getParamNames()
virtual StringVec NeuronModels::Poisson::getParamNames ( ) const [inline], [override], [virtual]
```

Gets names of of (independent) model parameters.

Reimplemented from Snippet::Base.

20.93.3.4 getSimCode()

```
virtual std::string NeuronModels::Poisson::getSimCode ( ) const [inline], [override], [virtual]
```

Gets the code that defines the execution of one timestep of integration of the neuron model.

The code will refer to for the value of the variable with name "NN". It needs to refer to the predefined variable "ISYN", i.e. contain, if it is to receive input.

Reimplemented from NeuronModels::Base.

20.93.3.5 getThresholdConditionCode()

```
virtual std::string NeuronModels::Poisson::getThresholdConditionCode ( ) const [inline],
[override], [virtual]
```

Gets code which defines the condition for a true spike in the described neuron model.

This evaluates to a bool (e.g. "V > 20").

Reimplemented from NeuronModels::Base.

20.93.3.6 getVars()

```
virtual VarVec NeuronModels::Poisson::getVars ( ) const [inline], [override], [virtual]
```

Gets names and types (as strings) of model variables.

Reimplemented from Models::Base.

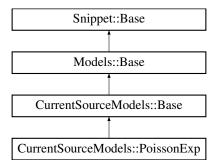
The documentation for this class was generated from the following file:

· neuronModels.h

20.94 CurrentSourceModels::PoissonExp Class Reference

```
#include <currentSourceModels.h>
```

Inheritance diagram for CurrentSourceModels::PoissonExp:



Public Types

typedef Snippet::ValueBase< 3 > ParamValues

- typedef Models::VarInitContainerBase< 1 > VarValues
- typedef Models::VarInitContainerBase< 0 > PreVarValues
- typedef Models::VarInitContainerBase< 0 > PostVarValues

Public Member Functions

- SET_INJECTION_CODE ("scalar p = 1.0f;\ "unsigned int numSpikes=0;\" "do\" "{\" " numSpikes++;\" " p *=\$(gennrand_uniform);\" "} while(p > \$(ExpMinusLambda));\" "\$(current)+=\$(Init) *(scalar)(numSpikes 1);\" "\$(injectCurrent, \$(current));\" "\$(current) *=\$(ExpDecay);\")
- · virtual StringVec getParamNames () const override

Gets names of of (independent) model parameters.

- virtual VarVec getVars () const override
 - Gets names and types (as strings) of model variables.
- virtual DerivedParamVec getDerivedParams () const override

Static Public Member Functions

static const PoissonExp * getInstance ()

Additional Inherited Members

20.94.1 Detailed Description

Current source for injecting a current equivalent to a population of Poisson spike sources, one-to-one connected with exponential synapses

It has 3 parameters:

- weight synaptic weight of the Poisson spikes [nA]
- tauSyn decay time constant [ms]
- rate mean firing rate [Hz]

20.94.2 Member Typedef Documentation

20.94.2.1 ParamValues

typedef Snippet::ValueBase< 3 > CurrentSourceModels::PoissonExp::ParamValues

20.94.2.2 PostVarValues

typedef Models::VarInitContainerBase<0> CurrentSourceModels::PoissonExp::PostVarValues

20.94.2.3 PreVarValues

typedef Models::VarInitContainerBase<0> CurrentSourceModels::PoissonExp::PreVarValues

20.94.2.4 VarValues

typedef Models::VarInitContainerBase< 1 > CurrentSourceModels::PoissonExp::VarValues

20.94.3 Member Function Documentation

20.94.3.1 getDerivedParams()

```
virtual DerivedParamVec CurrentSourceModels::PoissonExp::getDerivedParams ( ) const [inline],
[override], [virtual]
```

Gets names of derived model parameters and the function objects to call to Calculate their value from a vector of model parameter values

Reimplemented from Snippet::Base.

20.94.3.2 getInstance()

```
static const PoissonExp* CurrentSourceModels::PoissonExp::getInstance ( ) [inline], [static]
```

20.94.3.3 getParamNames()

```
virtual StringVec CurrentSourceModels::PoissonExp::getParamNames ( ) const [inline], [override],
[virtual]
```

Gets names of of (independent) model parameters.

Reimplemented from Snippet::Base.

20.94.3.4 getVars()

```
virtual VarVec CurrentSourceModels::PoissonExp::getVars ( ) const [inline], [override], [virtual]
```

Gets names and types (as strings) of model variables.

Reimplemented from Models::Base.

```
20.94.3.5 SET_INJECTION_CODE()
```

```
CurrentSourceModels::PoissonExp::SET_INJECTION_CODE ( )
```

The documentation for this class was generated from the following file:

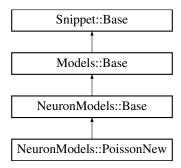
· currentSourceModels.h

20.95 NeuronModels::PoissonNew Class Reference

Poisson neurons.

```
#include <neuronModels.h>
```

Inheritance diagram for NeuronModels::PoissonNew:



Public Types

- typedef Snippet::ValueBase< 1 > ParamValues
- typedef Models::VarInitContainerBase< 1 > VarValues
- typedef Models::VarInitContainerBase< 0 > PreVarValues
- typedef Models::VarInitContainerBase< 0 > PostVarValues

Public Member Functions

• virtual std::string getSimCode () const override

Gets the code that defines the execution of one timestep of integration of the neuron model.

virtual std::string getThresholdConditionCode () const override

Gets code which defines the condition for a true spike in the described neuron model.

virtual StringVec getParamNames () const override

Gets names of of (independent) model parameters.

· virtual VarVec getVars () const override

Gets names and types (as strings) of model variables.

- virtual DerivedParamVec getDerivedParams () const override
- SET_NEEDS_AUTO_REFRACTORY (false)

Static Public Member Functions

static const NeuronModels::PoissonNew * getInstance ()

Additional Inherited Members

20.95.1 Detailed Description

Poisson neurons.

It has 1 state variable:

• timeStepToSpike - Number of timesteps to next spike

and 1 parameter:

• rate - Mean firing rate (Hz)

Note

Internally this samples from the exponential distribution using the C++11 < random > library on the CPU and by transforming the uniform distribution, generated using cuRAND, with a natural log on the GPU.

20.95.2 Member Typedef Documentation

20.95.2.1 ParamValues

typedef Snippet::ValueBase< 1 > NeuronModels::PoissonNew::ParamValues

20.95.2.2 PostVarValues

typedef Models::VarInitContainerBase<0> NeuronModels::PoissonNew::PostVarValues

20.95.2.3 PreVarValues

typedef Models::VarInitContainerBase<0> NeuronModels::PoissonNew::PreVarValues

20.95.2.4 VarValues

typedef Models::VarInitContainerBase< 1 > NeuronModels::PoissonNew::VarValues

20.95.3 Member Function Documentation

20.95.3.1 getDerivedParams()

virtual DerivedParamVec NeuronModels::PoissonNew::getDerivedParams () const [inline], [override],
[virtual]

Gets names of derived model parameters and the function objects to call to Calculate their value from a vector of model parameter values

Reimplemented from Snippet::Base.

20.95.3.2 getInstance()

static const NeuronModels::PoissonNew* NeuronModels::PoissonNew::getInstance () [inline],
[static]

20.95.3.3 getParamNames()

virtual StringVec NeuronModels::PoissonNew::getParamNames () const [inline], [override],

Gets names of of (independent) model parameters.

Reimplemented from Snippet::Base.

20.95.3.4 getSimCode()

virtual std::string NeuronModels::PoissonNew::getSimCode () const [inline], [override],
[virtual]

Gets the code that defines the execution of one timestep of integration of the neuron model.

The code will refer to for the value of the variable with name "NN". It needs to refer to the predefined variable "ISYN", i.e. contain, if it is to receive input.

Reimplemented from NeuronModels::Base.

20.95.3.5 getThresholdConditionCode()

```
virtual std::string NeuronModels::PoissonNew::getThresholdConditionCode ( ) const [inline],
[override], [virtual]
```

Gets code which defines the condition for a true spike in the described neuron model.

This evaluates to a bool (e.g. "V>20").

Reimplemented from NeuronModels::Base.

20.95.3.6 getVars()

```
virtual VarVec NeuronModels::PoissonNew::getVars ( ) const [inline], [override], [virtual]
```

Gets names and types (as strings) of model variables.

Reimplemented from Models::Base.

20.95.3.7 SET_NEEDS_AUTO_REFRACTORY()

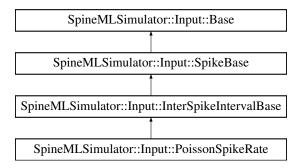
The documentation for this class was generated from the following file:

· neuronModels.h

20.96 SpineMLSimulator::Input::PoissonSpikeRate Class Reference

```
#include <input.h>
```

Inheritance diagram for SpineMLSimulator::Input::PoissonSpikeRate:



Public Member Functions

PoissonSpikeRate (double dt, const pugi::xml_node &node, std::unique_ptr< InputValue::Base > value, unsigned int popSize, unsigned int *spikeQueuePtr, unsigned int *hostSpikeCount, unsigned int *hostSpikes, PushCurrentSpikesFunc pushCurrentSpikes)

Protected Member Functions

virtual double getTimeToSpike (double isiMs)

Additional Inherited Members

20.96.1 Constructor & Destructor Documentation

20.96.1.1 PoissonSpikeRate()

20.96.2 Member Function Documentation

20.96.2.1 getTimeToSpike()

Implements SpineMLSimulator::Input::InterSpikeIntervalBase.

The documentation for this class was generated from the following files:

- · input.h
- input.cc

20.97 CodeGenerator::CUDA::PresynapticUpdateStrategy::PostSpan Class Reference

Postsynaptic parallelism.

```
#include <presynapticUpdateStrategy.h>
```

 $Inheritance\ diagram\ for\ Code Generator :: CUDA :: Presynaptic Update Strategy :: Post Span:$

```
CodeGenerator::CUDA::PresynapticUpdateStrategy::Base

CodeGenerator::CUDA::PresynapticUpdateStrategy::PostSpan
```

Public Member Functions

- virtual size_t getNumThreads (const SynapseGroupInternal &sg) const override
 Get the number of threads that presynaptic updates should be parallelised across.
- virtual size_t getSynapticMatrixRowStride (const SynapseGroupInternal &sg) const override

Gets the stride used to access synaptic matrix rows, taking into account sparse data structure, padding etc.

• virtual bool isCompatible (const SynapseGroupInternal &sg, const cudaDeviceProp &deviceProps, const Preferences &preferences) const override

Is this presynaptic update strategy compatible with a given synapse group?

 virtual size_t getSharedMemoryPerThread (const PresynapticUpdateGroupMerged &sg, const Backend &backend) const override

How many neurons does each thread accumulate the outputs of into shared memory.

- virtual void genPreamble (CodeStream &os, const ModelSpecMerged &modelMerged, const Presynaptic
 UpdateGroupMerged &sg, const Substitutions &popSubs, const Backend &backend, size_t idStart) const
 override
- virtual void genUpdate (CodeStream &os, const ModelSpecMerged &modelMerged, const Presynaptic
 UpdateGroupMerged &sg, const Substitutions &popSubs, const Backend &backend, bool trueSpike,
 size_t idStart, BackendBase::PresynapticUpdateGroupMergedHandler wumThreshHandler, Backend
 Base::PresynapticUpdateGroupMergedHandler wumSimHandler, BackendBase::PresynapticUpdateGroup
 MergedHandler wumProceduralConnectHandler) const override

Generate presynaptic update code.

virtual void genPostamble (CodeStream &os, const ModelSpecMerged &modelMerged, const Presynaptic
 UpdateGroupMerged &sg, const Substitutions &popSubs, const Backend &backend, size_t idStart) const
 override

20.97.1 Detailed Description

Postsynaptic parallelism.

20.97.2 Member Function Documentation

20.97.2.1 genPostamble()

Implements CodeGenerator::CUDA::PresynapticUpdateStrategy::Base.

20.97.2.2 genPreamble()

Implements CodeGenerator::CUDA::PresynapticUpdateStrategy::Base.

20.97.2.3 genUpdate()

Generate presynaptic update code.

Implements CodeGenerator::CUDA::PresynapticUpdateStrategy::Base.

20.97.2.4 getNumThreads()

Get the number of threads that presynaptic updates should be parallelised across.

Implements CodeGenerator::CUDA::PresynapticUpdateStrategy::Base.

20.97.2.5 getSharedMemoryPerThread()

How many neurons does each thread accumulate the outputs of into shared memory.

Implements CodeGenerator::CUDA::PresynapticUpdateStrategy::Base.

20.97.2.6 getSynapticMatrixRowStride()

Gets the stride used to access synaptic matrix rows, taking into account sparse data structure, padding etc.

 $Implements\ Code Generator :: CUDA :: Presynaptic Update Strategy :: Base.$

20.97.2.7 isCompatible()

Is this presynaptic update strategy compatible with a given synapse group?

Implements CodeGenerator::CUDA::PresynapticUpdateStrategy::Base.

The documentation for this class was generated from the following files:

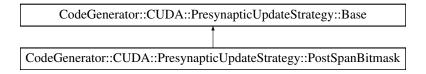
- · presynapticUpdateStrategy.h
- presynapticUpdateStrategy.cc

20.98 CodeGenerator::CUDA::PresynapticUpdateStrategy::PostSpanBitmask Class Reference

Postsynaptic parallelism.

#include presynapticUpdateStrategy.h>

Inheritance diagram for CodeGenerator::CUDA::PresynapticUpdateStrategy::PostSpanBitmask:



Public Member Functions

virtual size_t getNumThreads (const SynapseGroupInternal &sg) const override

Get the number of threads that presynaptic updates should be parallelised across.

virtual size_t getSynapticMatrixRowStride (const SynapseGroupInternal &sg) const override

Gets the stride used to access synaptic matrix rows, taking into account sparse data structure, padding etc.

• virtual bool isCompatible (const SynapseGroupInternal &sg, const cudaDeviceProp &deviceProps, const Preferences &preferences) const override

Is this presynaptic update strategy compatible with a given synapse group?

 virtual size_t getSharedMemoryPerThread (const PresynapticUpdateGroupMerged &sg, const Backend &backend) const override

How many neurons does each thread accumulate the outputs of into shared memory.

- virtual void genPreamble (CodeStream &os, const ModelSpecMerged &modelMerged, const Presynaptic
 UpdateGroupMerged &sg, const Substitutions &popSubs, const Backend &backend, size_t idStart) const
 override
- virtual void genUpdate (CodeStream &os, const ModelSpecMerged &modelMerged, const Presynaptic
 UpdateGroupMerged &sg, const Substitutions &popSubs, const Backend &backend, bool trueSpike,
 size_t idStart, BackendBase::PresynapticUpdateGroupMergedHandler wumThreshHandler, Backend
 Base::PresynapticUpdateGroupMergedHandler wumSimHandler, BackendBase::PresynapticUpdateGroup
 MergedHandler wumProceduralConnectHandler) const override

Generate presynaptic update code.

virtual void genPostamble (CodeStream &os, const ModelSpecMerged &modelMerged, const Presynaptic
 UpdateGroupMerged &sg, const Substitutions &popSubs, const Backend &backend, size_t idStart) const
 override

20.98.1 Detailed Description

Postsynaptic parallelism.

20.98.2 Member Function Documentation

20.98.2.1 genPostamble()

 $Implements\ Code Generator :: CUDA :: Presynaptic Update Strategy :: Base.$

20.98.2.2 genPreamble()

Implements CodeGenerator::CUDA::PresynapticUpdateStrategy::Base.

20.98.2.3 genUpdate()

Generate presynaptic update code.

 $Implements\ Code Generator :: CUDA :: Presynaptic Update Strategy :: Base.$

20.98.2.4 getNumThreads()

Get the number of threads that presynaptic updates should be parallelised across.

 $Implements\ Code Generator :: CUDA :: Presynaptic Update Strategy :: Base.$

20.98.2.5 getSharedMemoryPerThread()

```
const Backend & backend ) const [override], [virtual]
```

How many neurons does each thread accumulate the outputs of into shared memory.

Implements CodeGenerator::CUDA::PresynapticUpdateStrategy::Base.

20.98.2.6 getSynapticMatrixRowStride()

```
\label{lem:size_todeGenerator::CUDA::PresynapticUpdateStrategy::PostSpanBitmask::getSynapticMatrixRow \\ \\ \text{Stride (} \\ \text{const SynapseGroupInternal & } sg \text{ ) const [override], [virtual]}
```

Gets the stride used to access synaptic matrix rows, taking into account sparse data structure, padding etc.

Implements CodeGenerator::CUDA::PresynapticUpdateStrategy::Base.

20.98.2.7 isCompatible()

Is this presynaptic update strategy compatible with a given synapse group?

Implements CodeGenerator::CUDA::PresynapticUpdateStrategy::Base.

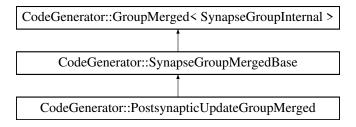
The documentation for this class was generated from the following files:

- · presynapticUpdateStrategy.h
- · presynapticUpdateStrategy.cc

20.99 CodeGenerator::PostsynapticUpdateGroupMerged Class Reference

```
#include <groupMerged.h>
```

Inheritance diagram for CodeGenerator::PostsynapticUpdateGroupMerged:



Public Member Functions

- PostsynapticUpdateGroupMerged (size_t index, const std::vector< std::reference_wrapper< const SynapseGroupInternal >> &groups)
- void generate (const BackendBase &backend, CodeStream &definitionsInternal, CodeStream &definitions
 — InternalFunc, CodeStream &definitionsInternalVar, CodeStream &runnerVarDecl, CodeStream &runner
 — MergedStructAlloc, MergedStructData &mergedStructData, const std::string &precision, const std::string &timePrecision) const

Protected Member Functions

• virtual std::string getArchetypeCode () const override

Additional Inherited Members

20.99.1 Constructor & Destructor Documentation

20.99.1.1 PostsynapticUpdateGroupMerged()

20.99.2 Member Function Documentation

20.99.2.1 generate()

20.99.2.2 getArchetypeCode()

```
virtual std::string CodeGenerator::PostsynapticUpdateGroupMerged::getArchetypeCode ( ) const
[inline], [override], [protected], [virtual]
```

Reimplemented from CodeGenerator::SynapseGroupMergedBase.

The documentation for this class was generated from the following file:

· groupMerged.h

20.100 CodeGenerator::SingleThreadedCPU::Preferences Struct Reference

```
#include <backend.h>
```

 $Inheritance\ diagram\ for\ Code Generator :: Single Threaded CPU :: Preferences:$

```
CodeGenerator::PreferencesBase

CodeGenerator::SingleThreadedCPU::Preferences
```

Additional Inherited Members

The documentation for this struct was generated from the following file:

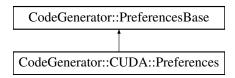
· single threaded cpu/backend.h

20.101 CodeGenerator::CUDA::Preferences Struct Reference

Preferences for CUDA backend.

#include <backend.h>

Inheritance diagram for CodeGenerator::CUDA::Preferences:



Public Member Functions

• Preferences ()

Public Attributes

• bool showPtxInfo = false

Should PTX assembler information be displayed for each CUDA kernel during compilation?

• bool generateLineInfo = false

Should line info be included in resultant executable for debugging/profiling purposes?

- bool selectGPUByDeviceID = false
- DeviceSelect deviceSelectMethod = DeviceSelect::OPTIMAL

How to select GPU device.

• unsigned int manualDeviceID = 0

If device select method is set to DeviceSelect::MANUAL, id of device to use.

BlockSizeSelect blockSizeSelectMethod = BlockSizeSelect::OCCUPANCY

How to select CUDA blocksize.

KernelBlockSize manualBlockSizes

If block size select method is set to BlockSizeSelect::MANUAL, block size to use for each kernel.

• size_t constantCacheOverhead = 72 * 4

How much constant cache is already used and therefore can't be used by GeNN?

• std::string userNvccFlags = ""

NVCC compiler options for all GPU code.

20.101.1 Detailed Description

Preferences for CUDA backend.

20.101.2 Constructor & Destructor Documentation

20.101.2.1 Preferences()

CodeGenerator::CUDA::Preferences::Preferences () [inline]

20.101.3 Member Data Documentation

20.101.3.1 blockSizeSelectMethod

BlockSizeSelect CodeGenerator::CUDA::Preferences::blockSizeSelectMethod = BlockSizeSelect::OC← CUPANCY

How to select CUDA blocksize.

20.101.3.2 constantCacheOverhead

size_t CodeGenerator::CUDA::Preferences::constantCacheOverhead = 72 * 4

How much constant cache is already used and therefore can't be used by GeNN?

Each of the four modules which includes CUDA headers(neuronUpdate, synapseUpdate, init and runner) Takes 72 bytes of constant memory for a lookup table used by cuRAND. If your application requires additional constant cache, increase this

20.101.3.3 deviceSelectMethod

DeviceSelect CodeGenerator::CUDA::Preferences::deviceSelectMethod = DeviceSelect::OPTIMAL

How to select GPU device.

20.101.3.4 generateLineInfo

bool CodeGenerator::CUDA::Preferences::generateLineInfo = false

Should line info be included in resultant executable for debugging/profiling purposes?

20.101.3.5 manualBlockSizes

KernelBlockSize CodeGenerator::CUDA::Preferences::manualBlockSizes

If block size select method is set to BlockSizeSelect::MANUAL, block size to use for each kernel.

20.101.3.6 manualDeviceID

unsigned int CodeGenerator::CUDA::Preferences::manualDeviceID = 0

If device select method is set to DeviceSelect::MANUAL, id of device to use.

20.101.3.7 selectGPUByDeviceID

bool CodeGenerator::CUDA::Preferences::selectGPUByDeviceID = false

GeNN normally identifies devices by PCI bus ID to ensure that the model is run on the same device it was optimized for. However if, for example, you are running on a cluser with NVML this is not desired behaviour.

20.101.3.8 showPtxInfo

```
bool CodeGenerator::CUDA::Preferences::showPtxInfo = false
```

Should PTX assembler information be displayed for each CUDA kernel during compilation?

20.101.3.9 userNvccFlags

```
std::string CodeGenerator::CUDA::Preferences::userNvccFlags = ""
```

NVCC compiler options for all GPU code.

The documentation for this struct was generated from the following file:

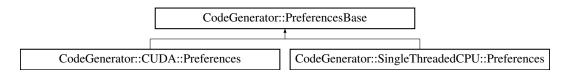
· cuda/backend.h

20.102 CodeGenerator::PreferencesBase Struct Reference

Base class for backend preferences - can be accessed via a global in 'classic' C++ code generator.

```
#include <backendBase.h>
```

Inheritance diagram for CodeGenerator::PreferencesBase:



Public Attributes

• bool optimizeCode = false

Generate speed-optimized code, potentially at the expense of floating-point accuracy.

• bool debugCode = false

Generate code with debug symbols.

- bool enableBitmaskOptimisations = false
- bool automaticCopy = false

If backend/device supports it, copy data automatically when required rather than requiring push and pull.

• bool generateEmptyStatePushPull = true

Should GeNN generate empty state push and pull functions.

bool generateExtraGlobalParamPull = true

Should GeNN generate pull functions for extra global parameters? These are very rarely used.

std::string userCxxFlagsGNU = ""

C++ compiler options to be used for building all host side code (used for unix based platforms)

• std::string userNvccFlagsGNU = ""

NVCC compiler options they may want to use for all GPU code (used for unix based platforms)

• plog::Severity logLevel = plog::info

Logging level to use for code generation.

20.102.1 Detailed Description

Base class for backend preferences - can be accessed via a global in 'classic' C++ code generator.

20.102.2 Member Data Documentation

20.102.2.1 automaticCopy

bool CodeGenerator::PreferencesBase::automaticCopy = false

If backend/device supports it, copy data automatically when required rather than requiring push and pull.

20.102.2.2 debugCode

bool CodeGenerator::PreferencesBase::debugCode = false

Generate code with debug symbols.

20.102.2.3 enableBitmaskOptimisations

bool CodeGenerator::PreferencesBase::enableBitmaskOptimisations = false

New optimizations made to kernels for simulating synapse groups with BITMASK connectivity Improve performance but break backward compatibility due to word-padding each row

20.102.2.4 generateEmptyStatePushPull

bool CodeGenerator::PreferencesBase::generateEmptyStatePushPull = true

Should GeNN generate empty state push and pull functions.

20.102.2.5 generateExtraGlobalParamPull

bool CodeGenerator::PreferencesBase::generateExtraGlobalParamPull = true

Should GeNN generate pull functions for extra global parameters? These are very rarely used.

20.102.2.6 logLevel

plog::Severity CodeGenerator::PreferencesBase::logLevel = plog::info

Logging level to use for code generation.

20.102.2.7 optimizeCode

bool CodeGenerator::PreferencesBase::optimizeCode = false

Generate speed-optimized code, potentially at the expense of floating-point accuracy.

20.102.2.8 userCxxFlagsGNU

std::string CodeGenerator::PreferencesBase::userCxxFlagsGNU = ""

C++ compiler options to be used for building all host side code (used for unix based platforms)

20.102.2.9 userNvccFlagsGNU

```
std::string CodeGenerator::PreferencesBase::userNvccFlagsGNU = ""
```

NVCC compiler options they may want to use for all GPU code (used for unix based platforms)

The documentation for this struct was generated from the following file:

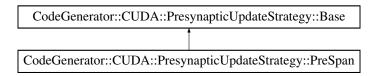
· backendBase.h

20.103 CodeGenerator::CUDA::PresynapticUpdateStrategy::PreSpan Class Reference

Presynaptic parallelism.

#include ynapticUpdateStrategy.h>

Inheritance diagram for CodeGenerator::CUDA::PresynapticUpdateStrategy::PreSpan:



Public Member Functions

- virtual size_t getNumThreads (const SynapseGroupInternal &sg) const override
 - Get the number of threads that presynaptic updates should be parallelised across.
- virtual size_t getSynapticMatrixRowStride (const SynapseGroupInternal &sg) const override
 - Gets the stride used to access synaptic matrix rows, taking into account sparse data structure, padding etc.
- virtual bool isCompatible (const SynapseGroupInternal &sg, const cudaDeviceProp &deviceProps, const Preferences &preferences) const override
 - Is this presynaptic update strategy compatible with a given synapse group?
- virtual size_t getSharedMemoryPerThread (const PresynapticUpdateGroupMerged &sg, const Backend &backend) const override
 - How many neurons does each thread accumulate the outputs of into shared memory.
- virtual void genPreamble (CodeStream &os, const ModelSpecMerged &modelMerged, const Presynaptic
 UpdateGroupMerged &sg, const Substitutions &popSubs, const Backend &backend, size_t idStart) const
 override
- virtual void genUpdate (CodeStream &os, const ModelSpecMerged &modelMerged, const Presynaptic
 UpdateGroupMerged &sg, const Substitutions &popSubs, const Backend &backend, bool trueSpike,
 size_t idStart, BackendBase::PresynapticUpdateGroupMergedHandler wumThreshHandler, Backend
 Base::PresynapticUpdateGroupMergedHandler wumSimHandler, BackendBase::PresynapticUpdateGroup
 MergedHandler wumProceduralConnectHandler) const override
 - Generate presynaptic update code.
- virtual void genPostamble (CodeStream &os, const ModelSpecMerged &modelMerged, const Presynaptic
 UpdateGroupMerged &sg, const Substitutions &popSubs, const Backend &backend, size_t idStart) const
 override

20.103.1 Detailed Description

Presynaptic parallelism.

20.103.2 Member Function Documentation

20.103.2.1 genPostamble()

 $Implements\ Code Generator:: CUDA:: Presynaptic Update Strategy:: Base.$

20.103.2.2 genPreamble()

Implements CodeGenerator::CUDA::PresynapticUpdateStrategy::Base.

20.103.2.3 genUpdate()

Generate presynaptic update code.

Implements CodeGenerator::CUDA::PresynapticUpdateStrategy::Base.

20.103.2.4 getNumThreads()

```
 size\_t \ CodeGenerator:: CUDA:: PresynapticUpdateStrategy:: PreSpan:: getNumThreads \ ( const \ SynapseGroupInternal \& sg \ ) \ const \ [override], \ [virtual]
```

Get the number of threads that presynaptic updates should be parallelised across.

 $Implements\ Code Generator :: CUDA :: Presynaptic Update Strategy :: Base.$

20.103.2.5 getSharedMemoryPerThread()

How many neurons does each thread accumulate the outputs of into shared memory.

Implements CodeGenerator::CUDA::PresynapticUpdateStrategy::Base.

20.103.2.6 getSynapticMatrixRowStride()

Gets the stride used to access synaptic matrix rows, taking into account sparse data structure, padding etc.

Implements CodeGenerator::CUDA::PresynapticUpdateStrategy::Base.

20.103.2.7 isCompatible()

Is this presynaptic update strategy compatible with a given synapse group?

Implements CodeGenerator::CUDA::PresynapticUpdateStrategy::Base.

The documentation for this class was generated from the following files:

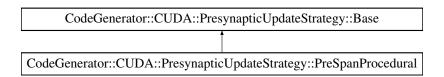
- · presynapticUpdateStrategy.h
- presynapticUpdateStrategy.cc

20.104 CodeGenerator::CUDA::PresynapticUpdateStrategy::PreSpanProcedural Class Reference

Presynaptic parallelism with procedural connectivity.

```
#include ynapticUpdateStrategy.h>
```

Inheritance diagram for CodeGenerator::CUDA::PresynapticUpdateStrategy::PreSpanProcedural:



Public Member Functions

- virtual size_t getNumThreads (const SynapseGroupInternal &sg) const override
 - Get the number of threads that presynaptic updates should be parallelised across.
- virtual size_t getSynapticMatrixRowStride (const SynapseGroupInternal &sg) const override
 - Gets the stride used to access synaptic matrix rows, taking into account sparse data structure, padding etc.
- virtual bool isCompatible (const SynapseGroupInternal &sg, const cudaDeviceProp &deviceProps, const Preferences &preferences) const override

Is this presynaptic update strategy compatible with a given synapse group?

virtual size_t getSharedMemoryPerThread (const PresynapticUpdateGroupMerged &sg, const Backend &backend) const override

How many neurons does each thread accumulate the outputs of into shared memory.

- virtual void genPreamble (CodeStream &os, const ModelSpecMerged &modelMerged, const Presynaptic
 UpdateGroupMerged &sg, const Substitutions &popSubs, const Backend &backend, size_t idStart) const
 override
- virtual void genUpdate (CodeStream &os, const ModelSpecMerged &modelMerged, const Presynaptic
 UpdateGroupMerged &sg, const Substitutions &popSubs, const Backend &backend, bool trueSpike,
 size_t idStart, BackendBase::PresynapticUpdateGroupMergedHandler wumThreshHandler, Backend
 Base::PresynapticUpdateGroupMergedHandler wumSimHandler, BackendBase::PresynapticUpdateGroup
 MergedHandler wumProceduralConnectHandler) const override

Generate presynaptic update code.

virtual void genPostamble (CodeStream &os, const ModelSpecMerged &modelMerged, const Presynaptic
 UpdateGroupMerged &sg, const Substitutions &popSubs, const Backend &backend, size_t idStart) const
 override

20.104.1 Detailed Description

Presynaptic parallelism with procedural connectivity.

20.104.2 Member Function Documentation

20.104.2.1 genPostamble()

Implements CodeGenerator::CUDA::PresynapticUpdateStrategy::Base.

20.104.2.2 genPreamble()

Implements CodeGenerator::CUDA::PresynapticUpdateStrategy::Base.

20.104.2.3 genUpdate()

```
BackendBase::PresynapticUpdateGroupMergedHandler wumThreshHandler,
BackendBase::PresynapticUpdateGroupMergedHandler wumSimHandler,
BackendBase::PresynapticUpdateGroupMergedHandler wumProceduralConnectHandler)
const [override], [virtual]
```

Generate presynaptic update code.

Implements CodeGenerator::CUDA::PresynapticUpdateStrategy::Base.

20.104.2.4 getNumThreads()

Get the number of threads that presynaptic updates should be parallelised across.

Implements CodeGenerator::CUDA::PresynapticUpdateStrategy::Base.

20.104.2.5 getSharedMemoryPerThread()

How many neurons does each thread accumulate the outputs of into shared memory.

Implements CodeGenerator::CUDA::PresynapticUpdateStrategy::Base.

20.104.2.6 getSynapticMatrixRowStride()

```
\label{lem:size_todeGenerator::CUDA::PresynapticUpdateStrategy::PreSpanProcedural::getSynapticMatrix \\ \\ \text{RowStride (} \\ \\ \text{const SynapseGroupInternal & } sg \text{ ) const [override], [virtual]} \\
```

Gets the stride used to access synaptic matrix rows, taking into account sparse data structure, padding etc.

Implements CodeGenerator::CUDA::PresynapticUpdateStrategy::Base.

20.104.2.7 isCompatible()

Is this presynaptic update strategy compatible with a given synapse group?

 $Implements\ Code Generator :: CUDA :: Presynaptic Update Strategy :: Base.$

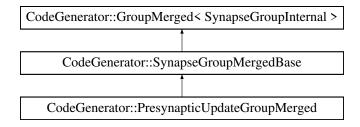
The documentation for this class was generated from the following files:

- presynapticUpdateStrategy.h
- presynapticUpdateStrategy.cc

20.105 CodeGenerator::PresynapticUpdateGroupMerged Class Reference

```
#include <groupMerged.h>
```

Inheritance diagram for CodeGenerator::PresynapticUpdateGroupMerged:



Public Member Functions

- PresynapticUpdateGroupMerged (size_t index, const std::vector < std::reference_wrapper < const Synapse
 GroupInternal >> &groups)
- void generate (const BackendBase &backend, CodeStream &definitionsInternal, CodeStream &definitions
 — InternalFunc, CodeStream &definitionsInternalVar, CodeStream &runnerVarDecl, CodeStream &runner
 — MergedStructAlloc, MergedStructData &mergedStructData, const std::string &precision, const std::string &timePrecision) const

Protected Member Functions

virtual std::string getArchetypeCode () const override

Additional Inherited Members

20.105.1 Constructor & Destructor Documentation

20.105.1.1 PresynapticUpdateGroupMerged()

20.105.2 Member Function Documentation

20.105.2.1 generate()

20.105.2.2 getArchetypeCode()

```
virtual std::string CodeGenerator::PresynapticUpdateGroupMerged::getArchetypeCode ( ) const
[inline], [override], [protected], [virtual]
```

 $Reimplemented\ from\ Code Generator :: Synapse Group Merged Base.$

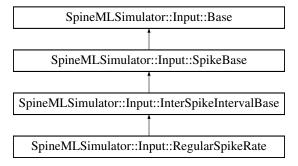
The documentation for this class was generated from the following file:

· groupMerged.h

20.106 SpineMLSimulator::Input::RegularSpikeRate Class Reference

```
#include <input.h>
```

Inheritance diagram for SpineMLSimulator::Input::RegularSpikeRate:



Public Member Functions

• RegularSpikeRate (double dt, const pugi::xml_node &node, std::unique_ptr< InputValue::Base > value, unsigned int popSize, unsigned int *spikeQueuePtr, unsigned int *hostSpikeCount, unsigned int *hostSpikes, PushCurrentSpikesFunc pushCurrentSpikes)

Protected Member Functions

• virtual double getTimeToSpike (double isiMs) override

Additional Inherited Members

20.106.1 Constructor & Destructor Documentation

20.106.1.1 RegularSpikeRate()

20.106.2 Member Function Documentation

20.106.2.1 getTimeToSpike()

Implements SpineMLSimulator::Input::InterSpikeIntervalBase.

The documentation for this class was generated from the following files:

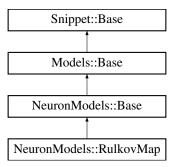
- input.h
- · input.cc

20.107 NeuronModels::RulkovMap Class Reference

Rulkov Map neuron.

```
#include <neuronModels.h>
```

Inheritance diagram for NeuronModels::RulkovMap:



Public Types

- typedef Snippet::ValueBase< 4 > ParamValues
- typedef Models::VarInitContainerBase< 2 > VarValues
- typedef Models::VarInitContainerBase< 0 > PreVarValues
- typedef Models::VarInitContainerBase< 0 > PostVarValues

Public Member Functions

• virtual std::string getSimCode () const override

Gets the code that defines the execution of one timestep of integration of the neuron model.

virtual std::string getThresholdConditionCode () const override

Gets code which defines the condition for a true spike in the described neuron model.

virtual StringVec getParamNames () const override

Gets names of of (independent) model parameters.

virtual VarVec getVars () const override

Gets names and types (as strings) of model variables.

virtual DerivedParamVec getDerivedParams () const override

Static Public Member Functions

static const NeuronModels::RulkovMap * getInstance ()

Additional Inherited Members

20.107.1 Detailed Description

Rulkov Map neuron.

The RulkovMap type is a map based neuron model based on [8] but in the 1-dimensional map form used in [6]:

$$V(t + \Delta t) = \begin{cases} V_{\text{spike}} \left(\frac{\alpha V_{\text{spike}}}{V_{\text{spike}} - V(t)\beta I_{\text{syn}}} + y \right) & V(t) \leq 0 \\ V_{\text{spike}} \left(\alpha + y \right) & V(t) \leq V_{\text{spike}} \left(\alpha + y \right) & \& V(t - \Delta t) \leq 0 \\ -V_{\text{spike}} & \text{otherwise} \end{cases}$$

Note

The RulkovMap type only works as intended for the single time step size of DT= 0.5.

The RulkovMap type has 2 variables:

- V the membrane potential
- preV the membrane potential at the previous time step

and it has 4 parameters:

- Vspike determines the amplitude of spikes, typically -60mV
- alpha determines the shape of the iteration function, typically α = 3
- y "shift / excitation" parameter, also determines the iteration function, originally, y= -2.468
- beta roughly speaking equivalent to the input resistance, i.e. it regulates the scale of the input into the neuron, typically β = 2.64 M Ω .

Note

The initial values array for the RulkovMap type needs two entries for V and Vpre and the parameter array needs four entries for Vspike, alpha, y and beta, in that order.

20.107.2 Member Typedef Documentation

20.107.2.1 ParamValues

typedef Snippet::ValueBase< 4 > NeuronModels::RulkovMap::ParamValues

20.107.2.2 PostVarValues

typedef Models::VarInitContainerBase<0> NeuronModels::RulkovMap::PostVarValues

20.107.2.3 PreVarValues

typedef Models::VarInitContainerBase<0> NeuronModels::RulkovMap::PreVarValues

20.107.2.4 VarValues

typedef Models::VarInitContainerBase< 2 > NeuronModels::RulkovMap::VarValues

20.107.3 Member Function Documentation

20.107.3.1 getDerivedParams()

```
virtual DerivedParamVec NeuronModels::RulkovMap::getDerivedParams ( ) const [inline], [override],
[virtual]
```

Gets names of derived model parameters and the function objects to call to Calculate their value from a vector of model parameter values

Reimplemented from Snippet::Base.

20.107.3.2 getInstance()

```
static const NeuronModels::RulkovMap* NeuronModels::RulkovMap::getInstance ( ) [inline], [static]
```

20.107.3.3 getParamNames()

```
virtual StringVec NeuronModels::RulkovMap::getParamNames ( ) const [inline], [override],
[virtual]
```

Gets names of of (independent) model parameters.

Reimplemented from Snippet::Base.

20.107.3.4 getSimCode()

```
virtual std::string NeuronModels::RulkovMap::getSimCode ( ) const [inline], [override], [virtual]
```

Gets the code that defines the execution of one timestep of integration of the neuron model.

The code will refer to for the value of the variable with name "NN". It needs to refer to the predefined variable "ISYN", i.e. contain, if it is to receive input.

Reimplemented from NeuronModels::Base.

20.107.3.5 getThresholdConditionCode()

```
virtual std::string NeuronModels::RulkovMap::getThresholdConditionCode ( ) const [inline],
[override], [virtual]
```

Gets code which defines the condition for a true spike in the described neuron model.

This evaluates to a bool (e.g. "V > 20").

 $\label{lem:lemented_from_NeuronModels::Base.} Reimplemented from \begin{subarray}{l} NeuronModels::Base. \end{subarray}$

20.107.3.6 getVars()

```
virtual VarVec NeuronModels::RulkovMap::getVars ( ) const [inline], [override], [virtual]
```

Gets names and types (as strings) of model variables.

Reimplemented from Models::Base.

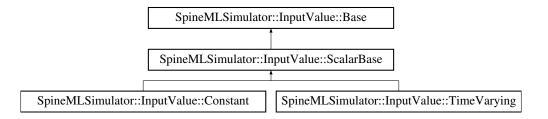
The documentation for this class was generated from the following file:

· neuronModels.h

20.108 SpineMLSimulator::InputValue::ScalarBase Class Reference

```
#include <inputValue.h>
```

Inheritance diagram for SpineMLSimulator::InputValue::ScalarBase:



Protected Member Functions

- ScalarBase (unsigned int numNeurons, const pugi::xml_node &node)
- void applyScalar (double value, std::function < void(unsigned int, double) > applyValueFunc) const

Additional Inherited Members

20.108.1 Constructor & Destructor Documentation

20.108.1.1 ScalarBase()

```
SpineMLSimulator::InputValue::ScalarBase::ScalarBase (
          unsigned int numNeurons,
          const pugi::xml_node & node ) [inline], [protected]
```

20.108.2 Member Function Documentation

20.108.2.1 applyScalar()

The documentation for this class was generated from the following files:

- · inputValue.h
- · inputValue.cc

20.109 CodeGenerator::CodeStream::Scope Class Reference

```
#include <codeStream.h>
```

Public Member Functions

- Scope (CodeStream &codeStream)
- ∼Scope ()

20.109.1 Constructor & Destructor Documentation

The documentation for this class was generated from the following files:

- · codeStream.h
- · codeStream.cc

20.110 SharedLibraryModel < scalar > Class Template Reference

```
#include <sharedLibraryModel.h>
```

Public Member Functions

- SharedLibraryModel ()
- SharedLibraryModel (const std::string &pathToModel, const std::string &modelName)
- virtual ∼SharedLibraryModel ()
- bool open (const std::string &pathToModel, const std::string &modelName)
- void allocateExtraGlobalParam (const std::string &popName, const std::string &egpName, unsigned int count)
- void allocateExtraGlobalParam (const std::string &popName, const std::string &varName, const std::string &egpName, unsigned int count)
- void freeExtraGlobalParam (const std::string &popName, const std::string &egpName)
- void freeExtraGlobalParam (const std::string &popName, const std::string &varName, const std::string &egp
 Name)
- void pullStateFromDevice (const std::string &popName)
- void pullSpikesFromDevice (const std::string &popName)
- void pullCurrentSpikesFromDevice (const std::string &popName)
- void pullConnectivityFromDevice (const std::string &popName)
- void pullVarFromDevice (const std::string &popName, const std::string &varName)
- void pullExtraGlobalParam (const std::string &popName, const std::string &egpName, unsigned int count)
- void pullExtraGlobalParam (const std::string &popName, const std::string &varName, const std::string &egp
 Name, unsigned int count)
- void pushStateToDevice (const std::string &popName, bool uninitialisedOnly=false)
- void pushSpikesToDevice (const std::string &popName, bool uninitialisedOnly=false)
- void pushCurrentSpikesToDevice (const std::string &popName, bool uninitialisedOnly=false)
- void pushConnectivityToDevice (const std::string &popName, bool uninitialisedOnly=false)
- void pushVarToDevice (const std::string &popName, const std::string &varName, bool uninitialisedOnly=false)
- void pushExtraGlobalParam (const std::string &popName, const std::string &egpName, unsigned int count)

- void pushExtraGlobalParam (const std::string &popName, const std::string &varName, const std::string &egpName, unsigned int count)
- template<typename Writer, typename... WriterArgs>
 SpikeRecorder
 WriterArgs &&... writerArgs &const std::string &popName, WriterArgs &&... writerArgs)
- template<typename T >
 - T * getArray (const std::string &varName)
- template<typename T >
 - T * getScalar (const std::string &varName)
- void allocateMem ()
- · void freeMem ()
- size_t getFreeDeviceMemBytes ()
- void initialize ()
- · void initializeSparse ()
- void stepTime ()
- scalar getTime () const
- unsigned long long getTimestep () const
- void setTime (scalar t)
- void setTimestep (unsigned long long iT)
- double getNeuronUpdateTime () const
- double getInitTime () const
- double getPresynapticUpdateTime () const
- double getPostsynapticUpdateTime () const
- double getSynapseDynamicsTime () const
- double getInitSparseTime () const
- void * getSymbol (const std::string &symbolName, bool allowMissing=false, void *defaultSymbol=nullptr)
 const

20.110.1 Constructor & Destructor Documentation

20.110.2 Member Function Documentation

```
20.110.2.1 allocateExtraGlobalParam() [1/2]
template<typename scalar = float>
void SharedLibraryModel< scalar >::allocateExtraGlobalParam (
             const std::string & popName,
             const std::string & egpName,
             unsigned int count ) [inline]
20.110.2.2 allocateExtraGlobalParam() [2/2]
template<typename scalar = float>
\verb|void SharedLibraryModel| < scalar >:: allocateExtraGlobalParam | (
             const std::string & popName,
             const std::string & varName,
             const std::string & egpName,
             unsigned int count ) [inline]
20.110.2.3 allocateMem()
{\tt template}{<}{\tt typename} \ {\tt scalar} = {\tt float}{>}
void SharedLibraryModel< scalar >::allocateMem ( ) [inline]
20.110.2.4 freeExtraGlobalParam() [1/2]
template<typename scalar = float>
void SharedLibraryModel< scalar >::freeExtraGlobalParam (
             const std::string & popName,
             const std::string & egpName ) [inline]
20.110.2.5 freeExtraGlobalParam() [2/2]
template<typename scalar = float>
void SharedLibraryModel< scalar >::freeExtraGlobalParam (
             const std::string & popName,
             const std::string & varName,
             const std::string & egpName ) [inline]
20.110.2.6 freeMem()
template<typename scalar = float>
void SharedLibraryModel< scalar >::freeMem ( ) [inline]
20.110.2.7 getArray()
template<typename scalar = float>
template < typename T >
T* SharedLibraryModel< scalar >::getArray (
             const std::string & varName ) [inline]
```

```
20.110.2.8 getFreeDeviceMemBytes()
template<typename scalar = float>
size_t SharedLibraryModel< scalar >::getFreeDeviceMemBytes ( ) [inline]
20.110.2.9 getInitSparseTime()
template<typename scalar = float>
double SharedLibraryModel< scalar >::getInitSparseTime ( ) const [inline]
20.110.2.10 getInitTime()
template<typename scalar = float>
double SharedLibraryModel< scalar >::getInitTime ( ) const [inline]
20.110.2.11 getNeuronUpdateTime()
template<typename scalar = float>
double SharedLibraryModel< scalar >::getNeuronUpdateTime ( ) const [inline]
20.110.2.12 getPostsynapticUpdateTime()
template < typename scalar = float >
double SharedLibraryModel< scalar >::getPostsynapticUpdateTime ( ) const [inline]
20.110.2.13 getPresynapticUpdateTime()
template<typename scalar = float>
double SharedLibraryModel< scalar >::getPresynapticUpdateTime ( ) const [inline]
20.110.2.14 getScalar()
template<typename scalar = float>
template < typename T >
T* SharedLibraryModel< scalar >::getScalar (
             const std::string & varName ) [inline]
20.110.2.15 getSpikeRecorder()
template<typename scalar = float>
template<typename Writer , typename... WriterArgs>
SpikeRecorder<Writer> SharedLibraryModel< scalar >::getSpikeRecorder (
             const std::string & popName,
             WriterArgs &&... writerArgs ) [inline]
20.110.2.16 getSymbol()
template<typename scalar = float>
void* SharedLibraryModel< scalar >::getSymbol (
```

```
const std::string & symbolName,
             bool allowMissing = false,
             void * defaultSymbol = nullptr ) const [inline]
20.110.2.17 getSynapseDynamicsTime()
template<typename scalar = float>
double SharedLibraryModel< scalar >::getSynapseDynamicsTime ( ) const [inline]
20.110.2.18 getTime()
template<typename scalar = float>
scalar SharedLibraryModel< scalar >::getTime ( ) const [inline]
20.110.2.19 getTimestep()
template<typename scalar = float>
unsigned long long SharedLibraryModel< scalar >::getTimestep ( ) const [inline]
20.110.2.20 initialize()
template<typename scalar = float>
void SharedLibraryModel< scalar >::initialize ( ) [inline]
20.110.2.21 initializeSparse()
template<typename scalar = float>
void SharedLibraryModel< scalar >::initializeSparse ( ) [inline]
20.110.2.22 open()
template<typename scalar = float>
bool SharedLibraryModel< scalar >::open (
             const std::string & pathToModel,
             const std::string & modelName ) [inline]
20.110.2.23 pullConnectivityFromDevice()
template<typename scalar = float>
void SharedLibraryModel< scalar >::pullConnectivityFromDevice (
             const std::string & popName ) [inline]
20.110.2.24 pullCurrentSpikesFromDevice()
template<typename scalar = float>
void SharedLibraryModel< scalar >::pullCurrentSpikesFromDevice (
             const std::string & popName ) [inline]
```

```
20.110.2.25 pullExtraGlobalParam() [1/2]
template<typename scalar = float>
void SharedLibraryModel< scalar >::pullExtraGlobalParam (
             const std::string & popName,
             const std::string & egpName,
             unsigned int count ) [inline]
20.110.2.26 pullExtraGlobalParam() [2/2]
template<typename scalar = float>
\verb"void SharedLibraryModel< scalar >::pullExtraGlobalParam (
             const std::string & popName,
             const std::string & varName,
             const std::string & egpName,
             unsigned int count ) [inline]
20.110.2.27 pullSpikesFromDevice()
template<typename scalar = float>
void SharedLibraryModel< scalar >::pullSpikesFromDevice (
             const std::string & popName ) [inline]
20.110.2.28 pullStateFromDevice()
template<typename scalar = float>
void SharedLibraryModel< scalar >::pullStateFromDevice (
             const std::string & popName ) [inline]
20.110.2.29 pullVarFromDevice()
template<typename scalar = float>
void SharedLibraryModel< scalar >::pullVarFromDevice (
            const std::string & popName,
             const std::string & varName ) [inline]
20.110.2.30 pushConnectivityToDevice()
template<typename scalar = float>
void SharedLibraryModel< scalar >::pushConnectivityToDevice (
             const std::string & popName,
             bool uninitialisedOnly = false ) [inline]
20.110.2.31 pushCurrentSpikesToDevice()
template<typename scalar = float>
void SharedLibraryModel< scalar >::pushCurrentSpikesToDevice (
             const std::string & popName,
             bool uninitialisedOnly = false ) [inline]
```

```
20.110.2.32 pushExtraGlobalParam() [1/2]
template<typename scalar = float>
void SharedLibraryModel< scalar >::pushExtraGlobalParam (
             const std::string & popName,
             const std::string & egpName,
             unsigned int count ) [inline]
20.110.2.33 pushExtraGlobalParam() [2/2]
template<typename scalar = float>
\verb|void SharedLibraryModel| < scalar >:: pushExtraGlobalParam | (
             const std::string & popName,
             const std::string & varName,
             const std::string & egpName,
             unsigned int count ) [inline]
20.110.2.34 pushSpikesToDevice()
template<typename scalar = float>
void SharedLibraryModel< scalar >::pushSpikesToDevice (
             const std::string & popName,
             bool uninitialisedOnly = false ) [inline]
20.110.2.35 pushStateToDevice()
template<typename scalar = float>
void SharedLibraryModel< scalar >::pushStateToDevice (
             const std::string & popName,
             bool uninitialisedOnly = false ) [inline]
20.110.2.36 pushVarToDevice()
template<typename scalar = float>
void SharedLibraryModel< scalar >::pushVarToDevice (
             const std::string & popName,
             const std::string & varName,
             bool uninitialisedOnly = false ) [inline]
20.110.2.37 setTime()
template<typename scalar = float>
void SharedLibraryModel< scalar >::setTime (
             scalar t ) [inline]
20.110.2.38 setTimestep()
template < typename scalar = float >
\verb"void SharedLibraryModel< scalar >:: setTimestep (
             unsigned long long iT ) [inline]
```

20.110.2.39 stepTime()

```
template<typename scalar = float>
void SharedLibraryModel< scalar >::stepTime ( ) [inline]
```

The documentation for this class was generated from the following file:

· sharedLibraryModel.h

20.111 SpineMLSimulator::Simulator Class Reference

```
#include <simulator.h>
```

Public Member Functions

- Simulator (plog::Severity logLevel=plog::warning)
- Simulator (const std::string &experimentXML, const std::string &overrideOutputPath="", plog::Severity log
 Level=plog::warning)
- ∼Simulator ()
- void load (const std::string &experimentXML, const std::string &overrideOutputPath="")

Load model from XML file.

• void stepTime ()

Advance simulation by one timestep.

const LogOutput::AnalogueExternal * getExternalLogger (const std::string &name) const

Get an external logger by name.

• InputValue::External * getExternalInput (const std::string &name) const

Get an external input by name.

double getDT () const

Get the simulation timestep (in ms)

• double getDurationMs () const

Get duration of simulation read from experiment in ms.

• double getInputMs () const

Get the total times accumulated in each stage of the simulation.

- double getSimulateMs () const
- double getLogMs () const
- double getNeuronUpdateTime () const

Timings of individual kernels provided by GeNN.

- double getInitTime () const
- double getPresynapticUpdateTime () const
- double getPostsynapticUpdateTime () const
- double getSynapseDynamicsTime () const
- double getInitSparseTime () const
- unsigned long long calcNumTimesteps () const

Calculate duration of simulation read from experiment in timesteps.

20.111.1 Constructor & Destructor Documentation

```
20.111.1.2 Simulator() [2/2]
SpineMLSimulator::Simulator::Simulator (
             const std::string & experimentXML,
             const std::string & overrideOutputPath = "",
             plog::Severity logLevel = plog::warning )
20.111.1.3 ∼Simulator()
SpineMLSimulator::Simulator::~Simulator ( )
20.111.2 Member Function Documentation
20.111.2.1 calcNumTimesteps()
unsigned long long SpineMLSimulator::Simulator::calcNumTimesteps ( ) const [inline]
Calculate duration of simulation read from experiment in timesteps.
20.111.2.2 getDT()
double SpineMLSimulator::Simulator::getDT ( ) const [inline]
Get the simulation timestep (in ms)
20.111.2.3 getDurationMs()
double SpineMLSimulator::Simulator::getDurationMs ( ) const [inline]
Get duration of simulation read from experiment in ms.
20.111.2.4 getExternalInput()
InputValue::External * SpineMLSimulator::Simulator::getExternalInput (
             const std::string & name ) const
Get an external input by name.
20.111.2.5 getExternalLogger()
const LogOutput::AnalogueExternal * SpineMLSimulator::Simulator::getExternalLogger (
              const std::string & name ) const
Get an external logger by name.
20.111.2.6 getInitSparseTime()
double SpineMLSimulator::Simulator::getInitSparseTime ( ) const
```

```
20.111.2.7 getInitTime()
double SpineMLSimulator::Simulator::getInitTime ( ) const
20.111.2.8 getInputMs()
double SpineMLSimulator::Simulator::getInputMs ( ) const [inline]
Get the total times accumulated in each stage of the simulation.
20.111.2.9 getLogMs()
double SpineMLSimulator::Simulator::getLogMs ( ) const [inline]
20.111.2.10 getNeuronUpdateTime()
double SpineMLSimulator::Simulator::getNeuronUpdateTime ( ) const
Timings of individual kernels provided by GeNN.
20.111.2.11 getPostsynapticUpdateTime()
double SpineMLSimulator::Simulator::getPostsynapticUpdateTime ( ) const
20.111.2.12 getPresynapticUpdateTime()
double SpineMLSimulator::Simulator::getPresynapticUpdateTime ( ) const
20.111.2.13 getSimulateMs()
double SpineMLSimulator::Simulator::getSimulateMs ( ) const [inline]
20.111.2.14 getSynapseDynamicsTime()
\verb|double SpineMLSimulator::getSynapseDynamicsTime () const|\\
20.111.2.15 load()
void SpineMLSimulator::Simulator::load (
             const std::string & experimentXML,
             const std::string & overrideOutputPath = """ )
Load model from XML file.
20.111.2.16 stepTime()
void SpineMLSimulator::Simulator::stepTime ( )
```

Advance simulation by one timestep.

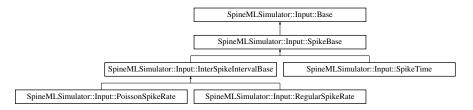
The documentation for this class was generated from the following files:

- · simulator.h
- · simulator.cc

20.112 SpineMLSimulator::Input::SpikeBase Class Reference

```
#include <input.h>
```

Inheritance diagram for SpineMLSimulator::Input::SpikeBase:



Protected Types

typedef void(* PushCurrentSpikesFunc) ()

Protected Member Functions

- SpikeBase (double dt, const pugi::xml_node &node, std::unique_ptr< InputValue::Base > value, unsigned int popSize, unsigned int *spikeQueuePtr, unsigned int *hostSpikeCount, unsigned int *hostSpikes, Push
 CurrentSpikesFunc pushCurrentSpikes)
- void injectSpike (unsigned int neuronID)
- · void uploadSpikes ()

Additional Inherited Members

20.112.1 Member Typedef Documentation

20.112.1.1 PushCurrentSpikesFunc

```
typedef void(* SpineMLSimulator::Input::SpikeBase::PushCurrentSpikesFunc) () [protected]
```

20.112.2 Constructor & Destructor Documentation

20.112.2.1 SpikeBase()

20.112.3 Member Function Documentation

20.112.3.1 injectSpike()

void SpineMLSimulator::Input::SpikeBase::uploadSpikes () [protected]

The documentation for this class was generated from the following files:

- · input.h
- · input.cc

20.113 NeuronGroup::SpikeEventThreshold Struct Reference

Structure used for storing spike event data.

```
#include <neuronGroup.h>
```

Public Member Functions

- SpikeEventThreshold (const std::string &e, const std::string &s, bool egp, SynapseGroupInternal *sg)
- bool operator< (const SpikeEventThreshold &other) const
- bool operator== (const SpikeEventThreshold &other) const

Public Attributes

- const std::string eventThresholdCode
- const std::string supportCode
- · const bool egpInThresholdCode
- SynapseGroupInternal * synapseGroup

20.113.1 Detailed Description

Structure used for storing spike event data.

20.113.2 Constructor & Destructor Documentation

20.113.2.1 SpikeEventThreshold()

20.113.3 Member Function Documentation

20.113.3.1 operator<()

Less than operator (used for std::set::insert), lexicographically compares all three struct members - meaning that event thresholds featuring extra global parameters from different synapse groups will not get combined together in neuron update

20.113.3.2 operator==()

Equality operator (used for set::set equality used when testing neuron groups mergability), Compares only the two code strings as neuron groups with threshold conditions featuring extra global parameters from different synapse groups can still be merged

20.113.4 Member Data Documentation

20.113.4.1 egpInThresholdCode

const bool NeuronGroup::SpikeEventThreshold::egpInThresholdCode

20.113.4.2 eventThresholdCode

 $\verb|const| std::string NeuronGroup::SpikeEventThreshold::eventThresholdCode| \\$

20.113.4.3 supportCode

const std::string NeuronGroup::SpikeEventThreshold::supportCode

20.113.4.4 synapseGroup

 ${\tt SynapseGroupInternal*\ NeuronGroup::SpikeEventThreshold::synapseGroup}$

The documentation for this struct was generated from the following file:

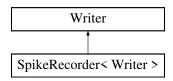
neuronGroup.h

20.114 SpikeRecorder < Writer > Class Template Reference

Class to read spikes from neuron groups.

```
#include <spikeRecorder.h>
```

Inheritance diagram for SpikeRecorder< Writer >:



Public Types

- typedef unsigned int &(* GetCurrentSpikeCountFunc) ()
- typedef unsigned int *(* GetCurrentSpikesFunc) ()

Public Member Functions

- template<typename... WriterArgs>
 SpikeRecorder (GetCurrentSpikesFunc getCurrentSpikes, GetCurrentSpikeCountFunc getCurrentSpike
 Count, WriterArgs &&... writerArgs)
- SpikeRecorder (SpikeRecorder < Writer > &&other)=default
- void record (double t)
- unsigned int getSum () const

20.114.1 Detailed Description

```
template < typename Writer = SpikeWriterText > class SpikeRecorder < Writer >
```

Class to read spikes from neuron groups.

20.114.2 Member Typedef Documentation

20.114.2.1 GetCurrentSpikeCountFunc

```
template<typename Writer = SpikeWriterText>
typedef unsigned int&(* SpikeRecorder< Writer >::GetCurrentSpikeCountFunc) ()
```

20.114.2.2 GetCurrentSpikesFunc

```
template<typename Writer = SpikeWriterText>
typedef unsigned int*(* SpikeRecorder< Writer >::GetCurrentSpikesFunc) ()
```

20.114.3 Constructor & Destructor Documentation

20.114.3.1 SpikeRecorder() [1/2]

20.114.3.2 SpikeRecorder() [2/2]

20.114.4 Member Function Documentation

20.114.4.1 getSum()

```
template<typename Writer = SpikeWriterText>
unsigned int SpikeRecorder< Writer >::getSum ( ) const [inline]
```

20.114.4.2 record()

The documentation for this class was generated from the following file:

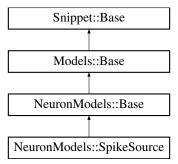
· spikeRecorder.h

20.115 NeuronModels::SpikeSource Class Reference

Empty neuron which allows setting spikes from external sources.

```
#include <neuronModels.h>
```

Inheritance diagram for NeuronModels::SpikeSource:



Public Types

- typedef Snippet::ValueBase< 0 > ParamValues
- typedef Models::VarInitContainerBase< 0 > VarValues
- typedef Models::VarInitContainerBase< 0 > PreVarValues
- typedef Models::VarInitContainerBase< 0 > PostVarValues

Public Member Functions

virtual std::string getThresholdConditionCode () const override

Gets code which defines the condition for a true spike in the described neuron model.

SET_NEEDS_AUTO_REFRACTORY (false)

Static Public Member Functions

• static const NeuronModels::SpikeSource * getInstance ()

Additional Inherited Members

20.115.1 Detailed Description

Empty neuron which allows setting spikes from external sources.

This model does not contain any update code and can be used to implement the equivalent of a SpikeGenerator Group in Brian or a SpikeSourceArray in PyNN.

20.115.2 Member Typedef Documentation

20.115.2.1 ParamValues

```
typedef Snippet::ValueBase< 0 > NeuronModels::SpikeSource::ParamValues
```

20.115.2.2 PostVarValues

```
typedef Models::VarInitContainerBase<0> NeuronModels::SpikeSource::PostVarValues
```

20.115.2.3 PreVarValues

```
typedef Models::VarInitContainerBase<0> NeuronModels::SpikeSource::PreVarValues
```

20.115.2.4 VarValues

```
{\tt typedef\ Models::} VarInitContainerBase<{\tt 0} {\tt > NeuronModels::} SpikeSource:: VarValues
```

20.115.3 Member Function Documentation

20.115.3.1 getInstance()

```
static const NeuronModels::SpikeSource* NeuronModels::SpikeSource::getInstance ( ) [inline],
[static]
```

20.115.3.2 getThresholdConditionCode()

```
virtual std::string NeuronModels::SpikeSource::getThresholdConditionCode ( ) const [inline],
[override], [virtual]
```

Gets code which defines the condition for a true spike in the described neuron model.

This evaluates to a bool (e.g. "V > 20").

Reimplemented from NeuronModels::Base.

20.115.3.3 SET_NEEDS_AUTO_REFRACTORY()

The documentation for this class was generated from the following file:

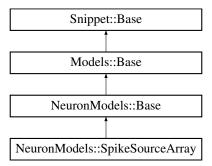
· neuronModels.h

20.116 NeuronModels::SpikeSourceArray Class Reference

Spike source array.

```
#include <neuronModels.h>
```

Inheritance diagram for NeuronModels::SpikeSourceArray:



Public Types

- typedef Snippet::ValueBase< 0 > ParamValues
- typedef Models::VarInitContainerBase< 2 > VarValues
- typedef Models::VarInitContainerBase< 0 > PreVarValues
- typedef Models::VarInitContainerBase< 0 > PostVarValues

Public Member Functions

• virtual std::string getSimCode () const override

Gets the code that defines the execution of one timestep of integration of the neuron model.

• virtual std::string getThresholdConditionCode () const override

Gets code which defines the condition for a true spike in the described neuron model.

• virtual std::string getResetCode () const override

Gets code that defines the reset action taken after a spike occurred. This can be empty.

· virtual VarVec getVars () const override

Gets names and types (as strings) of model variables.

- virtual EGPVec getExtraGlobalParams () const override
- SET_NEEDS_AUTO_REFRACTORY (false)

Static Public Member Functions

static const NeuronModels::SpikeSourceArray * getInstance ()

Additional Inherited Members

20.116.1 Detailed Description

Spike source array.

A neuron which reads spike times from a global spikes array It has 2 variables:

- startSpike Index of the next spike in the global array
- endSpike Index of the spike next to the last in the globel array

and 1 global parameter:

• spikeTimes - Array with all spike times

20.116.2 Member Typedef Documentation

20.116.2.1 ParamValues

```
{\tt typedef~Snippet::ValueBase<~0~>~NeuronModels::SpikeSourceArray::ParamValues}
```

20.116.2.2 PostVarValues

```
typedef Models::VarInitContainerBase<0> NeuronModels::SpikeSourceArray::PostVarValues
```

20.116.2.3 PreVarValues

```
{\tt typedef\ Models::} VarInitContainerBase < 0 > {\tt NeuronModels::} SpikeSourceArray:: PreVarValues
```

20.116.2.4 VarValues

```
{\tt typedef~Models::VarInitContainerBase<~2~>~NeuronModels::SpikeSourceArray::VarValues}
```

20.116.3 Member Function Documentation

20.116.3.1 getExtraGlobalParams()

```
virtual EGPVec NeuronModels::SpikeSourceArray::getExtraGlobalParams ( ) const [inline], [override],
[virtual]
```

Gets names and types (as strings) of additional per-population parameters for the snippet

Reimplemented from Snippet::Base.

20.116.3.2 getInstance()

```
static const NeuronModels::SpikeSourceArray* NeuronModels::SpikeSourceArray::getInstance ( )
[inline], [static]
```

20.116.3.3 getResetCode()

virtual std::string NeuronModels::SpikeSourceArray::getResetCode () const [inline], [override],
[virtual]

Gets code that defines the reset action taken after a spike occurred. This can be empty.

Reimplemented from NeuronModels::Base.

20.116.3.4 getSimCode()

virtual std::string NeuronModels::SpikeSourceArray::getSimCode () const [inline], [override],
[virtual]

Gets the code that defines the execution of one timestep of integration of the neuron model.

The code will refer to for the value of the variable with name "NN". It needs to refer to the predefined variable "ISYN", i.e. contain, if it is to receive input.

Reimplemented from NeuronModels::Base.

20.116.3.5 getThresholdConditionCode()

```
virtual std::string NeuronModels::SpikeSourceArray::getThresholdConditionCode ( ) const [inline],
[override], [virtual]
```

Gets code which defines the condition for a true spike in the described neuron model.

This evaluates to a bool (e.g. "V > 20").

Reimplemented from NeuronModels::Base.

20.116.3.6 getVars()

```
virtual VarVec NeuronModels::SpikeSourceArray::getVars ( ) const [inline], [override], [virtual]
```

Gets names and types (as strings) of model variables.

Reimplemented from Models::Base.

20.116.3.7 SET_NEEDS_AUTO_REFRACTORY()

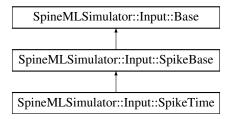
The documentation for this class was generated from the following file:

· neuronModels.h

20.117 SpineMLSimulator::Input::SpikeTime Class Reference

```
#include <input.h>
```

Inheritance diagram for SpineMLSimulator::Input::SpikeTime:



Public Member Functions

- SpikeTime (double dt, const pugi::xml_node &node, std::unique_ptr< InputValue::Base > value, unsigned int popSize, unsigned int *spikeQueuePtr, unsigned int *hostSpikeCount, unsigned int *hostSpikes, Push
 CurrentSpikesFunc pushCurrentSpikes)
- · virtual void apply (double dt, unsigned long long timestep) override

Additional Inherited Members

20.117.1 Constructor & Destructor Documentation

20.117.1.1 SpikeTime()

20.117.2 Member Function Documentation

20.117.2.1 apply()

Implements SpineMLSimulator::Input::Base.

The documentation for this class was generated from the following files:

- input.h
- · input.cc

20.118 SpikeWriterText Class Reference

Class to write spikes to text file.

```
#include <spikeRecorder.h>
```

Public Member Functions

- SpikeWriterText (const std::string &filename, const std::string &delimiter=" ", bool header=false)
- SpikeWriterText (SpikeWriterText &&other)

Protected Member Functions

• void recordSpikes (double t, unsigned int spikeCount, const unsigned int *currentSpikes)

20.118.1 Detailed Description

Class to write spikes to text file.

20.118.2 Constructor & Destructor Documentation

```
20.118.2.1 SpikeWriterText() [1/2]
```

20.118.2.2 SpikeWriterText() [2/2]

20.118.3 Member Function Documentation

20.118.3.1 recordSpikes()

The documentation for this class was generated from the following file:

· spikeRecorder.h

20.119 SpikeWriterTextCached Class Reference

Class to write spikes to text file, caching in memory before writing.

```
#include <spikeRecorder.h>
```

Public Member Functions

- SpikeWriterTextCached (const std::string &filename, const std::string &delimiter=" ", bool header=false)
- SpikeWriterTextCached (SpikeWriterTextCached &&other)

- ∼SpikeWriterTextCached ()
- void writeCache ()

Protected Member Functions

• void recordSpikes (double t, unsigned int spikeCount, const unsigned int *currentSpikes)

20.119.1 Detailed Description

Class to write spikes to text file, caching in memory before writing.

20.119.2 Constructor & Destructor Documentation

```
20.119.2.1 SpikeWriterTextCached() [1/2]
```

```
SpikeWriterTextCached::SpikeWriterTextCached (
    const std::string & filename,
    const std::string & delimiter = " ",
    bool header = false ) [inline]
```

20.119.2.2 SpikeWriterTextCached() [2/2]

20.119.2.3 ~SpikeWriterTextCached()

```
SpikeWriterTextCached::~SpikeWriterTextCached ( ) [inline]
```

20.119.3 Member Function Documentation

20.119.3.1 recordSpikes()

20.119.3.2 writeCache()

```
void SpikeWriterTextCached::writeCache ( ) [inline]
```

The documentation for this class was generated from the following file:

· spikeRecorder.h

20.120 SpineMLSimulator::StateVar < T > Class Template Reference

```
#include <stateVar.h>
```

Public Member Functions

- StateVar (const std::string &stateVarName, std::function < void *(const char *, bool) > getLibrarySymbol ← Func)
- bool isAccessible () const
- · void push () const
- void pull () const
- T * get ()
- const T * get () const

20.120.1 Constructor & Destructor Documentation

```
20.120.1.1 StateVar()
```

20.120.2 Member Function Documentation

```
20.120.2.1 get() [1/2]

template<typename T>
T* SpineMLSimulator::StateVar< T >::get ( ) [inline]

20.120.2.2 get() [2/2]

template<typename T>
const T* SpineMLSimulator::StateVar< T >::get ( ) const [inline]

20.120.2.3 isAccessible()

template<typename T>
bool SpineMLSimulator::StateVar< T >::isAccessible ( ) const [inline]

20.120.2.4 pull()

template<typename T>
void SpineMLSimulator::StateVar< T >::pull ( ) const [inline]
```

20.120.2.5 push()

```
template<typename T>
void SpineMLSimulator::StateVar< T >::push ( ) const [inline]
```

20.120.3 Member Data Documentation

20.120.3.1 m_Direct

```
template<typename T>
Direct SpineMLSimulator::StateVar< T >::m_Direct
```

20.120.3.2 m Indirect

```
template<typename T>
Indirect SpineMLSimulator::StateVar< T >::m_Indirect
```

The documentation for this class was generated from the following file:

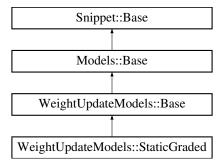
· stateVar.h

20.121 WeightUpdateModels::StaticGraded Class Reference

Graded-potential, static synapse.

```
#include <weightUpdateModels.h>
```

Inheritance diagram for WeightUpdateModels::StaticGraded:



Public Member Functions

- DECLARE_WEIGHT_UPDATE_MODEL (StaticGraded, 2, 1, 0, 0)
- virtual StringVec getParamNames () const override

Gets names of of (independent) model parameters.

• virtual VarVec getVars () const override

Gets names and types (as strings) of model variables.

• virtual std::string getEventCode () const override

Gets code run when events (all the instances where event threshold condition is met) are received.

virtual std::string getEventThresholdConditionCode () const override

Gets codes to test for events.

Additional Inherited Members

20.121.1 Detailed Description

Graded-potential, static synapse.

In a graded synapse, the conductance is updated gradually with the rule:

$$gSyn = g * tanh((V - E_{pre})/V_{slope})$$

whenever the membrane potential V is larger than the threshold E_{pre} . The model has 1 variable:

• g: conductance of scalar type

The parameters are:

- Epre: Presynaptic threshold potential
- Vslope: Activation slope of graded release

event code is:

```
 \begin{tabular}{ll} $(addToInSyn, $(g)* tanh(($(V_pre)-($(Epre)))*DT*2/$(Vslope))); \\ \end{tabular}
```

event threshold condition code is:

```
$(V_pre) > $(Epre)
```

Note

The pre-synaptic variables are referenced with the suffix $_pre$ in synapse related code such as an the event threshold test. Users can also access post-synaptic neuron variables using the suffix $_post$.

20.121.2 Member Function Documentation

20.121.2.1 DECLARE_WEIGHT_UPDATE_MODEL()

20.121.2.2 getEventCode()

```
virtual std::string WeightUpdateModels::StaticGraded::getEventCode ( ) const [inline], [override],
[virtual]
```

Gets code run when events (all the instances where event threshold condition is met) are received.

Reimplemented from WeightUpdateModels::Base.

20.121.2.3 getEventThresholdConditionCode()

virtual std::string WeightUpdateModels::StaticGraded::getEventThresholdConditionCode () const
[inline], [override], [virtual]

Gets codes to test for events.

Reimplemented from WeightUpdateModels::Base.

20.121.2.4 getParamNames()

virtual StringVec WeightUpdateModels::StaticGraded::getParamNames () const [inline], [override],
[virtual]

Gets names of of (independent) model parameters.

Reimplemented from Snippet::Base.

20.121.2.5 getVars()

```
virtual VarVec WeightUpdateModels::StaticGraded::getVars ( ) const [inline], [override],
[virtual]
```

Gets names and types (as strings) of model variables.

Reimplemented from Models::Base.

The documentation for this class was generated from the following file:

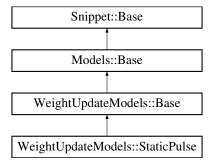
· weightUpdateModels.h

20.122 WeightUpdateModels::StaticPulse Class Reference

Pulse-coupled, static synapse.

#include <weightUpdateModels.h>

Inheritance diagram for WeightUpdateModels::StaticPulse:



Public Member Functions

- DECLARE_WEIGHT_UPDATE_MODEL (StaticPulse, 0, 1, 0, 0)
- virtual VarVec getVars () const override

Gets names and types (as strings) of model variables.

• virtual std::string getSimCode () const override

Gets simulation code run when 'true' spikes are received.

Additional Inherited Members

20.122.1 Detailed Description

Pulse-coupled, static synapse.

No learning rule is applied to the synapse and for each pre-synaptic spikes, the synaptic conductances are simply added to the postsynaptic input variable. The model has 1 variable:

g - conductance of scalar type and no other parameters.

```
sim code is:
```

```
"$(addToInSyn, $(g)); \n"
```

20.122.2 Member Function Documentation

20.122.2.1 DECLARE_WEIGHT_UPDATE_MODEL()

20.122.2.2 getSimCode()

```
virtual std::string WeightUpdateModels::StaticPulse::getSimCode ( ) const [inline], [override],
[virtual]
```

Gets simulation code run when 'true' spikes are received.

Reimplemented from WeightUpdateModels::Base.

20.122.2.3 getVars()

```
virtual VarVec WeightUpdateModels::StaticPulse::getVars ( ) const [inline], [override], [virtual]
```

Gets names and types (as strings) of model variables.

Reimplemented from Models::Base.

The documentation for this class was generated from the following file:

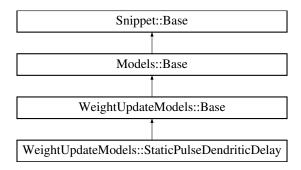
· weightUpdateModels.h

20.123 WeightUpdateModels::StaticPulseDendriticDelay Class Reference

Pulse-coupled, static synapse with heterogenous dendritic delays.

```
#include <weightUpdateModels.h>
```

Inheritance diagram for WeightUpdateModels::StaticPulseDendriticDelay:



Public Member Functions

- DECLARE_WEIGHT_UPDATE_MODEL (StaticPulseDendriticDelay, 0, 2, 0, 0)
- · virtual VarVec getVars () const override

Gets names and types (as strings) of model variables.

• virtual std::string getSimCode () const override

Gets simulation code run when 'true' spikes are received.

Additional Inherited Members

20.123.1 Detailed Description

Pulse-coupled, static synapse with heterogenous dendritic delays.

No learning rule is applied to the synapse and for each pre-synaptic spikes, the synaptic conductances are simply added to the postsynaptic input variable. The model has 2 variables:

- g conductance of scalar type
- · d dendritic delay in timesteps and no other parameters.

sim code is:

20.123.2 Member Function Documentation

20.123.2.1 DECLARE_WEIGHT_UPDATE_MODEL()

20.123.2.2 getSimCode()

```
virtual std::string WeightUpdateModels::StaticPulseDendriticDelay::getSimCode ( ) const [inline],
[override], [virtual]
```

Gets simulation code run when 'true' spikes are received.

Reimplemented from WeightUpdateModels::Base.

20.123.2.3 getVars()

```
virtual VarVec WeightUpdateModels::StaticPulseDendriticDelay::getVars ( ) const [inline],
[override], [virtual]
```

Gets names and types (as strings) of model variables.

Reimplemented from Models::Base.

The documentation for this class was generated from the following file:

· weightUpdateModels.h

20.124 CodeGenerator::Substitutions Class Reference

```
#include <substitutions.h>
```

Classes

struct FunctionTemplate

Public Member Functions

- Substitutions (const Substitutions *parent=nullptr)
- Substitutions (const std::vector< FunctionTemplate > &functions, const std::string &ftype)
- template<typename T >
 void addVarNameSubstitution (const std::vector< T > &variables, const std::string &sourceSuffix="", const std::string &destPrefix="")
- template<typename T >
 void addVarValueSubstitution (const std::vector< T > &variables, const std::vector< double > &values, const std::string &sourceSuffix="")
- void addParamValueSubstitution (const std::vector< std::string > ¶mNames, const std::vector< double > &values, const std::string &sourceSuffix="")
- template<typename G >
 void addParamValueSubstitution (const std::vector< std::string > ¶mNames, const std::vector< double
 > &values, G isHeterogeneousFn, const std::string &sourceSuffix="", const std::string &destPrefix="", const std::string &destSuffix="")
- template<typename T, typename G >
 void addVarValueSubstitution (const std::vector< T > &variables, const std::vector< double > &values,
 G isHeterogeneousFn, const std::string &sourceSuffix="", const std::string &destPrefix="", const std::string &destSuffix="")
- void addVarSubstitution (const std::string &source, const std::string &destionation, bool allowOverride=false)
- void addFuncSubstitution (const std::string &source, unsigned int numArguments, const std::string &func
 — Template, bool allowOverride=false)
- bool hasVarSubstitution (const std::string &source) const
- const std::string & getVarSubstitution (const std::string &source) const
- void apply (std::string &code) const
- void applyCheckUnreplaced (std::string &code, const std::string &context) const
- const std::string operator[] (const std::string &source) const

20.124.1 Constructor & Destructor Documentation

```
20.124.1.1 Substitutions() [1/2]
CodeGenerator::Substitutions::Substitutions (
             const Substitutions * parent = nullptr ) [inline]
20.124.1.2 Substitutions() [2/2]
CodeGenerator::Substitutions::Substitutions (
            const std::vector< FunctionTemplate > & functions,
             const std::string & ftype ) [inline]
20.124.2 Member Function Documentation
20.124.2.1 addFuncSubstitution()
void CodeGenerator::Substitutions::addFuncSubstitution (
             const std::string & source,
             unsigned int numArguments,
             const std::string & funcTemplate,
             bool allowOverride = false )
20.124.2.2 addParamValueSubstitution() [1/2]
void CodeGenerator::Substitutions::addParamValueSubstitution (
             const std::vector< std::string > & paramNames,
             const std::vector< double > & values,
             const std::string & sourceSuffix = "" )
20.124.2.3 addParamValueSubstitution() [2/2]
{\tt template}{<}{\tt typename}~{\tt G}~>
void CodeGenerator::Substitutions::addParamValueSubstitution (
             const std::vector< std::string > & paramNames,
             const std::vector< double > & values,
             G isHeterogeneousFn,
             const std::string & sourceSuffix = "",
             const std::string & destPrefix = "",
             const std::string & destSuffix = "" ) [inline]
20.124.2.4 addVarNameSubstitution()
template<typename T >
void CodeGenerator::Substitutions::addVarNameSubstitution (
             const std::vector< T > & variables,
             const std::string & sourceSuffix = "",
             const std::string & destPrefix = "",
             const std::string & destSuffix = "" ) [inline]
```

20.124.2.5 addVarSubstitution()

```
void CodeGenerator::Substitutions::addVarSubstitution (
             const std::string & source,
             const std::string & destionation,
             bool allowOverride = false )
20.124.2.6 addVarValueSubstitution() [1/2]
template < typename T >
void CodeGenerator::Substitutions::addVarValueSubstitution (
             const std::vector< T > & variables,
             const std::vector< double > & values,
             const std::string & sourceSuffix = "" ) [inline]
20.124.2.7 addVarValueSubstitution() [2/2]
template<typename T , typename G >
\verb"void CodeGenerator": Substitutions:: add Var Value Substitution (
             const std::vector< T > & variables,
             const std::vector< double > & values,
             G isHeterogeneousFn,
             const std::string & sourceSuffix = "",
             const std::string & destPrefix = "",
             const std::string & destSuffix = "" ) [inline]
20.124.2.8 apply()
void CodeGenerator::Substitutions::apply (
             std::string & code ) const
20.124.2.9 applyCheckUnreplaced()
void CodeGenerator::Substitutions::applyCheckUnreplaced (
            std::string & code,
             const std::string & context ) const
20.124.2.10 getVarSubstitution()
const std::string & CodeGenerator::Substitutions::getVarSubstitution (
             const std::string & source ) const
20.124.2.11 hasVarSubstitution()
bool CodeGenerator::Substitutions::hasVarSubstitution (
             const std::string & source ) const
20.124.2.12 operator[]()
const std::string CodeGenerator::Substitutions::operator[] (
```

```
const std::string & source ) const [inline]
```

The documentation for this class was generated from the following files:

- · substitutions.h
- · substitutions.cc

20.125 CodeGenerator::SupportCodeMerged Class Reference

```
#include <supportCodeMerged.h>
```

Public Member Functions

- SupportCodeMerged (const std::string &namespacePrefix)
- void addSupportCode (const std::string &code)

Add support code string.

- const std::string & getSupportCodeNamespace (const std::string &code) const
 - Gets the name of the support code namespace which should be 'used' to provide this support code.
- void gen (CodeStream &os, const std::string &ftype) const

Generate support code.

size_t getNumSupportCodeString () const

Gets the number of support code strings hence namespaces which will be generated.

20.125.1 Constructor & Destructor Documentation

20.125.1.1 SupportCodeMerged()

20.125.2 Member Function Documentation

20.125.2.1 addSupportCode()

Add support code string.

20.125.2.2 gen()

Generate support code.

20.125.2.3 getNumSupportCodeString()

```
size_t CodeGenerator::SupportCodeMerged::getNumSupportCodeString ( ) const [inline]
```

Gets the number of support code strings hence namespaces which will be generated.

20.125.2.4 getSupportCodeNamespace()

Gets the name of the support code namespace which should be 'used' to provide this support code.

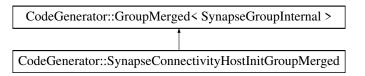
The documentation for this class was generated from the following file:

· supportCodeMerged.h

20.126 CodeGenerator::SynapseConnectivityHostInitGroupMerged Class Reference

```
#include <groupMerged.h>
```

Inheritance diagram for CodeGenerator::SynapseConnectivityHostInitGroupMerged:



Public Member Functions

- SynapseConnectivityHostInitGroupMerged (size_t index, const std::vector< std::reference_wrapper< const SynapseGroupInternal >> &groups)
- void generate (const BackendBase &backend, CodeStream &definitionsInternal, CodeStream &definitions
 — InternalFunc, CodeStream &definitionsInternalVar, CodeStream &runnerVarDecl, CodeStream &runner
 — MergedStructAlloc, MergedStructData &mergedStructData, const std::string &precision) const
- bool isConnectivityInitParamHeterogeneous (size_t paramIndex) const

Should the connectivity initialization parameter be implemented heterogeneously for EGP init?

• bool isConnectivityInitDerivedParamHeterogeneous (size_t paramIndex) const

Should the connectivity initialization derived parameter be implemented heterogeneously for EGP init?

Additional Inherited Members

20.126.1 Constructor & Destructor Documentation

20.126.1.1 SynapseConnectivityHostInitGroupMerged()

20.126.2 Member Function Documentation

20.126.2.1 generate()

20.126.2.2 isConnectivityInitDerivedParamHeterogeneous()

```
bool CodeGenerator::SynapseConnectivityHostInitGroupMerged::isConnectivityInitDerivedParam←
Heterogeneous (
size_t paramIndex ) const
```

Should the connectivity initialization derived parameter be implemented heterogeneously for EGP init?

20.126.2.3 isConnectivityInitParamHeterogeneous()

```
\label{lem:bool} \begin{tabular}{ll} bool & CodeGenerator::SynapseConnectivityHostInitGroupMerged::isConnectivityInitParamHeterogeneous ( & size_t & paramIndex ) & const \end{tabular}
```

Should the connectivity initialization parameter be implemented heterogeneously for EGP init?

The documentation for this class was generated from the following files:

- · groupMerged.h
- groupMerged.cc

20.127 CodeGenerator::SynapseConnectivityInitGroupMerged Class Reference

```
#include <groupMerged.h>
```

Inheritance diagram for CodeGenerator::SynapseConnectivityInitGroupMerged:

```
CodeGenerator::GroupMerged < SynapseGroupInternal >

CodeGenerator::SynapseConnectivityInitGroupMerged
```

Public Member Functions

- SynapseConnectivityInitGroupMerged (size_t index, const std::vector< std::reference_wrapper< const SynapseGroupInternal >> &groups)
- bool isConnectivityInitParamHeterogeneous (size_t paramIndex) const

Should the connectivity initialization parameter be implemented heterogeneously?

- $\bullet \ \ bool\ is Connectivity In it Derived Param Heterogeneous\ (size_t\ param Index)\ const$
 - Should the connectivity initialization parameter be implemented heterogeneously?
- void generate (const BackendBase &backend, CodeStream &definitionsInternal, CodeStream &definitions
 — InternalFunc, CodeStream &definitionsInternalVar, CodeStream &runnerVarDecl, CodeStream &runner
 — MergedStructAlloc, MergedStructData &mergedStructData, const std::string &precision) const

Additional Inherited Members

20.127.1 Constructor & Destructor Documentation

20.127.1.1 SynapseConnectivityInitGroupMerged()

20.127.2 Member Function Documentation

20.127.2.1 generate()

20.127.2.2 isConnectivityInitDerivedParamHeterogeneous()

Should the connectivity initialization parameter be implemented heterogeneously?

20.127.2.3 isConnectivityInitParamHeterogeneous()

```
\label{local_code_Generator::SynapseConnectivityInitGroupMerged::isConnectivityInitParamHeterogeneous ( size\_t paramIndex ) const
```

Should the connectivity initialization parameter be implemented heterogeneously?

The documentation for this class was generated from the following files:

- · groupMerged.h
- groupMerged.cc

20.128 CodeGenerator::SynapseDendriticDelayUpdateGroupMerged Class Reference

```
#include <groupMerged.h>
```

Inheritance diagram for CodeGenerator::SynapseDendriticDelayUpdateGroupMerged:

```
CodeGenerator::GroupMerged < SynapseGroupInternal >

CodeGenerator::SynapseDendriticDelayUpdateGroupMerged
```

Public Member Functions

- SynapseDendriticDelayUpdateGroupMerged (size_t index, const std::vector< std::reference_wrapper< const SynapseGroupInternal >> &groups)
- void generate (const BackendBase &backend, CodeStream &definitionsInternal, CodeStream &definitions
 — InternalFunc, CodeStream &definitionsInternalVar, CodeStream &runnerVarDecl, CodeStream &runner
 — MergedStructAlloc, MergedStructData &mergedStructData, const std::string &precision) const

Additional Inherited Members

20.128.1 Constructor & Destructor Documentation

20.128.1.1 SynapseDendriticDelayUpdateGroupMerged()

20.128.2 Member Function Documentation

20.128.2.1 generate()

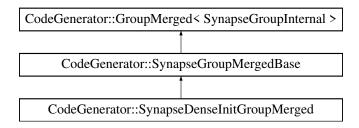
The documentation for this class was generated from the following files:

- · groupMerged.h
- groupMerged.cc

20.129 CodeGenerator::SynapseDenseInitGroupMerged Class Reference

```
#include <groupMerged.h>
```

Inheritance diagram for CodeGenerator::SynapseDenseInitGroupMerged:



Public Member Functions

- SynapseDenseInitGroupMerged (size_t index, const std::vector < std::reference_wrapper < const Synapse←
 GroupInternal >> &groups)
- void generate (const BackendBase &backend, CodeStream &definitionsInternal, CodeStream &definitions
 — InternalFunc, CodeStream &definitionsInternalVar, CodeStream &runnerVarDecl, CodeStream &runner
 — MergedStructAlloc, MergedStructData &mergedStructData, const std::string &precision, const std::string &timePrecision) const

Additional Inherited Members

20.129.1 Constructor & Destructor Documentation

20.129.1.1 SynapseDenseInitGroupMerged()

20.129.2 Member Function Documentation

20.129.2.1 generate()

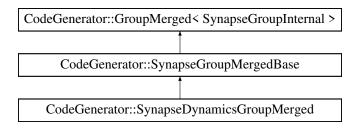
The documentation for this class was generated from the following file:

groupMerged.h

20.130 CodeGenerator::SynapseDynamicsGroupMerged Class Reference

```
#include <groupMerged.h>
```

Inheritance diagram for CodeGenerator::SynapseDynamicsGroupMerged:



Public Member Functions

- SynapseDynamicsGroupMerged (size_t index, const std::vector < std::reference_wrapper < const Synapse
 GroupInternal >> &groups)
- void generate (const BackendBase &backend, CodeStream &definitionsInternal, CodeStream &definitions
 — InternalFunc, CodeStream &definitionsInternalVar, CodeStream &runnerVarDecl, CodeStream &runner
 — MergedStructAlloc, MergedStructData &mergedStructData, const std::string &precision, const std::string &timePrecision) const

Protected Member Functions

· virtual std::string getArchetypeCode () const override

Additional Inherited Members

20.130.1 Constructor & Destructor Documentation

20.130.1.1 SynapseDynamicsGroupMerged()

20.130.2 Member Function Documentation

20.130.2.1 generate()

```
const std::string & precision,
const std::string & timePrecision ) const [inline]
```

20.130.2.2 getArchetypeCode()

```
virtual std::string CodeGenerator::SynapseDynamicsGroupMerged::getArchetypeCode ( ) const
[inline], [override], [protected], [virtual]
```

Reimplemented from CodeGenerator::SynapseGroupMergedBase.

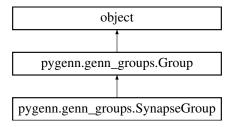
The documentation for this class was generated from the following file:

· groupMerged.h

20.131 pygenn.genn_groups.SynapseGroup Class Reference

Class representing synaptic connection between two groups of neurons.

Inheritance diagram for pygenn.genn_groups.SynapseGroup:



Public Member Functions

- def __init__ (self, name, model, weight_sharing_master=None)
 Init SynapseGroup.
- def num_synapses (self)

Number of synapses in group.

def weight_update_var_size (self)

Size of each weight update variable.

- def max_row_length (self)
- def set_psm_var (self, var_name, values)

Set values for a postsynaptic model variable.

• def set_pre_var (self, var_name, values)

Set values for a presynaptic variable.

def set_post_var (self, var_name, values)

Set values for a postsynaptic variable.

• def set_weight_update (self, model, param_space, var_space, pre_var_space, post_var_space)

Set weight update model, its parameters and initial variables.

def set_post_syn (self, model, param_space, var_space)

Set postsynaptic model, its parameters and initial variables.

- def get_var_values (self, var_name)
- def is_connectivity_init_required (self)
- def matrix_type (self)

Type of the projection matrix.

- def matrix_type (self, matrix_type)
- def has_procedural_connectivity (self)

Tests whether synaptic connectivity is procedural.

def has_procedural_weights (self)

Tests whether synaptic weights are procedural.

• def is_ragged (self)

Tests whether synaptic connectivity uses Ragged format.

def is_bitmask (self)

Tests whether synaptic connectivity uses Bitmask format.

· def is_dense (self)

Tests whether synaptic connectivity uses dense format.

• def has_individual_synapse_vars (self)

Tests whether synaptic connectivity has individual weights.

def has_individual_postsynaptic_vars (self)

Tests whether synaptic connectivity has individual postsynaptic model variables.

• def set_sparse_connections (self, pre_indices, post_indices)

Set ragged format connections between two groups of neurons.

def get_sparse_pre_inds (self)

Get presynaptic indices of synapse group connections.

def get_sparse_post_inds (self)

Get postsynaptic indices of synapse group connections.

def set_connected_populations (self, source, target)

Set two groups of neurons connected by this SynapseGroup.

def add to (self, model spec, delay steps)

Add this SynapseGroup to the a model.

• def add_extra_global_param (self, param_name, param_values)

Set extra global parameter.

• def set_extra_global_param (self, param_name, param_values)

Set extra global parameter to weight update model.

• def set_psm_extra_global_param (self, param_name, param_values)

Set extra global parameter to postsynaptic model.

• def set_connectivity_extra_global_param (self, param_name, param_values)

Set extra global parameter to connectivity initialisation snippet.

• def pull connectivity from device (self)

Wrapper around GeNNModel.pull_connectivity_from_device.

def push_connectivity_to_device (self)

Wrapper around GeNNModel.push_connectivity_to_device.

- def load (self)
- def load init egps (self)
- def reinitialise (self)

Reinitialise synapse group.

Public Attributes

- · connections_set
- w_update
- postsyn
- src
- trg
- psm_vars
- pre_vars
- post_vars
- psm_extra_global_params

- · connectivity_extra_global_params
- · connectivity_initialiser
- · weight_sharing_master
- synapse_order
- ind
- · row_lengths
- pop

20.131.1 Detailed Description

Class representing synaptic connection between two groups of neurons.

20.131.2 Constructor & Destructor Documentation

Init SynapseGroup.

Parameters

```
name string name of the group
```

20.131.3 Member Function Documentation

20.131.3.1 add_extra_global_param()

Set extra global parameter.

Parameters

param_name	string with the name of the extra global parameter
param_values	iterable or a single value

20.131.3.2 add_to()

```
delay_steps )
```

Add this SynapseGroup to the a model.

Parameters

model_spec	pygenn.genn_model.GeNNModel to add to
delay_steps	number of axonal delay timesteps to simulate for this synapse group

20.131.3.3 get_sparse_post_inds()

```
\begin{tabular}{ll} \tt def pygenn.genn\_groups.SynapseGroup.get\_sparse\_post\_inds \ ( \\ self \ ) \end{tabular}
```

Get postsynaptic indices of synapse group connections.

Returns

ndarrays of postsynaptic indices

20.131.3.4 get_sparse_pre_inds()

```
\label{lem:control_gen} $$ def pygenn.genn_groups.SynapseGroup.get_sparse_pre_inds ( $$ self ) $$
```

Get presynaptic indices of synapse group connections.

Returns

ndarray of presynaptic indices

20.131.3.5 get_var_values()

```
def pygenn.genn_groups.SynapseGroup.get_var_values ( self, \\ var_name \ )
```

20.131.3.6 has_individual_postsynaptic_vars()

```
\label{lem:constraint}  \mbox{def pygenn.genn\_groups.SynapseGroup.has\_individual\_postsynaptic\_vars (} \\ self \mbox{)}
```

Tests whether synaptic connectivity has individual postsynaptic model variables.

20.131.3.7 has_individual_synapse_vars()

```
\label{lem:constraint} $\operatorname{def pygenn.genn\_groups.SynapseGroup.has\_individual\_synapse\_vars} \ ( \\ \operatorname{self} \ )
```

Tests whether synaptic connectivity has individual weights.

```
20.131.3.8 has_procedural_connectivity()
def pygenn.genn_groups.SynapseGroup.has_procedural_connectivity (
               self )
Tests whether synaptic connectivity is procedural.
20.131.3.9 has_procedural_weights()
def pygenn.genn_groups.SynapseGroup.has_procedural_weights (
               self )
Tests whether synaptic weights are procedural.
20.131.3.10 is_bitmask()
def pygenn.genn_groups.SynapseGroup.is_bitmask (
               self )
Tests whether synaptic connectivity uses Bitmask format.
20.131.3.11 is_connectivity_init_required()
def pygenn.genn_groups.SynapseGroup.is_connectivity_init_required (
               self )
20.131.3.12 is_dense()
def pygenn.genn_groups.SynapseGroup.is_dense (
               self )
Tests whether synaptic connectivity uses dense format.
20.131.3.13 is_ragged()
def pygenn.genn_groups.SynapseGroup.is_ragged (
               self )
Tests whether synaptic connectivity uses Ragged format.
20.131.3.14 load()
def pygenn.genn_groups.SynapseGroup.load (
               self )
20.131.3.15 load_init_egps()
def pygenn.genn_groups.SynapseGroup.load_init_egps (
```

self)

```
20.131.3.16 matrix_type() [1/2]
def pygenn.genn_groups.SynapseGroup.matrix_type (
               self )
Type of the projection matrix.
20.131.3.17 matrix_type() [2/2]
def pygenn.genn_groups.SynapseGroup.matrix_type (
              self,
               matrix_type )
20.131.3.18 max_row_length()
{\tt def pygenn.genn\_groups.SynapseGroup.max\_row\_length} \ \ (
               self )
20.131.3.19 num_synapses()
def pygenn.genn_groups.SynapseGroup.num_synapses (
               self )
Number of synapses in group.
20.131.3.20 pull_connectivity_from_device()
{\tt def pygenn.genn\_groups.SynapseGroup.pull\_connectivity\_from\_device \ (}
Wrapper around GeNNModel.pull_connectivity_from_device.
20.131.3.21 push_connectivity_to_device()
def pygenn.genn_groups.SynapseGroup.push_connectivity_to_device (
               self )
Wrapper around GeNNModel.push_connectivity_to_device.
20.131.3.22 reinitialise()
def pygenn.genn_groups.SynapseGroup.reinitialise (
               self )
Reinitialise synapse group.
20.131.3.23 set_connected_populations()
def pygenn.genn_groups.SynapseGroup.set_connected_populations (
               source,
               target )
```

Set two groups of neurons connected by this SynapseGroup.

Parameters

source	string name of the presynaptic neuron group
target	string name of the postsynaptic neuron group

20.131.3.24 set_connectivity_extra_global_param()

```
def pygenn.genn_groups.SynapseGroup.set_connectivity_extra_global_param ( self, \\ param_name, \\ param_values )
```

Set extra global parameter to connectivity initialisation snippet.

Parameters

param_name	string with the name of the extra global parameter
param_values	iterable or a single value

20.131.3.25 set_extra_global_param()

```
def pygenn.genn_groups.SynapseGroup.set_extra_global_param ( self, \\ param_name, \\ param_values )
```

Set extra global parameter to weight update model.

Parameters

param_name	string with the name of the extra global parameter
param_values	iterable or a single value

20.131.3.26 set_post_syn()

Set postsynaptic model, its parameters and initial variables.

Parameters

model	type as string of intance of the model
param_space	dict with model parameters
var_space	dict with model variables

20.131.3.27 set_post_var()

```
def pygenn.genn_groups.SynapseGroup.set_post_var ( self, \\ var_name, \\ values )
```

Set values for a postsynaptic variable.

Parameters

var_name	string with the name of the presynaptic variable
values	iterable or a single value

20.131.3.28 set_pre_var()

Set values for a presynaptic variable.

Parameters

var_name	string with the name of the presynaptic variable
values	iterable or a single value

20.131.3.29 set_psm_extra_global_param()

Set extra global parameter to postsynaptic model.

Parameters

param_name	string with the name of the extra global parameter
param_values	iterable or a single value

20.131.3.30 set_psm_var()

Set values for a postsynaptic model variable.

Parameters

var_name	string with the name of the postsynaptic model variable
values	iterable or a single value

20.131.3.31 set_sparse_connections()

```
def pygenn.genn_groups.SynapseGroup.set_sparse_connections ( self, \\ pre\_indices, \\ post\_indices \; )
```

Set ragged format connections between two groups of neurons.

Parameters

pre_indices	ndarray of presynaptic indices
post_indices	ndarray of postsynaptic indices

20.131.3.32 set_weight_update()

Set weight update model, its parameters and initial variables.

Parameters

model	type as string of intance of the model
param_space	dict with model parameters
var_space	dict with model variables
pre_var_space	dict with model presynaptic variables
post_var_space	dict with model postsynaptic variables

20.131.3.33 weight_update_var_size()

```
\label{lem:constraint} $\operatorname{def pygenn.genn\_groups.SynapseGroup.weight\_update\_var\_size} \ ( \\ self )
```

Size of each weight update variable.

20.131.4 Member Data Documentation

20.131.4.1 connections_set

pygenn.genn_groups.SynapseGroup.connections_set

20.131.4.2 connectivity_extra_global_params

pygenn.genn_groups.SynapseGroup.connectivity_extra_global_params

20.131.4.3 connectivity_initialiser

 $\verb"pygenn.genn_groups.SynapseGroup.connectivity_initial iser"$

20.131.4.4 ind

pygenn.genn_groups.SynapseGroup.ind

20.131.4.5 pop

pygenn.genn_groups.SynapseGroup.pop

20.131.4.6 post_vars

pygenn.genn_groups.SynapseGroup.post_vars

20.131.4.7 postsyn

pygenn.genn_groups.SynapseGroup.postsyn

20.131.4.8 pre_vars

pygenn.genn_groups.SynapseGroup.pre_vars

20.131.4.9 psm_extra_global_params

pygenn.genn_groups.SynapseGroup.psm_extra_global_params

20.131.4.10 psm_vars

pygenn.genn_groups.SynapseGroup.psm_vars

20.131.4.11 row_lengths

pygenn.genn_groups.SynapseGroup.row_lengths

20.131.4.12 src

pygenn.genn_groups.SynapseGroup.src

20.131.4.13 synapse_order

pygenn.genn_groups.SynapseGroup.synapse_order

20.131.4.14 trg

pygenn.genn_groups.SynapseGroup.trg

20.131.4.15 w_update

pygenn.genn_groups.SynapseGroup.w_update

20.131.4.16 weight_sharing_master

 $\verb"pygenn.genn_groups.SynapseGroup.weight_sharing_master"$

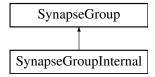
The documentation for this class was generated from the following file:

genn_groups.py

20.132 SynapseGroup Class Reference

#include <synapseGroup.h>

Inheritance diagram for SynapseGroup:



Public Types

enum SpanType { SpanType::POSTSYNAPTIC, SpanType::PRESYNAPTIC }

Public Member Functions

- SynapseGroup (const SynapseGroup &)=delete
- SynapseGroup ()=delete
- void setWUVarLocation (const std::string &varName, VarLocation loc)

Set location of weight update model state variable.

void setWUPreVarLocation (const std::string &varName, VarLocation loc)

Set location of weight update model presynaptic state variable.

void setWUPostVarLocation (const std::string &varName, VarLocation loc)

Set location of weight update model postsynaptic state variable.

• void setWUExtraGlobalParamLocation (const std::string ¶mName, VarLocation loc)

Set location of weight update model extra global parameter.

void setPSVarLocation (const std::string &varName, VarLocation loc)

Set location of postsynaptic model state variable.

void setPSExtraGlobalParamLocation (const std::string ¶mName, VarLocation loc)

Set location of postsynaptic model extra global parameter.

void setSparseConnectivityExtraGlobalParamLocation (const std::string ¶mName, VarLocation loc)

Set location of sparse connectivity initialiser extra global parameter.

void setInSynVarLocation (VarLocation loc)

Set location of variables used to combine input from this synapse group.

void setSparseConnectivityLocation (VarLocation loc)

Set variable mode used for sparse connectivity.

void setDendriticDelayLocation (VarLocation loc)

Set variable mode used for this synapse group's dendritic delay buffers.

void setMaxConnections (unsigned int maxConnections)

Sets the maximum number of target neurons any source neurons can connect to.

void setMaxSourceConnections (unsigned int maxPostConnections)

Sets the maximum number of source neurons any target neuron can connect to.

void setMaxDendriticDelayTimesteps (unsigned int maxDendriticDelay)

Sets the maximum dendritic delay for synapses in this synapse group.

void setSpanType (SpanType spanType)

Set how CUDA implementation is parallelised.

void setNumThreadsPerSpike (unsigned int numThreadsPerSpike)

Set how many threads CUDA implementation uses to process each spike when span type is PRESYNAPTIC.

void setBackPropDelaySteps (unsigned int timesteps)

Sets the number of delay steps used to delay postsynaptic spikes travelling back along dendrites to synapses.

void setNarrowSparseIndEnabled (bool enabled)

Enables or disables using narrow i.e. less than 32-bit types for sparse matrix indices.

- const std::string & getName () const
- SpanType getSpanType () const
- unsigned int getNumThreadsPerSpike () const
- unsigned int getDelaySteps () const
- unsigned int getBackPropDelaySteps () const
- unsigned int getMaxConnections () const
- unsigned int getMaxSourceConnections () const
- unsigned int getMaxDendriticDelayTimesteps () const
- SynapseMatrixType getMatrixType () const
- VarLocation getInSynLocation () const

Get variable mode used for variables used to combine input from this synapse group.

VarLocation getSparseConnectivityLocation () const

Get variable mode used for sparse connectivity.

VarLocation getDendriticDelayLocation () const

Get variable mode used for this synapse group's dendritic delay buffers.

bool isTrueSpikeRequired () const

Does synapse group need to handle 'true' spikes.

bool isSpikeEventRequired () const

Does synapse group need to handle spike-like events.

bool isWeightSharingSlave () const

Is this synapse group a weight-sharing slave.

- const WeightUpdateModels::Base * getWUModel () const
- const std::vector< double > & getWUParams () const
- const std::vector< Models::VarInit > & getWUVarInitialisers () const
- const std::vector< Models::VarInit > & getWUPreVarInitialisers () const

- const std::vector< Models::VarInit > & getWUPostVarInitialisers () const
- const std::vector< double > getWUConstInitVals () const
- const PostsynapticModels::Base * getPSModel () const
- const std::vector< double > & getPSParams () const
- const std::vector< Models::VarInit > & getPSVarInitialisers () const
- const std::vector< double > getPSConstInitVals () const
- const InitSparseConnectivitySnippet::Init & getConnectivityInitialiser () const
- bool isZeroCopyEnabled () const
- VarLocation getWUVarLocation (const std::string &var) const

Get location of weight update model per-synapse state variable by name.

VarLocation getWUVarLocation (size_t index) const

Get location of weight update model per-synapse state variable by index.

VarLocation getWUPreVarLocation (const std::string &var) const

Get location of weight update model presynaptic state variable by name.

VarLocation getWUPreVarLocation (size_t index) const

Get location of weight update model presynaptic state variable by index.

VarLocation getWUPostVarLocation (const std::string &var) const

Get location of weight update model postsynaptic state variable by name.

VarLocation getWUPostVarLocation (size_t index) const

Get location of weight update model postsynaptic state variable by index.

VarLocation getWUExtraGlobalParamLocation (const std::string ¶mName) const

Get location of weight update model extra global parameter by name.

VarLocation getWUExtraGlobalParamLocation (size t index) const

Get location of weight update model extra global parameter by index.

VarLocation getPSVarLocation (const std::string &var) const

Get location of postsynaptic model state variable.

VarLocation getPSVarLocation (size t index) const

Get location of postsynaptic model state variable.

VarLocation getPSExtraGlobalParamLocation (const std::string ¶mName) const

Get location of postsynaptic model extra global parameter by name.

VarLocation getPSExtraGlobalParamLocation (size_t index) const

Get location of postsynaptic model extra global parameter by index.

VarLocation getSparseConnectivityExtraGlobalParamLocation (const std::string ¶mName) const

Get location of sparse connectivity initialiser extra global parameter by name.

VarLocation getSparseConnectivityExtraGlobalParamLocation (size_t index) const

Get location of sparse connectivity initialiser extra global parameter by index.

· bool isDendriticDelayRequired () const

Does this synapse group require dendritic delay?

bool isProceduralConnectivityRNGRequired () const

Does this synapse group require an RNG to generate procedural connectivity?

• bool isPSInitRNGRequired () const

Does this synapse group require an RNG for it's postsynaptic init code?

· bool isWUInitRNGRequired () const

Does this synapse group require an RNG for it's weight update init code?

· bool isWUPreInitRNGRequired () const

Does this synapse group require an RNG for it's weight update presynaptic variable init code?

· bool isWUPostInitRNGRequired () const

Does this synapse group require an RNG for it's weight update postsynaptic variable init code?

· bool isHostInitRNGRequired () const

Does this synapse group require a RNG for any sort of initialization.

• bool isWUVarInitRequired () const

Is var init code required for any variables in this synapse group's weight update model?

bool isSparseConnectivityInitRequired () const

Is sparse connectivity initialisation code required for this synapse group?

Protected Member Functions

- SynapseGroup (const std::string &name, SynapseMatrixType matrixType, unsigned int delaySteps, const WeightUpdateModels::Base *wu, const std::vector< double > &wuParams, const std::vector< Models:: VarInit > &wuVarInitialisers, const std::vector< Models::VarInit > &wuPreVarInitialisers, const std::vector< Models::VarInit > &wuPostVarInitialisers, const PostsynapticModels::Base *ps, const std::vector< double > &psParams, const std::vector< Models::VarInit > &psVarInitialisers, NeuronGroupInternal *srcNeuron← Group, NeuronGroupInternal *trgNeuronGroup, const SynapseGroupInternal *weightSharingMaster, const InitSparseConnectivitySnippet::Init &connectivityInitialiser, VarLocation defaultVarLocation, VarLocation defaultExtraGlobalParamLocation, VarLocation defaultSparseConnectivityLocation, bool defaultNarrow← SparseIndEnabled)
- NeuronGroupInternal * getSrcNeuronGroup ()
- NeuronGroupInternal * getTrgNeuronGroup ()
- void setEventThresholdReTestRequired (bool req)
- void setPSModelMergeTarget (const std::string &targetName)
- void initDerivedParams (double dt)
- const NeuronGroupInternal * getSrcNeuronGroup () const
- const NeuronGroupInternal * getTrgNeuronGroup () const
- const std::vector< double > & getWUDerivedParams () const
- const std::vector< double > & getPSDerivedParams () const
- const SynapseGroupInternal * getWeightSharingMaster () const

Does the event threshold needs to be retested in the synapse kernel?

- · bool isEventThresholdReTestRequired () const
- const std::string & getPSModelTargetName () const
- bool isPSModelMerged () const
- std::string getSparseIndType () const

Get the type to use for sparse connectivity indices for synapse group.

bool canWUBeMerged (const SynapseGroup &other) const

Can weight update component of this synapse group be merged with other? i.e. can they be simulated using same generated code.

bool canWUPreBeMerged (const SynapseGroup &other) const

Can presynaptic update of this synapse group be merged with other? i.e. can they be simulated using same generated code

• bool canWUPostBeMerged (const SynapseGroup &other) const

Can postsynaptic update of this synapse group be merged with other? i.e. can they be simulated using same generated code.

bool canPSBeMerged (const SynapseGroup &other) const

Can postsynaptic update component of this synapse group be merged with other? i.e. can they be simulated using same generated code.

• bool canPSBeLinearlyCombined (const SynapseGroup &other) const

Can postsynaptic update component of this synapse group not only be merged with other, but combined so only one needs simulating at all.

• bool canWUInitBeMerged (const SynapseGroup &other) const

Can initialisation for this synapse group be merged with other? i.e. can they be performed using same generated code.

bool canWUPreInitBeMerged (const SynapseGroup &other) const

Can initialisation for this synapse group's presynaptic variables be merged with other? i.e. can they be performed using same generated code.

bool canWUPostInitBeMerged (const SynapseGroup &other) const

Can initialisation for this synapse group's presynaptic variables be merged with other? i.e. can they be performed using same generated code.

bool canPSInitBeMerged (const SynapseGroup &other) const

Can postsynaptic initialisation for this synapse group be merged with other? i.e. can they be performed using same generated code.

bool canConnectivityInitBeMerged (const SynapseGroup &other) const

Can connectivity initialisation for this synapse group be merged with other? i.e. can they be performed using same generated code.

• bool canConnectivityHostInitBeMerged (const SynapseGroup &other) const

Can connectivity host initialisation for this synapse group be merged with other? i.e. can they be performed using same generated code.

20.132.1 Member Enumeration Documentation

20.132.1.1 SpanType

```
enum SynapseGroup::SpanType [strong]
```

Enumerator

```
POSTSYNAPTIC PRESYNAPTIC
```

20.132.2 Constructor & Destructor Documentation

const PostsynapticModels::Base * ps,
const std::vector< double > & psParams,

NeuronGroupInternal * srcNeuronGroup,
NeuronGroupInternal * trgNeuronGroup,

VarLocation defaultVarLocation,

const std::vector< Models::VarInit > & wuVarInitialisers,
const std::vector< Models::VarInit > & wuPreVarInitialisers,
const std::vector< Models::VarInit > & wuPostVarInitialisers,

const std::vector< Models::VarInit > & psVarInitialisers,

const InitSparseConnectivitySnippet::Init & connectivityInitialiser,

const SynapseGroupInternal * weightSharingMaster,

VarLocation defaultExtraGlobalParamLocation, VarLocation defaultSparseConnectivityLocation, bool defaultNarrowSparseIndEnabled) [protected]

20.132.3 Member Function Documentation

20.132.3.1 canConnectivityHostInitBeMerged()

Can connectivity host initialisation for this synapse group be merged with other? i.e. can they be performed using same generated code.

NOTE: this can only be called after model is finalized

20.132.3.2 canConnectivityInitBeMerged()

Can connectivity initialisation for this synapse group be merged with other? i.e. can they be performed using same generated code.

NOTE: this can only be called after model is finalized

20.132.3.3 canPSBeLinearlyCombined()

Can postsynaptic update component of this synapse group not only be merged with other, but combined so only one needs simulating at all.

NOTE: this can only be called after model is finalized

20.132.3.4 canPSBeMerged()

Can postsynaptic update component of this synapse group be merged with other? i.e. can they be simulated using same generated code.

NOTE: this can only be called after model is finalized

20.132.3.5 canPSInitBeMerged()

Can postsynaptic initialisation for this synapse group be merged with other? i.e. can they be performed using same generated code.

NOTE: this can only be called after model is finalized

20.132.3.6 canWUBeMerged()

Can weight update component of this synapse group be merged with other? i.e. can they be simulated using same generated code.

NOTE: this can only be called after model is finalized

20.132.3.7 canWUInitBeMerged()

Can initialisation for this synapse group be merged with other? i.e. can they be performed using same generated code.

NOTE: this can only be called after model is finalized

20.132.3.8 canWUPostBeMerged()

Can postsynaptic update of this synapse group be merged with other? i.e. can they be simulated using same generated code.

NOTE: this can only be called after model is finalized

20.132.3.9 canWUPostInitBeMerged()

Can initialisation for this synapse group's presynaptic variables be merged with other? i.e. can they be performed using same generated code.

NOTE: this can only be called after model is finalized

20.132.3.10 canWUPreBeMerged()

Can presynaptic update of this synapse group be merged with other? i.e. can they be simulated using same generated code.

NOTE: this can only be called after model is finalized

20.132.3.11 canWUPreInitBeMerged()

Can initialisation for this synapse group's presynaptic variables be merged with other? i.e. can they be performed using same generated code.

NOTE: this can only be called after model is finalized

20.132.3.12 getBackPropDelaySteps()

```
unsigned\ int\ SynapseGroup::getBackPropDelaySteps\ (\ )\ const\ [inline]
```

20.132.3.13 getConnectivityInitialiser()

```
const InitSparseConnectivitySnippet::Init& SynapseGroup::getConnectivityInitialiser ( ) const
[inline]
```

20.132.3.14 getDelaySteps()

```
unsigned int SynapseGroup::getDelaySteps ( ) const [inline]
```

```
20.132.3.15 getDendriticDelayLocation()
VarLocation SynapseGroup::getDendriticDelayLocation ( ) const [inline]
Get variable mode used for this synapse group's dendritic delay buffers.
20.132.3.16 getInSynLocation()
VarLocation SynapseGroup::getInSynLocation ( ) const [inline]
Get variable mode used for variables used to combine input from this synapse group.
20.132.3.17 getMatrixType()
SynapseMatrixType SynapseGroup::getMatrixType ( ) const [inline]
20.132.3.18 getMaxConnections()
unsigned int SynapseGroup::getMaxConnections ( ) const
20.132.3.19 getMaxDendriticDelayTimesteps()
unsigned int SynapseGroup::getMaxDendriticDelayTimesteps ( ) const [inline]
20.132.3.20 getMaxSourceConnections()
unsigned int SynapseGroup::getMaxSourceConnections ( ) const
20.132.3.21 getName()
const std::string& SynapseGroup::getName ( ) const [inline]
20.132.3.22 getNumThreadsPerSpike()
unsigned int SynapseGroup::getNumThreadsPerSpike ( ) const [inline]
20.132.3.23 getPSConstInitVals()
const std::vector< double > SynapseGroup::getPSConstInitVals ( ) const
20.132.3.24 getPSDerivedParams()
const std::vector<double>& SynapseGroup::getPSDerivedParams ( ) const [inline], [protected]
```

```
20.132.3.25 getPSExtraGlobalParamLocation() [1/2]
VarLocation SynapseGroup::getPSExtraGlobalParamLocation (
              const std::string & paramName ) const
Get location of postsynaptic model extra global parameter by name.
This is only used by extra global parameters which are pointers
20.132.3.26 getPSExtraGlobalParamLocation() [2/2]
VarLocation SynapseGroup::getPSExtraGlobalParamLocation (
              size_t index ) const [inline]
Get location of postsynaptic model extra global parameter by index.
This is only used by extra global parameters which are pointers
20.132.3.27 getPSModel()
const PostsynapticModels::Base* SynapseGroup::getPSModel ( ) const [inline]
20.132.3.28 getPSModelTargetName()
const std::string& SynapseGroup::getPSModelTargetName ( ) const [inline], [protected]
20.132.3.29 getPSParams()
\verb|const| std::vector<double>& SynapseGroup::getPSParams ( ) const [inline]|
20.132.3.30 getPSVarInitialisers()
const std::vector<Models::VarInit>& SynapseGroup::getPSVarInitialisers ( ) const [inline]
20.132.3.31 getPSVarLocation() [1/2]
VarLocation SynapseGroup::getPSVarLocation (
              const std::string & var ) const
Get location of postsynaptic model state variable.
20.132.3.32 getPSVarLocation() [2/2]
VarLocation SynapseGroup::getPSVarLocation (
              size_t index ) const [inline]
Get location of postsynaptic model state variable.
20.132.3.33 getSpanType()
SpanType SynapseGroup::getSpanType ( ) const [inline]
```

```
20.132.3.34 getSparseConnectivityExtraGlobalParamLocation() [1/2]
{\tt VarLocation} \  \, {\tt SynapseGroup::getSparseConnectivityExtraGlobalParamLocation} \  \, (
              const std::string & paramName ) const
Get location of sparse connectivity initialiser extra global parameter by name.
This is only used by extra global parameters which are pointers
20.132.3.35 getSparseConnectivityExtraGlobalParamLocation() [2/2]
VarLocation SynapseGroup::getSparseConnectivityExtraGlobalParamLocation (
              size_t index ) const
Get location of sparse connectivity initialiser extra global parameter by index.
This is only used by extra global parameters which are pointers
20.132.3.36 getSparseConnectivityLocation()
VarLocation SynapseGroup::getSparseConnectivityLocation ( ) const
Get variable mode used for sparse connectivity.
20.132.3.37 getSparseIndType()
std::string SynapseGroup::getSparseIndType ( ) const [protected]
Get the type to use for sparse connectivity indices for synapse group.
20.132.3.38 getSrcNeuronGroup() [1/2]
NeuronGroupInternal* SynapseGroup::getSrcNeuronGroup ( ) [inline], [protected]
20.132.3.39 getSrcNeuronGroup() [2/2]
const NeuronGroupInternal* SynapseGroup::getSrcNeuronGroup ( ) const [inline], [protected]
20.132.3.40 getTrgNeuronGroup() [1/2]
NeuronGroupInternal* SynapseGroup::getTrgNeuronGroup ( ) [inline], [protected]
20.132.3.41 getTrgNeuronGroup() [2/2]
const NeuronGroupInternal* SynapseGroup::getTrgNeuronGroup ( ) const [inline], [protected]
20.132.3.42 getWeightSharingMaster()
const SynapseGroupInternal* SynapseGroup::getWeightSharingMaster ( ) const [inline], [protected]
Does the event threshold needs to be retested in the synapse kernel?
```

```
20.132.3.43 getWUConstInitVals()
const std::vector< double > SynapseGroup::getWUConstInitVals ( ) const
20.132.3.44 getWUDerivedParams()
const std::vector<double>& SynapseGroup::getWUDerivedParams ( ) const [inline], [protected]
20.132.3.45 getWUExtraGlobalParamLocation() [1/2]
VarLocation SynapseGroup::getWUExtraGlobalParamLocation (
              const std::string & paramName ) const
Get location of weight update model extra global parameter by name.
This is only used by extra global parameters which are pointers
20.132.3.46 getWUExtraGlobalParamLocation() [2/2]
VarLocation SynapseGroup::getWUExtraGlobalParamLocation (
              size_t index ) const [inline]
Get location of weight update model extra global parameter by index.
This is only used by extra global parameters which are pointers
20.132.3.47 getWUModel()
const WeightUpdateModels::Base* SynapseGroup::getWUModel ( ) const [inline]
20.132.3.48 getWUParams()
const std::vector<double>& SynapseGroup::getWUParams ( ) const [inline]
20.132.3.49 getWUPostVarInitialisers()
const std::vector<Models::VarInit>& SynapseGroup::getWUPostVarInitialisers ( ) const [inline]
20.132.3.50 getWUPostVarLocation() [1/2]
VarLocation SynapseGroup::getWUPostVarLocation (
              const std::string & var ) const
Get location of weight update model postsynaptic state variable by name.
20.132.3.51 getWUPostVarLocation() [2/2]
VarLocation SynapseGroup::getWUPostVarLocation (
              size_t index ) const [inline]
```

Generated on September 3, 2020 for GeNN by Doxygen

Get location of weight update model postsynaptic state variable by index.

```
20.132.3.52 getWUPreVarInitialisers()
const std::vector<Models::VarInit>& SynapseGroup::getWUPreVarInitialisers ( ) const [inline]
20.132.3.53 getWUPreVarLocation() [1/2]
VarLocation SynapseGroup::getWUPreVarLocation (
              const std::string & var ) const
Get location of weight update model presynaptic state variable by name.
20.132.3.54 getWUPreVarLocation() [2/2]
VarLocation SynapseGroup::getWUPreVarLocation (
              size_t index ) const [inline]
Get location of weight update model presynaptic state variable by index.
20.132.3.55 getWUVarInitialisers()
const std::vector<Models::VarInit>& SynapseGroup::getWUVarInitialisers ( ) const [inline]
20.132.3.56 getWUVarLocation() [1/2]
VarLocation SynapseGroup::getWUVarLocation (
              const std::string & var ) const
Get location of weight update model per-synapse state variable by name.
20.132.3.57 getWUVarLocation() [2/2]
VarLocation SynapseGroup::getWUVarLocation (
              size_t index ) const
Get location of weight update model per-synapse state variable by index.
20.132.3.58 initDerivedParams()
void SynapseGroup::initDerivedParams (
              double dt ) [protected]
20.132.3.59 isDendriticDelayRequired()
bool SynapseGroup::isDendriticDelayRequired ( ) const
Does this synapse group require dendritic delay?
20.132.3.60 isEventThresholdReTestRequired()
bool SynapseGroup::isEventThresholdReTestRequired ( ) const [inline], [protected]
This is required when the pre-synaptic neuron population's outgoing synapse groups require different event thresh-
```

```
old
20.132.3.61 isHostInitRNGRequired()
bool SynapseGroup::isHostInitRNGRequired ( ) const
Does this synapse group require a RNG for any sort of initialization.
20.132.3.62 isProceduralConnectivityRNGRequired()
bool SynapseGroup::isProceduralConnectivityRNGRequired ( ) const
Does this synapse group require an RNG to generate procedural connectivity?
20.132.3.63 isPSInitRNGRequired()
bool SynapseGroup::isPSInitRNGRequired ( ) const
Does this synapse group require an RNG for it's postsynaptic init code?
20.132.3.64 isPSModelMerged()
bool SynapseGroup::isPSModelMerged ( ) const [inline], [protected]
20.132.3.65 isSparseConnectivityInitRequired()
bool SynapseGroup::isSparseConnectivityInitRequired ( ) const
Is sparse connectivity initialisation code required for this synapse group?
20.132.3.66 isSpikeEventRequired()
bool SynapseGroup::isSpikeEventRequired ( ) const
Does synapse group need to handle spike-like events.
20.132.3.67 isTrueSpikeRequired()
bool SynapseGroup::isTrueSpikeRequired ( ) const
Does synapse group need to handle 'true' spikes.
20.132.3.68 isWeightSharingSlave()
bool SynapseGroup::isWeightSharingSlave ( ) const [inline]
Is this synapse group a weight-sharing slave.
20.132.3.69 isWUInitRNGRequired()
```

bool SynapseGroup::isWUInitRNGRequired () const

Does this synapse group require an RNG for it's weight update init code?

20.132.3.70 isWUPostInitRNGRequired()

```
bool SynapseGroup::isWUPostInitRNGRequired ( ) const
```

Does this synapse group require an RNG for it's weight update postsynaptic variable init code?

20.132.3.71 isWUPreInitRNGRequired()

```
bool SynapseGroup::isWUPreInitRNGRequired ( ) const
```

Does this synapse group require an RNG for it's weight update presynaptic variable init code?

20.132.3.72 isWUVarInitRequired()

```
bool SynapseGroup::isWUVarInitRequired ( ) const
```

Is var init code required for any variables in this synapse group's weight update model?

20.132.3.73 isZeroCopyEnabled()

```
bool SynapseGroup::isZeroCopyEnabled ( ) const
```

20.132.3.74 setBackPropDelaySteps()

Sets the number of delay steps used to delay postsynaptic spikes travelling back along dendrites to synapses.

20.132.3.75 setDendriticDelayLocation()

Set variable mode used for this synapse group's dendritic delay buffers.

20.132.3.76 setEventThresholdReTestRequired()

20.132.3.77 setInSynVarLocation()

Set location of variables used to combine input from this synapse group.

This is ignored for simulations on hardware with a single memory space

20.132.3.78 setMaxConnections()

```
void SynapseGroup::setMaxConnections (
          unsigned int maxConnections )
```

Sets the maximum number of target neurons any source neurons can connect to.

Use with synaptic matrix types with SynapseMatrixConnectivity::SPARSE to optimise CUDA implementation

20.132.3.79 setMaxDendriticDelayTimesteps()

Sets the maximum dendritic delay for synapses in this synapse group.

20.132.3.80 setMaxSourceConnections()

Sets the maximum number of source neurons any target neuron can connect to.

Use with synaptic matrix types with SynapseMatrixConnectivity::SPARSE and postsynaptic learning to optimise CUDA implementation

20.132.3.81 setNarrowSparseIndEnabled()

Enables or disables using narrow i.e. less than 32-bit types for sparse matrix indices.

20.132.3.82 setNumThreadsPerSpike()

Set how many threads CUDA implementation uses to process each spike when span type is PRESYNAPTIC.

20.132.3.83 setPSExtraGlobalParamLocation()

Set location of postsynaptic model extra global parameter.

This is ignored for simulations on hardware with a single memory space and only applies to extra global parameters which are pointers.

20.132.3.84 setPSModelMergeTarget()

20.132.3.85 setPSVarLocation()

```
void SynapseGroup::setPSVarLocation (
```

```
const std::string & varName,
VarLocation loc )
```

Set location of postsynaptic model state variable.

This is ignored for simulations on hardware with a single memory space

```
20.132.3.86 setSpanType()
```

Set how CUDA implementation is parallelised.

with a thread per target neuron (default) or a thread per source spike

20.132.3.87 setSparseConnectivityExtraGlobalParamLocation()

Set location of sparse connectivity initialiser extra global parameter.

This is ignored for simulations on hardware with a single memory space and only applies to extra global parameters which are pointers.

20.132.3.88 setSparseConnectivityLocation()

Set variable mode used for sparse connectivity.

This is ignored for simulations on hardware with a single memory space

20.132.3.89 setWUExtraGlobalParamLocation()

Set location of weight update model extra global parameter.

This is ignored for simulations on hardware with a single memory space and only applies to extra global parameters which are pointers.

20.132.3.90 setWUPostVarLocation()

Set location of weight update model postsynaptic state variable.

This is ignored for simulations on hardware with a single memory space

20.132.3.91 setWUPreVarLocation()

Set location of weight update model presynaptic state variable.

This is ignored for simulations on hardware with a single memory space

20.132.3.92 setWUVarLocation()

Set location of weight update model state variable.

This is ignored for simulations on hardware with a single memory space

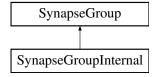
The documentation for this class was generated from the following files:

- · synapseGroup.h
- synapseGroup.cc

20.133 SynapseGroupInternal Class Reference

```
#include <synapseGroupInternal.h>
```

Inheritance diagram for SynapseGroupInternal:



Public Member Functions

SynapseGroupInternal (const std::string &name, const SynapseGroupInternal *weightSharingMaster, SynapseMatrixType matrixType, unsigned int delaySteps, const WeightUpdateModels::Base *wu, const std::vector< double > &wuParams, const std::vector< Models::VarInit > &wuVarInitialisers, const std::vector< Models::VarInit > &wuPostVarInitialisers, const std::vector< Models::VarInit > &wuPostVarInitialisers, const PostsynapticModels::Base *ps, const std::vector< double > &psParams, const std::vector< Models::VarInit > &psVarInitialisers, NeuronGroupInternal *srcNeuronGroup, NeuronGroupInternal *trgNeuronGroup, const InitSparseConnectivitySnippet::Init &connectivityInitialiser, VarLocation defaultVarLocation, VarLocation defaultExtraGlobalParamLocation, VarLocation defaultSparseConnectivityLocation, bool defaultNarrow← SparseIndEnabled)

Additional Inherited Members

20.133.1 Constructor & Destructor Documentation

20.133.1.1 SynapseGroupInternal()

```
const PostsynapticModels::Base * ps,
const std::vector< double > & psParams,
const std::vector< Models::VarInit > & psVarInitialisers,
NeuronGroupInternal * srcNeuronGroup,
NeuronGroupInternal * trgNeuronGroup,
const InitSparseConnectivitySnippet::Init & connectivityInitialiser,
VarLocation defaultVarLocation,
VarLocation defaultExtraGlobalParamLocation,
VarLocation defaultSparseConnectivityLocation,
bool defaultNarrowSparseIndEnabled ) [inline]
```

The documentation for this class was generated from the following file:

· synapseGroupInternal.h

20.134 CodeGenerator::SynapseGroupMergedBase Class Reference

```
#include <groupMerged.h>
```

Inheritance diagram for CodeGenerator::SynapseGroupMergedBase:



Public Member Functions

- std::string getPresynapticAxonalDelaySlot () const
- std::string getPostsynapticBackPropDelaySlot () const
- std::string getDendriticDelayOffset (const std::string &offset="") const
- bool isWUParamHeterogeneous (size_t paramIndex) const

Should the weight update model parameter be implemented heterogeneously?

bool isWUDerivedParamHeterogeneous (size_t paramIndex) const

Should the weight update model derived parameter be implemented heterogeneously?

• bool isWUGlobalVarHeterogeneous (size_t varIndex) const

Should the GLOBALG weight update model variable be implemented heterogeneously?

bool isWUVarInitParamHeterogeneous (size_t varIndex, size_t paramIndex) const

Should the weight update model variable initialization parameter be implemented heterogeneously?

• bool isWUVarInitDerivedParamHeterogeneous (size_t varIndex, size_t paramIndex) const

Should the weight update model variable initialization derived parameter be implemented heterogeneously?

• bool isConnectivityInitParamHeterogeneous (size_t paramIndex) const

Should the connectivity initialization parameter be implemented heterogeneously?

• bool isConnectivityInitDerivedParamHeterogeneous (size_t paramIndex) const

Should the connectivity initialization parameter be implemented heterogeneously?

bool isSrcNeuronParamHeterogeneous (size t paramIndex) const

Is presynaptic neuron parameter heterogeneous.

• bool isSrcNeuronDerivedParamHeterogeneous (size_t paramIndex) const

Is presynaptic neuron derived parameter heterogeneous.

bool isTrgNeuronParamHeterogeneous (size_t paramIndex) const

Is postsynaptic neuron parameter heterogeneous.

bool isTrgNeuronDerivedParamHeterogeneous (size_t paramIndex) const

Is postsynaptic neuron derived parameter heterogeneous.

Protected Types

enum Role {
 Role::PresynapticUpdate, Role::PostsynapticUpdate, Role::SynapseDynamics, Role::DenseInit,
 Role::SparseInit }

Protected Member Functions

- SynapseGroupMergedBase (size_t index, const std::vector< std::reference_wrapper< const Synapse
 GroupInternal >> &groups)
- virtual std::string getArchetypeCode () const
- void generate (const BackendBase &backend, CodeStream &definitionsInternal, CodeStream &definitions
 — InternalFunc, CodeStream &definitionsInternalVar, CodeStream &runnerVarDecl, CodeStream &runner
 — MergedStructAlloc, MergedStructData &mergedStructData, const std::string &precision, const std::string &timePrecision, const std::string &name, Role role) const

Additional Inherited Members

20.134.1 Member Enumeration Documentation

20.134.1.1 Role

```
enum CodeGenerator::SynapseGroupMergedBase::Role [strong], [protected]
```

Enumerator

PresynapticUpdate	
PostsynapticUpdate	
SynapseDynamics	
Denselnit	
SparseInit	

20.134.2 Constructor & Destructor Documentation

20.134.2.1 SynapseGroupMergedBase()

20.134.3 Member Function Documentation

20.134.3.1 generate()

```
CodeStream & definitionsInternalFunc,
CodeStream & definitionsInternalVar,
CodeStream & runnerVarDecl,
CodeStream & runnerMergedStructAlloc,
MergedStructData & mergedStructData,
const std::string & precision,
const std::string & timePrecision,
const std::string & name,
Role role ) const [protected]
```

20.134.3.2 getArchetypeCode()

```
virtual std::string CodeGenerator::SynapseGroupMergedBase::getArchetypeCode ( ) const [inline],
[protected], [virtual]
```

Reimplemented in CodeGenerator::SynapseDynamicsGroupMerged, CodeGenerator::PostsynapticUpdateGroup Merged, and CodeGenerator::PresynapticUpdateGroupMerged.

20.134.3.3 getDendriticDelayOffset()

20.134.3.4 getPostsynapticBackPropDelaySlot()

```
std::string CodeGenerator::SynapseGroupMergedBase::getPostsynapticBackPropDelaySlot ( ) const
```

Get the expression to calculate the delay slot for accessing Postsynaptic neuron state variables, taking into account back propagation delay

20.134.3.5 getPresynapticAxonalDelaySlot()

```
std::string CodeGenerator::SynapseGroupMergedBase::getPresynapticAxonalDelaySlot ( ) const
```

Get the expression to calculate the delay slot for accessing Presynaptic neuron state variables, taking into account axonal delay

20.134.3.6 isConnectivityInitDerivedParamHeterogeneous()

```
\label{local_code_Generator} bool \ \ Code Generator :: Synapse Group Merged Base :: is Connectivity Init Derived Param Heterogeneous \ ( \\ size\_t \ param Index \ ) \ const
```

Should the connectivity initialization parameter be implemented heterogeneously?

20.134.3.7 isConnectivityInitParamHeterogeneous()

```
\label{local_param} bool \ \ Code Generator:: Synapse Group Merged Base:: is Connectivity Init Param Heterogeneous \ ( \\ size\_t \ param Index \ ) \ const
```

Should the connectivity initialization parameter be implemented heterogeneously?

20.134.3.8 isSrcNeuronDerivedParamHeterogeneous()

Is presynaptic neuron derived parameter heterogeneous.

```
20.134.3.9 isSrcNeuronParamHeterogeneous()
```

```
\label{local_code_Generator} bool \ \mbox{CodeGenerator::SynapseGroupMergedBase::} is SrcNeuronParamHeterogeneous \ ( \\ \mbox{size\_t} \ paramIndex \ ) \ \mbox{const}
```

Is presynaptic neuron parameter heterogeneous.

20.134.3.10 isTrgNeuronDerivedParamHeterogeneous()

```
\label{local_param} bool \ \ Code Generator:: Synapse Group Merged Base:: is TrgNeuron Derived Param Heterogeneous \ ( \\ size\_t \ param Index \ ) \ const
```

Is postsynaptic neuron derived parameter heterogeneous.

20.134.3.11 isTrgNeuronParamHeterogeneous()

Is postsynaptic neuron parameter heterogeneous.

20.134.3.12 isWUDerivedParamHeterogeneous()

```
\label{thm:bool} \begin{tabular}{ll} bool & CodeGenerator::SynapseGroupMergedBase::isWUDerivedParamHeterogeneous ( & size\_t & paramIndex ) & const \\ \end{tabular}
```

Should the weight update model derived parameter be implemented heterogeneously?

20.134.3.13 isWUGlobalVarHeterogeneous()

```
\label{local_code_generator} bool \ \mbox{CodeGenerator::SynapseGroupMergedBase::isWUGlobalVarHeterogeneous} \ ( \\ \mbox{size\_t} \ \ \mbox{varIndex} \ ) \ \mbox{const}
```

Should the GLOBALG weight update model variable be implemented heterogeneously?

20.134.3.14 isWUParamHeterogeneous()

Should the weight update model parameter be implemented heterogeneously?

20.134.3.15 isWUVarInitDerivedParamHeterogeneous()

Should the weight update model variable initialization derived parameter be implemented heterogeneously?

20.134.3.16 isWUVarInitParamHeterogeneous()

Should the weight update model variable initialization parameter be implemented heterogeneously?

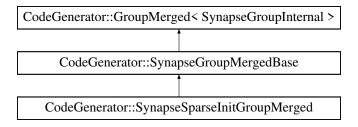
The documentation for this class was generated from the following files:

- · groupMerged.h
- · groupMerged.cc

20.135 CodeGenerator::SynapseSparseInitGroupMerged Class Reference

```
#include <groupMerged.h>
```

Inheritance diagram for CodeGenerator::SynapseSparseInitGroupMerged:



Public Member Functions

- SynapseSparseInitGroupMerged (size_t index, const std::vector < std::reference_wrapper < const Synapse ←
 GroupInternal >> &groups)
- void generate (const BackendBase &backend, CodeStream &definitionsInternal, CodeStream &definitions
 — InternalFunc, CodeStream &definitionsInternalVar, CodeStream &runnerVarDecl, CodeStream &runner
 — MergedStructAlloc, MergedStructData &mergedStructData, const std::string &precision, const std::string &timePrecision) const

Additional Inherited Members

20.135.1 Constructor & Destructor Documentation

20.135.1.1 SynapseSparseInitGroupMerged()

20.135.2 Member Function Documentation

20.135.2.1 generate()

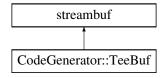
The documentation for this class was generated from the following file:

· groupMerged.h

20.136 CodeGenerator::TeeBuf Class Reference

```
#include <teeStream.h>
```

Inheritance diagram for CodeGenerator::TeeBuf:



Public Member Functions

template<typename... T>
 TeeBuf (T &&... streamBufs)

20.136.1 Constructor & Destructor Documentation

```
20.136.1.1 TeeBuf()
```

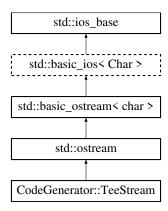
The documentation for this class was generated from the following file:

· teeStream.h

20.137 CodeGenerator::TeeStream Class Reference

```
#include <teeStream.h>
```

Inheritance diagram for CodeGenerator::TeeStream:



Public Member Functions

template<typename... T>
 TeeStream (T &&... streamBufs)

20.137.1 Constructor & Destructor Documentation

20.137.1.1 TeeStream()

The documentation for this class was generated from the following file:

• teeStream.h

20.138 SpineMLSimulator::Timer Class Reference

```
#include <timer.h>
```

Public Member Functions

- Timer (const std::string &title)
- ∼Timer ()
- · double get () const

20.138.1 Constructor & Destructor Documentation

20.138.1.1 Timer()

20.138.1.2 \sim Timer()

 ${\tt SpineMLSimulator::Timer::} {\sim} {\tt Timer} \ \ (\) \quad [{\tt inline}]$

20.138.2 Member Function Documentation

```
20.138.2.1 get()
double SpineMLSimulator::Timer::get ( ) const [inline]
```

The documentation for this class was generated from the following file:

• include/spineml/simulator/timer.h

20.139 Timer Class Reference

A generic timer which can give the current elapsed time.

```
#include <timer.h>
```

Public Member Functions

Timer (const std::string &message, const std::string &filename="")

Create a new Timer with the specified name and optionally a filename to append time to.

• ∼Timer ()

Stop the timer and print current elapsed time to terminal.

• double get () const

Get the elapsed time since this object was created.

20.139.1 Detailed Description

A generic timer which can give the current elapsed time.

20.139.2 Constructor & Destructor Documentation

Create a new Timer with the specified name and optionally a filename to append time to.

```
20.139.2.2 \simTimer()

Timer::\simTimer ( ) [inline]
```

Stop the timer and print current elapsed time to terminal.

20.139.3 Member Function Documentation

```
20.139.3.1 get()
double Timer::get ( ) const [inline]
```

Get the elapsed time since this object was created.

The documentation for this class was generated from the following file:

· userproject/include/timer.h

20.140 SpineMLSimulator::TimerAccumulate Class Reference

```
#include <timer.h>
```

Public Member Functions

- TimerAccumulate (double &accumulator)
- ∼TimerAccumulate ()
- · double get () const

20.140.1 Constructor & Destructor Documentation

20.140.1.1 TimerAccumulate()

20.140.1.2 ~TimerAccumulate()

```
SpineMLSimulator::TimerAccumulate::~TimerAccumulate ( ) [inline]
```

20.140.2 Member Function Documentation

```
20.140.2.1 get()
```

```
double SpineMLSimulator::TimerAccumulate::get ( ) const [inline]
```

The documentation for this class was generated from the following file:

· include/spineml/simulator/timer.h

20.141 TimerAccumulate Class Reference

A timer which adds its elapsed time to an accumulator variable on destruction.

```
#include <timer.h>
```

Public Member Functions

- TimerAccumulate (double &accumulator)
- ∼TimerAccumulate ()

· double get () const

Get the elapsed time since this object was created.

20.141.1 Detailed Description

A timer which adds its elapsed time to an accumulator variable on destruction.

20.141.2 Constructor & Destructor Documentation

20.141.2.1 TimerAccumulate()

20.141.2.2 ~TimerAccumulate()

```
TimerAccumulate::~TimerAccumulate ( ) [inline]
```

20.141.3 Member Function Documentation

20.141.3.1 get()

```
double TimerAccumulate::get ( ) const [inline]
```

Get the elapsed time since this object was created.

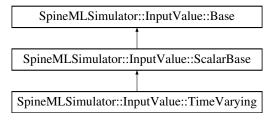
The documentation for this class was generated from the following file:

userproject/include/timer.h

20.142 SpineMLSimulator::InputValue::TimeVarying Class Reference

```
#include <inputValue.h>
```

Inheritance diagram for SpineMLSimulator::InputValue::TimeVarying:



Public Member Functions

- TimeVarying (double dt, unsigned int numNeurons, const pugi::xml_node &node)
- virtual void update (double dt, unsigned long long timestep, std::function< void(unsigned int, double)> applyValueFunc) override

Additional Inherited Members

20.142.1 Constructor & Destructor Documentation

20.142.1.1 TimeVarying()

20.142.2 Member Function Documentation

20.142.2.1 update()

Implements SpineMLSimulator::InputValue::Base.

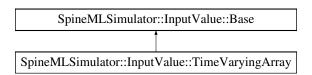
The documentation for this class was generated from the following files:

- inputValue.h
- · inputValue.cc

20.143 SpineMLSimulator::InputValue::TimeVaryingArray Class Reference

```
#include <inputValue.h>
```

Inheritance diagram for SpineMLSimulator::InputValue::TimeVaryingArray:



Public Member Functions

- TimeVaryingArray (double dt, unsigned int numNeurons, const pugi::xml_node &node)
- virtual void update (double dt, unsigned long long timestep, std::function< void(unsigned int, double)> applyValueFunc) override

Additional Inherited Members

20.143.1 Constructor & Destructor Documentation

20.143.1.1 TimeVaryingArray()

20.143.2 Member Function Documentation

20.143.2.1 update()

Implements SpineMLSimulator::InputValue::Base.

The documentation for this class was generated from the following files:

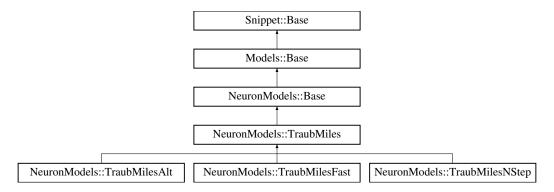
- inputValue.h
- · inputValue.cc

20.144 NeuronModels::TraubMiles Class Reference

Hodgkin-Huxley neurons with Traub & Miles algorithm.

```
#include <neuronModels.h>
```

Inheritance diagram for NeuronModels::TraubMiles:



Public Types

- typedef Snippet::ValueBase< 7 > ParamValues
- typedef Models::VarInitContainerBase< 4 > VarValues
- typedef Models::VarInitContainerBase< 0 > PreVarValues
- typedef Models::VarInitContainerBase< 0 > PostVarValues

Public Member Functions

• virtual std::string getSimCode () const override

Gets the code that defines the execution of one timestep of integration of the neuron model.

- virtual std::string getThresholdConditionCode () const override
 - Gets code which defines the condition for a true spike in the described neuron model.
- virtual StringVec getParamNames () const override
 - Gets names of of (independent) model parameters.
- virtual VarVec getVars () const override
 - Gets names and types (as strings) of model variables.

Static Public Member Functions

static const NeuronModels::TraubMiles * getInstance ()

Additional Inherited Members

20.144.1 Detailed Description

Hodgkin-Huxley neurons with Traub & Miles algorithm.

This conductance based model has been taken from [10] and can be described by the equations:

$$\begin{split} C\frac{dV}{dt} &= -I_{\text{Na}} - I_K - I_{\text{leak}} - I_M - I_{i,DC} - I_{i,\text{syn}} - I_i, \\ I_{\text{Na}}(t) &= g_{\text{Na}} m_i(t)^3 h_i(t) (V_i(t) - E_{\text{Na}}) \\ I_{\text{K}}(t) &= g_{\text{K}} n_i(t)^4 (V_i(t) - E_{\text{K}}) \\ \frac{dy(t)}{dt} &= \alpha_y(V(t)) (1 - y(t)) - \beta_y(V(t)) y(t), \end{split}$$

where $y_i = m, h, n$, and

$$\begin{array}{rcl}
\alpha_n & = & 0.032(-50-V)/\big(\exp((-50-V)/5)-1\big) \\
\beta_n & = & 0.5\exp((-55-V)/40) \\
\alpha_m & = & 0.32(-52-V)/\big(\exp((-52-V)/4)-1\big) \\
\beta_m & = & 0.28(25+V)/\big(\exp((25+V)/5)-1\big) \\
\alpha_h & = & 0.128\exp((-48-V)/18) \\
\beta_h & = & 4/\big(\exp((-25-V)/5)+1\big).
\end{array}$$

and typical parameters are C=0.143 nF, $g_{\rm leak}=0.02672~\mu$ S, $E_{\rm leak}=-63.563$ mV, $g_{\rm Na}=7.15~\mu$ S, $E_{\rm Na}=50$ mV, $g_{\rm K}=1.43~\mu$ S, $E_{\rm K}=-95$ mV.

It has 4 variables:

- ∨ membrane potential E
- m probability for Na channel activation m
- · h probability for not Na channel blocking h
- n probability for K channel activation n

and 7 parameters:

- gNa Na conductance in 1/(mOhms * cm²)
- ENa Na equi potential in mV
- gK K conductance in 1/(mOhms * cm^{^2})
- EK K equi potential in mV

- gl Leak conductance in 1/(mOhms * cm^2)
- El Leak equi potential in mV
- Cmem Membrane capacity density in muF/cm²

Note

Internally, the ordinary differential equations defining the model are integrated with a linear Euler algorithm and GeNN integrates 25 internal time steps for each neuron for each network time step. I.e., if the network is simulated at DT= 0.1 ms, then the neurons are integrated with a linear Euler algorithm with 1DT= 0.004 ms. This variant uses IF statements to check for a value at which a singularity would be hit. If so, value calculated by L'Hospital rule is used.

20.144.2 Member Typedef Documentation

20.144.2.1 ParamValues typedef Snippet::ValueBase< 7 > NeuronModels::TraubMiles::ParamValues 20.144.2.2 PostVarValues typedef Models::VarInitContainerBase<0> NeuronModels::TraubMiles::PostVarValues 20.144.2.3 PreVarValues typedef Models::VarInitContainerBase<0> NeuronModels::TraubMiles::PreVarValues

20.144.2.4 VarValues

```
typedef Models::VarInitContainerBase< 4 > NeuronModels::TraubMiles::VarValues
```

20.144.3 Member Function Documentation

20.144.3.1 getInstance()

```
static const NeuronModels::TraubMiles* NeuronModels::TraubMiles::getInstance ( ) [inline],
[static]
```

20.144.3.2 getParamNames()

```
virtual StringVec NeuronModels::TraubMiles::getParamNames ( ) const [inline], [override],
[virtual]
```

Gets names of of (independent) model parameters.

Reimplemented from Snippet::Base.

Reimplemented in NeuronModels::TraubMilesNStep.

20.144.3.3 getSimCode()

virtual std::string NeuronModels::TraubMiles::getSimCode () const [inline], [override],
[virtual]

Gets the code that defines the execution of one timestep of integration of the neuron model.

The code will refer to for the value of the variable with name "NN". It needs to refer to the predefined variable "ISYN", i.e. contain, if it is to receive input.

Reimplemented from NeuronModels::Base.

Reimplemented in NeuronModels::TraubMilesNStep, NeuronModels::TraubMilesAlt, and NeuronModels::Traub⊷ MilesFast.

20.144.3.4 getThresholdConditionCode()

virtual std::string NeuronModels::TraubMiles::getThresholdConditionCode () const [inline],
[override], [virtual]

Gets code which defines the condition for a true spike in the described neuron model.

This evaluates to a bool (e.g. "V > 20").

Reimplemented from NeuronModels::Base.

20.144.3.5 getVars()

virtual VarVec NeuronModels::TraubMiles::getVars () const [inline], [override], [virtual]

Gets names and types (as strings) of model variables.

Reimplemented from Models::Base.

The documentation for this class was generated from the following file:

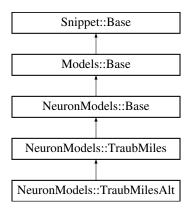
· neuronModels.h

20.145 NeuronModels::TraubMilesAlt Class Reference

Hodgkin-Huxley neurons with Traub & Miles algorithm.

#include <neuronModels.h>

Inheritance diagram for NeuronModels::TraubMilesAlt:



Public Types

- typedef Snippet::ValueBase< 7 > ParamValues
- typedef Models::VarInitContainerBase< 4 > VarValues
- typedef Models::VarInitContainerBase< 0 > PreVarValues
- typedef Models::VarInitContainerBase< 0 > PostVarValues

Public Member Functions

• virtual std::string getSimCode () const override

Gets the code that defines the execution of one timestep of integration of the neuron model.

Static Public Member Functions

• static const NeuronModels::TraubMilesAlt * getInstance ()

Additional Inherited Members

20.145.1 Detailed Description

Hodgkin-Huxley neurons with Traub & Miles algorithm.

Using a workaround to avoid singularity: adding the munimum numerical value of the floating point precision used.

20.145.2 Member Typedef Documentation

20.145.2.1 ParamValues

```
typedef Snippet::ValueBase< 7 > NeuronModels::TraubMilesAlt::ParamValues
```

20.145.2.2 PostVarValues

typedef Models::VarInitContainerBase<0> NeuronModels::TraubMilesAlt::PostVarValues

20.145.2.3 PreVarValues

typedef Models::VarInitContainerBase<0> NeuronModels::TraubMilesAlt::PreVarValues

20.145.2.4 VarValues

typedef Models::VarInitContainerBase< 4 > NeuronModels::TraubMilesAlt::VarValues

20.145.3 Member Function Documentation

20.145.3.1 getInstance()

static const NeuronModels::TraubMilesAlt* NeuronModels::TraubMilesAlt::getInstance () [inline],
[static]

20.145.3.2 getSimCode()

virtual std::string NeuronModels::TraubMilesAlt::getSimCode () const [inline], [override],
[virtual]

Gets the code that defines the execution of one timestep of integration of the neuron model.

The code will refer to for the value of the variable with name "NN". It needs to refer to the predefined variable "ISYN", i.e. contain, if it is to receive input.

Reimplemented from NeuronModels::TraubMiles.

The documentation for this class was generated from the following file:

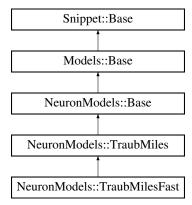
neuronModels.h

20.146 NeuronModels::TraubMilesFast Class Reference

Hodgkin-Huxley neurons with Traub & Miles algorithm: Original fast implementation, using 25 inner iterations.

#include <neuronModels.h>

Inheritance diagram for NeuronModels::TraubMilesFast:



Public Types

- typedef Snippet::ValueBase< 7 > ParamValues
- typedef Models::VarInitContainerBase< 4 > VarValues
- typedef Models::VarInitContainerBase< 0 > PreVarValues
- typedef Models::VarInitContainerBase< 0 > PostVarValues

Public Member Functions

virtual std::string getSimCode () const override
 Gets the code that defines the execution of one timestep of integration of the neuron model.

Static Public Member Functions

• static const NeuronModels::TraubMilesFast * getInstance ()

Additional Inherited Members

20.146.1 Detailed Description

Hodgkin-Huxley neurons with Traub & Miles algorithm: Original fast implementation, using 25 inner iterations.

There are singularities in this model, which can be easily hit in float precision

20.146.2 Member Typedef Documentation

20.146.2.1 ParamValues

```
typedef Snippet::ValueBase< 7 > NeuronModels::TraubMilesFast::ParamValues
```

20.146.2.2 PostVarValues

```
typedef Models::VarInitContainerBase<0> NeuronModels::TraubMilesFast::PostVarValues
```

20.146.2.3 PreVarValues

```
typedef Models::VarInitContainerBase<0> NeuronModels::TraubMilesFast::PreVarValues
```

20.146.2.4 VarValues

```
typedef Models::VarInitContainerBase< 4 > NeuronModels::TraubMilesFast::VarValues
```

20.146.3 Member Function Documentation

20.146.3.1 getInstance()

```
static const NeuronModels::TraubMilesFast* NeuronModels::TraubMilesFast::getInstance ( ) [inline],
[static]
```

20.146.3.2 getSimCode()

```
virtual std::string NeuronModels::TraubMilesFast::getSimCode ( ) const [inline], [override],
[virtual]
```

Gets the code that defines the execution of one timestep of integration of the neuron model.

The code will refer to for the value of the variable with name "NN". It needs to refer to the predefined variable "ISYN", i.e. contain, if it is to receive input.

Reimplemented from NeuronModels::TraubMiles.

The documentation for this class was generated from the following file:

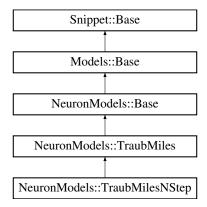
· neuronModels.h

20.147 NeuronModels::TraubMilesNStep Class Reference

Hodgkin-Huxley neurons with Traub & Miles algorithm.

#include <neuronModels.h>

Inheritance diagram for NeuronModels::TraubMilesNStep:



Public Types

- typedef Snippet::ValueBase< 8 > ParamValues
- typedef Models::VarInitContainerBase< 4 > VarValues
- typedef Models::VarInitContainerBase< 0 > PreVarValues
- typedef Models::VarInitContainerBase< 0 > PostVarValues

Public Member Functions

virtual std::string getSimCode () const override

Gets the code that defines the execution of one timestep of integration of the neuron model.

virtual StringVec getParamNames () const override

Gets names of of (independent) model parameters.

Static Public Member Functions

static const NeuronModels::TraubMilesNStep * getInstance ()

Additional Inherited Members

20.147.1 Detailed Description

Hodgkin-Huxley neurons with Traub & Miles algorithm.

Same as standard TraubMiles model but number of inner loops can be set using a parameter

20.147.2 Member Typedef Documentation

20.147.2.1 ParamValues

typedef Snippet::ValueBase< 8 > NeuronModels::TraubMilesNStep::ParamValues

20.147.2.2 PostVarValues

typedef Models::VarInitContainerBase<0> NeuronModels::TraubMilesNStep::PostVarValues

20.147.2.3 PreVarValues

typedef Models::VarInitContainerBase<0> NeuronModels::TraubMilesNStep::PreVarValues

20.147.2.4 VarValues

typedef Models::VarInitContainerBase< 4 > NeuronModels::TraubMilesNStep::VarValues

20.147.3 Member Function Documentation

20.147.3.1 getInstance()

```
static const NeuronModels::TraubMilesNStep* NeuronModels::TraubMilesNStep::getInstance ( )
[inline], [static]
```

20.147.3.2 getParamNames()

virtual StringVec NeuronModels::TraubMilesNStep::getParamNames () const [inline], [override],
[virtual]

Gets names of of (independent) model parameters.

Reimplemented from NeuronModels::TraubMiles.

20.147.3.3 getSimCode()

```
virtual std::string NeuronModels::TraubMilesNStep::getSimCode ( ) const [inline], [override],
[virtual]
```

Gets the code that defines the execution of one timestep of integration of the neuron model.

The code will refer to for the value of the variable with name "NN". It needs to refer to the predefined variable "ISYN", i.e. contain, if it is to receive input.

Reimplemented from NeuronModels::TraubMiles.

The documentation for this class was generated from the following file:

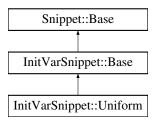
· neuronModels.h

20.148 InitVarSnippet::Uniform Class Reference

Initialises variable by sampling from the uniform distribution.

```
#include <initVarSnippet.h>
```

Inheritance diagram for InitVarSnippet::Uniform:



Public Member Functions

- DECLARE_SNIPPET (InitVarSnippet::Uniform, 2)
- SET_CODE ("const scalar scale = \$(max) \$(min);\ "\$(value)=\$(min)+(\$(gennrand_uniform) *scale);")
- virtual StringVec getParamNames () const override

Gets names of of (independent) model parameters.

Additional Inherited Members

20.148.1 Detailed Description

Initialises variable by sampling from the uniform distribution.

This snippet takes 2 parameters:

- min The minimum value
- max The maximum value

20.148.2 Member Function Documentation

20.148.2.1 DECLARE_SNIPPET()

20.148.2.2 getParamNames()

```
virtual StringVec InitVarSnippet::Uniform::getParamNames ( ) const [inline], [override],
[virtual]
```

Gets names of of (independent) model parameters.

Reimplemented from Snippet::Base.

```
20.148.2.3 SET_CODE()
```

The documentation for this class was generated from the following file:

• initVarSnippet.h

20.149 SpineMLSimulator::ModelProperty::UniformDistribution Class Reference

```
#include <modelProperty.h>
```

Inheritance diagram for SpineMLSimulator::ModelProperty::UniformDistribution:

```
SpineMLSimulator::ModelProperty::Base

SpineMLSimulator::ModelProperty::UniformDistribution
```

Public Member Functions

- UniformDistribution (const pugi::xml_node &node, const StateVar < scalar > &stateVar, unsigned int size)
- void setValue (scalar min, scalar max)

20.149.1 Constructor & Destructor Documentation

20.149.1.1 UniformDistribution()

20.149.2 Member Function Documentation

20.149.2.1 setValue()

The documentation for this class was generated from the following files:

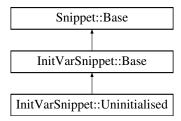
- modelProperty.h
- · modelProperty.cc

20.150 InitVarSnippet::Uninitialised Class Reference

Used to mark variables as uninitialised - no initialisation code will be run.

```
#include <initVarSnippet.h>
```

Inheritance diagram for InitVarSnippet::Uninitialised:



Public Member Functions

• DECLARE_SNIPPET (InitVarSnippet::Uninitialised, 0)

Additional Inherited Members

20.150.1 Detailed Description

Used to mark variables as uninitialised - no initialisation code will be run.

20.150.2 Member Function Documentation

20.150.2.1 DECLARE_SNIPPET()

The documentation for this class was generated from the following file:

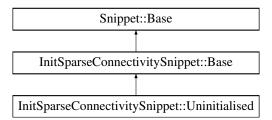
• initVarSnippet.h

20.151 InitSparseConnectivitySnippet::Uninitialised Class Reference

Used to mark connectivity as uninitialised - no initialisation code will be run.

```
#include <initSparseConnectivitySnippet.h>
```

Inheritance diagram for InitSparseConnectivitySnippet::Uninitialised:



Public Member Functions

• DECLARE_SNIPPET (InitSparseConnectivitySnippet::Uninitialised, 0)

Additional Inherited Members

20.151.1 Detailed Description

Used to mark connectivity as uninitialised - no initialisation code will be run.

20.151.2 Member Function Documentation

20.151.2.1 DECLARE_SNIPPET()

The documentation for this class was generated from the following file:

· initSparseConnectivitySnippet.h

20.152 Snippet::ValueBase < NumVars > Class Template Reference

```
#include <snippet.h>
```

Public Member Functions

- template<typename... T>
 ValueBase (T &&... vals)
- const std::vector< double > & getValues () const

Gets values as a vector of doubles.

double operator[] (size_t pos) const

20.152.1 Constructor & Destructor Documentation

20.152.1.1 ValueBase()

20.152.2 Member Function Documentation

20.152.2.1 getValues()

```
template<size_t NumVars>
const std::vector<double>& Snippet::ValueBase< NumVars >::getValues ( ) const [inline]
```

Gets values as a vector of doubles.

20.152.2.2 operator[]()

The documentation for this class was generated from the following file:

· snippet.h

20.153 Snippet::ValueBase < 0 > Class Template Reference

```
#include <snippet.h>
```

Public Member Functions

- template<typename... T>
 ValueBase (T &&... vals)
- std::vector< double > getValues () const

Gets values as a vector of doubles.

20.153.1 Detailed Description

```
template<> class Snippet::ValueBase< 0 >
```

Template specialisation of ValueBase to avoid compiler warnings in the case when a model requires no parameters or state variables

20.153.2 Constructor & Destructor Documentation

20.153.2.1 ValueBase()

20.153.3 Member Function Documentation

```
20.153.3.1 getValues()
```

```
std::vector<double> Snippet::ValueBase< 0 >::getValues ( ) const [inline]
```

Gets values as a vector of doubles.

The documentation for this class was generated from the following file:

snippet.h

20.154 SpineMLSimulator::ModelProperty::ValueList Class Reference

```
#include <modelProperty.h>
```

Inheritance diagram for SpineMLSimulator::ModelProperty::ValueList:

```
SpineMLSimulator::ModelProperty::Base

SpineMLSimulator::ModelProperty::ValueList
```

Public Member Functions

- ValueList (const pugi::xml_node &node, const filesystem::path &basePath, const std::vector< unsigned int > *remapIndices, const StateVar< scalar > &stateVar, unsigned int size)
- void setValue (const std::vector< scalar > &values, const std::vector< unsigned int > *remapIndices)

20.154.1 Constructor & Destructor Documentation

20.154.1.1 ValueList()

20.154.2 Member Function Documentation

20.154.2.1 setValue()

The documentation for this class was generated from the following files:

- · modelProperty.h
- · modelProperty.cc

20.155 Models::Base::Var Struct Reference

A variable has a name, a type and an access type.

```
#include <models.h>
```

Public Member Functions

Var (const std::string &n, const std::string &t, VarAccess a)

- Var (const std::string &n, const std::string &t)
- Var ()
- bool operator== (const Var &other) const

Public Attributes

- std::string name
- std::string type
- · VarAccess access

20.155.1 Detailed Description

A variable has a name, a type and an access type.

Explicit constructors required as although, through the wonders of C++ aggregate initialization, access would default to VarAccess::READ_WRITE if not specified, this results in a -Wmissing-field-initializers warning on GCC and Clang

20.155.2 Constructor & Destructor Documentation

20.155.4 Member Data Documentation

20.155.4.1 access

VarAccess Models::Base::Var::access

20.155.4.2 name

std::string Models::Base::Var::name

20.155.4.3 type

```
std::string Models::Base::Var::type
```

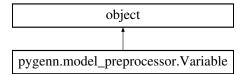
The documentation for this struct was generated from the following file:

· models.h

20.156 pygenn.model_preprocessor.Variable Class Reference

Class holding information about GeNN variables.

Inheritance diagram for pygenn.model_preprocessor.Variable:



Public Member Functions

- def __init__ (self, variable_name, variable_type, values, group)
 Init Variable.
- def set_extra_global_init_param (self, param_name, param_values)
- def set_values (self, values)

Set Variable's values.

Public Attributes

- name
- type
- group
- · extra global params
- view
- needs_allocation
- · init_required
- · init val
- values

20.156.1 Detailed Description

Class holding information about GeNN variables.

20.156.2 Constructor & Destructor Documentation

Init Variable.

Parameters

variable_name	string name of the variable
variable_type	string type of the variable
values	iterable, single value or VarInit instance
group	group variable belongs to

20.156.3 Member Function Documentation

20.156.3.1 set_extra_global_init_param()

20.156.3.2 set_values()

```
def pygenn.model_preprocessor.Variable.set_values ( self, \\ values \ )
```

Set Variable's values.

Parameters

```
values iterable, single value or VarInit instance
```

20.156.4 Member Data Documentation

20.156.4.1 extra_global_params

```
pygenn.model_preprocessor.Variable.extra_global_params
```

20.156.4.2 group

pygenn.model_preprocessor.Variable.group

20.156.4.3 init_required

pygenn.model_preprocessor.Variable.init_required

20.156.4.4 init_val

pygenn.model_preprocessor.Variable.init_val

20.156.4.5 name

 $\verb|pygenn.model_preprocessor.Variable.name| \\$

20.156.4.6 needs_allocation

 $\verb|pygenn.model_preprocessor.Variable.needs_allocation|\\$

20.156.4.7 type

pygenn.model_preprocessor.Variable.type

20.156.4.8 values

pygenn.model_preprocessor.Variable.values

20.156.4.9 view

pygenn.model_preprocessor.Variable.view

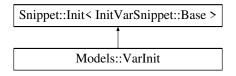
The documentation for this class was generated from the following file:

model_preprocessor.py

20.157 Models::VarInit Class Reference

#include <models.h>

Inheritance diagram for Models::VarInit:



Public Member Functions

- VarInit (const InitVarSnippet::Base *snippet, const std::vector< double > ¶ms)
- VarInit (double constant)

20.157.1 Constructor & Destructor Documentation

The documentation for this class was generated from the following file:

· models.h

20.158 Models::VarInitContainerBase < NumVars > Class Template Reference

```
#include <models.h>
```

Public Member Functions

- template<typename... T>
 VarInitContainerBase (T &&... initialisers)
- const std::vector< VarInit > & getInitialisers () const

Gets initialisers as a vector of Values.

• const VarInit & operator[] (size_t pos) const

20.158.1 Detailed Description

```
\label{lem:lemplate} \begin{tabular}{ll} template < size\_t \ Num Vars > \\ class \ Models:: VarInit Container Base < Num Vars > \\ \end{tabular}
```

Wrapper to ensure at compile time that correct number of value initialisers are used when specifying the values of a model's initial state.

20.158.2 Constructor & Destructor Documentation

20.158.2.1 VarInitContainerBase()

20.158.3 Member Function Documentation

20.158.3.1 getInitialisers()

```
template<size_t NumVars>
const std::vector<VarInit>& Models::VarInitContainerBase< NumVars >::getInitialisers ( )
const [inline]
```

Gets initialisers as a vector of Values.

20.158.3.2 operator[]()

The documentation for this class was generated from the following file:

models.h

20.159 Models::VarInitContainerBase < 0 > Class Template Reference

```
#include <models.h>
```

Public Member Functions

- template<typename... T>
 VarInitContainerBase (T &&... initialisers)
- VarInitContainerBase (const Snippet::ValueBase< 0 > &)
- std::vector< VarInit > getInitialisers () const

Gets initialisers as a vector of Values.

20.159.1 Detailed Description

```
\label{lem:lemplate} template <> \\ class \ Models:: VarInitContainerBase < 0 >
```

Template specialisation of ValueInitBase to avoid compiler warnings in the case when a model requires no variable initialisers

20.159.2 Constructor & Destructor Documentation

20.159.2.1 VarInitContainerBase() [1/2]

20.159.2.2 VarInitContainerBase() [2/2]

20.159.3 Member Function Documentation

20.159.3.1 getInitialisers()

```
std::vector<VarInit> Models::VarInitContainerBase< 0 >::getInitialisers ( ) const [inline]
```

Gets initialisers as a vector of Values.

The documentation for this class was generated from the following file:

· models.h

21 File Documentation

- 21.1 00_MainPage.dox File Reference
- 21.2 01_Installation.dox File Reference
- 21.3 02_Quickstart.dox File Reference
- 21.4 03_Examples.dox File Reference
- 21.5 05_SpineML.dox File Reference
- 21.6 06_Brian2GeNN.dox File Reference
- 21.7 07_PyGeNN.dox File Reference
- 21.8 09_ReleaseNotes.dox File Reference
- 21.9 10_UserManual.dox File Reference
- 21.10 11_Tutorial.dox File Reference
- 21.11 12_Tutorial.dox File Reference
- 21.12 13_UserGuide.dox File Reference
- 21.13 14_Credits.dox File Reference
- 21.14 __init__.py File Reference

Namespaces

pygenn

21.15 analogueRecorder.h File Reference

```
#include <fstream>
#include <initializer_list>
#include <string>
#include <vector>
```

Classes

class AnalogueRecorder< T >

21.16 array.h File Reference

```
#include "features/compilerfeatures.h"
#include "features/sse.h"
```

Namespaces

• r123

Macros

- #define CXXMETHODS(N, W, T)
- #define CXXOVERLOADS(_N, W, T)
- #define _r123array_tpl(_N, W, T)
- #define R123_W(a) (8*sizeof(((a *)0)->v[0]))

Functions

- _r123array_tpl (1, 32, uint32_t) _r123array_tpl(2
- uint32_t _r123array_tpl (4, 32, uint32_t) _r123array_tpl(8
- uint32_t uint32_t _r123array_tpl (1, 64, uint64_t) _r123array_tpl(2
- uint32_t uint32_t uint64_t _r123array_tpl (4, 64, uint64_t) _r123array_tpl(16

21.16.1 Macro Definition Documentation

21.16.1.1 _r123array_tpl

Value:

```
struct r123array##_N##x##W{
    T v[_N];
    CXXMETHODS(_N, W, T)
};

CXXOVERLOADS(_N, W, T)
```

21.16.1.2 CXXMETHODS

```
#define CXXMETHODS( \_^{N}, \\ _{W}, \\ _{T} )
```

21.16.1.3 CXXOVERLOADS

```
#define CXXOVERLOADS(  {\_N, \atop W, \atop T})
```

21.16.1.4 R123_W

21.16.2 Function Documentation

```
21.16.2.1 _r123array_tpl() [1/4]
```

21.16.2.2 _r123array_tpl() [2/4]

21.16.2.3 _r123array_tpl() [3/4]

21.16.2.4 _r123array_tpl() [4/4]

21.17 backend.cc File Reference

```
#include "backend.h"
#include <algorithm>
#include <iterator>
#include "gennUtils.h"
#include "logging.h"
#include "code_generator/codeStream.h"
#include "code_generator/codeGenUtils.h"
#include "code_generator/modelSpecMerged.h"
#include "code_generator/substitutions.h"
#include "utils.h"
```

Namespaces

CodeGenerator

Helper class for generating code - automatically inserts brackets, indents etc.

· CodeGenerator::CUDA

21.18 backend.cc File Reference

```
#include "backend.h"
#include "gennUtils.h"
#include "code_generator/codeGenUtils.h"
#include "code_generator/codeStream.h"
#include "code_generator/modelSpecMerged.h"
#include "code_generator/substitutions.h"
```

Namespaces

CodeGenerator

Helper class for generating code - automatically inserts brackets, indents etc.

CodeGenerator::SingleThreadedCPU

21.19 backend.h File Reference

```
#include <algorithm>
#include <array>
#include <functional>
#include <map>
#include <numeric>
#include <string>
#include <unordered_set>
#include <cuda.h>
#include <cuda.runtime.h>
#include "backendExport.h"
#include "code_generator/backendBase.h"
#include "code_generator/substitutions.h"
#include "presynapticUpdateStrategy.h"
```

Classes

struct CodeGenerator::CUDA::Preferences

Preferences for CUDA backend.

· class CodeGenerator::CUDA::Backend

Namespaces

- · filesystem
- CodeGenerator

Helper class for generating code - automatically inserts brackets, indents etc.

CodeGenerator::CUDA

Typedefs

using CodeGenerator::CUDA::KernelBlockSize = std::array < size_t, KernelMax >
 Array of block sizes for each kernel.

Enumerations

enum CodeGenerator::CUDA::DeviceSelect { CodeGenerator::CUDA::DeviceSelect::OPTIMAL, Code
 Generator::CUDA::DeviceSelect::MOST_MEMORY, CodeGenerator::CUDA::DeviceSelect::MANUAL }

Methods for selecting CUDA device.

 enum CodeGenerator::CUDA::BlockSizeSelect { CodeGenerator::CUDA::BlockSizeSelect::OCCUPANCY, CodeGenerator::CUDA::BlockSizeSelect::MANUAL }

Methods for selecting CUDA kernel block size.

enum CodeGenerator::CUDA::Kernel {

CodeGenerator::CUDA::KernelPresynapticUpdate, CodeGenerator::CUDA::KernelPresynapticUpdate, CodeGenerator::CUDA::KernelPresynapticUpdate, CodeGenerator::CUDA::KernelSynapseDynamicsUpdate, CodeGenerator::CUDA::KernelInitializeSparse, CodeGenerator::CUDA::KernelInitializeSparse, CodeGenerator::CUDA::KernelPreSynapseReset, CodeGenerator::CUDA::KernelPreSynapseReset, CodeGenerator::CUDA::KernelMax }

Kernels generated by CUDA backend.

21.20 backend.h File Reference

```
#include <functional>
#include <map>
#include <string>
#include "backendExport.h"
#include "code generator/backendBase.h"
```

Classes

- struct CodeGenerator::SingleThreadedCPU::Preferences
- · class CodeGenerator::SingleThreadedCPU::Backend

Namespaces

- · filesystem
- CodeGenerator

Helper class for generating code - automatically inserts brackets, indents etc.

· CodeGenerator::SingleThreadedCPU

21.21 backendBase.cc File Reference

```
#include "code_generator/backendBase.h"
#include "gennUtils.h"
#include "logging.h"
```

Macros

#define TYPE(T) {#T, sizeof(T)}

21.21.1 Macro Definition Documentation

21.21.1.1 TYPE

```
#define TYPE( T \ ) \ \{ \mbox{\tt \#T, sizeof(T)} \}
```

21.22 backendBase.h File Reference

```
#include <functional>
#include <map>
#include <string>
#include <unordered_map>
#include <vector>
#include <plog/Severity.h>
#include "codeStream.h"
#include "gennExport.h"
#include "variableMode.h"
```

Classes

• struct CodeGenerator::PreferencesBase

Base class for backend preferences - can be accessed via a global in 'classic' C++ code generator.

- class CodeGenerator::MemAlloc
- · class CodeGenerator::BackendBase

Namespaces

CodeGenerator

Helper class for generating code - automatically inserts brackets, indents etc.

21.23 backendExport.h File Reference

Macros

• #define BACKEND_EXPORT

21.23.1 Macro Definition Documentation

21.23.1.1 BACKEND_EXPORT

```
#define BACKEND_EXPORT
```

21.24 binomial.cc File Reference

```
#include "binomial.h"
#include <stdexcept>
#include <cassert>
#include <cmath>
#include <cstdint>
```

Functions

• unsigned int binomialInverseCDF (double cdf, unsigned int n, double p)

21.24.1 Function Documentation

21.24.1.1 binomialInverseCDF()

```
unsigned int binomialInverseCDF ( double cdf, unsigned int n, double p )
```

21.25 binomial.h File Reference

```
#include "gennExport.h"
```

Functions

• GENN_EXPORT unsigned int binomialInverseCDF (double cdf, unsigned int n, double p)

21.25.1 Function Documentation

21.25.1.1 binomialInverseCDF()

```
GENN_EXPORT unsigned int binomialInverseCDF ( \mbox{double } cdf, \\ \mbox{unsigned int } n, \\ \mbox{double } p \mbox{)}
```

21.26 clangfeatures.h File Reference

```
#include "gccfeatures.h"
```

Macros

- #define R123_USE_X86INTRIN_H ((defined(__x86_64__))||defined(__i386__)))
- #define R123_USE_CXX11_UNRESTRICTED_UNIONS __has_feature(cxx_unrestricted_unions)
- #define R123_USE_CXX11_STATIC_ASSERT __has_feature(cxx_static_assert)
- #define R123_USE_CXX11_CONSTEXPR __has_feature(cxx_constexpr)
- #define R123_USE_CXX11_EXPLICIT_CONVERSIONS __has_feature(cxx_explicit_conversions)
- #define R123_USE_CXX11_RANDOM 0
- #define R123_USE_CXX11_TYPE_TRAITS 0

21.26.1 Macro Definition Documentation

```
21.26.1.1 R123_USE_CXX11_CONSTEXPR
```

```
#define R123_USE_CXX11_CONSTEXPR __has_feature(cxx_constexpr)
```

21.26.1.2 R123_USE_CXX11_EXPLICIT_CONVERSIONS

#define R123_USE_CXX11_EXPLICIT_CONVERSIONS __has_feature(cxx_explicit_conversions)

21.26.1.3 R123_USE_CXX11_RANDOM

#define R123_USE_CXX11_RANDOM 0

21.26.1.4 R123_USE_CXX11_STATIC_ASSERT

#define R123_USE_CXX11_STATIC_ASSERT __has_feature(cxx_static_assert)

21.26.1.5 R123_USE_CXX11_TYPE_TRAITS

#define R123_USE_CXX11_TYPE_TRAITS 0

21.26.1.6 R123_USE_CXX11_UNRESTRICTED_UNIONS

#define R123_USE_CXX11_UNRESTRICTED_UNIONS __has_feature(cxx_unrestricted_unions)

21.26.1.7 R123_USE_X86INTRIN_H

 $\texttt{\#define R123_USE_X86INTRIN_H ((defined(__x86_64__)||defined(__i386__))))}$

21.27 cIRNG.c File Reference

```
#include <clRNG/clRNG.h>
#include "private.h"
#include <stdio.h>
#include <stdarg.h>
#include <stdlib.h>
#include <unistd.h>
```

Macros

- #define CASE_ERR_(code, msg) case code: base = msg; break
- #define CASE_ERR(code) CASE_ERR_(CLRNG_ ## code, MSG_ ## code)

Functions

```
• const char * clrngGetErrorString ()
```

Retrieve the last error message.

const char * clrngGetLibraryRoot ()

Retrieve the library installation path.

const char * clrngGetLibraryDeviceIncludes (cl_int *err)

Generate an include option string for use with the OpenCL C compiler.

Variables

• char errorString [1024]

21.27.1 Macro Definition Documentation

```
21.27.1.1 CASE_ERR
```

21.27.1.2 CASE_ERR_

21.27.2 Function Documentation

21.27.2.1 clrngGetErrorString()

```
const char* clrngGetErrorString ( )
```

Retrieve the last error message.

The buffer containing the error message is internally allocated and must not be freed by the client.

Returns

Error message or NULL.

21.27.2.2 clrngGetLibraryDeviceIncludes()

Generate an include option string for use with the OpenCL C compiler.

Generate and return "-I\${CLRNG_ROOT}/include", where $\{CLRNG_ROOT\}$ is the value of the $CLRNG_ROOT$ environment variable. This string is meant to be passed as an option to the OpenCL C compiler for programs that make use of the clRNG device-side headers. If the $CLRNG_ROOT$ environment variable is not defined, it defaults /usr if the file /usr/include/clRNG/clRNG.h exists, else to the current directory of execution of the program.

A static buffer is return and need not be released; it could change upon successive calls to the function.

An error is returned in err if the preallocated buffer is too small to contain the include string.

Parameters

0	ut	err	Error status variable, or NULL.
---	----	-----	---------------------------------

Returns

An OpenCL C compiler option to indicate where to find the device-side cIRNG headers.

21.27.2.3 clrngGetLibraryRoot()

```
const char* clrngGetLibraryRoot ( )
```

Retrieve the library installation path.

Returns

Value of the CLRNG_ROOT environment variable, if defined; else, /usr if the file /usr/include/clR↔ NG/clRNG.h exists; or, the current directory (.) of execution of the program otherwise.

21.27.3 Variable Documentation

21.27.3.1 errorString

char errorString[1024]

21.28 clRNG.h File Reference

Library definitions common to all RNG's.

```
#include <CL/cl.h>
```

Macros

- #define CLRNG H
- #define _CLRNG_FPTYPE cl_double
- #define _CLRNG_TAG_FPTYPE(name) _CLRNG_TAG_FPTYPE_(name, _CLRNG_FPTYPE)
- #define _CLRNG_TAG_FPTYPE_(name, fptype) _CLRNG_TAG_FPTYPE__(name, fptype)
- #define CLRNG TAG FPTYPE (name, fptype) name## ##fptype
- #define CLRNGAPI

Typedefs

 typedef enum clrngStatus_clrngStatus Error codes.

Enumerations

enum clrngStatus_{
 CLRNG_SUCCESS = CL_SUCCESS, CLRNG_OUT_OF_RESOURCES = CL_OUT_OF_RESOURCES,
 CLRNG_INVALID_VALUE = CL_INVALID_VALUE, CLRNG_INVALID_RNG_TYPE,
 CLRNG_INVALID_STREAM_CREATOR, CLRNG_INVALID_SEED, CLRNG_FUNCTION_NOT_IMPLEM←
 ENTED }

Error codes.

Functions

CLRNGAPI const char * clrngGetErrorString ()

Retrieve the last error message.

CLRNGAPI const char * clrngGetLibraryDeviceIncludes (cl_int *err)

Generate an include option string for use with the OpenCL C compiler.

CLRNGAPI const char * clrngGetLibraryRoot ()

Retrieve the library installation path.

21.28.1 Detailed Description

Library definitions common to all RNG's.

See also

cIRNG_template.h

21.28.2 Macro Definition Documentation

```
21.28.2.1 _CLRNG_FPTYPE
```

```
#define _CLRNG_FPTYPE cl_double
```

21.28.2.2 _CLRNG_TAG_FPTYPE

21.28.2.3 _CLRNG_TAG_FPTYPE_

21.28.2.4 _CLRNG_TAG_FPTYPE_

21.28.2.5 CLRNG H

#define CLRNG_H

21.28.2.6 CLRNGAPI

#define CLRNGAPI

21.28.3 Typedef Documentation

21.28.3.1 clrngStatus

```
typedef enum clrngStatus_ clrngStatus
```

Error codes.

Most library functions return an error status indicating the success or error state of the operation carried by the function. In case of success, the error status is set to CLRNG_SUCCESS. Otherwise, an error message can be retrieved by invoking clrngGetErrorString().

Note

In naming this type clrngStatus, we follow the convention from clFFT and clBLAS, where the homologous types are name clfftStatus and clblasStatus, respectively.

21.28.4 Enumeration Type Documentation

21.28.4.1 clrngStatus_

```
enum clrngStatus_
```

Error codes.

Most library functions return an error status indicating the success or error state of the operation carried by the function. In case of success, the error status is set to CLRNG_SUCCESS. Otherwise, an error message can be retrieved by invoking clrngGetErrorString().

Note

In naming this type clrngStatus, we follow the convention from cIFFT and cIBLAS, where the homologous types are name clfftStatus and clblasStatus, respectively.

Enumerator

CLRNG_SUCCESS	
CLRNG_OUT_OF_RESOURCES	
CLRNG_INVALID_VALUE	
CLRNG_INVALID_RNG_TYPE	
CLRNG_INVALID_STREAM_CREATOR	
CLRNG_INVALID_SEED	
CLRNG_FUNCTION_NOT_IMPLEMENTED	

21.28.5 Function Documentation

21.28.5.1 clrngGetErrorString()

```
CLRNGAPI const char* clrngGetErrorString ( )
```

Retrieve the last error message.

The buffer containing the error message is internally allocated and must not be freed by the client.

Returns

Error message or NULL.

21.28.5.2 clrngGetLibraryDeviceIncludes()

Generate an include option string for use with the OpenCL C compiler.

Generate and return "-I\${CLRNG_ROOT}/include", where $\{CLRNG_ROOT\}$ is the value of the CLRNG_ROOT environment variable. This string is meant to be passed as an option to the OpenCL C compiler for programs that make use of the clRNG device-side headers. If the CLRNG_ROOT environment variable is not defined, it defaults /usr if the file /usr/include/clRNG/clRNG.h exists, else to the current directory of execution of the program.

A static buffer is return and need not be released; it could change upon successive calls to the function.

An error is returned in err if the preallocated buffer is too small to contain the include string.

Parameters

out	err	Error status variable, or NULL.

Returns

An OpenCL C compiler option to indicate where to find the device-side cIRNG headers.

21.28.5.3 clrngGetLibraryRoot()

```
CLRNGAPI const char* clrngGetLibraryRoot ( )
```

Retrieve the library installation path.

Returns

Value of the CLRNG_ROOT environment variable, if defined; else, /usr if the file $/usr/include/clR \leftarrow NG/clRNG$. h exists; or, the current directory (.) of execution of the program otherwise.

21.29 codeGenUtils.cc File Reference

```
#include "code_generator/codeGenUtils.h"
#include <regex>
#include <cstring>
#include "modelSpec.h"
#include "code_generator/groupMerged.h"
#include "code_generator/substitutions.h"
```

Namespaces

CodeGenerator

Helper class for generating code - automatically inserts brackets, indents etc.

Enumerations

enum MathsFunc

Functions

- void CodeGenerator::substitute (std::string &s, const std::string &trg, const std::string &rep)

 Tool for substituting strings in the neuron code strings or other templates.
- bool CodeGenerator::regexVarSubstitute (std::string &s, const std::string &trg, const std::string &rep)

Tool for substituting variable names in the neuron code strings or other templates using regular expressions.

bool CodeGenerator::regexFuncSubstitute (std::string &s, const std::string &trg, const std::string &rep)

Tool for substituting function names in the neuron code strings or other templates using regular expressions.

void CodeGenerator::functionSubstitute (std::string &code, const std::string &funcName, unsigned int num
 —
 Params, const std::string &replaceFuncTemplate)

This function substitutes function calls in the form:

- void CodeGenerator::genScalarEGPPush (CodeStream &os, const MergedStructData &mergedStructData, const std::string &suffix, const BackendBase &backend)
- std::string CodeGenerator::ensureFtype (const std::string &oldcode, const std::string &type)

This function implements a parser that converts any floating point constant in a code snippet to a floating point constant with an explicit precision (by appending "f" or removing it).

void CodeGenerator::checkUnreplacedVariables (const std::string &code, const std::string &codeName)

This function checks for unknown variable definitions and returns a gennError if any are found.

21.29.1 Enumeration Type Documentation

21.29.1.1 MathsFunc

enum MathsFunc

21.30 codeGenUtils.h File Reference

```
#include <algorithm>
#include <set>
#include <string>
#include <unordered_map>
#include <vector>
#include "gennExport.h"
#include "gennUtils.h"
#include "neuronGroupInternal.h"
#include "variableMode.h"
#include "backendBase.h"
#include "codeStream.h"
#include "substitutions.h"
#include "teeStream.h"
```

Classes

- class CodeGenerator::MergedStructData
- struct CodeGenerator::MergedStructData::MergedEGP

Immutable structure for tracking where an extra global variable ends up after merging.

Namespaces

CodeGenerator

Helper class for generating code - automatically inserts brackets, indents etc.

Functions

- void CodeGenerator::substitute (std::string &s, const std::string &trg, const std::string &rep)
 - Tool for substituting strings in the neuron code strings or other templates.
- bool CodeGenerator::regexVarSubstitute (std::string &s, const std::string &trg, const std::string &rep)

Tool for substituting variable names in the neuron code strings or other templates using regular expressions.

- bool CodeGenerator::regexFuncSubstitute (std::string &s, const std::string &trg, const std::string &rep)
 - Tool for substituting function names in the neuron code strings or other templates using regular expressions.
- void CodeGenerator::functionSubstitute (std::string &code, const std::string &funcName, unsigned int num
 —
 Params, const std::string &replaceFuncTemplate)

This function substitutes function calls in the form:

size_t CodeGenerator::ceilDivide (size_t numerator, size_t denominator)

Divide two integers, rounding up i.e. effectively taking ceil.

• size_t CodeGenerator::padSize (size_t size, size_t blockSize)

Pad an integer to a multiple of another.

- template<typename T >
 - void CodeGenerator::genMergedGroupPush (CodeStream &os, const std::vector< T > &groups, const MergedStructData &mergedStructData, const std::string &suffix, const BackendBase &backend, Backend← Base::MemorySpaces &memorySpaces)
- void CodeGenerator::genScalarEGPPush (CodeStream &os, const MergedStructData &mergedStructData, const std::string &suffix, const BackendBase &backend)
- std::string CodeGenerator::ensureFtype (const std::string &oldcode, const std::string &type)

This function implements a parser that converts any floating point constant in a code snippet to a floating point constant with an explicit precision (by appending "f" or removing it).

• void CodeGenerator::checkUnreplacedVariables (const std::string &code, const std::string &codeName)

This function checks for unknown variable definitions and returns a gennError if any are found.

template<typename P, typename D >
 void CodeGenerator::neuronSubstitutionsInSynapticCode (CodeGenerator::Substitutions &substitutions, const NeuronGroupInternal *archetypeNG, const std::string &offset, const std::string &delayOffset, const std::string &idx, const std::string &sourceSuffix, const std::string &destSuffix, const std::string &varPrefix, const std::string &varSuffix, P isParamHeterogeneousFn, D isDerivedParamHeterogeneousFn)

Function for performing the code and value substitutions necessary to insert neuron related variables, parameters, and extraGlobal parameters into synaptic code.

21.31 codeStream.cc File Reference

```
#include "code_generator/codeStream.h"
#include <algorithm>
```

Namespaces

CodeGenerator

Helper class for generating code - automatically inserts brackets, indents etc.

Functions

- GENN_EXPORT std::ostream & CodeGenerator::operator<< (std::ostream &s, const CodeStream::OB &ob)
- GENN_EXPORT std::ostream & CodeGenerator::operator<< (std::ostream &s, const CodeStream::CB &cb)

21.32 codeStream.h File Reference

```
#include <ostream>
#include <stdexcept>
#include <streambuf>
#include <string>
#include <vector>
#include "logging.h"
#include "gennExport.h"
```

Classes

- · class CodeGenerator::CodeStream
- struct CodeGenerator::CodeStream::OB

An open bracket marker.

struct CodeGenerator::CodeStream::CB

A close bracket marker.

class CodeGenerator::CodeStream::Scope

Namespaces

CodeGenerator

Helper class for generating code - automatically inserts brackets, indents etc.

Functions

- GENN_EXPORT std::ostream & CodeGenerator::operator<< (std::ostream &s, const CodeStream::OB &ob)
- GENN_EXPORT std::ostream & CodeGenerator::operator<< (std::ostream &s, const CodeStream::CB &cb)

21.33 compilerfeatures.h File Reference

21.34 connectors.cc File Reference

```
#include "connectors.h"
#include <algorithm>
#include <fstream>
#include <iostream>
#include <numeric>
#include <random>
#include <tuple>
#include <vector>
#include <cassert>
#include <cstdint>
#include "path.h"
#include "pugixml/pugixml.hpp"
#include "spineMLLogging.h"
```

21.35 connectors.h File Reference

```
#include <vector>
#include <cstdint>
```

Namespaces

- pugi
- · filesystem
- SpineMLSimulator
- · SpineMLSimulator::Connectors

Functions

• unsigned int SpineMLSimulator::Connectors::create (const pugi::xml_node &node, double dt, unsigned int numPre, unsigned int numPost, unsigned int **rowLength, unsigned int **ind, uint8_t **delay, const unsigned int *maxRowLength, const filesystem::path &basePath, std::vector< unsigned int > &remapIndices)

21.36 currentSource.cc File Reference

```
#include "currentSource.h"
#include <algorithm>
#include <cmath>
#include "gennUtils.h"
```

21.37 currentSource.h File Reference

```
#include <map>
#include <set>
#include <string>
#include <vector>
#include "currentSourceModels.h"
#include "gennExport.h"
#include "variableMode.h"
```

Classes

class CurrentSource

21.38 currentSourceInternal.h File Reference

```
#include "currentSource.h"
```

Classes

· class CurrentSourceInternal

21.39 currentSourceModels.cc File Reference

```
#include "currentSourceModels.h"
```

Functions

- IMPLEMENT_MODEL (CurrentSourceModels::DC)
- IMPLEMENT_MODEL (CurrentSourceModels::GaussianNoise)
- IMPLEMENT_MODEL (CurrentSourceModels::PoissonExp)

21.39.1 Function Documentation

21.39.1.3 IMPLEMENT_MODEL() [3/3]

21.40 currentSourceModels.h File Reference

```
#include <array>
#include <functional>
#include <string>
#include <tuple>
#include <vector>
#include <cmath>
#include "gennExport.h"
#include "models.h"
```

Classes

· class CurrentSourceModels::Base

Base class for all current source models.

· class CurrentSourceModels::DC

DC source.

· class CurrentSourceModels::GaussianNoise

Noisy current source with noise drawn from normal distribution.

· class CurrentSourceModels::PoissonExp

Namespaces

· CurrentSourceModels

Macros

#define SET_INJECTION_CODE(INJECTION_CODE) virtual std::string getInjectionCode() const override{
 return INJECTION_CODE; }

21.40.1 Macro Definition Documentation

21.40.1.1 SET_INJECTION_CODE

21.41 gccfeatures.h File Reference

```
#include <including_a_nonexistent_file_will_stop_some_compilers_from_continuing
_with_a_hopeless_task>
#include <assert.h>
#include <stdint.h>
```

Macros

- #define R123_STATIC_INLINE static __inline__
- #define R123 FORCE INLINE(decl) decl
- #define R123 CUDA DEVICE
- #define R123_ASSERT(x) assert(x)
- #define R123_BUILTIN_EXPECT(expr, likely) __builtin_expect(expr, likely)
- #define R123_USE_CXX11_UNRESTRICTED_UNIONS ((R123_GNUC_VERSION >= 40600) && GNU_←
 CXX11)
- #define R123_USE_CXX11_STATIC_ASSERT ((R123_GNUC_VERSION >= 40300) && GNU_CXX11)
- #define R123_USE_CXX11_CONSTEXPR ((R123_GNUC_VERSION >= 40600) && GNU_CXX11)
- #define R123_USE_CXX11_EXPLICIT_CONVERSIONS ((R123_GNUC_VERSION >= 40500) && GNU_← CXX11)
- #define R123_USE_CXX11_RANDOM ((R123_GNUC_VERSION>=40500) && GNU_CXX11)
- #define R123 USE CXX11 TYPE TRAITS ((R123 GNUC VERSION>=40400) && GNU CXX11)
- #define R123_USE_AES_NI 0
- #define R123 USE SSE4 20
- #define R123 USE SSE4 1 0
- #define R123 USE SSE 0
- #define R123 USE AES OPENSSL 0
- #define R123 USE GNU UINT128 0
- #define R123_USE_ASM_GNU (defined(__x86_64__)||defined(__i386__))
- #define R123 USE CPUID MSVC 0
- #define R123_USE_X86INTRIN_H ((defined(__x86_64__)||defined(__i386__)) && R123_GNUC_VERSION
 >= 40402)
- #define R123_USE_IA32INTRIN_H 0
- #define R123_USE_XMMINTRIN_H 0
- #define R123_USE_EMMINTRIN_H (R123_USE_SSE && (R123_GNUC_VERSION < 40402))
- #define R123_USE_SMMINTRIN_H ((R123_USE_SSE4_1 || R123_USE_SSE4_2) && (R123_GNUC_VE↔ RSION < 40402))
- #define R123_USE_WMMINTRIN_H 0
- #define R123_USE_INTRIN_H 0
- #define R123_USE_MULHILO32_ASM 0
- #define R123_USE_MULHILO64_ASM 0
- #define R123_USE_MULHILO64_MSVC_INTRIN 0
- #define R123_USE_MULHILO64_CUDA_INTRIN 0
- #define R123 USE MULHILO64 OPENCL INTRIN 0
- #define R123_USE_MULHILO64_MULHI_INTRIN (defined(__powerpc64__))
- #define R123 MULHILO64 MULHI INTRIN mulhdu
- #define R123 USE MULHILO32 MULHI INTRIN 0
- #define R123_MULHILO32_MULHI_INTRIN __mulhwu
- #define STDC CONSTANT MACROS

21.41.1 Macro Definition Documentation

21.41.1.1 __STDC_CONSTANT_MACROS

#define ___STDC_CONSTANT_MACROS

21.41.1.2 GNU_CXX11

21.41.1.3 R123_ASSERT

```
#define R123_ASSERT( x ) assert(x)
```

21.41.1.4 R123_BUILTIN_EXPECT

21.41.1.5 R123_CUDA_DEVICE

#define R123_CUDA_DEVICE

21.41.1.6 R123_FORCE_INLINE

```
#define R123_FORCE_INLINE( dec1 ) dec1
```

21.41.1.7 R123_GNUC_VERSION

```
#define R123_GNUC_VERSION (__GNUC__*10000 + __GNUC_MINOR__*100 + __GNUC_PATCHLEVEL__)
```

21.41.1.8 R123_MULHILO32_MULHI_INTRIN

#define R123_MULHILO32_MULHI_INTRIN ___mulhwu

21.41.1.9 R123_MULHILO64_MULHI_INTRIN

#define R123_MULHILO64_MULHI_INTRIN ___mulhdu

21.41.1.10 R123_STATIC_INLINE

#define R123_STATIC_INLINE static __inline__

21.41.1.11 R123_USE_AES_NI

#define R123_USE_AES_NI 0

```
21.41.1.12 R123_USE_AES_OPENSSL
#define R123_USE_AES_OPENSSL 0
21.41.1.13 R123 USE ASM GNU
#define R123_USE_ASM_GNU (defined(__x86_64__)||defined(__i386__))
21.41.1.14 R123_USE_CPUID_MSVC
#define R123_USE_CPUID_MSVC 0
21.41.1.15 R123_USE_CXX11_CONSTEXPR
#define R123_USE_CXX11_CONSTEXPR ((R123_GNUC_VERSION >= 40600) && GNU_CXX11)
21.41.1.16 R123_USE_CXX11_EXPLICIT_CONVERSIONS
#define R123_USE_CXX11_EXPLICIT_CONVERSIONS ((R123_GNUC_VERSION >= 40500) && GNU_CXX11)
21.41.1.17 R123_USE_CXX11_RANDOM
#define R123_USE_CXX11_RANDOM ((R123_GNUC_VERSION>=40500) && GNU_CXX11)
21.41.1.18 R123_USE_CXX11_STATIC_ASSERT
#define R123_USE_CXX11_STATIC_ASSERT ((R123_GNUC_VERSION >= 40300) && GNU_CXX11)
21.41.1.19 R123_USE_CXX11_TYPE_TRAITS
#define R123_USE_CXX11_TYPE_TRAITS ((R123_GNUC_VERSION>=40400) && GNU_CXX11)
21.41.1.20 R123_USE_CXX11_UNRESTRICTED_UNIONS
#define R123_USE_CXX11_UNRESTRICTED_UNIONS ((R123_GNUC_VERSION >= 40600) && GNU_CXX11)
21.41.1.21 R123_USE_EMMINTRIN_H
\#define R123_USE_EMMINTRIN_H (R123_USE_SSE && (R123_GNUC_VERSION < 40402))
21.41.1.22 R123_USE_GNU_UINT128
#define R123_USE_GNU_UINT128 0
```

21.41.1.23 R123_USE_IA32INTRIN_H

#define R123_USE_IA32INTRIN_H 0

21.41.1.24 R123 USE INTRIN H

#define R123_USE_INTRIN_H 0

21.41.1.25 R123_USE_MULHILO32_ASM

#define R123_USE_MULHILO32_ASM 0

21.41.1.26 R123_USE_MULHILO32_MULHI_INTRIN

#define R123_USE_MULHILO32_MULHI_INTRIN 0

21.41.1.27 R123_USE_MULHILO64_ASM

#define R123_USE_MULHILO64_ASM 0

21.41.1.28 R123_USE_MULHILO64_CUDA_INTRIN

#define R123_USE_MULHILO64_CUDA_INTRIN 0

21.41.1.29 R123_USE_MULHILO64_MSVC_INTRIN

#define R123_USE_MULHILO64_MSVC_INTRIN 0

21.41.1.30 R123_USE_MULHILO64_MULHI_INTRIN

#define R123_USE_MULHILO64_MULHI_INTRIN (defined(__powerpc64__))

21.41.1.31 R123_USE_MULHILO64_OPENCL_INTRIN

#define R123_USE_MULHILO64_OPENCL_INTRIN 0

21.41.1.32 R123_USE_SMMINTRIN_H

21.41.1.33 R123_USE_SSE

#define R123_USE_SSE 0

```
21.41.1.34 R123_USE_SSE4_1 0

21.41.1.35 R123_USE_SSE4_2

#define R123_USE_SSE4_2 0

21.41.1.36 R123_USE_WMMINTRIN_H

#define R123_USE_WMMINTRIN_H 0

21.41.1.37 R123_USE_WMMINTRIN_H

#define R123_USE_X86INTRIN_H

#define R123_USE_X86INTRIN_H ((defined(__x86_64__)||defined(__i386__)) && R123_GNUC_VERSION >= 40402)
```

21.41.1.38 R123_USE_XMMINTRIN_H

#define R123_USE_XMMINTRIN_H 0

21.42 generateAll.cc File Reference

```
#include "code_generator/generateAll.h"
#include <fstream>
#include <vector>
#include <plog/Log.h>
#include "path.h"
#include "modelSpecInternal.h"
#include "code_generator/codeStream.h"
#include "code_generator/generateInit.h"
#include "code_generator/generateSupportCode.h"
#include "code_generator/generateSupportCode.h"
#include "code_generator/generateSupportCode.h"
#include "code_generator/generateSupportCode.h"
#include "code_generator/generateSupportCode.h"
#include "code_generator/generateSupportCode.h"
#include "code_generator/generateRunner.h"
#include "code_generator/modelSpecMerged.h"
```

21.43 generateAll.h File Reference

```
#include <string>
#include <vector>
#include "gennExport.h"
#include "backendBase.h"
```

Namespaces

- · filesystem
- CodeGenerator

Helper class for generating code - automatically inserts brackets, indents etc.

Functions

• GENN_EXPORT std::pair< std::vector< std::string >, MemAlloc > CodeGenerator::generateAll (const ModelSpecInternal &model, const BackendBase &backend, const filesystem::path &outputPath, bool standaloneModules=false)

21.44 generateInit.cc File Reference

```
#include "code_generator/generateInit.h"
#include <string>
#include "models.h"
#include "code_generator/codeStream.h"
#include "code_generator/modelSpecMerged.h"
#include "code_generator/substitutions.h"
```

21.45 generateInit.h File Reference

```
#include "code_generator/backendBase.h"
#include "code_generator/codeGenUtils.h"
```

Namespaces

CodeGenerator

Helper class for generating code - automatically inserts brackets, indents etc.

Functions

 void CodeGenerator::generateInit (CodeStream &os, const MergedStructData &mergedStructData, BackendBase::MemorySpaces &memorySpaces, const ModelSpecMerged &modelMerged, const BackendBase &backend)

21.46 generateMakefile.cc File Reference

```
#include "code_generator/generateMakefile.h"
#include <string>
#include "modelSpec.h"
#include "code_generator/backendBase.h"
```

21.47 generateMakefile.h File Reference

```
#include <string>
#include <vector>
#include "gennExport.h"
```

Namespaces

CodeGenerator

Helper class for generating code - automatically inserts brackets, indents etc.

Functions

void GENN_EXPORT CodeGenerator::generateMakefile (std::ostream &os, const BackendBase &backend, const std::vector< std::string > &moduleNames)

21.48 generateMSBuild.cc File Reference

```
#include "code_generator/generateMSBuild.h"
#include <string>
#include "code_generator/backendBase.h"
```

21.49 generateMSBuild.h File Reference

```
#include <string>
#include <vector>
#include "gennExport.h"
```

Namespaces

CodeGenerator

Helper class for generating code - automatically inserts brackets, indents etc.

Functions

void GENN_EXPORT CodeGenerator::generateMSBuild (std::ostream &os, const BackendBase &backend, const std::string &projectGUID, const std::vector< std::string > &moduleNames)

21.50 generateNeuronUpdate.cc File Reference

```
#include "code_generator/generateNeuronUpdate.h"
#include <iostream>
#include <string>
#include <plog/Log.h>
#include "models.h"
#include "code_generator/codeStream.h"
#include "code_generator/modelSpecMerged.h"
#include "code_generator/substitutions.h"
#include "code_generator/teeStream.h"
```

21.51 generateNeuronUpdate.h File Reference

```
#include "code_generator/backendBase.h"
#include "code_generator/codeGenUtils.h"
```

Namespaces

CodeGenerator

Helper class for generating code - automatically inserts brackets, indents etc.

Functions

void CodeGenerator::generateNeuronUpdate (CodeStream &os, const MergedStructData &mergedStruct
 Data, BackendBase::MemorySpaces &memorySpaces, const ModelSpecMerged &modelMerged, const
 BackendBase &backend)

21.52 generateRun.h File Reference

```
#include <algorithm>
#include <fstream>
#include <iostream>
#include <string>
#include <thread>
#include <cstdlib>
#include "../../include/genn/third_party/CLI11.hpp"
```

Classes

· class GenerateRunBase

21.53 generateRunner.cc File Reference

```
#include "code_generator/generateRunner.h"
#include <random>
#include <sstream>
#include "gennUtils.h"
#include "code_generator/codeGenUtils.h"
#include "code_generator/codeStream.h"
#include "code_generator/groupMerged.h"
#include "code_generator/substitutions.h"
#include "code_generator/teeStream.h"
#include "code_generator/backendBase.h"
#include "code_generator/mergedStructGenerator.h"
#include "code_generator/modelSpecMerged.h"
```

21.54 generateRunner.h File Reference

```
#include "code_generator/backendBase.h"
#include "code_generator/codeGenUtils.h"
```

Namespaces

CodeGenerator

Helper class for generating code - automatically inserts brackets, indents etc.

Functions

 MemAlloc CodeGenerator::generateRunner (CodeStream &definitions, CodeStream &definitionsInternal, CodeStream &runner, MergedStructData &mergedStructData, const ModelSpecMerged &modelMerged, const BackendBase &backend)

21.55 generateSupportCode.cc File Reference

```
#include "code_generator/generateSupportCode.h"
#include <string>
#include "code_generator/codeGenUtils.h"
#include "code_generator/codeStream.h"
#include "code_generator/modelSpecMerged.h"
```

21.56 generateSupportCode.h File Reference

Namespaces

CodeGenerator

Helper class for generating code - automatically inserts brackets, indents etc.

Functions

void CodeGenerator::generateSupportCode (CodeStream &os, const ModelSpecMerged &modelMerged)

21.57 generateSynapseUpdate.cc File Reference

```
#include "code_generator/generateSynapseUpdate.h"
#include "code_generator/codeGenUtils.h"
#include "code_generator/codeStream.h"
#include "code_generator/groupMerged.h"
#include "code_generator/modelSpecMerged.h"
#include "code_generator/substitutions.h"
#include "code_generator/teeStream.h"
```

21.58 generateSynapseUpdate.h File Reference

```
#include "code_generator/backendBase.h"
#include "code_generator/codeGenUtils.h"
```

Namespaces

CodeGenerator

Helper class for generating code - automatically inserts brackets, indents etc.

Functions

void CodeGenerator::generateSynapseUpdate (CodeStream &os, const MergedStructData &mergedStructData &mergedStructData, BackendBase::MemorySpaces &memorySpaces, const ModelSpecMerged &modelMerged, const BackendBase &backend)

21.59 genn groups.py File Reference

Classes

· class pygenn.genn_groups.Group

Parent class of NeuronGroup, SynapseGroup and CurrentSource.

• class pygenn.genn_groups.NeuronGroup

Class representing a group of neurons.

• class pygenn.genn_groups.SynapseGroup

Class representing synaptic connection between two groups of neurons.

• class pygenn.genn_groups.CurrentSource

Class representing a current injection into a group of neurons.

Namespaces

• pygenn.genn_groups

Variables

• pygenn.genn_groups.xrange = range

GeNNGroups This module provides classes which automatize model checks and parameter convesions for GeNN Groups.

21.60 genn_model.py File Reference

Classes

· class pygenn.genn_model.GeNNModel

GeNNModel class This class helps to define, build and run a GeNN model from python.

Namespaces

· pygenn.genn_model

Functions

def pygenn.genn_model.init_var (init_var_snippet, param_space)

This helper function creates a VarInit object to easily initialise a variable using a snippet.

def pygenn.genn_model.init_connectivity (init_sparse_connect_snippet, param_space)

This helper function creates a InitSparseConnectivitySnippet::Init object to easily initialise connectivity using a snippet.

def pygenn_genn_model.create_custom_neuron_class (class_name, param_names=None, var_name_
 types=None, derived_params=None, sim_code=None, threshold_condition_code=None, reset_code=None,
 support_code=None, extra_global_params=None, additional_input_vars=None, is_auto_refractory_
 required=None, custom_body=None)

This helper function creates a custom NeuronModel class.

def pygenn.genn_model.create_custom_postsynaptic_class (class_name, param_names=None, var
 __name_types=None, derived_params=None, decay_code=None, apply_input_code=None, support_
 code=None, custom_body=None)

This helper function creates a custom PostsynapticModel class.

def pygenn_genn_model.create_custom_weight_update_class (class_name, param_names=None, var
 _name_types=None, pre_var_name_types=None, post_var_name_types=None, derived_params=None,
 sim_code=None, event_code=None, learn_post_code=None, synapse_dynamics_code=None, event_
 threshold_condition_code=None, pre_spike_code=None, post_spike_code=None, sim_support_code=None,
 learn_post_support_code=None, synapse_dynamics_support_code=None, extra_global_params=None,
 is pre_spike_time_required=None, is post_spike_time_required=None, custom_body=None)

This helper function creates a custom WeightUpdateModel class.

def pygenn.genn_model.create_custom_current_source_class (class_name, param_names=None, var_
 name_types=None, derived_params=None, injection_code=None, extra_global_params=None, custom_
 body=None)

This helper function creates a custom NeuronModel class.

 def pygenn.genn_model.create_custom_model_class (class_name, base, param_names, var_name_types, derived params, custom body)

This helper function completes a custom model class creation.

def pygenn.genn_model.create_dpf_class (dp_func)

Helper function to create derived parameter function class.

def pygenn.genn_model.create_cmlf_class (cml_func)

Helper function to create function class for calculating sizes of matrices initialised with sparse connectivity initialisation snippet.

def pygenn.genn_model.create_custom_init_var_snippet_class (class_name, param_names=None, derived_params=None, var_init_code=None, extra_global_params=None, custom_body=None)

This helper function creates a custom InitVarSnippet class.

This helper function creates a custom InitSparseConnectivitySnippet class.

Variables

- pygenn.genn_model.backend_modules = OrderedDict()
- pygenn.genn_model.m = import_module(".genn_wrapper." + b + "Backend", "pygenn")

21.61 gennExport.h File Reference

Macros

• #define GENN EXPORT

21.61.1 Macro Definition Documentation

21.61.1.1 GENN_EXPORT

#define GENN_EXPORT

21.62 gennUtils.cc File Reference

```
#include "gennUtils.h"
#include <algorithm>
#include "models.h"
```

Namespaces

Utils

Functions

• GENN_EXPORT bool Utils::isRNGRequired (const std::string &code)

Does the code string contain any functions requiring random number generator.

GENN_EXPORT bool Utils::isRNGRequired (const std::vector < Models::VarInit > &varInitialisers)

Does the model with the vectors of variable initialisers and modes require an RNG for the specified init location i.e. host or device.

GENN_EXPORT bool Utils::isTypePointer (const std::string &type)

Function to determine whether a string containing a type is a pointer.

• GENN EXPORT bool Utils::isTypePointerToPointer (const std::string &type)

Function to determine whether a string containing a type is a pointer to a pointer.

• GENN_EXPORT std::string Utils::getUnderlyingType (const std::string &type)

Assuming type is a string containing a pointer type, function to return the underlying type.

21.63 gennUtils.h File Reference

```
#include <iomanip>
#include <limits>
#include <sstream>
#include <string>
#include <vector>
#include "gennExport.h"
```

Namespaces

- Models
- Utils

Functions

GENN EXPORT bool Utils::isRNGRequired (const std::string &code)

Does the code string contain any functions requiring random number generator.

GENN_EXPORT bool Utils::isRNGRequired (const std::vector < Models::VarInit > &varInitialisers)

Does the model with the vectors of variable initialisers and modes require an RNG for the specified init location i.e. host or device.

GENN_EXPORT bool Utils::isTypePointer (const std::string &type)

Function to determine whether a string containing a type is a pointer.

GENN_EXPORT bool Utils::isTypePointerToPointer (const std::string &type)

Function to determine whether a string containing a type is a pointer to a pointer.

GENN_EXPORT std::string Utils::getUnderlyingType (const std::string &type)

Assuming type is a string containing a pointer type, function to return the underlying type.

• template<class T , typename std::enable_if< std::is_floating_point< T >::value >::type * = nullptr> void Utils::writePreciseString (std::ostream &os, T value)

This function writes a floating point value to a stream -setting the precision so no digits are lost.

template < class T , typename std::enable_if < std::is_floating_point < T >::value >::type * = nullptr>
std::string Utils::writePreciseString (T value)

This function writes a floating point value to a string - setting the precision so no digits are lost.

21.64 groupMerged.cc File Reference

```
#include "code_generator/groupMerged.h"
#include 'plog/Log.h>
#include "modelSpecInternal.h"
#include "code_generator/backendBase.h"
#include "code_generator/codeGenUtils.h"
#include "code_generator/codeStream.h"
#include "code_generator/mergedStructGenerator.h"
```

21.65 groupMerged.h File Reference

```
#include <algorithm>
#include <functional>
#include <type_traits>
#include vector>
#include "gennExport.h"
#include "currentSourceInternal.h"
#include "neuronGroupInternal.h"
#include "synapseGroupInternal.h"
#include "code generator/mergedStructGenerator.h"
```

Classes

- class CodeGenerator::GroupMerged< G >
- class CodeGenerator::NeuronSpikeQueueUpdateGroupMerged
- class CodeGenerator::NeuronGroupMergedBase
- class CodeGenerator::NeuronUpdateGroupMerged
- · class CodeGenerator::NeuronInitGroupMerged
- class CodeGenerator::SynapseDendriticDelayUpdateGroupMerged
- class CodeGenerator::SynapseConnectivityHostInitGroupMerged
- · class CodeGenerator::SynapseConnectivityInitGroupMerged
- class CodeGenerator::SynapseGroupMergedBase
- class CodeGenerator::PresynapticUpdateGroupMerged
- class CodeGenerator::PostsynapticUpdateGroupMerged
- class CodeGenerator::SynapseDynamicsGroupMerged
- class CodeGenerator::SynapseDenseInitGroupMerged
- class CodeGenerator::SynapseSparseInitGroupMerged

Namespaces

CodeGenerator

Helper class for generating code - automatically inserts brackets, indents etc.

21.66 initSparseConnectivitySnippet.cc File Reference

```
#include "initSparseConnectivitySnippet.h"
```

Functions

- IMPLEMENT_SNIPPET (InitSparseConnectivitySnippet::Uninitialised)
- IMPLEMENT_SNIPPET (InitSparseConnectivitySnippet::OneToOne)
- IMPLEMENT_SNIPPET (InitSparseConnectivitySnippet::FixedProbability)
- IMPLEMENT_SNIPPET (InitSparseConnectivitySnippet::FixedProbabilityNoAutapse)
- IMPLEMENT_SNIPPET (InitSparseConnectivitySnippet::FixedNumberPostWithReplacement)
- IMPLEMENT_SNIPPET (InitSparseConnectivitySnippet::FixedNumberTotalWithReplacement)

21.66.1 Function Documentation

```
21.66.1.1 IMPLEMENT_SNIPPET() [1/6]
IMPLEMENT_SNIPPET (
              InitSparseConnectivitySnippet::Uninitialised )
21.66.1.2 IMPLEMENT_SNIPPET() [2/6]
IMPLEMENT_SNIPPET (
             InitSparseConnectivitySnippet::OneToOne )
21.66.1.3 IMPLEMENT_SNIPPET() [3/6]
IMPLEMENT_SNIPPET (
             InitSparseConnectivitySnippet::FixedProbability )
21.66.1.4 IMPLEMENT_SNIPPET() [4/6]
IMPLEMENT_SNIPPET (
              InitSparseConnectivitySnippet::FixedProbabilityNoAutapse )
21.66.1.5 IMPLEMENT_SNIPPET() [5/6]
IMPLEMENT_SNIPPET (
              {\tt InitSparseConnectivitySnippet::} {\tt FixedNumberPostWithReplacement} \quad {\tt )}
21.66.1.6 IMPLEMENT_SNIPPET() [6/6]
IMPLEMENT_SNIPPET (
             InitSparseConnectivitySnippet::FixedNumberTotalWithReplacement )
```

21.67 initSparseConnectivitySnippet.h File Reference

```
#include <functional>
#include <vector>
#include <cassert>
#include <cmath>
#include "binomial.h"
#include "snippet.h"
```

Classes

- · class InitSparseConnectivitySnippet::Base
- · class InitSparseConnectivitySnippet::Init
- · class InitSparseConnectivitySnippet::Uninitialised

Used to mark connectivity as uninitialised - no initialisation code will be run.

class InitSparseConnectivitySnippet::OneToOne

Initialises connectivity to a 'one-to-one' diagonal matrix.

- · class InitSparseConnectivitySnippet::FixedProbabilityBase
- class InitSparseConnectivitySnippet::FixedProbability
- class InitSparseConnectivitySnippet::FixedProbabilityNoAutapse
- class InitSparseConnectivitySnippet::FixedNumberPostWithReplacement

Initialises connectivity with a fixed number of random synapses per row.

class InitSparseConnectivitySnippet::FixedNumberTotalWithReplacement

Namespaces

InitSparseConnectivitySnippet

Base class for all sparse connectivity initialisation snippets.

Macros

- #define SET_ROW_BUILD_CODE(CODE) virtual std::string getRowBuildCode() const override{ return CO← DE; }
- #define SET_ROW_BUILD_STATE_VARS(...) virtual ParamValVec getRowBuildStateVars() const override{
 return __VA_ARGS__;}
- #define SET_HOST_INIT_CODE(CODE) virtual std::string getHostInitCode() const override{ return CODE; }
- #define SET_CALC_MAX_ROW_LENGTH_FUNC(FUNC) virtual CalcMaxLengthFunc getCalcMaxRow
 LengthFunc() const override{ return FUNC; }
- #define SET_CALC_MAX_COL_LENGTH_FUNC(FUNC) virtual CalcMaxLengthFunc getCalcMaxCol
 LengthFunc() const override{ return FUNC; }
- #define SET_MAX_ROW_LENGTH(MAX_ROW_LENGTH) virtual CalcMaxLengthFunc getCalcMaxRow
 LengthFunc() const override{ return [](unsigned int, unsigned int, const std::vector<double> &){ return M←
 AX_ROW_LENGTH; }; }
- #define SET_MAX_COL_LENGTH(MAX_COL_LENGTH) virtual CalcMaxLengthFunc getCalcMaxCol
 LengthFunc() const override{ return [](unsigned int, unsigned int, const std::vector < double > &){ return
 MAX_COL_LENGTH; }; }

21.67.1 Macro Definition Documentation

21.67.1.1 SET_CALC_MAX_COL_LENGTH_FUNC #define SET_CALC_MAX_COL_LENGTH_FUNC(FUNC) virtual CalcMaxLengthFunc getCalcMaxColLengthFunc() const override{ return FUNC; } 21.67.1.2 SET_CALC_MAX_ROW_LENGTH_FUNC #define SET_CALC_MAX_ROW_LENGTH_FUNC(FUNC) virtual CalcMaxLengthFunc getCalcMaxRowLengthFunc() const override{ return FUNC; } 21.67.1.3 SET_HOST_INIT_CODE #define SET_HOST_INIT_CODE(CODE) virtual std::string getHostInitCode() const override{ return CODE; } 21.67.1.4 SET_MAX_COL_LENGTH #define SET_MAX_COL_LENGTH(MAX_COL_LENGTH) virtual CalcMaxLengthFunc getCalcMaxColLengthFunc() const override{ return [](unsigned int, unsigned int, const std::vector<double> &){ return MAX_COL_LENGTH; }; } 21.67.1.5 SET_MAX_ROW_LENGTH #define SET_MAX_ROW_LENGTH(MAX_ROW_LENGTH) virtual CalcMaxLengthFunc getCalcMaxRowLengthFunc() const override{ return [](unsigned int, unsigned int, const std::vector<double> &) { return MAX_ROW_LENGTH; }; 21.67.1.6 SET_ROW_BUILD_CODE #define SET_ROW_BUILD_CODE(CODE) virtual std::string getRowBuildCode() const override{ return CODE; } 21.67.1.7 SET_ROW_BUILD_STATE_VARS #define SET_ROW_BUILD_STATE_VARS(...) virtual ParamValVec getRowBuildStateVars() const override{ return __VA_A↔ RGS__; } 21.68 initVarSnippet.cc File Reference #include "initVarSnippet.h"

Functions

IMPLEMENT_SNIPPET (InitVarSnippet::Uninitialised)

- IMPLEMENT_SNIPPET (InitVarSnippet::Constant)
- IMPLEMENT SNIPPET (InitVarSnippet::Uniform)
- IMPLEMENT_SNIPPET (InitVarSnippet::Normal)
- IMPLEMENT_SNIPPET (InitVarSnippet::NormalClipped)
- IMPLEMENT_SNIPPET (InitVarSnippet::NormalClippedDelay)
- IMPLEMENT_SNIPPET (InitVarSnippet::Exponential)
- IMPLEMENT_SNIPPET (InitVarSnippet::Gamma)

21.68.1 Function Documentation

```
21.68.1.1 IMPLEMENT_SNIPPET() [1/8]
IMPLEMENT_SNIPPET (
            InitVarSnippet::Uninitialised )
21.68.1.2 IMPLEMENT_SNIPPET() [2/8]
IMPLEMENT_SNIPPET (
             InitVarSnippet::Constant )
21.68.1.3 IMPLEMENT_SNIPPET() [3/8]
IMPLEMENT_SNIPPET (
            InitVarSnippet::Uniform )
21.68.1.4 IMPLEMENT_SNIPPET() [4/8]
IMPLEMENT_SNIPPET (
            InitVarSnippet::Normal )
21.68.1.5 IMPLEMENT_SNIPPET() [5/8]
IMPLEMENT_SNIPPET (
             InitVarSnippet::NormalClipped )
21.68.1.6 IMPLEMENT_SNIPPET() [6/8]
IMPLEMENT_SNIPPET (
            InitVarSnippet::NormalClippedDelay )
21.68.1.7 IMPLEMENT_SNIPPET() [7/8]
IMPLEMENT_SNIPPET (
```

InitVarSnippet::Exponential)

21.68.1.8 IMPLEMENT_SNIPPET() [8/8]

21.69 initVarSnippet.h File Reference

```
#include "snippet.h"
```

Classes

- · class InitVarSnippet::Base
- class InitVarSnippet::Uninitialised

Used to mark variables as uninitialised - no initialisation code will be run.

class InitVarSnippet::Constant

Initialises variable to a constant value.

class InitVarSnippet::Uniform

Initialises variable by sampling from the uniform distribution.

· class InitVarSnippet::Normal

Initialises variable by sampling from the normal distribution.

- · class InitVarSnippet::NormalClipped
- class InitVarSnippet::NormalClippedDelay
- class InitVarSnippet::Exponential

Initialises variable by sampling from the exponential distribution.

class InitVarSnippet::Gamma

Initialises variable by sampling from the exponential distribution.

Namespaces

InitVarSnippet

Base class for all value initialisation snippets.

Macros

#define SET_CODE(CODE) virtual std::string getCode() const override{ return CODE; }

21.69.1 Macro Definition Documentation

21.69.1.1 SET_CODE

21.70 input.cc File Reference

```
#include "input.h"
#include <limits>
#include <iostream>
#include <cmath>
```

```
#include "pugixml/pugixml.hpp"
#include "spineMLLogging.h"
#include "inputValue.h"
#include "modelProperty.h"
```

21.71 input.h File Reference

```
#include <functional>
#include <map>
#include <memory>
#include <random>
```

Classes

- class SpineMLSimulator::Input::Base
- class SpineMLSimulator::Input::SpikeBase
- class SpineMLSimulator::Input::InterSpikeIntervalBase
- class SpineMLSimulator::Input::RegularSpikeRate
- class SpineMLSimulator::Input::PoissonSpikeRate
- · class SpineMLSimulator::Input::SpikeTime
- class SpineMLSimulator::Input::Analogue

Namespaces

- pugi
- SpineMLSimulator
- SpineMLSimulator::InputValue
- SpineMLSimulator::ModelProperty
- SpineMLSimulator::Input

21.72 inputValue.cc File Reference

```
#include "inputValue.h"
#include <iostream>
#include <sstream>
#include <cassert>
#include <cmath>
#include <cstring>
#include "pugixml/pugixml.hpp"
#include "spineMLLogging.h"
#include "spineMLUtils.h"
```

21.73 inputValue.h File Reference

```
#include <functional>
#include <map>
#include <memory>
#include <string>
#include <vector>
#include "networkClient.h"
```

Classes

- class SpineMLSimulator::InputValue::Base
- class SpineMLSimulator::InputValue::ScalarBase
- · class SpineMLSimulator::InputValue::Constant
- class SpineMLSimulator::InputValue::ConstantArray
- class SpineMLSimulator::InputValue::TimeVarying
- class SpineMLSimulator::InputValue::TimeVaryingArray
- class SpineMLSimulator::InputValue::External
- class SpineMLSimulator::InputValue::ExternalNetwork

Namespaces

- pugi
- SpineMLSimulator
- SpineMLSimulator::InputValue

Functions

 std::unique_ptr< Base > SpineMLSimulator::InputValue::create (double dt, unsigned int numNeurons, const pugi::xml_node &node, std::map< std::string, InputValue::External *> &externalInputs)

21.74 Ifsr113.c File Reference

```
#include <clRNG/lfsr113.h>
#include "private.h"
#include <stdlib.h>
#include "clRNG/private/lfsr113.c.h"
```

Classes

struct clrngLfsr113StreamCreator_

Macros

#define BASE_CREATOR_STATE { 987654321, 987654321, 987654321, 987654321 }
 Default initial seed of the first stream.

Functions

- clrngLfsr113StreamCreator * clrngLfsr113CopyStreamCreator (const clrngLfsr113StreamCreator *creator, clrngStatus *err)
- clrngStatus clrngLfsr113DestroyStreamCreator (clrngLfsr113StreamCreator *creator)
- clrngStatus clrngLfsr113RewindStreamCreator (clrngLfsr113StreamCreator *creator)
- clrngStatus clrngLfsr113SetBaseCreatorState (clrngLfsr113StreamCreator *creator, const clrngLfsr113↔ StreamState *baseState)
- clrngStatus clrngLfsr113ChangeStreamsSpacing (clrngLfsr113StreamCreator *creator, cl_int e, cl_int c)
- clrngLfsr113Stream * clrngLfsr113AllocStreams (size t count, size t *bufSize, clrngStatus *err)
- clrngStatus clrngLfsr113DestroyStreams (clrngLfsr113Stream *streams)
- void lfsr113AdvanceState (clrngLfsr113StreamState *currentState)
- clrngStatus clrngLfsr113CreateOverStreams (clrngLfsr113StreamCreator *creator, size_t count, clrng← Lfsr113Stream *streams)

- clrngLfsr113Stream * clrngLfsr113CreateStreams (clrngLfsr113StreamCreator *creator, size_t count, size
 _t *bufSize, clrngStatus *err)
- clrngLfsr113Stream * clrngLfsr113CopyStreams (size_t count, const clrngLfsr113Stream *streams, clrng← Status *err)
- clrngLfsr113Stream * clrngLfsr113MakeSubstreams (clrngLfsr113Stream *stream, size_t count, size_← t *bufSize, clrngStatus *err)
- clrngStatus clrngLfsr113AdvanceStreams (size_t count, clrngLfsr113Stream *streams, cl_int e, cl_int c)
- clrngStatus clrngLfsr113WriteStreamInfo (const clrngLfsr113Stream *stream, FILE *file)
- clrngStatus clrngLfsr113DeviceRandomU01Array_ (size_t streamCount, cl_mem streams, size_t number
 — Count, cl_mem outBuffer, cl_uint numQueuesAndEvents, cl_command_queue *commQueues, cl_uint num
 — WaitEvents, const cl_event *waitEvents, cl_event *outEvents, cl_bool singlePrecision)

21.74.1 Macro Definition Documentation

```
21.74.1.1 BASE CREATOR STATE
```

```
#define BASE_CREATOR_STATE { 987654321, 987654321, 987654321, 987654321 }
```

Default initial seed of the first stream.

21.74.2 Function Documentation

21.74.2.1 clrngLfsr113AdvanceStreams()

See also

clrngAdvanceStreams()

21.74.2.2 clrngLfsr113AllocStreams()

See also

clrngAllocStreams()

21.74.2.3 clrngLfsr113ChangeStreamsSpacing()

```
See also
     clrngChangeStreamsSpacing()
21.74.2.4 clrngLfsr113CopyStreamCreator()
{\tt clrngLfsr113StreamCreator*~clrngLfsr113CopyStreamCreator~(}
              const clrngLfsr113StreamCreator * creator,
              clrngStatus * err )
See also
     clrngCopyStreamCreator()
21.74.2.5 clrngLfsr113CopyStreams()
clrngLfsr113Stream* clrngLfsr113CopyStreams (
              size_t count,
              const clrngLfsr113Stream * streams,
              clrngStatus * err )
See also
     clrngCopyStreams()
21.74.2.6 clrngLfsr113CreateOverStreams()
{\tt clrngStatus} \ {\tt clrngLfsr113CreateOverStreams} \ (
              clrngLfsr113StreamCreator * creator,
              size_t count,
              clrngLfsr113Stream * streams)
See also
     clrngCreateOverStreams()
21.74.2.7 clrngLfsr113CreateStreams()
clrngLfsr113Stream* clrngLfsr113CreateStreams (
              clrngLfsr113StreamCreator * creator,
              size_t count,
              size_t * bufSize,
              clrngStatus * err )
See also
     clrngCreateStreams()
```

21.74.2.8 clrngLfsr113DestroyStreamCreator()

```
See also
```

```
clrngDestroyStreamCreator()
```

21.74.2.9 clrngLfsr113DestroyStreams()

See also

clrngDestroyStreams()

21.74.2.10 clrngLfsr113DeviceRandomU01Array_()

21.74.2.11 clrngLfsr113MakeSubstreams()

See also

clrngMakeSubstreams()

21.74.2.12 clrngLfsr113RewindStreamCreator()

See also

clrngRewindStreamCreator()

21.74.2.13 clrngLfsr113SetBaseCreatorState()

See also

clrngSetBaseCreatorState()

21.74.2.14 clrngLfsr113WriteStreamInfo()

See also

clrngWriteStreamInfo()

21.74.2.15 Ifsr113AdvanceState()

21.75 Ifsr113.c.h File Reference

Macros

- #define PRIVATE_LFSR113_CH
- #define Lfsr113_NORM_cl_double 1.0 / 0x100000001L
- #define Lfsr113 NORM cl float 2.3283063e-10f
- #define IMPLEMENT_GENERATE_FOR_TYPE(fptype)

Functions

- clrngStatus clrngLfsr113CopyOverStreams (size_t count, clrngLfsr113Stream *destStreams, const clrng← Lfsr113Stream *srcStreams)
- clrngStatus clrngLfsr113RewindStreams (size_t count, clrngLfsr113Stream *streams)
- clrngStatus clrngLfsr113RewindSubstreams (size_t count, clrngLfsr113Stream *streams)
- void lfsr113ResetNextSubStream (clrngLfsr113Stream *stream)
- clrngStatus clrngLfsr113ForwardToNextSubstreams (size t count, clrngLfsr113Stream *streams)
- clrngStatus clrngLfsr113MakeOverSubstreams (clrngLfsr113Stream *stream, size_t count, clrngLfsr113← Stream *substreams)

21.75.1 Macro Definition Documentation

21.75.1.1 IMPLEMENT_GENERATE_FOR_TYPE

Value:

```
fptype clrngLfsr113RandomU01_##fptype(clrngLfsr113Stream* stream) { \
    return clrngLfsr113NextState(&stream->current) * Lfsr113_NORM_##fptype; \
} \
```

```
cl_int clrngLfsr113RandomInteger_##fptype(clrngLfsr113Stream* stream, cl_int i,
                   return i + (cl_int)((j - i + 1) * clrngLfsrl13RandomU01_##fptype(stream)); \
          `clrngStatus clrngLfsr113RandomU01Array_##fptype(clrngLfsr113Stream* stream, size_t
               count, fptype* buffer) { \
                   if (!stream) \
                              return clrngSetErrorString(CLRNG_INVALID_VALUE); \
                   if (!buffer) \
                              return clrngSetErrorString(CLRNG_INVALID_VALUE); \
                    for (size_t i = 0; i < count; i++)
                            buffer[i] = clrngLfsr113RandomU01_##fptype(stream); \
                   return CLRNG_SUCCESS; \
          \verb|clrngStatus| \verb|clrngLfsr113RandomIntegerArray_##fptype(clrngLfsr113Stream*| stream, the stream is a stream in the stream in the stream is a stream in the 
               cl_int i, cl_int j, size_t count, cl_int* buffer) { \
                  if (!stream) \
                             return clrngSetErrorString(CLRNG_INVALID_VALUE); \
                   if (!buffer) \
                              return clrngSetErrorString(CLRNG_INVALID_VALUE); \
                   for (size_t k = 0; k < count; k++) \
   buffer[k] = clrngLfsr113RandomInteger_##fptype(stream, i, j); \
return CLRNG_SUCCESS; \</pre>
21.75.1.2 Lfsr113_NORM_cl_double
#define Lfsr113_NORM_cl_double 1.0 / 0x100000001L
21.75.1.3 Lfsr113_NORM_cl_float
#define Lfsr113_NORM_cl_float 2.3283063e-10f
21.75.1.4 PRIVATE_LFSR113_CH
#define PRIVATE_LFSR113_CH
21.75.2 Function Documentation
21.75.2.1 clrngLfsr113CopyOverStreams()
{\tt clrngStatus} \ {\tt clrngLfsr113CopyOverStreams} \ (
                                  size_t count,
                                   clrngLfsr113Stream * destStreams,
                                    const clrngLfsr113Stream * srcStreams )
See also
             clrngCopyOverStreams()
21.75.2.2 clrngLfsr113ForwardToNextSubstreams()
clrngStatus clrngLfsr113ForwardToNextSubstreams (
                                  size_t count,
```

clrngLfsr113Stream * streams)

```
See also
```

clrngForwardToNextSubstreams()

21.75.2.3 clrngLfsr113MakeOverSubstreams()

See also

clrngMakeOverSubstreams()

21.75.2.4 clrngLfsr113RewindStreams()

See also

clrngRewindStreams()

21.75.2.5 clrngLfsr113RewindSubstreams()

See also

clrngRewindSubstreams()

21.75.2.6 Ifsr113ResetNextSubStream()

21.76 Ifsr113.c.h File Reference

Macros

- #define PRIVATE_LFSR113_CH
- #define Lfsr113_NORM_cl_double 1.0 / 0x10000001L
- #define Lfsr113_NORM_cl_float 2.3283063e-10f
- #define IMPLEMENT_GENERATE_FOR_TYPE(fptype)

Functions

- clrngStatus clrngLfsr113CopyOverStreams (size_t count, clrngLfsr113Stream *destStreams, const clrng← Lfsr113Stream *srcStreams)
- clrngStatus clrngLfsr113RewindStreams (size_t count, clrngLfsr113Stream *streams)
- clrngStatus clrngLfsr113RewindSubstreams (size_t count, clrngLfsr113Stream *streams)
- void lfsr113ResetNextSubStream (clrngLfsr113Stream *stream)
- clrngStatus clrngLfsr113ForwardToNextSubstreams (size_t count, clrngLfsr113Stream *streams)
- clrngStatus clrngLfsr113MakeOverSubstreams (clrngLfsr113Stream *stream, size_t count, clrngLfsr113↔ Stream *substreams)

21.76.1 Macro Definition Documentation

21.76.1.1 IMPLEMENT GENERATE FOR TYPE

Value:

```
fptype clrngLfsr113RandomU01_##fptype(clrngLfsr113Stream* stream) {
    return clrngLfsr113NextState(&stream->current) * Lfsr113_NORM_##fptype; \
cl_int clrngLfsr113RandomInteger_##fptype(clrngLfsr113Stream* stream, cl_int i,
 cl int i) { \
   return i + (cl_int)((j - i + 1) * clrngLfsr113RandomU01_##fptype(stream)); \
clrngStatus clrngLfsr113RandomU01Array_##fptype(clrngLfsr113Stream* stream, size_t
 count, fptype* buffer) {
  if (!stream) \
        return clrngSetErrorString(CLRNG INVALID VALUE, "%s():
   stream cannot be NULL", __func__); \
    if (!buffer) \
        return clrngSetErrorString(CLRNG_INVALID_VALUE, "%s():
  buffer cannot be NULL", __func__); \
for (size_t i = 0; i < count; i++) \
  buffer[i] = clrngLfsr113RandomU01_##fptype(stream); \</pre>
    return CLRNG_SUCCESS; \
cl_int i, cl_int j, size_t count, cl_int* buffer) { \
    if (!stream) '
        return clrngSetErrorString(CLRNG_INVALID_VALUE, "%s():
   stream cannot be NULL", __func__); \
    if (!buffer) \
        return clrngSetErrorString(CLRNG_INVALID_VALUE, "%s():
   buffer cannot be NULL", __func__);
for (size_t k = 0; k < count; k++) \
    buffer[k] = clrngLfsr113RandomInteger_##fptype(stream, i, j); \</pre>
    return CLRNG_SUCCESS; \
```

21.76.1.2 Lfsr113_NORM_cl_double

```
#define Lfsr113_NORM_cl_double 1.0 / 0x10000001L
```

21.76.1.3 Lfsr113_NORM_cl_float

```
#define Lfsr113_NORM_cl_float 2.3283063e-10f
```

```
21.76.1.4 PRIVATE_LFSR113_CH
#define PRIVATE_LFSR113_CH
21.76.2 Function Documentation
21.76.2.1 clrngLfsr113CopyOverStreams()
clrngStatus clrngLfsr113CopyOverStreams (
             size_t count,
              clrngLfsr113Stream * destStreams,
              const clrngLfsr113Stream * srcStreams )
See also
     clrngCopyOverStreams()
21.76.2.2 clrngLfsr113ForwardToNextSubstreams()
{\tt clrngStatus} \ {\tt clrngLfsrl13ForwardToNextSubstreams} \ \ (
              size_t count,
              clrngLfsr113Stream * streams )
See also
     clrngForwardToNextSubstreams()
21.76.2.3 clrngLfsr113MakeOverSubstreams()
clrngStatus clrngLfsr113MakeOverSubstreams (
              clrngLfsr113Stream * stream,
              size_t count,
              clrngLfsr113Stream * substreams )
See also
     clrngMakeOverSubstreams()
21.76.2.4 clrngLfsr113RewindStreams()
clrngStatus clrngLfsr113RewindStreams (
              size_t count,
              clrngLfsr113Stream * streams )
See also
```

clrngRewindStreams()

21.76.2.5 clrngLfsr113RewindSubstreams()

See also

clrngRewindSubstreams()

21.76.2.6 Ifsr113ResetNextSubStream()

21.77 Ifsr113.h File Reference

```
#include <clRNG/clRNG.h>
#include <stdio.h>
```

Classes

- · struct clrngLfsr113StreamState
- struct clrngLfsr113Stream_

Macros

- #define LFSR113_H
- #define clrngLfsr113RandomU01 _CLRNG_TAG_FPTYPE(clrngLfsr113RandomU01)
- #define clrngLfsr113RandomInteger CLRNG TAG FPTYPE(clrngLfsr113RandomInteger)
- #define clrngLfsr113RandomU01Array _CLRNG_TAG_FPTYPE(clrngLfsr113RandomU01Array)
- #define clrngLfsr113RandomIntegerArray _CLRNG_TAG_FPTYPE(clrngLfsr113RandomIntegerArray)
- #define clrngLfsr113DeviceRandomU01Array(...) clrngLfsr113DeviceRandomU01Array_(__VA_ARGS__, CL_FALSE)

Typedefs

- typedef struct clrngLfsr113Stream_ clrngLfsr113Stream
- typedef struct clrngLfsr113StreamCreator_ clrngLfsr113StreamCreator

Functions

- CLRNGAPI clrngLfsr113StreamCreator * clrngLfsr113CopyStreamCreator (const clrngLfsr113Stream← Creator *creator, clrngStatus *err)
- CLRNGAPI clrngStatus clrngLfsr113DestroyStreamCreator (clrngLfsr113StreamCreator *creator)
- CLRNGAPI clrngStatus clrngLfsr113RewindStreamCreator (clrngLfsr113StreamCreator *creator)
- CLRNGAPI clrngStatus clrngLfsr113SetBaseCreatorState (clrngLfsr113StreamCreator *creator, const clrngLfsr113StreamState *baseState)
- CLRNGAPI clrngStatus clrngLfsr113ChangeStreamsSpacing (clrngLfsr113StreamCreator *creator, cl_int e, cl_int c)
- CLRNGAPI clrngLfsr113Stream * clrngLfsr113AllocStreams (size_t count, size_t *bufSize, clrngStatus *err)

- CLRNGAPI clrngStatus clrngLfsr113DestroyStreams (clrngLfsr113Stream *streams)
- CLRNGAPI clrngStatus clrngLfsr113CreateOverStreams (clrngLfsr113StreamCreator *creator, size_t count, clrngLfsr113Stream *streams)
- CLRNGAPI clrngLfsr113Stream * clrngLfsr113CreateStreams (clrngLfsr113StreamCreator *creator, size_t count, size_t *bufSize, clrngStatus *err)
- CLRNGAPI clrngStatus clrngLfsr113CopyOverStreams (size_t count, clrngLfsr113Stream *destStreams, const clrngLfsr113Stream *srcStreams)
- CLRNGAPI clrngLfsr113Stream * clrngLfsr113CopyStreams (size_t count, const clrngLfsr113Stream *streams, clrngStatus *err)
- CLRNGAPI_CLRNG_FPTYPE clrngLfsr113RandomU01 (clrngLfsr113Stream *stream)
- CLRNGAPI cl float clrngLfsr113RandomU01 cl float (clrngLfsr113Stream *stream)
- CLRNGAPI cl_double clrngLfsr113RandomU01_cl_double (clrngLfsr113Stream *stream)
- CLRNGAPI cl_int clrngLfsr113RandomInteger (clrngLfsr113Stream *stream, cl_int i, cl_int j)
- CLRNGAPI cl_int clrngLfsr113RandomInteger_cl_float (clrngLfsr113Stream *stream, cl_int i, cl_int j)
- CLRNGAPI cl_int clrngLfsr113RandomInteger_cl_double (clrngLfsr113Stream *stream, cl_int i, cl_int j)
- CLRNGAPI clrngStatus clrngLfsr113RandomU01Array (clrngLfsr113Stream *stream, size_t count, _CLR ← NG_FPTYPE *buffer)
- CLRNGAPI clrngStatus clrngLfsr113RandomU01Array_cl_float (clrngLfsr113Stream *stream, size_t count, cl float *buffer)
- CLRNGAPI clrngStatus clrngLfsr113RandomU01Array_cl_double (clrngLfsr113Stream *stream, size_t count, cl_double *buffer)
- CLRNGAPI clrngStatus clrngLfsr113RandomIntegerArray (clrngLfsr113Stream *stream, cl_int i, cl_int j, size t count, cl int *buffer)
- CLRNGAPI clrngStatus clrngLfsr113RandomIntegerArray_cl_float (clrngLfsr113Stream *stream, cl_int i, cl—int j, size_t count, cl_int *buffer)
- CLRNGAPI clrngStatus clrngLfsr113RandomIntegerArray_cl_double (clrngLfsr113Stream *stream, cl_int i, cl_int j, size_t count, cl_int *buffer)
- CLRNGAPI clrngStatus clrngLfsr113RewindStreams (size_t count, clrngLfsr113Stream *streams)
- CLRNGAPI clrngStatus clrngLfsr113RewindSubstreams (size_t count, clrngLfsr113Stream *streams)
- CLRNGAPI clrngStatus clrngLfsr113ForwardToNextSubstreams (size_t count, clrngLfsr113Stream *streams)
- CLRNGAPI clrngLfsr113Stream * clrngLfsr113MakeSubstreams (clrngLfsr113Stream *stream, size_t count, size_t *bufSize, clrngStatus *err)
- CLRNGAPI clrngStatus clrngLfsr113MakeOverSubstreams (clrngLfsr113Stream *stream, size_t count, clrngLfsr113Stream *substreams)
- CLRNGAPI clrngStatus clrngLfsr113AdvanceStreams (size_t count, clrngLfsr113Stream *streams, cl_int e, cl int c)
- CLRNGAPI clrngStatus clrngLfsr113DeviceRandomU01Array_ (size_t streamCount, cl_mem streams, size
 _ t numberCount, cl_mem outBuffer, cl_uint numQueuesAndEvents, cl_command_queue *commQueues, cl
 _ uint numWaitEvents, const cl_event *waitEvents, cl_event *outEvents, cl_bool singlePrecision)
- CLRNGAPI clrngStatus clrngLfsr113WriteStreamInfo (const clrngLfsr113Stream *stream, FILE *file)

21.77.1 Macro Definition Documentation

21.77.1.1 clrngLfsr113DeviceRandomU01Array

See also

clrngDeviceRandomU01Array()

```
21.77.1.2 clrngLfsr113RandomInteger
#define clrngLfsr113RandomInteger _CLRNG_TAG_FPTYPE(clrngLfsr113RandomInteger)
21.77.1.3 clrngLfsr113RandomIntegerArray
#define clrngLfsr113RandomIntegerArray _CLRNG_TAG_FPTYPE(clrngLfsr113RandomIntegerArray)
21.77.1.4 clrngLfsr113RandomU01
#define clrngLfsr113RandomU01 _CLRNG_TAG_FPTYPE(clrngLfsr113RandomU01)
21.77.1.5 clrngLfsr113RandomU01Array
#define clrngLfsr113RandomU01Array _CLRNG_TAG_FPTYPE(clrngLfsr113RandomU01Array)
21.77.1.6 LFSR113_H
#define LFSR113_H
21.77.2 Typedef Documentation
21.77.2.1 clrngLfsr113Stream
typedef struct clrngLfsr113Stream_ clrngLfsr113Stream
See also
     clrngStream
21.77.2.2 clrngLfsr113StreamCreator
typedef struct clrngLfsr113StreamCreator_ clrngLfsr113StreamCreator
See also
     clrngStreamCreator
21.77.3 Function Documentation
21.77.3.1 clrngLfsr113AdvanceStreams()
CLRNGAPI clrngStatus clrngLfsr113AdvanceStreams (
             size_t count,
             clrngLfsr113Stream * streams,
             cl_int e,
             cl_int c)
```

```
See also
     clrngAdvanceStreams()
21.77.3.2 clrngLfsr113AllocStreams()
CLRNGAPI clrngLfsr113Stream* clrngLfsr113AllocStreams (
              size_t count,
             size_t * bufSize,
             clrngStatus * err )
See also
     clrngAllocStreams()
21.77.3.3 clrngLfsr113ChangeStreamsSpacing()
CLRNGAPI clrngStatus clrngLfsr113ChangeStreamsSpacing (
              clrngLfsr113StreamCreator * creator,
             cl_int e,
             cl\_int c )
See also
     clrngChangeStreamsSpacing()
21.77.3.4 clrngLfsr113CopyOverStreams()
CLRNGAPI clrngStatus clrngLfsr113CopyOverStreams (
             size_t count,
              clrngLfsr113Stream * destStreams,
             const clrngLfsr113Stream * srcStreams )
See also
     clrngCopyOverStreams()
21.77.3.5 clrngLfsr113CopyStreamCreator()
CLRNGAPI clrngLfsr113StreamCreator* clrngLfsr113CopyStreamCreator (
             const clrnqLfsr113StreamCreator * creator,
             clrngStatus * err )
See also
     clrngCopyStreamCreator()
21.77.3.6 clrngLfsr113CopyStreams()
CLRNGAPI clrngLfsr113Stream* clrngLfsr113CopyStreams (
             size_t count,
             const clrngLfsr113Stream * streams,
```

clrngStatus * err)

```
See also
     clrngCopyStreams()
21.77.3.7 clrngLfsr113CreateOverStreams()
CLRNGAPI clrngStatus clrngLfsr113CreateOverStreams (
             clrngLfsr113StreamCreator * creator,
             size_t count,
              clrngLfsr113Stream * streams )
See also
     clrngCreateOverStreams()
21.77.3.8 clrngLfsr113CreateStreams()
CLRNGAPI clrngLfsr113Stream* clrngLfsr113CreateStreams (
             clrngLfsr113StreamCreator * creator,
             size_t count,
             size_t * bufSize,
              clrngStatus * err )
See also
     clrngCreateStreams()
21.77.3.9 clrngLfsr113DestroyStreamCreator()
CLRNGAPI clrngStatus clrngLfsr113DestroyStreamCreator (
              clrngLfsr113StreamCreator * creator )
See also
     clrngDestroyStreamCreator()
21.77.3.10 clrngLfsr113DestroyStreams()
CLRNGAPI clrngStatus clrngLfsr113DestroyStreams (
             clrngLfsr113Stream * streams )
See also
     clrngDestroyStreams()
21.77.3.11 clrngLfsr113DeviceRandomU01Array_()
CLRNGAPI clrngStatus clrngLfsr113DeviceRandomU01Array_ (
             size_t streamCount,
```

cl_mem streams,
size_t numberCount,

```
cl_mem outBuffer,
              cl_uint numQueuesAndEvents,
              cl_command_queue * commQueues,
              cl_uint numWaitEvents,
              const cl_event * waitEvents,
              cl_event * outEvents,
              cl_bool singlePrecision )
21.77.3.12 clrngLfsr113ForwardToNextSubstreams()
{\tt CLRNGAPI\ clrngStatus\ clrngLfsrl13ForwardToNextSubstreams\ (}
              size_t count,
              clrngLfsr113Stream * streams )
See also
     clrngForwardToNextSubstreams()
21.77.3.13 clrngLfsr113MakeOverSubstreams()
CLRNGAPI clrngStatus clrngLfsr113MakeOverSubstreams (
             clrngLfsr113Stream * stream,
             size_t count,
             clrngLfsr113Stream * substreams )
See also
     clrngMakeOverSubstreams()
21.77.3.14 clrngLfsr113MakeSubstreams()
CLRNGAPI clrngLfsr113Stream* clrngLfsr113MakeSubstreams (
             clrngLfsr113Stream * stream,
             size_t count,
             size_t * bufSize,
              clrngStatus * err )
See also
     clrngMakeSubstreams()
21.77.3.15 clrngLfsr113RandomInteger()
CLRNGAPI cl_int clrngLfsr113RandomInteger (
             clrngLfsr113Stream * stream,
             cl_int i,
             cl_int j )
See also
```

clrngRandomInteger()

```
21.77.3.16 clrngLfsr113RandomInteger_cl_double()
CLRNGAPI cl_int clrngLfsr113RandomInteger_cl_double (
             clrngLfsr113Stream * stream,
             cl_int i,
             cl_int j)
21.77.3.17 clrngLfsr113RandomInteger_cl_float()
CLRNGAPI cl_int clrngLfsr113RandomInteger_cl_float (
             clrngLfsr113Stream * stream,
             cl_int i,
              cl_int j)
21.77.3.18 clrngLfsr113RandomIntegerArray()
CLRNGAPI clrngStatus clrngLfsr113RandomIntegerArray (
              clrngLfsr113Stream * stream,
             cl_int i,
             cl_int j,
             size_t count,
             cl_int * buffer )
See also
     clrngRandomIntegerArray()
21.77.3.19 clrngLfsr113RandomIntegerArray_cl_double()
{\tt CLRNGAPI\ clrngStatus\ clrngLfsr113RandomIntegerArray\_cl\_double\ (}
              clrngLfsr113Stream * stream,
              cl_int i,
```

```
cl_int j,
size_t count,
cl_int * buffer )
```

21.77.3.20 clrngLfsr113RandomIntegerArray_cl_float()

```
CLRNGAPI clrngStatus clrngLfsr113RandomIntegerArray_cl_float (
            clrngLfsr113Stream * stream,
            cl_int i,
            cl_int j,
            size_t count,
            cl_int * buffer )
```

21.77.3.21 clrngLfsr113RandomU01()

```
CLRNGAPI _CLRNG_FPTYPE clrngLfsr113RandomU01 (
            clrngLfsr113Stream * stream )
```

```
See also
     clrngRandomU01()
21.77.3.22 clrngLfsr113RandomU01_cl_double()
CLRNGAPI cl_double clrngLfsr113RandomU01_cl_double (
              clrngLfsr113Stream * stream )
21.77.3.23 clrngLfsr113RandomU01_cl_float()
CLRNGAPI cl_float clrngLfsr113RandomU01_cl_float (
              clrngLfsr113Stream * stream )
21.77.3.24 clrngLfsr113RandomU01Array()
CLRNGAPI clrngStatus clrngLfsr113RandomU01Array (
             clrngLfsr113Stream * stream,
             size_t count,
              _CLRNG_FPTYPE * buffer )
See also
     clrngRandomU01Array()
21.77.3.25 clrngLfsr113RandomU01Array_cl_double()
CLRNGAPI clrngStatus clrngLfsr113RandomU01Array_cl_double (
              clrngLfsr113Stream * stream,
              size_t count,
              cl_double * buffer )
21.77.3.26 clrngLfsr113RandomU01Array_cl_float()
{\tt CLRNGAPI\ clrngStatus\ clrngLfsrll3RandomU0lArray\_cl\_float\ (}
             clrngLfsr113Stream * stream,
             size_t count,
             cl_float * buffer )
21.77.3.27 clrngLfsr113RewindStreamCreator()
{\tt CLRNGAPI\ clrngStatus\ clrngLfsr113RewindStreamCreator\ (}
              clrngLfsr113StreamCreator * creator)
See also
     clrngRewindStreamCreator()
21.77.3.28 clrngLfsr113RewindStreams()
CLRNGAPI clrngStatus clrngLfsr113RewindStreams (
```

```
size_t count,
             clrngLfsr113Stream * streams )
See also
     clrngRewindStreams()
21.77.3.29 clrngLfsr113RewindSubstreams()
{\tt CLRNGAPI\ clrngStatus\ clrngLfsr113RewindSubstreams\ (}
             size_t count,
             clrngLfsr113Stream * streams )
See also
     clrngRewindSubstreams()
21.77.3.30 clrngLfsr113SetBaseCreatorState()
CLRNGAPI clrngStatus clrngLfsr113SetBaseCreatorState (
             clrngLfsr113StreamCreator * creator,
             const clrngLfsr113StreamState * baseState )
See also
     clrngSetBaseCreatorState()
21.77.3.31 clrngLfsr113WriteStreamInfo()
CLRNGAPI clrngStatus clrngLfsrl13WriteStreamInfo (
             const clrngLfsr113Stream * stream,
             FILE * file )
See also
     clrngWriteStreamInfo()
21.78 logging.cc File Reference
#include "logging.h"
21.79 logging.h File Reference
#include <plog/Log.h>
#include <plog/Severity.h>
#include "gennExport.h"
```

Namespaces

- plog
- Logging

Macros

- #define LOGV_GENN LOGV_(Logging::CHANNEL_GENN)
- #define LOGD_GENN LOGD_(Logging::CHANNEL_GENN)
- #define LOGI_GENN LOGI_(Logging::CHANNEL_GENN)
- #define LOGW GENN LOGW (Logging::CHANNEL GENN)
- #define LOGE_GENN LOGE_(Logging::CHANNEL_GENN)
- #define LOGF_GENN LOGF_(Logging::CHANNEL_GENN)
- #define LOGV_CODE_GEN LOGV_(Logging::CHANNEL_CODE_GEN)
- #define LOGD_CODE_GEN LOGD_(Logging::CHANNEL_CODE_GEN)
- #define LOGI_CODE_GEN LOGI_(Logging::CHANNEL_CODE_GEN)
- #define LOGW_CODE_GEN LOGW_(Logging::CHANNEL_CODE_GEN)
- #define LOGE_CODE_GEN LOGE_(Logging::CHANNEL_CODE_GEN)
- #define LOGF_CODE_GEN LOGF_(Logging::CHANNEL_CODE_GEN)
- #define LOGV_BACKEND LOGV_(Logging::CHANNEL_BACKEND)
- #define LOGD_BACKEND LOGD_(Logging::CHANNEL_BACKEND)
- #define LOGI_BACKEND LOGI_(Logging::CHANNEL_BACKEND)
- #define LOGW_BACKEND LOGW_(Logging::CHANNEL_BACKEND)
- #define LOGE_BACKEND LOGE_(Logging::CHANNEL_BACKEND)
- #define LOGF_BACKEND LOGF_(Logging::CHANNEL_BACKEND)

Enumerations

enum Logging::Channel { Logging::CHANNEL_GENN = 0, Logging::CHANNEL_CODE_GEN = 1, Logging
 ::CHANNEL BACKEND = 2, Logging::CHANNEL MAX }

Functions

GENN_EXPORT void Logging::init (plog::Severity gennLevel, plog::Severity codeGeneratorLevel, plog::I←
 Appender *gennAppender, plog::IAppender *codeGeneratorAppender)

21.79.1 Macro Definition Documentation

```
21.79.1.1 LOGD_BACKEND
#define LOGD_BACKEND LOGD_(Logging::CHANNEL_BACKEND)

21.79.1.2 LOGD_CODE_GEN
#define LOGD_CODE_GEN LOGD_(Logging::CHANNEL_CODE_GEN)

21.79.1.3 LOGD_GENN
#define LOGD_GENN LOGD_(Logging::CHANNEL_GENN)
```

21.79.1.4 LOGE_BACKEND

#define LOGE_BACKEND LOGE_(Logging::CHANNEL_BACKEND)

```
21.79.1.5 LOGE_CODE_GEN
#define LOGE_CODE_GEN LOGE_(Logging::CHANNEL_CODE_GEN)
21.79.1.6 LOGE_GENN
#define LOGE_GENN LOGE_(Logging::CHANNEL_GENN)
21.79.1.7 LOGF_BACKEND
#define LOGF_BACKEND LOGF_(Logging::CHANNEL_BACKEND)
21.79.1.8 LOGF_CODE_GEN
#define LOGF_CODE_GEN LOGF_(Logging::CHANNEL_CODE_GEN)
21.79.1.9 LOGF_GENN
#define LOGF_GENN LOGF_(Logging::CHANNEL_GENN)
21.79.1.10 LOGI_BACKEND
#define LOGI_BACKEND LOGI_(Logging::CHANNEL_BACKEND)
21.79.1.11 LOGI_CODE_GEN
#define LOGI_CODE_GEN LOGI_(Logging::CHANNEL_CODE_GEN)
21.79.1.12 LOGI_GENN
#define LOGI_GENN LOGI_(Logging::CHANNEL_GENN)
21.79.1.13 LOGV_BACKEND
#define LOGV_BACKEND LOGV_(Logging::CHANNEL_BACKEND)
21.79.1.14 LOGV_CODE_GEN
#define LOGV_CODE_GEN LOGV_(Logging::CHANNEL_CODE_GEN)
21.79.1.15 LOGV_GENN
#define LOGV_GENN LOGV_(Logging::CHANNEL_GENN)
```

21.79.1.16 LOGW_BACKEND

```
#define LOGW_BACKEND LOGW_(Logging::CHANNEL_BACKEND)

21.79.1.17 LOGW_CODE_GEN

#define LOGW_CODE_GEN LOGW_(Logging::CHANNEL_CODE_GEN)

21.79.1.18 LOGW_GENN
```

#define LOGW_GENN LOGW_(Logging::CHANNEL_GENN)

21.80 logOutput.cc File Reference

```
#include "logOutput.h"
#include <algorithm>
#include <limits>
#include <iostream>
#include <cassert>
#include <cmath>
#include "path.h"
#include "pugixml/pugixml.hpp"
#include "spineMLLogging.h"
#include "spineMLUtils.h"
```

21.81 logOutput.h File Reference

```
#include <fstream>
#include <set>
#include <string>
#include <vector>
#include "modelProperty.h"
#include "networkClient.h"
```

Classes

- · class SpineMLSimulator::LogOutput::Base
- class SpineMLSimulator::LogOutput::AnalogueBase
- · class SpineMLSimulator::LogOutput::AnalogueFile
- · class SpineMLSimulator::LogOutput::AnalogueExternal
- · class SpineMLSimulator::LogOutput::AnalogueNetwork
- · class SpineMLSimulator::LogOutput::Event

Namespaces

- pugi
- · filesystem
- SpineMLSimulator
- SpineMLSimulator::LogOutput

21.82 logWriter.h File Reference

21.83 mergedStructGenerator.h File Reference

```
#include <functional>
#include <vector>
#include <unordered_map>
#include "gennUtils.h"
#include "code_generator/codeGenUtils.h"
#include "code_generator/codeStream.h"
#include "code_generator/groupMerged.h"
```

Classes

class CodeGenerator::MergedStructGenerator< T >

Namespaces

CodeGenerator

Helper class for generating code - automatically inserts brackets, indents etc.

21.84 model_preprocessor.py File Reference

Classes

· class pygenn.model_preprocessor.Variable

Class holding information about GeNN variables.

· class pygenn.model_preprocessor.ExtraGlobalVariable

Class holding information about GeNN extra global pointer variable.

Namespaces

• pygenn.model_preprocessor

Functions

• def pygenn.model_preprocessor.prepare_model (model, group, param_space, var_space, pre_var_compare=None, post var space=None, model family=None)

Prepare a model by checking its validity and extracting information about variables and parameters.

• def pygenn.model_preprocessor.prepare_snippet (snippet, param_space, snippet_family)

Prepare a snippet by checking its validity and extracting information about parameters.

def pygenn.model preprocessor.is model valid (model, model family)

Check whether the model is valid, i.e is native or derived from model_family.Custom.

• def pygenn.model_preprocessor.param_space_to_vals (model, param_space)

Convert a param_space dict to ParamValues.

• def pygenn.model_preprocessor.param_space_to_val_vec (model, param_space)

Convert a param_space dict to a std::vector<double>

def pygenn.model_preprocessor.var_space_to_vals (model, var_space)

Convert a var_space dict to VarValues.

def pygenn.model_preprocessor.pre_var_space_to_vals (model, var_space)

Convert a var_space dict to PreVarValues.

• def pygenn.model_preprocessor.post_var_space_to_vals (model, var_space)

Convert a var_space dict to PostVarValues.

Variables

- pygenn.model_preprocessor.GeNNType = namedtuple("GeNNType", ["np_dtype", "assign_ext_ptr_array", "assign_ext_ptr_single"])
- dictionary pygenn.model_preprocessor.genn_types

21.85 modelProperty.cc File Reference

```
#include "modelProperty.h"
#include <algorithm>
#include <fstream>
#include <iostream>
#include <cassert>
#include <cstring>
#include "path.h"
#include "pugixml/pugixml.hpp"
#include "spineMLLogging.h"
```

21.86 modelProperty.h File Reference

```
#include <memory>
#include <random>
#include <vector>
#include "stateVar.h"
```

Classes

- class SpineMLSimulator::ModelProperty::Base
- · class SpineMLSimulator::ModelProperty::Fixed
- · class SpineMLSimulator::ModelProperty::ValueList
- class SpineMLSimulator::ModelProperty::UniformDistribution
- class SpineMLSimulator::ModelProperty::NormalDistribution
- class SpineMLSimulator::ModelProperty::ExponentialDistribution

Namespaces

- pugi
- · filesystem
- · SpineMLSimulator
- SpineMLSimulator::ModelProperty

Typedefs

typedef float SpineMLSimulator::scalar

Functions

• std::unique_ptr< Base > SpineMLSimulator::ModelProperty::create (const pugi::xml_node &node, const StateVar< scalar > &stateVar, unsigned int size, bool skipGeNNInitialised, const filesystem::path &basePath, const std::string &valueNamespace, const std::vector< unsigned int > *remapIndices)

21.87 models.h File Reference

```
#include <algorithm>
#include <string>
#include <vector>
#include "snippet.h"
#include "initVarSnippet.h"
```

Classes

- · class Models::VarInit
- class Models::VarInitContainerBase< NumVars >
- class Models::VarInitContainerBase< 0 >
- · class Models::Base

Base class for all models - in addition to the parameters snippets have, models can have state variables.

• struct Models::Base::Var

A variable has a name, a type and an access type.

Namespaces

Models

Macros

- #define DECLARE_MODEL(TYPE, NUM_PARAMS, NUM_VARS)
- #define IMPLEMENT_MODEL(TYPE) IMPLEMENT_SNIPPET(TYPE)
- #define SET_VARS(...) virtual VarVec getVars() const override{ return __VA_ARGS__; }

Enumerations

enum VarAccess { VarAccess::READ_WRITE = 0, VarAccess::READ_ONLY }
 How is this variable accessed by model?

21.87.1 Macro Definition Documentation

21.87.1.1 DECLARE_MODEL

Value:

21.87.1.2 IMPLEMENT_MODEL

21.87.1.3 SET_VARS

21.87.2 Enumeration Type Documentation

21.87.2.1 VarAccess

```
enum VarAccess [strong]
```

How is this variable accessed by model?

Enumerator

READ_WRITE	
READ_ONLY	This variable is both read and written by the model.

21.88 modelSpec.cc File Reference

```
#include <algorithm>
#include <numeric>
#include <typeinfo>
#include <cstdio>
#include <cmath>
#include <cassert>
#include "modelSpec.h"
#include "code_generator/codeGenUtils.h"
#include "code_generator/substitutions.h"
```

21.89 modelSpec.h File Reference

Header file that contains the class (struct) definition of neuronModel for defining a neuron model and the class definition of ModelSpec for defining a neuronal network model. Part of the code generation and generated code sections.

```
#include <map>
#include <set>
#include <string>
#include <vector>
```

```
#include "currentSourceInternal.h"
#include "gennExport.h"
#include "neuronGroupInternal.h"
#include "synapseGroupInternal.h"
```

Classes

· class ModelSpec

Object used for specifying a neuronal network model.

Macros

• #define NO DELAY 0

Macro used to indicate no synapse delay for the group (only one queue slot will be generated)

Typedefs

• typedef ModelSpec NNmodel

Enumerations

enum FloatType { , GENN_LONG_DOUBLE }

Floating point precision to use for models.

enum TimePrecision { TimePrecision::DEFAULT, TimePrecision::FLOAT, TimePrecision::DOUBLE }

Precision to use for variables which store time.

Functions

template<typename S >

Models::VarInit initVar (const typename S::ParamValues ¶ms)

Initialise a variable using an initialisation snippet.

• template<typename S >

```
std::enable_if< std::is_same< typename S::ParamValues, Snippet::ValueBase< 0 > >::value, Models::

VarInit >::type initVar ()
```

Initialise a variable using an initialisation snippet with no parameters.

• Models::VarInit uninitialisedVar ()

Mark a variable as uninitialised.

• template<typename S >

InitSparseConnectivitySnippet::Init initConnectivity (const typename S::ParamValues ¶ms)

Initialise connectivity using a sparse connectivity snippet.

template<typename S >

```
std::enable_if< std::is_same< typename S::ParamValues, Snippet::ValueBase< 0 > >::value, InitSparse ConnectivitySnippet::Init >::type initConnectivity ()
```

Initialise connectivity using a sparse connectivity snippet with no parameters.

InitSparseConnectivitySnippet::Init uninitialisedConnectivity ()

Mark a synapse group's sparse connectivity as uninitialised.

21.89.1 Detailed Description

Header file that contains the class (struct) definition of neuronModel for defining a neuron model and the class definition of ModelSpec for defining a neuronal network model. Part of the code generation and generated code sections.

21.89.2 Macro Definition Documentation

21.89.2.1 NO_DELAY

```
#define NO_DELAY 0
```

Macro used to indicate no synapse delay for the group (only one queue slot will be generated)

21.89.3 Typedef Documentation

21.89.3.1 NNmodel

```
typedef ModelSpec NNmodel
```

21.89.4 Enumeration Type Documentation

21.89.4.1 FloatType

```
enum FloatType
```

Floating point precision to use for models.

Enumerator

```
GENN_LONG_DOUBLE
```

21.89.4.2 TimePrecision

```
enum TimePrecision [strong]
```

Precision to use for variables which store time.

Enumerator

DEFAULT	Time uses default model precision.
FLOAT	Time uses single precision - not suitable for long simulations.
DOUBLE	Time uses double precision - may reduce performance.

21.89.5 Function Documentation

21.89.5.1 initConnectivity() [1/2]

Initialise connectivity using a sparse connectivity snippet.

Template Parameters

S type of sparse connectivitiy initialisation snippet (derived from InitSparseConnectivitySnippet::Base).

Parameters

params parameters for snippet wrapped in S::ParamValues object.

Returns

InitSparseConnectivitySnippet::Init object for passing to ModelSpec::addSynapsePopulation

21.89.5.2 initConnectivity() [2/2]

```
template<typename S >
std::enable_if<std::is_same<typename S::ParamValues, Snippet::ValueBase<0> >::value, Init←
SparseConnectivitySnippet::Init>::type initConnectivity ( ) [inline]
```

Initialise connectivity using a sparse connectivity snippet with no parameters.

Template Parameters

S type of sparse connectivity initialisation snippet (derived from InitSparseConnectivitySnippet::Base).

Returns

InitSparseConnectivitySnippet::Init object for passing to ModelSpec::addSynapsePopulation

```
21.89.5.3 initVar() [1/2]

template<typename S >
Models::VarInit initVar (
```

Initialise a variable using an initialisation snippet.

Template Parameters

S type of variable initialisation snippet (derived from InitVarSnippet::Base).

const typename S::ParamValues & params) [inline]

Parameters

params parameters for snippet wrapped in S::ParamValues object.

Returns

Models::VarInit object for use within model's VarValues

```
21.89.5.4 initVar() [2/2]
```

```
template<typename S >
std::enable_if<std::is_same<typename S::ParamValues, Snippet::ValueBase<0> >::value, Models←
::VarInit>::type initVar ( ) [inline]
```

Initialise a variable using an initialisation snippet with no parameters.

Template Parameters

S type of variable initialisation snippet (derived from InitVarSnippet::Base).

Returns

Models::VarInit object for use within model's VarValues

21.89.5.5 uninitialisedConnectivity()

```
InitSparseConnectivitySnippet::Init uninitialisedConnectivity ( ) [inline]
```

Mark a synapse group's sparse connectivity as uninitialised.

This means that the backend will not generate any automatic initialization code, but will instead copy the connectivity from host to device during initializeSparse function (and, if necessary generate any additional data structures it requires)

21.89.5.6 uninitialisedVar()

```
Models::VarInit uninitialisedVar ( ) [inline]
```

Mark a variable as uninitialised.

This means that the backend will not generate any automatic initialization code, but will instead copy the variable from host to device during initializeSparse function

21.90 modelSpecInternal.h File Reference

```
#include "modelSpec.h"
```

Classes

· class ModelSpecInternal

21.91 modelSpecMerged.cc File Reference

```
#include "code_generator/modelSpecMerged.h"
#include "logging.h"
#include "modelSpecInternal.h"
#include "code_generator/backendBase.h"
```

21.92 modelSpecMerged.h File Reference

```
#include <vector>
#include "modelSpecInternal.h"
```

```
#include "code_generator/groupMerged.h"
#include "code_generator/supportCodeMerged.h"
```

Classes

• class CodeGenerator::ModelSpecMerged

Namespaces

CodeGenerator

Helper class for generating code - automatically inserts brackets, indents etc.

21.93 modular.c.h File Reference

Macros

- #define MATRIX_ELEM(mat, i, j) (mat[i * N + j])
- #define modMult(a, s, c, m) ((MODULAR_NUMBER_TYPE)(((cl_ulong) a * s + c) % m))
 Compute (a*s + c) % m.

Functions

void modMatVec (size_t N, MODULAR_NUMBER_TYPE *A, MODULAR_NUMBER_TYPE *s, MODULA←
 R_NUMBER_TYPE *v, MODULAR_NUMBER_TYPE m)

Matrix-vector modular multiplication.

21.93.1 Macro Definition Documentation

21.93.1.1 MATRIX_ELEM

21.93.1.2 modMult

Compute (a*s + c) % m.

21.93.2 Function Documentation

21.93.2.1 modMatVec()

```
void modMatVec (
    size_t N,
    MODULAR_NUMBER_TYPE * A,
    MODULAR_NUMBER_TYPE * s,
    MODULAR_NUMBER_TYPE * v,
    MODULAR_NUMBER_TYPE m )
```

Matrix-vector modular multiplication.

21.94 modular.c.h File Reference

Macros

- #define MATRIX_ELEM(mat, i, j) (mat[i * N + j])
- #define modMult(a, s, c, m) ((MODULAR_NUMBER_TYPE)(((cl_ulong) a * s + c) % m))
 Compute (a*s + c) % m.

Functions

void modMatVec (size_t N, MODULAR_NUMBER_TYPE *A, MODULAR_NUMBER_TYPE *s, MODULAR_NUMBER_TYPE *v, MODULAR_NUMBER_TYPE m)

Matrix-vector modular multiplication.

21.94.1 Macro Definition Documentation

21.94.1.1 MATRIX_ELEM

21.94.1.2 modMult

Compute (a*s + c) % m.

21.94.2 Function Documentation

21.94.2.1 modMatVec()

```
void modMatVec ( \label{eq:size_t N, MODULAR_NUMBER_TYPE * A,}
```

```
MODULAR_NUMBER_TYPE * s,
MODULAR_NUMBER_TYPE * v,
MODULAR_NUMBER_TYPE m )
```

Matrix-vector modular multiplication.

21.95 modularHost.c.h File Reference

```
#include "clRNG/private/modular.c.h"
```

Macros

- #define MODULAR CH
- #define MATRIX_ELEM(mat, i, j) (mat[i * N + j])

Functions

• void modMatMat (size_t N, MODULAR_NUMBER_TYPE *A, MODULAR_NUMBER_TYPE *B, MODULA← R_NUMBER_TYPE *C, MODULAR_NUMBER_TYPE m)

Compute A*B % m.

Compute matrix $B = (A^{\wedge}(2^{\wedge}e) \% m)$

void modMatPow (size_t N, MODULAR_NUMBER_TYPE *A, MODULAR_NUMBER_TYPE *B, MODULA←
 R_NUMBER_TYPE m, cl_uint n)

Compute matrix $B = A^{\wedge} n \% m$.

21.95.1 Macro Definition Documentation

21.95.1.1 MATRIX_ELEM

21.95.1.2 MODULAR_CH

```
#define MODULAR_CH
```

21.95.2 Function Documentation

21.95.2.1 modMatMat()

```
MODULAR_NUMBER_TYPE * C,
             MODULAR_NUMBER_TYPE m )
Compute A*B % m.
21.95.2.2 modMatPow()
void modMatPow (
             size_t N,
             MODULAR_NUMBER_TYPE * A,
             MODULAR_NUMBER_TYPE * B,
             MODULAR_NUMBER_TYPE m,
             cl_uint n )
Compute matrix B = A^n \% m.
21.95.2.3 modMatPowLog2()
void modMatPowLog2 (
             size_t N,
             MODULAR_NUMBER_TYPE * A,
             MODULAR_NUMBER_TYPE * B,
             MODULAR_NUMBER_TYPE m,
             cl_uint e )
Compute matrix B = (A^{\wedge}(2^{\wedge}e) \% m)
21.96 msvcfeatures.h File Reference
#include <assert.h>
#include <stdint.h>
Macros
   • #define R123_STATIC_INLINE static __inline
   • #define R123_FORCE_INLINE(decl) _forceinline decl

    #define R123_CUDA_DEVICE

    #define R123_ASSERT(x) assert(x)

   • #define R123_BUILTIN_EXPECT(expr, likely) expr
   • #define R123 USE AES NI 0

    #define R123_USE_SSE4_2 0

   • #define R123 USE SSE4 1 0
   #define R123_USE_SSE 1
   • #define R123_USE_AES_OPENSSL 0
   #define R123_USE_GNU_UINT128 0

    #define R123_USE_ASM_GNU 0

    #define R123 USE CPUID MSVC 1

   #define R123_USE_X86INTRIN_H 0
   • #define R123 USE IA32INTRIN H 0
   • #define R123_USE_XMMINTRIN_H 0
   • #define R123 USE EMMINTRIN H1

    #define R123_USE_SMMINTRIN_H 1

   • #define R123 USE WMMINTRIN H 1
```

#define R123_USE_INTRIN_H 1

- #define R123_USE_MULHILO16_ASM 0
- #define R123_USE_MULHILO32_ASM 0
- #define R123_USE_MULHILO64_ASM 0
- #define R123_USE_MULHILO64_MSVC_INTRIN 0
- #define R123_USE_MULHILO64_CUDA_INTRIN 0
- #define R123_USE_MULHILO64_OPENCL_INTRIN 0
- #define __STDC_CONSTANT_MACROS

21.96.1 Macro Definition Documentation

21.96.1.1 __STDC_CONSTANT_MACROS

#define __STDC_CONSTANT_MACROS

21.96.1.2 R123_ASSERT

#define R123_ASSERT(x) assert(x)

21.96.1.3 R123_BUILTIN_EXPECT

21.96.1.4 R123_CUDA_DEVICE

#define R123_CUDA_DEVICE

21.96.1.5 R123_FORCE_INLINE

#define R123_FORCE_INLINE(decl) _forceinline decl

21.96.1.6 R123_STATIC_INLINE

#define R123_STATIC_INLINE static __inline

21.96.1.7 R123_USE_AES_NI

#define R123_USE_AES_NI 0

21.96.1.8 R123_USE_AES_OPENSSL

#define R123_USE_AES_OPENSSL 0

21.96.1.9 R123_USE_ASM_GNU

#define R123_USE_ASM_GNU 0

21.96.1.10 R123_USE_CPUID_MSVC

#define R123_USE_CPUID_MSVC 1

21.96.1.11 R123_USE_EMMINTRIN_H

#define R123_USE_EMMINTRIN_H 1

21.96.1.12 R123_USE_GNU_UINT128

#define R123_USE_GNU_UINT128 0

21.96.1.13 R123_USE_IA32INTRIN_H

#define R123_USE_IA32INTRIN_H 0

21.96.1.14 R123_USE_INTRIN_H

#define R123_USE_INTRIN_H 1

21.96.1.15 R123_USE_MULHILO16_ASM

#define R123_USE_MULHILO16_ASM 0

21.96.1.16 R123_USE_MULHILO32_ASM

#define R123_USE_MULHILO32_ASM 0

21.96.1.17 R123_USE_MULHILO64_ASM

#define R123_USE_MULHILO64_ASM 0

21.96.1.18 R123_USE_MULHILO64_CUDA_INTRIN

#define R123_USE_MULHILO64_CUDA_INTRIN 0

21.96.1.19 R123_USE_MULHILO64_MSVC_INTRIN

#define R123_USE_MULHILO64_MSVC_INTRIN 0

21.96.1.20 R123_USE_MULHILO64_OPENCL_INTRIN

#define R123_USE_MULHILO64_OPENCL_INTRIN 0

21.96.1.21 R123 USE SMMINTRIN H

#define R123_USE_SMMINTRIN_H 1

21.96.1.22 R123_USE_SSE

#define R123_USE_SSE 1

21.96.1.23 R123_USE_SSE4_1

#define R123_USE_SSE4_1 0

21.96.1.24 R123_USE_SSE4_2

#define R123_USE_SSE4_2 0

21.96.1.25 R123_USE_WMMINTRIN_H

#define R123_USE_WMMINTRIN_H 1

21.96.1.26 R123_USE_X86INTRIN_H

#define R123_USE_X86INTRIN_H 0

21.96.1.27 R123_USE_XMMINTRIN_H

#define R123_USE_XMMINTRIN_H 0

21.97 networkClient.cc File Reference

```
#include "networkClient.h"
#include <stdexcept>
#include <cstring>
#include "spineMLLogging.h"
```

21.98 networkClient.h File Reference

```
#include <iostream>
#include <string>
#include <vector>
#include <arpa/inet.h>
#include <netinet/in.h>
#include <netinet/tcp.h>
```

```
#include <sys/socket.h>
#include <sys/types.h>
#include <unistd.h>
#include "spineMLLogging.h"
```

Classes

· class SpineMLSimulator::NetworkClient

Namespaces

SpineMLSimulator

21.99 neuronGroup.cc File Reference

```
#include "neuronGroup.h"
#include <algorithm>
#include <cmath>
#include "currentSourceInternal.h"
#include "logging.h"
#include "neuronGroupInternal.h"
#include "synapseGroupInternal.h"
#include "gennUtils.h"
```

21.100 neuronGroup.h File Reference

```
#include <map>
#include <set>
#include <string>
#include <vector>
#include "gennExport.h"
#include "neuronModels.h"
#include "variableMode.h"
```

Classes

- class NeuronGroup
- · struct NeuronGroup::SpikeEventThreshold

Structure used for storing spike event data.

21.101 neuronGroupInternal.h File Reference

```
#include "neuronGroup.h"
```

Classes

class NeuronGroupInternal

21.102 neuronModels.cc File Reference

```
#include "neuronModels.h"
```

Functions

- IMPLEMENT MODEL (NeuronModels::RulkovMap)
- IMPLEMENT_MODEL (NeuronModels::Izhikevich)
- IMPLEMENT_MODEL (NeuronModels::IzhikevichVariable)
- IMPLEMENT_MODEL (NeuronModels::LIF)
- IMPLEMENT_MODEL (NeuronModels::SpikeSource)
- IMPLEMENT_MODEL (NeuronModels::SpikeSourceArray)
- IMPLEMENT_MODEL (NeuronModels::Poisson)
- IMPLEMENT_MODEL (NeuronModels::PoissonNew)
- IMPLEMENT_MODEL (NeuronModels::TraubMiles)
- IMPLEMENT_MODEL (NeuronModels::TraubMilesFast)
- IMPLEMENT_MODEL (NeuronModels::TraubMilesAlt)
- IMPLEMENT_MODEL (NeuronModels::TraubMilesNStep)

21.102.1 Function Documentation

```
21.102.1.6 IMPLEMENT_MODEL() [6/12]
IMPLEMENT_MODEL (
            NeuronModels::SpikeSourceArray )
21.102.1.7 IMPLEMENT_MODEL() [7/12]
IMPLEMENT_MODEL (
           NeuronModels::Poisson )
21.102.1.8 IMPLEMENT_MODEL() [8/12]
IMPLEMENT_MODEL (
            NeuronModels::PoissonNew )
21.102.1.9 IMPLEMENT_MODEL() [9/12]
IMPLEMENT_MODEL (
            NeuronModels::TraubMiles )
21.102.1.10 IMPLEMENT_MODEL() [10/12]
IMPLEMENT_MODEL (
            NeuronModels::TraubMilesFast )
21.102.1.11 IMPLEMENT_MODEL() [11/12]
IMPLEMENT_MODEL (
            NeuronModels::TraubMilesAlt )
21.102.1.12 IMPLEMENT_MODEL() [12/12]
IMPLEMENT_MODEL (
            NeuronModels::TraubMilesNStep )
21.103 neuronModels.h File Reference
#include <array>
#include <functional>
#include <string>
#include <tuple>
#include <vector>
#include <cmath>
#include "models.h"
```

Classes

• class NeuronModels::Base

Base class for all neuron models.

class NeuronModels::RulkovMap

Rulkov Map neuron.

class NeuronModels::Izhikevich

Izhikevich neuron with fixed parameters [1].

class NeuronModels::IzhikevichVariable

Izhikevich neuron with variable parameters [1].

- · class NeuronModels::LIF
- class NeuronModels::SpikeSource

Empty neuron which allows setting spikes from external sources.

class NeuronModels::SpikeSourceArray

Spike source array.

class NeuronModels::Poisson

Poisson neurons.

· class NeuronModels::PoissonNew

Poisson neurons.

class NeuronModels::TraubMiles

Hodgkin-Huxley neurons with Traub & Miles algorithm.

· class NeuronModels::TraubMilesFast

Hodgkin-Huxley neurons with Traub & Miles algorithm: Original fast implementation, using 25 inner iterations.

• class NeuronModels::TraubMilesAlt

Hodgkin-Huxley neurons with Traub & Miles algorithm.

class NeuronModels::TraubMilesNStep

Hodgkin-Huxley neurons with Traub & Miles algorithm.

Namespaces

NeuronModels

Macros

- #define SET_SIM_CODE(SIM_CODE) virtual std::string getSimCode() const override{ return SIM_CODE; }
- #define SET_THRESHOLD_CONDITION_CODE(THRESHOLD_CONDITION_CODE) virtual std::string getThresholdConditionCode() const override{ return THRESHOLD_CONDITION_CODE; }
- #define SET_RESET_CODE(RESET_CODE) virtual std::string getResetCode() const override{ return RE
 SET_CODE; }
- #define SET_SUPPORT_CODE(SUPPORT_CODE) virtual std::string getSupportCode() const override{ return SUPPORT_CODE; }
- #define SET_ADDITIONAL_INPUT_VARS(...) virtual ParamValVec getAdditionalInputVars() const override{
 return VA_ARGS__; }

21.103.1 Macro Definition Documentation

21.103.1.1 SET_ADDITIONAL_INPUT_VARS

21.103.1.2 SET_NEEDS_AUTO_REFRACTORY

21.103.1.3 SET_RESET_CODE

```
#define SET_RESET_CODE(  RESET\_CODE \ ) \ \ virtual \ std::string \ getResetCode() \ \ const \ \ override\{ \ return \ RESET\_CO \leftarrow DE; \ \}
```

21.103.1.4 SET_SIM_CODE

21.103.1.5 SET_SUPPORT_CODE

```
#define SET_SUPPORT_CODE( SUPPORT\_CODE\ ) \ \ virtual\ \ std::string\ getSupportCode() \ \ const\ \ override\{\ return\ \ SUPP\leftrightarrow\ \ \ \ \ \ \ \}
```

21.103.1.6 SET_THRESHOLD_CONDITION_CODE

21.104 normalDistribution.h File Reference

```
#include <cmath>
```

Functions

- double rational Approximation (double t)
- double normalCDFInverse (double p)

21.104.1 Function Documentation

21.104.1.1 normalCDFInverse()

```
double normalCDFInverse ( double p )
```

21.104.1.2 rational Approximation()

```
double rational Approximation ( double t )
```

21.105 open64features.h File Reference

```
#include "gccfeatures.h"
```

Macros

- #define R123 USE GNU UINT128 0
- #define R123_USE_MULHILO64_ASM 1

21.105.1 Macro Definition Documentation

21.105.1.1 R123_USE_GNU_UINT128

```
#define R123_USE_GNU_UINT128 0
```

21.105.1.2 R123_USE_MULHILO64_ASM

```
#define R123_USE_MULHILO64_ASM 1
```

21.106 openclfeatures.h File Reference

Macros

- #define R123_STATIC_INLINE inline
- #define R123_FORCE_INLINE(decl) decl __attribute__((always_inline))
- #define R123_CUDA_DEVICE
- #define R123_ASSERT(x)
- #define R123_BUILTIN_EXPECT(expr, likely) expr
- #define R123_USE_GNU_UINT128 0
- #define R123_USE_MULHILO64_ASM 0
- #define R123 USE MULHILO64 MSVC INTRIN 0
- #define R123_USE_MULHILO64_CUDA_INTRIN 0
- #define R123_USE_MULHILO64_OPENCL_INTRIN 1
- #define R123_USE_AES_NI 0
- #define UINT64_C(x) ((ulong)(x##UL))

21.106.1 Macro Definition Documentation

21.106.1.1 R123_ASSERT

```
#define R123_ASSERT(
```

21.106.1.2 R123_BUILTIN_EXPECT

21.106.1.3 R123_CUDA_DEVICE

#define R123_CUDA_DEVICE

21.106.1.4 R123_FORCE_INLINE

21.106.1.5 R123_STATIC_INLINE

#define R123_STATIC_INLINE inline

21.106.1.6 R123_USE_AES_NI

#define R123_USE_AES_NI 0

21.106.1.7 R123_USE_GNU_UINT128

#define R123_USE_GNU_UINT128 0

21.106.1.8 R123_USE_MULHILO64_ASM

#define R123_USE_MULHILO64_ASM 0

21.106.1.9 R123_USE_MULHILO64_CUDA_INTRIN

#define R123_USE_MULHILO64_CUDA_INTRIN 0

21.106.1.10 R123_USE_MULHILO64_MSVC_INTRIN

#define R123_USE_MULHILO64_MSVC_INTRIN 0

21.106.1.11 R123_USE_MULHILO64_OPENCL_INTRIN

#define R123_USE_MULHILO64_OPENCL_INTRIN 1

21.106.1.12 UINT64_C

#define UINT64_C(

```
x ) ((ulong)(x##UL))
```

21.107 optimiser.cc File Reference

```
#include "optimiser.h"
#include <algorithm>
#include <iostream>
#include <map>
#include <cstdlib>
#include <cstdlib>
#include <cuda.h>
#include <cuda_runtime.h>
#include <plog/Log.h>
#include "path.h"
#include "modelSpecInternal.h"
#include "code_generator/codeGenUtils.h"
#include "code_generator/generateAll.h"
#include "utils.h"
```

Namespaces

CodeGenerator

Helper class for generating code - automatically inserts brackets, indents etc.

- CodeGenerator::CUDA
- CodeGenerator::CUDA::Optimiser

Functions

 BACKEND_EXPORT Backend CodeGenerator::CUDA::Optimiser::createBackend (const ModelSpecInternal &model, const filesystem::path &outputPath, plog::Severity backendLevel, plog::IAppender *backend← Appender, const Preferences &preferences)

21.108 optimiser.cc File Reference

```
#include "optimiser.h"
#include "modelSpecInternal.h"
```

Namespaces

CodeGenerator

Helper class for generating code - automatically inserts brackets, indents etc.

- CodeGenerator::SingleThreadedCPU
- CodeGenerator::SingleThreadedCPU::Optimiser

Functions

 BACKEND_EXPORT Backend CodeGenerator::SingleThreadedCPU::Optimiser::createBackend (const ModelSpecInternal &model, const filesystem::path &outputPath, plog::Severity backendLevel, plog::I
 Appender *backendAppender, const Preferences &preferences)

21.109 optimiser.h File Reference

```
#include "backendExport.h"
#include "backend.h"
```

Namespaces

- plog
- CodeGenerator

Helper class for generating code - automatically inserts brackets, indents etc.

- CodeGenerator::CUDA
- · CodeGenerator::CUDA::Optimiser

Functions

 BACKEND_EXPORT Backend CodeGenerator::CUDA::Optimiser::createBackend (const ModelSpecInternal &model, const filesystem::path &outputPath, plog::Severity backendLevel, plog::IAppender *backend← Appender, const Preferences &preferences)

21.110 optimiser.h File Reference

```
#include <plog/Severity.h>
#include "backendExport.h"
#include "backend.h"
```

Namespaces

- plog
- CodeGenerator

Helper class for generating code - automatically inserts brackets, indents etc.

- CodeGenerator::SingleThreadedCPU
- CodeGenerator::SingleThreadedCPU::Optimiser

Functions

 BACKEND_EXPORT Backend CodeGenerator::SingleThreadedCPU::Optimiser::createBackend (const ModelSpecInternal &model, const filesystem::path &outputPath, plog::Severity backendLevel, plog::I
 Appender *backendAppender, const Preferences &preferences)

21.111 philox.h File Reference

Macros

- #define philox2x32(c, k) philox2x32_R(philox2x32_rounds, c, k)
- #define philox4x32(c, k) philox4x32_R(philox4x32_rounds, c, k)

Functions

• _philoxNxW_tpl (2, 1, 32, uint32_t) _philoxNxW_tpl(4

21.111.1 Macro Definition Documentation

21.111.1.1 philox2x32

21.111.1.2 philox4x32

21.111.2 Function Documentation

21.111.2.1 _philoxNxW_tpl()

21.112 philox432.c File Reference

```
#include <clRNG/philox432.h>
#include "private.h"
#include <stdlib.h>
#include "clRNG/private/philox432.c.h"
```

Classes

struct clrngPhilox432StreamCreator_

Macros

• #define BASE CREATOR STATE

Default initial seed of the first stream.

#define BASE_CREATOR_JUMP_DISTANCE {{ 16, 0},{ 0, 0 }}

 $\textit{Jump Struc for } 2^{100} \textit{ steps forward}.$

Functions

- clrngPhilox432StreamCreator * clrngPhilox432CopyStreamCreator (const clrngPhilox432StreamCreator *creator, clrngStatus *err)
- clrngStatus clrngPhilox432DestroyStreamCreator (clrngPhilox432StreamCreator *creator)
- clrngStatus clrngPhilox432RewindStreamCreator (clrngPhilox432StreamCreator *creator)

- clrngStatus clrngPhilox432SetBaseCreatorState (clrngPhilox432StreamCreator *creator, const clrng← Philox432StreamState *baseState)
- clrngStatus clrngPhilox432ChangeStreamsSpacing (clrngPhilox432StreamCreator *creator, cl_int e, cl_int c)
- clrngPhilox432Stream * clrngPhilox432AllocStreams (size_t count, size_t *bufSize, clrngStatus *err)
- clrngStatus clrngPhilox432DestroyStreams (clrngPhilox432Stream *streams)
- clrngStatus clrngPhilox432CreateOverStreams (clrngPhilox432StreamCreator *creator, size_t count, clrng← Philox432Stream *streams)
- clrngPhilox432Stream * clrngPhilox432CreateStreams (clrngPhilox432StreamCreator *creator, size_t count, size_t *bufSize, clrngStatus *err)
- clrngPhilox432Stream * clrngPhilox432CopyStreams (size_t count, const clrngPhilox432Stream *streams, clrngStatus *err)
- clrngPhilox432Stream * clrngPhilox432MakeSubstreams (clrngPhilox432Stream *stream, size_t count, size_t *bufSize, clrngStatus *err)
- clrngStatus clrngPhilox432WriteStreamInfo (const clrngPhilox432Stream *stream, FILE *file)
- void clrngPhilox432AdvanceStream (clrngPhilox432Stream *stream, cl int e, cl int c)
- clrngStatus clrngPhilox432AdvanceStreams (size_t count, clrngPhilox432Stream *streams, cl_int e, cl_int c)
- clrngStatus clrngPhilox432DeviceRandomU01Array_ (size_t streamCount, cl_mem streams, size_t number
 — Count, cl_mem outBuffer, cl_uint numQueuesAndEvents, cl_command_queue *commQueues, cl_uint num
 — WaitEvents, const cl_event *waitEvents, cl_event *outEvents, cl_bool singlePrecision)

21.112.1 Macro Definition Documentation

```
21.112.1.1 BASE_CREATOR_JUMP_DISTANCE
```

```
#define BASE_CREATOR_JUMP_DISTANCE {{ 16, 0},{ 0, 0 }}
```

Jump Struc for 2^{100} steps forward.

21.112.1.2 BASE_CREATOR_STATE

```
#define BASE_CREATOR_STATE
```

Value:

Default initial seed of the first stream.

21.112.2 Function Documentation

21.112.2.1 clrngPhilox432AdvanceStream_()

```
21.112.2.2 clrngPhilox432AdvanceStreams()
```

```
clrngStatus clrngPhilox432AdvanceStreams (
              size_t count,
              clrngPhilox432Stream * streams,
              cl_int e,
              cl_int c)
See also
     clrngAdvanceStreams()
21.112.2.3 clrngPhilox432AllocStreams()
clrngPhilox432Stream* clrngPhilox432AllocStreams (
              size_t count,
              size_t * bufSize,
              clrngStatus * err )
See also
     clrngAllocStreams()
21.112.2.4 clrngPhilox432ChangeStreamsSpacing()
{\tt clrngStatus} \ {\tt clrngPhilox432ChangeStreamsSpacing} \ (
              clrngPhilox432StreamCreator * creator,
              cl_int e,
              cl_int c)
See also
     clrngChangeStreamsSpacing()
21.112.2.5 clrngPhilox432CopyStreamCreator()
{\tt clrngPhilox432StreamCreator*~clrngPhilox432CopyStreamCreator~(}
             const clrngPhilox432StreamCreator * creator,
              clrngStatus * err )
See also
     clrngCopyStreamCreator()
21.112.2.6 clrngPhilox432CopyStreams()
clrngPhilox432Stream* clrngPhilox432CopyStreams (
             size_t count,
              const clrngPhilox432Stream * streams,
              clrngStatus * err )
See also
```

clrngCopyStreams()

21.112.2.7 clrngPhilox432CreateOverStreams()

```
clrngStatus clrngPhilox432CreateOverStreams (
             clrngPhilox432StreamCreator * creator,
             size_t count,
             clrngPhilox432Stream * streams )
See also
     clrngCreateOverStreams()
21.112.2.8 clrngPhilox432CreateStreams()
clrngPhilox432Stream* clrngPhilox432CreateStreams (
             clrngPhilox432StreamCreator * creator,
             size_t count,
             size_t * bufSize,
              clrngStatus * err )
See also
     clrngCreateStreams()
21.112.2.9 clrngPhilox432DestroyStreamCreator()
clrngStatus clrngPhilox432DestroyStreamCreator (
             clrngPhilox432StreamCreator * creator )
See also
     clrngDestroyStreamCreator()
21.112.2.10 clrngPhilox432DestroyStreams()
clrngStatus clrngPhilox432DestroyStreams (
             clrngPhilox432Stream * streams )
See also
     clrngDestroyStreams()
21.112.2.11 clrngPhilox432DeviceRandomU01Array_()
clrngStatus clrngPhilox432DeviceRandomU01Array_ (
             size_t streamCount,
              cl_mem streams,
              size_t numberCount,
              cl_mem outBuffer,
              cl_uint numQueuesAndEvents,
             cl_command_queue * commQueues,
             cl_uint numWaitEvents,
             const cl_event * waitEvents,
              cl_event * outEvents,
```

cl_bool singlePrecision)

21.112.2.12 clrngPhilox432MakeSubstreams()

See also

clrngMakeSubstreams()

21.112.2.13 clrngPhilox432RewindStreamCreator()

See also

clrngRewindStreamCreator()

21.112.2.14 clrngPhilox432SetBaseCreatorState()

See also

clrngSetBaseCreatorState()

21.112.2.15 clrngPhilox432WriteStreamInfo()

See also

clrngWriteStreamInfo()

21.113 philox432.c.h File Reference

```
#include "../Random123/philox.h"
```

Macros

- #define PRIVATE_PHILOX432_CH
- #define Philox432 NORM cl double 1.0 / 0x10000000L
- #define Philox432 NORM cl float 2.32830644e-010f;
- #define IMPLEMENT_GENERATE_FOR_TYPE(fptype)

Functions

- clrngPhilox432Counter clrngPhilox432Add (clrngPhilox432Counter a, clrngPhilox432Counter b)
- clrngPhilox432Counter clrngPhilox432Substract (clrngPhilox432Counter a, clrngPhilox432Counter b)
- clrngStatus clrngPhilox432CopyOverStreams (size_t count, clrngPhilox432Stream *destStreams, const clrngPhilox432Stream *srcStreams)
- void clrngPhilox432GenerateDeck (clrngPhilox432StreamState *currentState)
- clrngStatus clrngPhilox432RewindStreams (size_t count, clrngPhilox432Stream *streams)
- clrngStatus clrngPhilox432RewindSubstreams (size_t count, clrngPhilox432Stream *streams)
- void Philox432ResetNextSubStream (clrngPhilox432Stream *stream)
- clrngStatus clrngPhilox432ForwardToNextSubstreams (size_t count, clrngPhilox432Stream *streams)
- clrngStatus clrngPhilox432MakeOverSubstreams (clrngPhilox432Stream *stream, size_t count, clrng← Philox432Stream *substreams)

21.113.1 Macro Definition Documentation

21.113.1.1 IMPLEMENT_GENERATE_FOR_TYPE

Value:

```
fptype clrngPhilox432RandomU01_##fptype(clrngPhilox432Stream* stream) {
    return (clrngPhilox432NextState(&stream->current) + 0.5) * Philox432_NORM_##fptype; \
cl_int clrngPhilox432RandomInteger_##fptype(clrngPhilox432Stream* stream, cl_int i,
   cl int j) {
   return i + (cl_int)((j - i + 1) * clrngPhilox432RandomU01_##fptype(stream)); \
clrngStatus clrngPhilox432RandomU01Array_##fptype(clrngPhilox432Stream* stream,
  size_t count, fptype* buffer) { \
  if (!stream) \
        return clrngSetErrorString(CLRNG_INVALID_VALUE); \
    if (!buffer) \
        return clrngSetErrorString(CLRNG_INVALID_VALUE); \
    for (size_t i = 0; i < count; i++) \
buffer[i] = clrngPhilox432RandomU01_##fptype(stream); \</pre>
   return CLRNG_SUCCESS; \
} \
clrngStatus clrngPhilox432RandomIntegerArray_##fptype(clrngPhilox432Stream* stream,
   cl_int i, cl_int j, size_t count, cl_int* buffer) { \
    if (!stream) \
        return clrngSetErrorString(CLRNG INVALID VALUE); \
    if (!buffer) \
        return clrngSetErrorString(CLRNG_INVALID_VALUE); \
    for (size_t k = 0; k < count; k++) \
  buffer[k] = clrngPhilox432RandomInteger_##fptype(stream, i, j); \</pre>
    return CLRNG_SUCCESS; \
```

21.113.1.2 Philox432_NORM_cl_double

```
#define Philox432_NORM_cl_double 1.0 / 0x10000000L
```

21.113.1.3 Philox432_NORM_cl_float

```
#define Philox432_NORM_cl_float 2.32830644e-010f;
```

```
21.113.1.4 PRIVATE_PHILOX432_CH
#define PRIVATE_PHILOX432_CH
21.113.2 Function Documentation
21.113.2.1 clrngPhilox432Add()
clrngPhilox432Counter clrngPhilox432Add (
             clrngPhilox432Counter a,
              clrngPhilox432Counter b)
21.113.2.2 clrngPhilox432CopyOverStreams()
clrngStatus clrngPhilox432CopyOverStreams (
             size_t count,
             clrngPhilox432Stream * destStreams,
             const clrngPhilox432Stream * srcStreams )
See also
     clrngCopyOverStreams()
21.113.2.3 clrngPhilox432ForwardToNextSubstreams()
clrngStatus clrngPhilox432ForwardToNextSubstreams (
             size_t count,
             clrngPhilox432Stream * streams )
See also
     clrngForwardToNextSubstreams()
21.113.2.4 clrngPhilox432GenerateDeck()
void clrngPhilox432GenerateDeck (
              clrngPhilox432StreamState * currentState )
21.113.2.5 clrngPhilox432MakeOverSubstreams()
clrngStatus clrngPhilox432MakeOverSubstreams (
             clrngPhilox432Stream * stream,
             size_t count,
              clrngPhilox432Stream * substreams )
See also
     clrngMakeOverSubstreams()
```

21.113.2.6 clrngPhilox432RewindStreams()

See also

clrngRewindStreams()

21.113.2.7 clrngPhilox432RewindSubstreams()

See also

clrngRewindSubstreams()

21.113.2.8 clrngPhilox432Substract()

21.113.2.9 Philox432ResetNextSubStream()

21.114 philox432.c.h File Reference

```
#include "Random123/philox.h"
```

Macros

- #define PRIVATE_PHILOX432_CH
- #define Philox432 NORM cl double 1.0 / 0x10000000L
- #define Philox432_NORM_cl_float 2.32830644e-010f;
- #define IMPLEMENT_GENERATE_FOR_TYPE(fptype)

Functions

- clrngPhilox432Counter clrngPhilox432Add (clrngPhilox432Counter a, clrngPhilox432Counter b)
- clrngPhilox432Counter clrngPhilox432Substract (clrngPhilox432Counter a, clrngPhilox432Counter b)
- clrngStatus clrngPhilox432CopyOverStreams (size_t count, clrngPhilox432Stream *destStreams, const clrngPhilox432Stream *srcStreams)
- void clrngPhilox432GenerateDeck (clrngPhilox432StreamState *currentState)
- clrngStatus clrngPhilox432RewindStreams (size_t count, clrngPhilox432Stream *streams)

- clrngStatus clrngPhilox432RewindSubstreams (size_t count, clrngPhilox432Stream *streams)
- void Philox432ResetNextSubStream (clrngPhilox432Stream *stream)
- clrngStatus clrngPhilox432ForwardToNextSubstreams (size t count, clrngPhilox432Stream *streams)
- clrngStatus clrngPhilox432MakeOverSubstreams (clrngPhilox432Stream *stream, size_t count, clrng← Philox432Stream *substreams)

21.114.1 Macro Definition Documentation

```
21.114.1.1 IMPLEMENT_GENERATE_FOR_TYPE
```

```
\begin{tabular}{ll} \# define & IMPLEMENT\_GENERATE\_FOR\_TYPE ( \\ & fptype \end{tabular} )
```

Value:

```
fptype clrngPhilox432RandomU01_##fptype(clrngPhilox432Stream* stream) {
           return (clrngPhilox432NextState(&stream->current) + 0.5) * Philox432_NORM_##fptype; \
cl_int clrngPhilox432RandomInteger_##fptype(clrngPhilox432Stream* stream, cl_int i,
       cl_int j) { \
         return i + (cl_int)((j - i + 1) * clrngPhilox432RandomU01_##fptype(stream)); \
clrngStatus clrngPhilox432RandomU01Array_##fptype(clrngPhilox432Stream* stream,
     size_t count, fptype* buffer) { \
         if (!stream) \
                     return clrngSetErrorString(CLRNG_INVALID_VALUE, "%s():
        stream cannot be NULL", __func__); \
         if (!buffer) \
                       return clrngSetErrorString(CLRNG_INVALID_VALUE, "%s():
       buffer cannot be NULL", __func__); \
for (size_t i = 0; i < count; i++)</pre>
                     buffer[i] = clrngPhilox432RandomU01_##fptype(stream); \
          return CLRNG_SUCCESS; \
{\tt clrngPhilox432RandomIntegerArray\_\#\#fptype(clrngPhilox432Stream*\ stream, and all of the clrngPhilox432Stream*\ stream, and all of the clrngPhilox432RandomIntegerArray\_\#\#fptype(clrngPhilox432Stream*\ stream, and all of the clrngPhilox432Stream*\ stream, and all of the clrngPhilox432Stream, and all of the clrn
        cl_int i, cl_int j, size_t count, cl_int* buffer) { \
         if (!stream) \
                     return clrngSetErrorString(CLRNG_INVALID_VALUE, "%s():
        stream cannot be NULL", __func__); \
          if (!buffer)
                       return clrngSetErrorString(CLRNG_INVALID_VALUE, "%s():
       buffer cannot be NULL", __func__); \
for (size_t k = 0; k < count; k++) \
   buffer[k] = clrngPhilox432RandomInteger_##fptype(stream, i, j); \</pre>
          return CLRNG_SUCCESS; \
```

21.114.1.2 Philox432_NORM_cl_double

#define Philox432_NORM_cl_double 1.0 / 0x10000000L

21.114.1.3 Philox432_NORM_cl_float

#define Philox432_NORM_cl_float 2.32830644e-010f;

21.114.1.4 PRIVATE_PHILOX432_CH

#define PRIVATE_PHILOX432_CH

21.114.2 Function Documentation

```
21.114.2.1 clrngPhilox432Add()
```

21.114.2.2 clrngPhilox432CopyOverStreams()

See also

clrngCopyOverStreams()

21.114.2.3 clrngPhilox432ForwardToNextSubstreams()

See also

clrngForwardToNextSubstreams()

21.114.2.4 clrngPhilox432GenerateDeck()

21.114.2.5 clrngPhilox432MakeOverSubstreams()

See also

clrngMakeOverSubstreams()

21.114.2.6 clrngPhilox432RewindStreams()

See also

clrngRewindStreams()

21.114.2.7 clrngPhilox432RewindSubstreams()

See also

clrngRewindSubstreams()

21.114.2.8 clrngPhilox432Substract()

21.114.2.9 Philox432ResetNextSubStream()

21.115 philox432.h File Reference

```
#include <clRNG/clRNG.h>
#include <stdio.h>
```

Classes

- struct clrngPhilox432SB_
- · struct clrngPhilox432Counter_
- struct clrngPhilox432StreamState
- struct clrngPhilox432Stream_

Macros

- #define PHILOX432 H
- #define clrngPhilox432RandomU01 _CLRNG_TAG_FPTYPE(clrngPhilox432RandomU01)
- #define clrngPhilox432RandomInteger _CLRNG_TAG_FPTYPE(clrngPhilox432RandomInteger)
- #define clrngPhilox432RandomU01Array _CLRNG_TAG_FPTYPE(clrngPhilox432RandomU01Array)
- #define clrngPhilox432RandomIntegerArray _CLRNG_TAG_FPTYPE(clrngPhilox432RandomIntegerArray)
- #define clrngPhilox432DeviceRandomU01Array(...) clrngPhilox432DeviceRandomU01Array_(__VA_ARG

 S__, CL_FALSE)

Typedefs

- typedef struct clrngPhilox432SB clrngPhilox432SB
- typedef struct clrngPhilox432Counter_ clrngPhilox432Counter
- typedef struct clrngPhilox432Stream_ clrngPhilox432Stream
- typedef struct clrngPhilox432StreamCreator_clrngPhilox432StreamCreator

Functions

- CLRNGAPI clrngPhilox432StreamCreator * clrngPhilox432CopyStreamCreator (const clrngPhilox432← StreamCreator *creator, clrngStatus *err)
- CLRNGAPI clrngStatus clrngPhilox432DestroyStreamCreator (clrngPhilox432StreamCreator *creator)
- CLRNGAPI clrngStatus clrngPhilox432RewindStreamCreator (clrngPhilox432StreamCreator *creator)
- CLRNGAPI clrngStatus clrngPhilox432SetBaseCreatorState (clrngPhilox432StreamCreator *creator, const clrngPhilox432StreamState *baseState)
- CLRNGAPI clrngStatus clrngPhilox432ChangeStreamsSpacing (clrngPhilox432StreamCreator *creator, cl
 int e, cl int c)
- CLRNGAPI clrngPhilox432Stream * clrngPhilox432AllocStreams (size_t count, size_t *bufSize, clrngStatus *err)
- CLRNGAPI clrngStatus clrngPhilox432DestroyStreams (clrngPhilox432Stream *streams)
- CLRNGAPI clrngStatus clrngPhilox432CreateOverStreams (clrngPhilox432StreamCreator *creator, size_
 t count, clrngPhilox432Stream *streams)
- CLRNGAPI clrngPhilox432Stream * clrngPhilox432CreateStreams (clrngPhilox432StreamCreator *creator, size t count, size t *bufSize, clrngStatus *err)
- CLRNGAPI clrngStatus clrngPhilox432CopyOverStreams (size_t count, clrngPhilox432Stream *dest←
 Streams, const clrngPhilox432Stream *srcStreams)
- CLRNGAPI clrngPhilox432Stream * clrngPhilox432CopyStreams (size_t count, const clrngPhilox432Stream *streams, clrngStatus *err)
- CLRNGAPI _CLRNG_FPTYPE clrngPhilox432RandomU01 (clrngPhilox432Stream *stream)
- CLRNGAPI cl_float clrngPhilox432RandomU01_cl_float (clrngPhilox432Stream *stream)
- CLRNGAPI cl_double clrngPhilox432RandomU01_cl_double (clrngPhilox432Stream *stream)
- CLRNGAPI cl_int clrngPhilox432RandomInteger (clrngPhilox432Stream *stream, cl_int i, cl_int j)
- CLRNGAPI cl_int clrngPhilox432RandomInteger_cl_float (clrngPhilox432Stream *stream, cl_int i, cl_int j)
- CLRNGAPI cl_int clrngPhilox432RandomInteger_cl_double (clrngPhilox432Stream *stream, cl_int i, cl_int j)
- CLRNGAPI clrngStatus clrngPhilox432RandomU01Array_cl_float (clrngPhilox432Stream *stream, size_
 t count, cl_float *buffer)
- CLRNGAPI clrngStatus clrngPhilox432RandomU01Array_cl_double (clrngPhilox432Stream *stream, size_t count, cl_double *buffer)
- CLRNGAPI clrngStatus clrngPhilox432RandomIntegerArray (clrngPhilox432Stream *stream, cl_int i, cl_int j, size_t count, cl_int *buffer)
- CLRNGAPI clrngStatus clrngPhilox432RandomIntegerArray_cl_float (clrngPhilox432Stream *stream, cl_int i, cl int j, size t count, cl int *buffer)
- CLRNGAPI clrngStatus clrngPhilox432RandomIntegerArray_cl_double (clrngPhilox432Stream *stream, cl int i, cl_int j, size_t count, cl_int *buffer)
- CLRNGAPI clrngStatus clrngPhilox432RewindStreams (size_t count, clrngPhilox432Stream *streams)
- CLRNGAPI clrngStatus clrngPhilox432RewindSubstreams (size_t count, clrngPhilox432Stream *streams)
- CLRNGAPI clrngStatus clrngPhilox432ForwardToNextSubstreams (size_t count, clrngPhilox432Stream *streams)
- CLRNGAPI clrngPhilox432Stream * clrngPhilox432MakeSubstreams (clrngPhilox432Stream *stream, size ← t count, size t *bufSize, clrngStatus *err)
- CLRNGAPI clrngStatus clrngPhilox432MakeOverSubstreams (clrngPhilox432Stream *stream, size_t count, clrngPhilox432Stream *substreams)
- CLRNGAPI clrngStatus clrngPhilox432AdvanceStreams (size_t count, clrngPhilox432Stream *streams, cl
 __int e, cl_int c)

- CLRNGAPI clrngStatus clrngPhilox432DeviceRandomU01Array_ (size_t streamCount, cl_mem streams, size_t numberCount, cl_mem outBuffer, cl_uint numQueuesAndEvents, cl_command_queue *commQueues, cl_uint numWaitEvents, const cl_event *waitEvents, cl_event *outEvents, cl_bool singlePrecision)
- CLRNGAPI clrngStatus clrngPhilox432WriteStreamInfo (const clrngPhilox432Stream *stream, FILE *file)

21.115.1 Macro Definition Documentation

```
21.115.1.1 clrngPhilox432DeviceRandomU01Array
#define clrngPhilox432DeviceRandomU01Array(
               ... ) clrngPhilox432DeviceRandomU01Array_(__VA_ARGS___, CL_FALSE)
See also
     clrngDeviceRandomU01Array()
21.115.1.2 clrngPhilox432RandomInteger
#define clrngPhilox432RandomInteger _CLRNG_TAG_FPTYPE(clrngPhilox432RandomInteger)
21.115.1.3 clrngPhilox432RandomIntegerArray
#define clrngPhilox432RandomIntegerArray _CLRNG_TAG_FPTYPE(clrngPhilox432RandomIntegerArray)
21.115.1.4 clrngPhilox432RandomU01
#define clrngPhilox432RandomU01 _CLRNG_TAG_FPTYPE(clrngPhilox432RandomU01)
21.115.1.5 clrngPhilox432RandomU01Array
#define clrngPhilox432RandomU01Array _CLRNG_TAG_FPTYPE(clrngPhilox432RandomU01Array)
21.115.1.6 PHILOX432_H
#define PHILOX432_H
21.115.2 Typedef Documentation
21.115.2.1 clrngPhilox432Counter
typedef struct clrngPhilox432Counter_ clrngPhilox432Counter
21.115.2.2 clrngPhilox432SB
typedef struct clrngPhilox432SB_ clrngPhilox432SB
```

See also

clrngChangeStreamsSpacing()

```
21.115.2.3 clrngPhilox432Stream
typedef struct clrngPhilox432Stream_ clrngPhilox432Stream
See also
     clrngStream
21.115.2.4 clrngPhilox432StreamCreator
typedef struct clrngPhilox432StreamCreator_ clrngPhilox432StreamCreator
See also
     clrngStreamCreator
21.115.3 Function Documentation
21.115.3.1 clrngPhilox432AdvanceStreams()
CLRNGAPI clrngStatus clrngPhilox432AdvanceStreams (
             size_t count,
             clrngPhilox432Stream * streams,
             cl_int e,
             cl_int c)
See also
     clrngAdvanceStreams()
21.115.3.2 clrngPhilox432AllocStreams()
CLRNGAPI clrngPhilox432Stream* clrngPhilox432AllocStreams (
             size_t count,
             size_t * bufSize,
             clrngStatus * err )
See also
     clrngAllocStreams()
21.115.3.3 clrngPhilox432ChangeStreamsSpacing()
CLRNGAPI clrngStatus clrngPhilox432ChangeStreamsSpacing (
             clrngPhilox432StreamCreator * creator,
             cl_int e,
             cl_int c
```

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```
21.115.3.4 clrngPhilox432CopyOverStreams()
```

```
CLRNGAPI clrngStatus clrngPhilox432CopyOverStreams (
             size_t count,
             clrngPhilox432Stream * destStreams,
             const clrngPhilox432Stream * srcStreams )
See also
     clrngCopyOverStreams()
21.115.3.5 clrngPhilox432CopyStreamCreator()
{\tt CLRNGAPI\ clrngPhilox432StreamCreator*\ clrngPhilox432CopyStreamCreator\ (}
             const clrngPhilox432StreamCreator * creator,
             clrngStatus * err )
See also
     clrngCopyStreamCreator()
21.115.3.6 clrngPhilox432CopyStreams()
CLRNGAPI clrngPhilox432Stream* clrngPhilox432CopyStreams (
             size_t count,
              const clrngPhilox432Stream * streams,
              clrngStatus * err )
See also
     clrngCopyStreams()
21.115.3.7 clrngPhilox432CreateOverStreams()
CLRNGAPI clrngStatus clrngPhilox432CreateOverStreams (
             clrngPhilox432StreamCreator * creator,
             size_t count,
              clrngPhilox432Stream * streams)
See also
     clrngCreateOverStreams()
21.115.3.8 clrngPhilox432CreateStreams()
{\tt CLRNGAPI\ clrngPhilox432Stream*\ clrngPhilox432CreateStreams\ (}
             clrngPhilox432StreamCreator * creator,
             size_t count,
             size_t * bufSize,
              clrngStatus * err )
```

clrngCreateStreams()

See also

```
21.115.3.9 clrngPhilox432DestroyStreamCreator()
CLRNGAPI clrngStatus clrngPhilox432DestroyStreamCreator (
              clrngPhilox432StreamCreator * creator )
See also
     clrngDestroyStreamCreator()
21.115.3.10 clrngPhilox432DestroyStreams()
CLRNGAPI clrngStatus clrngPhilox432DestroyStreams (
             clrngPhilox432Stream * streams )
See also
     clrngDestroyStreams()
21.115.3.11 clrngPhilox432DeviceRandomU01Array_()
CLRNGAPI clrngStatus clrngPhilox432DeviceRandomU01Array_ (
             size_t streamCount,
             cl_mem streams,
             size_t numberCount,
             cl_mem outBuffer,
             cl_uint numQueuesAndEvents,
              cl_command_queue * commQueues,
              cl_uint numWaitEvents,
              const cl_event * waitEvents,
              cl_event * outEvents,
              cl_bool singlePrecision )
21.115.3.12 clrngPhilox432ForwardToNextSubstreams()
{\tt CLRNGAPI\ clrngStatus\ clrngPhilox432ForwardToNextSubstreams\ (}
             size_t count,
             clrngPhilox432Stream * streams )
See also
     clrngForwardToNextSubstreams()
21.115.3.13 clrngPhilox432MakeOverSubstreams()
CLRNGAPI clrngStatus clrngPhilox432MakeOverSubstreams (
             clrngPhilox432Stream * stream,
              size_t count,
              clrngPhilox432Stream * substreams )
See also
```

clrngMakeOverSubstreams()

```
21.115.3.14 clrngPhilox432MakeSubstreams()
CLRNGAPI clrngPhilox432Stream* clrngPhilox432MakeSubstreams (
             clrngPhilox432Stream * stream,
             size_t count,
             size_t * bufSize,
             clrngStatus * err )
See also
     clrngMakeSubstreams()
21.115.3.15 clrngPhilox432RandomInteger()
CLRNGAPI cl_int clrngPhilox432RandomInteger (
             clrngPhilox432Stream * stream,
             cl_int i,
             cl_int j )
See also
     clrngRandomInteger()
21.115.3.16 clrngPhilox432RandomInteger_cl_double()
CLRNGAPI cl_int clrngPhilox432RandomInteger_cl_double (
             clrngPhilox432Stream * stream,
             cl_int i,
             cl_int j)
21.115.3.17 clrngPhilox432RandomInteger_cl_float()
CLRNGAPI cl_int clrngPhilox432RandomInteger_cl_float (
             clrngPhilox432Stream * stream,
             cl_int i,
             cl_int j)
21.115.3.18 clrngPhilox432RandomIntegerArray()
CLRNGAPI clrngStatus clrngPhilox432RandomIntegerArray (
             clrngPhilox432Stream * stream,
             cl_int i,
             cl_int j,
              size_t count,
             cl_int * buffer )
See also
     clrngRandomIntegerArray()
```

21.115.3.19 clrngPhilox432RandomIntegerArray_cl_double()

```
{\tt CLRNGAPI\ clrngStatus\ clrngPhilox432RandomIntegerArray\_cl\_double\ (}
              clrngPhilox432Stream * stream,
```

```
cl_int i,
              cl_int j,
              size_t count,
              cl_int * buffer)
21.115.3.20 clrngPhilox432RandomIntegerArray_cl_float()
CLRNGAPI clrngStatus clrngPhilox432RandomIntegerArray_cl_float (
              clrngPhilox432Stream * stream,
              cl_int i,
              cl_int j,
              size_t count,
              cl_int * buffer )
21.115.3.21 clrngPhilox432RandomU01()
CLRNGAPI _CLRNG_FPTYPE clrngPhilox432RandomU01 (
              clrngPhilox432Stream * stream )
See also
     clrngRandomU01()
21.115.3.22 clrngPhilox432RandomU01_cl_double()
CLRNGAPI cl_double clrngPhilox432RandomU01_cl_double (
              clrngPhilox432Stream * stream )
21.115.3.23 clrngPhilox432RandomU01_cl_float()
{\tt CLRNGAPI} \ {\tt cl\_float} \ {\tt clrngPhilox432RandomU01\_cl\_float} \ \ (
              {\tt clrngPhilox432Stream} \ * \ stream \ )
21.115.3.24 clrngPhilox432RandomU01Array()
CLRNGAPI clrngStatus clrngPhilox432RandomU01Array (
              clrngPhilox432Stream * stream,
              size_t count,
              _CLRNG_FPTYPE * buffer )
See also
     clrngRandomU01Array()
21.115.3.25 clrngPhilox432RandomU01Array_cl_double()
CLRNGAPI clrngStatus clrngPhilox432RandomU01Array_cl_double (
              clrngPhilox432Stream * stream,
              size_t count,
              cl_double * buffer )
```

```
21.115.3.26 clrngPhilox432RandomU01Array_cl_float()
CLRNGAPI clrngStatus clrngPhilox432RandomU01Array_cl_float (
             clrngPhilox432Stream * stream,
             size_t count,
             cl_float * buffer )
21.115.3.27 clrngPhilox432RewindStreamCreator()
CLRNGAPI clrngStatus clrngPhilox432RewindStreamCreator (
             clrngPhilox432StreamCreator * creator )
See also
     clrngRewindStreamCreator()
21.115.3.28 clrngPhilox432RewindStreams()
CLRNGAPI clrngStatus clrngPhilox432RewindStreams (
             size_t count,
             clrngPhilox432Stream * streams )
See also
     clrngRewindStreams()
21.115.3.29 clrngPhilox432RewindSubstreams()
CLRNGAPI clrngStatus clrngPhilox432RewindSubstreams (
             size_t count,
             clrngPhilox432Stream * streams )
See also
     clrngRewindSubstreams()
21.115.3.30 clrngPhilox432SetBaseCreatorState()
CLRNGAPI clrngStatus clrngPhilox432SetBaseCreatorState (
             clrngPhilox432StreamCreator * creator,
             const clrngPhilox432StreamState * baseState )
See also
     clrngSetBaseCreatorState()
21.115.3.31 clrngPhilox432WriteStreamInfo()
CLRNGAPI clrngStatus clrngPhilox432WriteStreamInfo (
             const clrngPhilox432Stream * stream,
             FILE * file )
```

See also

clrngWriteStreamInfo()

21.116 postsynapticModels.cc File Reference

```
#include "postsynapticModels.h"
```

Functions

- IMPLEMENT_MODEL (PostsynapticModels::ExpCurr)
- IMPLEMENT_MODEL (PostsynapticModels::ExpCond)
- IMPLEMENT_MODEL (PostsynapticModels::DeltaCurr)

21.116.1 Function Documentation

21.117 postsynapticModels.h File Reference

```
#include <cmath>
#include "models.h"
```

Classes

• class PostsynapticModels::Base

Base class for all postsynaptic models.

class PostsynapticModels::ExpCurr

Exponential decay with synaptic input treated as a current value.

• class PostsynapticModels::ExpCond

Exponential decay with synaptic input treated as a conductance value.

class PostsynapticModels::DeltaCurr

Simple delta current synapse.

Namespaces

PostsynapticModels

Macros

- #define SET_DECAY_CODE(DECAY_CODE) virtual std::string getDecayCode() const override{ return DE
 CAY_CODE; }
- #define SET_CURRENT_CONVERTER_CODE(CURRENT_CONVERTER_CODE) virtual std::string get
 ApplyInputCode() const override{ return "\$(Isyn) += " CURRENT_CONVERTER_CODE ";"; }
- #define SET_APPLY_INPUT_CODE(APPLY_INPUT_CODE) virtual std::string getApplyInputCode() const override{ return APPLY_INPUT_CODE; }
- #define SET_SUPPORT_CODE(SUPPORT_CODE) virtual std::string getSupportCode() const override{ return SUPPORT_CODE;}

21.117.1 Macro Definition Documentation

21.117.1.1 SET_APPLY_INPUT_CODE

21.117.1.2 SET_CURRENT_CONVERTER_CODE

21.117.1.3 SET_DECAY_CODE

```
#define SET_DECAY_CODE ( {\it DECAY\_CODE}~)~ {\it virtual std::string getDecayCode()}~ {\it const override} \{~ {\it return DECAY\_CO} \leftarrow {\it DE;}~ \}
```

21.117.1.4 SET_SUPPORT_CODE

```
\label{thm:code} \verb| $\tt SUPPORT\_CODE($ $\tt SUPPORT\_CODE($) $\tt virtual std::string getSupportCode() const override{ return SUPP$$$$$$$$$$$ORT\_CODE; }
```

21.118 presynapticUpdateStrategy.cc File Reference

```
#include "presynapticUpdateStrategy.h"
#include <numeric>
#include <cuda_runtime.h>
#include "gennUtils.h"
#include "code_generator/codeGenUtils.h"
#include "code_generator/codeStream.h"
#include "code_generator/groupMerged.h"
```

```
#include "code_generator/modelSpecMerged.h"
#include "code_generator/substitutions.h"
#include "backend.h"
#include "utils.h"
```

Namespaces

CodeGenerator

Helper class for generating code - automatically inserts brackets, indents etc.

- CodeGenerator::CUDA
- · CodeGenerator::CUDA::PresynapticUpdateStrategy

21.119 presynapticUpdateStrategy.h File Reference

```
#include "code_generator/backendBase.h"
```

Classes

- class CodeGenerator::CUDA::PresynapticUpdateStrategy::Base
- class CodeGenerator::CUDA::PresynapticUpdateStrategy::PreSpan

Presynaptic parallelism.

• class CodeGenerator::CUDA::PresynapticUpdateStrategy::PostSpan

Postsynaptic parallelism.

• class CodeGenerator::CUDA::PresynapticUpdateStrategy::PostSpanBitmask

Postsynaptic parallelism.

• class CodeGenerator::CUDA::PresynapticUpdateStrategy::PreSpanProcedural

Presynaptic parallelism with procedural connectivity.

Namespaces

CodeGenerator

Helper class for generating code - automatically inserts brackets, indents etc.

- CodeGenerator::CUDA
- · CodeGenerator::CUDA::PresynapticUpdateStrategy

21.120 private.c File Reference

```
#include <clRNG/clRNG.h>
#include "private.h"
#include <stdio.h>
#include <stdarg.h>
#include <stdlib.h>
```

Macros

- #define CASE ERR (code, msg) case code: base = msg; break
- #define CASE_ERR(code) CASE_ERR_(CLRNG_ ## code, MSG_ ## code)

Functions

clrngStatus clrngSetErrorString (cl_int err, const char *msg,...)
 Set the current error string.

Variables

• char errorString [1024] = ""

21.120.1 Macro Definition Documentation

21.120.1.1 CASE_ERR

21.120.1.2 CASE_ERR_

21.120.2 Function Documentation

21.120.2.1 clrngSetErrorString()

Set the current error string.

The error string will be constructed based on the error code err and on the optional message msg.

Parameters

i	n	err	Error code.
i	.n	msg	Additional error message (format string). Can be NULL.
i	.n		Additional arguments for the format string.

Returns

The value of err (for convenience).

21.120.3 Variable Documentation

21.120.3.1 errorString

```
char errorString[1024] = ""
```

21.121 private.h File Reference

Macros

• #define PRIVATE H

Functions

• clrngStatus clrngSetErrorString (cl_int err, const char *msg,...)

Set the current error string.

21.121.1 Macro Definition Documentation

21.121.1.1 PRIVATE_H

```
#define PRIVATE_H
```

21.121.2 Function Documentation

21.121.2.1 clrngSetErrorString()

Set the current error string.

The error string will be constructed based on the error code ${\tt err}$ and on the optional message ${\tt msg}$.

Parameters

in	err	Error code.
in	msg	Additional error message (format string). Can be NULL.
in		Additional arguments for the format string.

Returns

The value of err (for convenience).

21.122 sharedLibraryModel.h File Reference

```
#include <algorithm>
#include <array>
#include <iostream>
#include <string>
#include <vector>
```

```
#include <utility>
#include <unordered_map>
#include <bitset>
#include <dlfcn.h>
#include "spikeRecorder.h"
```

class SharedLibraryModel< scalar >

21.123 simulator.cc File Reference

```
#include "simulator.h"
#include <functional>
#include <iostream>
#include <map>
#include <memory>
#include <random>
#include <set>
#include <string>
#include <cassert>
#include <cmath>
#include <cstdlib>
#include <dlfcn.h>
#include "path.h"
#include "pugixml/pugixml.hpp"
#include <plog/Log.h>
#include <plog/Appenders/ConsoleAppender.h>
#include "spineMLLogging.h"
#include "spineMLUtils.h"
#include "connectors.h"
#include "input.h"
#include "inputValue.h"
#include "logOutput.h"
#include "modelProperty.h"
#include "stateVar.h"
#include "timer.h"
```

Namespaces

SpineMLSimulator

21.124 simulator.h File Reference

```
#include <map>
#include <set>
#include <string>
#include <tuple>
#include <plog/Severity.h>
#include <plog/Appenders/ConsoleAppender.h>
#include "input.h"
#include "inputValue.h"
#include "logOutput.h"
```

```
#include "modelProperty.h"
```

· class SpineMLSimulator::Simulator

Namespaces

SpineMLSimulator

21.125 snippet.h File Reference

```
#include <algorithm>
#include <functional>
#include <string>
#include <vector>
#include <cassert>
#include "gennExport.h"
#include "gennUtils.h"
```

Classes

- class Snippet::ValueBase< NumVars >
- class Snippet::ValueBase< 0 >
- class Snippet::Base

Base class for all code snippets.

struct Snippet::Base::EGP

An extra global parameter has a name and a type.

• struct Snippet::Base::ParamVal

Additional input variables, row state variables and other things have a name, a type and an initial value.

struct Snippet::Base::DerivedParam

A derived parameter has a name and a function for obtaining its value.

class Snippet::Init< SnippetBase >

Namespaces

Snippet

Macros

- #define DECLARE_SNIPPET(TYPE, NUM_PARAMS)
- #define IMPLEMENT_SNIPPET(TYPE) TYPE *TYPE::s_Instance = NULL
- #define SET_PARAM_NAMES(...) virtual StringVec getParamNames() const override{ return __VA_ARGS
 __;}
- #define SET_DERIVED_PARAMS(...) virtual DerivedParamVec getDerivedParams() const override{ return __VA_ARGS__;}
- #define SET_EXTRA_GLOBAL_PARAMS(...) virtual EGPVec getExtraGlobalParams() const override{ return __VA_ARGS__; }

21.125.1 Macro Definition Documentation

```
21.125.1.1 DECLARE_SNIPPET
#define DECLARE_SNIPPET(
             TYPE,
             NUM_PARAMS )
Value:
private:
   GENN_EXPORT static TYPE *s_Instance;
public:
   static const TYPE *getInstance()
       if(s_Instance == NULL)
          s_Instance = new TYPE;
       return s_Instance;
   typedef Snippet::ValueBase<NUM_PARAMS> ParamValues
21.125.1.2 IMPLEMENT_SNIPPET
#define IMPLEMENT_SNIPPET(
             TYPE ) TYPE *TYPE::s_Instance = NULL
21.125.1.3 SET_DERIVED_PARAMS
#define SET_DERIVED_PARAMS(
            ... ) virtual DerivedParamVec getDerivedParams() const override{ return __VA_A↔
RGS__; }
21.125.1.4 SET_EXTRA_GLOBAL_PARAMS
#define SET_EXTRA_GLOBAL_PARAMS(
             ... ) virtual EGPVec getExtraGlobalParams() const override{ return __VA_ARGS__;
21.125.1.5 SET_PARAM_NAMES
#define SET_PARAM_NAMES(
              ... ) virtual StringVec getParamNames() const override{ return __VA_ARGS__; }
21.126 spikeRecorder.h File Reference
#include <algorithm>
#include <fstream>
#include <iterator>
#include <list>
#include <tuple>
```

#include <vector>

class SpikeWriterText

Class to write spikes to text file.

· class SpikeWriterTextCached

Class to write spikes to text file, caching in memory before writing.

class SpikeRecorder
 Writer >

Class to read spikes from neuron groups.

21.127 sse.h File Reference

Functions

• R123_STATIC_INLINE int haveAESNI ()

21.127.1 Function Documentation

21.127.1.1 haveAESNI()

```
R123_STATIC_INLINE int haveAESNI ( )
```

21.128 stateVar.h File Reference

```
#include <functional>
#include "spineMLLogging.h"
```

Classes

class SpineMLSimulator::StateVar< T >

Namespaces

SpineMLSimulator

21.129 substitutions.cc File Reference

```
#include "code_generator/substitutions.h"
#include "code_generator/codeGenUtils.h"
```

21.130 substitutions.h File Reference

```
#include <map>
#include <stdexcept>
#include <string>
#include <cassert>
#include "gennExport.h"
#include "gennUtils.h"
#include "logging.h"
```

- · class CodeGenerator::Substitutions
- struct CodeGenerator::Substitutions::FunctionTemplate

Namespaces

CodeGenerator

Helper class for generating code - automatically inserts brackets, indents etc.

21.131 supportCodeMerged.h File Reference

```
#include <unordered_map>
#include "code_generator/codeGenUtils.h"
#include "code_generator/codeStream.h"
```

Classes

• class CodeGenerator::SupportCodeMerged

Namespaces

CodeGenerator

Helper class for generating code - automatically inserts brackets, indents etc.

21.132 synapseGroup.cc File Reference

```
#include "synapseGroup.h"
#include <algorithm>
#include <cmath>
#include <iostream>
#include "gennUtils.h"
#include "neuronGroupInternal.h"
#include "synapseGroupInternal.h"
```

21.133 synapseGroup.h File Reference

```
#include <map>
#include <set>
#include <string>
#include <vector>
#include "gennExport.h"
#include "initSparseConnectivitySnippet.h"
#include "postsynapticModels.h"
#include "weightUpdateModels.h"
#include "synapseMatrixType.h"
#include "variableMode.h"
```

· class SynapseGroup

21.134 synapseGroupInternal.h File Reference

```
#include "synapseGroup.h"
```

Classes

· class SynapseGroupInternal

21.135 synapseMatrixType.h File Reference

Enumerations

- enum SynapseMatrixConnectivity:: unsigned int { SynapseMatrixConnectivity::DENSE = (1 << 0), SynapseMatrixConnectivity::BITMASK = (1 << 1), SynapseMatrixConnectivity::SPARSE = (1 << 2), SynapseMatrixConnectivity::PROCEDURAL = (1 << 3) }
 - < Flags defining differnet types of synaptic matrix connectivity
- enum SynapseMatrixWeight::unsigned int { SynapseMatrixWeight::GLOBAL = (1 << 5), SynapseMatrix
 Weight::INDIVIDUAL = (1 << 6), SynapseMatrixWeight::PROCEDURAL = (1 << 7), SynapseMatrix
 Weight::INDIVIDUAL PSM = (1 << 8) }
- enum SynapseMatrixType::unsigned int {
 SynapseMatrixType::DENSE_GLOBALG = static_cast<unsigned int>(SynapseMatrixConnectivity::DE↔
 NSE) | static_cast<unsigned int>(SynapseMatrixWeight::GLOBAL), SynapseMatrixType::DENSE_GL↔
 OBALG_INDIVIDUAL_PSM = static_cast<unsigned int>(SynapseMatrixConnectivity::DENSE) | static_⇔
 cast<unsigned int>(SynapseMatrixWeight::GLOBAL) | static_cast<unsigned int>(SynapseMatrixWeight↔
 ::INDIVIDUAL_PSM), SynapseMatrixType::DENSE_INDIVIDUALG = static_cast<unsigned int>(Synapse↔
 MatrixConnectivity::DENSE) | static_cast<unsigned int>(SynapseMatrixType::DENSE_PRO↔
 _cast<unsigned int>(SynapseMatrixType::DENSE_PRO↔
 CEDURALG = static_cast<unsigned int>(SynapseMatrixConnectivity::DENSE) | static_cast<unsigned int>(SynapseMatrixWeight::INDIVID↔
 UAL_PSM),

 $\label{thm:synapseMatrixType::BITMASK_GLOBALG} SynapseMatrixType::BITMASK_GLOBALG = static_cast < unsigned int > (SynapseMatrixType::BITMA > SK_GLOBALG_INDIVIDUAL_PSM = static_cast < unsigned int > (SynapseMatrixType::BITMASK) | static_cast < unsigned int > (SynapseMatrixWeight::GLOBAL) | static_cast < unsigned int > (SynapseMatrixWeight::INDIVIDUAL_PSM), SynapseMatrixType::SPARSE_GLOBALG = static_cast < unsigned int > (SynapseMatrixConnectivity::SPARSE) | static_cast < unsigned int > (SynapseMatrixWeight::GLOBAL), SynapseMatrixType::SPARSE_GLOBALG_INDIVIDUAL_PSM = static_cast < unsigned int > (SynapseMatrixType::SPARSE) | static_cast < unsigned int > (SynapseMatrixWeight::GLOBAL) | static_cas$

SynapseMatrixType::SPARSE_INDIVIDUALG = static_cast<unsigned int>(SynapseMatrixConnectivity ::SPARSE) | static_cast<unsigned int>(SynapseMatrixWeight::INDIVIDUAL) | static_cast<unsigned int>(SynapseMatrixWeight::INDIVIDUAL_PSM), SynapseMatrixType::PROCEDURAL_GLOBALG = static - cast<unsigned int>(SynapseMatrixConnectivity::PROCEDURAL) | static_cast<unsigned int>(Synapse - MatrixWeight::GLOBAL), SynapseMatrixType::PROCEDURAL_GLOBALG_INDIVIDUAL_PSM = static_cast<unsigned int>(Synapse - MatrixWeight::GLOBAL) | static_cast<unsigned int>(Synapse - MatrixWeight::INDIVIDUAL_PSM), Synapse - MatrixType::PROCEDURAL_PROCEDURALG = static_cast<unsigned int>(SynapseMatrixConnectivity - ::PROCEDURAL) | static_cast<unsigned int>(SynapseMatrixWeight::PROCEDURAL) | static_c

Functions

- bool operator& (SynapseMatrixType type, SynapseMatrixConnectivity connType)
- bool operator& (SynapseMatrixType type, SynapseMatrixWeight weightType)
- SynapseMatrixType operator| (SynapseMatrixWeight weightType, SynapseMatrixConnectivity connType)
- SynapseMatrixConnectivity getSynapseMatrixConnectivity (SynapseMatrixType type)
- SynapseMatrixWeight getSynapseMatrixWeight (SynapseMatrixType type)

21.135.1 Enumeration Type Documentation

21.135.1.1 SynapseMatrixConnectivity

```
enum SynapseMatrixConnectivity : unsigned int [strong]
```

< Flags defining differnet types of synaptic matrix connectivity

Enumerator

DENSE	
BITMASK	
SPARSE	
PROCEDURAL	

21.135.1.2 SynapseMatrixType

```
enum SynapseMatrixType : unsigned int [strong]
```

Enumerator

21.135.1.3 SynapseMatrixWeight

```
enum SynapseMatrixWeight : unsigned int [strong]
```

Enumerator

GLOBAL	
INDIVIDUAL	

Enumerator

```
PROCEDURAL INDIVIDUAL_PSM
```

21.135.2 Function Documentation

```
21.135.2.1 getSynapseMatrixConnectivity()
```

21.135.2.2 getSynapseMatrixWeight()

21.135.2.3 operator&() [1/2]

21.135.2.4 operator&() [2/2]

21.135.2.5 operator " | ()

21.136 teeStream.h File Reference

```
#include <ostream>
#include <streambuf>
#include <vector>
```

Classes

- · class CodeGenerator::TeeBuf
- class CodeGenerator::TeeStream

Namespaces

CodeGenerator

Helper class for generating code - automatically inserts brackets, indents etc.

21.137 timer.h File Reference

```
#include <chrono>
#include <iostream>
#include <string>
#include "spineMLLogging.h"
```

Classes

- · class SpineMLSimulator::Timer
- · class SpineMLSimulator::TimerAccumulate

Namespaces

SpineMLSimulator

21.138 timer.h File Reference

```
#include <chrono>
#include <fstream>
#include <iostream>
#include <string>
```

Classes

· class Timer

A generic timer which can give the current elapsed time.

• class TimerAccumulate

A timer which adds its elapsed time to an accumulator variable on destruction.

21.139 utils.h File Reference

```
#include <iostream>
#include "logging.h"
```

Macros

- #define CHECK_CU_ERRORS(call) call
- #define CHECK_CUDA_ERRORS(call)

21.139.1 Macro Definition Documentation

21.139.1.1 CHECK_CU_ERRORS

```
#define CHECK_CU_ERRORS( call ) call
```

21.139.1.2 CHECK_CUDA_ERRORS

```
\begin{tabular}{ll} \# define \ CHECK\_CUDA\_ERRORS ( \\ call \ ) \end{tabular}
```

Value:

```
{
    cudaError_t error = call;
    if (error != cudaSuccess) {
        LOGE_BACKEND << __FILE__ << ": " << __LINE__ << ": cuda runtime error " << error << ": " << cudaGetErrorString(error); \
        exit(EXIT_FAILURE);
    }
}</pre>
```

21.140 variableMode.h File Reference

```
#include <cstdint>
```

Enumerations

```
    enum VarLocation : uint8_t {
        VarLocation::HOST = (1 << 0), VarLocation::DEVICE = (1 << 1), VarLocation::ZERO_COPY = (1 << 2),
        VarLocation::HOST_DEVICE = HOST | DEVICE,
        VarLocation::HOST_DEVICE_ZERO_COPY = HOST | DEVICE | ZERO_COPY }</li>
```

< Flags defining which memory space variables should be allocated in

Functions

• bool operator& (VarLocation locA, VarLocation locB)

21.140.1 Enumeration Type Documentation

21.140.1.1 VarLocation

```
enum VarLocation : uint8_t [strong]
```

< Flags defining which memory space variables should be allocated in

Enumerator

HOST	
DEVICE	
ZERO_COPY	
HOST_DEVICE	
HOST_DEVICE_ZERO_COPY	

21.140.2 Function Documentation

21.140.2.1 operator&()

21.141 weightUpdateModels.cc File Reference

```
#include "weightUpdateModels.h"
```

Functions

- IMPLEMENT_MODEL (WeightUpdateModels::StaticPulse)
- IMPLEMENT_MODEL (WeightUpdateModels::StaticPulseDendriticDelay)
- IMPLEMENT_MODEL (WeightUpdateModels::StaticGraded)
- IMPLEMENT_MODEL (WeightUpdateModels::PiecewiseSTDP)

21.141.1 Function Documentation

21.142 weightUpdateModels.h File Reference

```
#include "models.h"
```

class WeightUpdateModels::Base

Base class for all weight update models.

class WeightUpdateModels::StaticPulse

Pulse-coupled, static synapse.

· class WeightUpdateModels::StaticPulseDendriticDelay

Pulse-coupled, static synapse with heterogenous dendritic delays.

class WeightUpdateModels::StaticGraded

Graded-potential, static synapse.

class WeightUpdateModels::PiecewiseSTDP

This is a simple STDP rule including a time delay for the finite transmission speed of the synapse.

Namespaces

· WeightUpdateModels

Macros

- #define DECLARE_WEIGHT_UPDATE_MODEL(TYPE, NUM_PARAMS, NUM_VARS, NUM_PRE_VARS, NUM_POST_VARS)
- #define SET_SIM_CODE(SIM_CODE) virtual std::string getSimCode() const override{ return SIM_CODE; }
- #define SET_EVENT_CODE(EVENT_CODE) virtual std::string getEventCode() const override{ return EV
 ENT_CODE; }
- #define SET_LEARN_POST_CODE(LEARN_POST_CODE) virtual std::string getLearnPostCode() const override{ return LEARN_POST_CODE; }
- #define SET_SYNAPSE_DYNAMICS_CODE(SYNAPSE_DYNAMICS_CODE) virtual std::string get
 SynapseDynamicsCode() const override{ return SYNAPSE_DYNAMICS_CODE; }
- #define SET_EVENT_THRESHOLD_CONDITION_CODE(EVENT_THRESHOLD_CONDITION_CODE) virtual std::string getEventThresholdConditionCode() const override{ return EVENT_THRESHOLD_CONDITI
 ON CODE;}
- #define SET_SIM_SUPPORT_CODE(SIM_SUPPORT_CODE) virtual std::string getSimSupportCode() const override{ return SIM_SUPPORT_CODE; }
- #define SET_LEARN_POST_SUPPORT_CODE(LEARN_POST_SUPPORT_CODE) virtual std::string get
 LearnPostSupportCode() const override{ return LEARN_POST_SUPPORT_CODE; }
- #define SET_SYNAPSE_DYNAMICS_SUPPORT_CODE(SYNAPSE_DYNAMICS_SUPPORT_CODE) virtual std::string getSynapseDynamicsSuppportCode() const override{ return SYNAPSE_DYNAMICS_SUPPORT_CODE;}
- #define SET_PRE_SPIKE_CODE(PRE_SPIKE_CODE) virtual std::string getPreSpikeCode() const override{
 return PRE_SPIKE_CODE; }
- #define SET_POST_SPIKE_CODE(POST_SPIKE_CODE) virtual std::string getPostSpikeCode() const override{ return POST_SPIKE_CODE; }
- #define SET_PRE_VARS(...) virtual VarVec getPreVars() const override{ return __VA_ARGS__; }
- #define SET_POST_VARS(...) virtual VarVec getPostVars() const override{ return __VA_ARGS__; }
- #define SET_NEEDS_PRE_SPIKE_TIME(PRE_SPIKE_TIME_REQUIRED) virtual bool isPreSpikeTime
 Required() const override{ return PRE_SPIKE_TIME_REQUIRED; }

21.142.1 Macro Definition Documentation

```
21.142.1.1 DECLARE_WEIGHT_UPDATE_MODEL
```

```
#define DECLARE_WEIGHT_UPDATE_MODEL(
              TYPE,
              NUM_PARAMS,
              NUM_VARS,
              NUM_PRE_VARS,
              NUM POST VARS )
Value:
DECLARE_SNIPPET(TYPE, NUM_PARAMS);
   typedef Models::VarInitContainerBase<NUM_VARS> VarValues;
   typedef Models::VarInitContainerBase<NUM_PRE_VARS>
   PreVarValues;
typedef Models::VarInitContainerBase<NUM_POST_VARS>
     PostVarValues
21.142.1.2 SET_EVENT_CODE
#define SET_EVENT_CODE(
              EVENT_CODE ) virtual std::string getEventCode() const override{ return EVENT_CO↔
DE; }
21.142.1.3 SET_EVENT_THRESHOLD_CONDITION_CODE
#define SET_EVENT_THRESHOLD_CONDITION_CODE(
             EVENT_THRESHOLD_CONDITION_CODE ) virtual std::string getEventThresholdCondition↔
Code() const override{ return EVENT_THRESHOLD_CONDITION_CODE; }
21.142.1.4 SET_LEARN_POST_CODE
#define SET_LEARN_POST_CODE(
              LEARN_POST_CODE ) virtual std::string getLearnPostCode() const override{ return
LEARN_POST_CODE; }
21.142.1.5 SET_LEARN_POST_SUPPORT_CODE
#define SET_LEARN_POST_SUPPORT_CODE(
             LEARN_POST_SUPPORT_CODE ) virtual std::string getLearnPostSupportCode() const
override{ return LEARN_POST_SUPPORT_CODE; }
21.142.1.6 SET_NEEDS_POST_SPIKE_TIME
#define SET_NEEDS_POST_SPIKE_TIME(
             POST_SPIKE_TIME_REQUIRED ) virtual bool isPostSpikeTimeRequired() const override{
return POST_SPIKE_TIME_REQUIRED; }
21.142.1.7 SET_NEEDS_PRE_SPIKE_TIME
#define SET_NEEDS_PRE_SPIKE_TIME(
```

PRE_SPIKE_TIME_REQUIRED) virtual bool isPreSpikeTimeRequired() const override{

```
return PRE_SPIKE_TIME_REQUIRED; }
21.142.1.8 SET_POST_SPIKE_CODE
#define SET_POST_SPIKE_CODE(
             POST_SPIKE_CODE ) virtual std::string getPostSpikeCode() const override{ return
POST_SPIKE_CODE; }
21.142.1.9 SET_POST_VARS
#define SET_POST_VARS(
              ... ) virtual VarVec getPostVars() const override{ return __VA_ARGS__; }
21.142.1.10 SET_PRE_SPIKE_CODE
#define SET_PRE_SPIKE_CODE(
             PRE\_SPIKE\_CODE ) virtual std::string getPreSpikeCode() const override{ return P} \leftarrow
RE_SPIKE_CODE; }
21.142.1.11 SET_PRE_VARS
#define SET_PRE_VARS(
              ... ) virtual VarVec getPreVars() const override{ return __VA_ARGS__; }
21.142.1.12 SET_SIM_CODE
#define SET_SIM_CODE(
             SIM_CODE ) virtual std::string getSimCode() const override{ return SIM_CODE; }
21.142.1.13 SET_SIM_SUPPORT_CODE
#define SET_SIM_SUPPORT_CODE(
             SIM_SUPPORT_CODE ) virtual std::string getSimSupportCode() const override{ return
SIM_SUPPORT_CODE; }
21.142.1.14 SET_SYNAPSE_DYNAMICS_CODE
#define SET_SYNAPSE_DYNAMICS_CODE(
              SYNAPSE_DYNAMICS_CODE ) virtual std::string getSynapseDynamicsCode() const override{
return SYNAPSE_DYNAMICS_CODE; }
21.142.1.15 SET_SYNAPSE_DYNAMICS_SUPPORT_CODE
#define SET_SYNAPSE_DYNAMICS_SUPPORT_CODE(
             SYNAPSE\_DYNAMICS\_SUPPORT\_CODE ) virtual std::string getSynapseDynamicsSuppport\leftrightarrow
Code() const override{ return SYNAPSE_DYNAMICS_SUPPORT_CODE; }
```

22 Example Documentation

22.1 HostOnly/hostonly.c

Very simple example client program that uses the clRNG host-only API

22.2 MultiStream/multistream.c

Example program that generates random numbers using multiple streams per work-item. The program also compares the device output with that obtained by using host code to generate the numbers.

22.3 RandomArray/randomarray.c

Example program that calls the clrngDeviceRandomU01Array() functionThe program also compares the device output with that obtained by using host code to generate the numbers.

22.4 WorkItem/workitem.c

Very simple example client program that uses the clRNG host and device API's

See also

workitem_kernel.cl

22.5 WorkItem/workitem_kernel.cl

Very simple example of a kernel that uses the cIRNG device API

See also

workitem.c

574 REFERENCES

References

[1] Eugene M Izhikevich. Simple model of spiking neurons. *IEEE Transactions on neural networks*, 14(6):1569–1572, 2003. 7, 8, 9, 71, 89, 253, 254, 256, 257, 529

- [2] James C. Knight and Thomas Nowotny. GPUs Outperform Current HPC and Neuromorphic Solutions in Terms of Speed and Energy When Simulating a Highly-Connected Cortical Model. *Frontiers in Neuroscience*, 12(December):1–19, 2018. 18
- [3] James C Knight and Thomas Nowotny. Larger GPU-accelerated brain simulations with procedural connectivity. bioRxiv, 2020. 18
- [4] Abigail Morrison, Markus Diesmann, and Wulfram Gerstner. Phenomenological models of synaptic plasticity based on spike timing. *Biological Cybernetics*, 98:459–478, 2008. 37
- [5] T. Nowotny. Parallel implementation of a spiking neuronal network model of unsupervised olfactory learning on NVidia CUDA. In P. Sobrevilla, editor, *IEEE World Congress on Computational Intelligence*, pages 3238–3245, Barcelona, 2010. IEEE. 37
- [6] Thomas Nowotny, Ramón Huerta, Henry DI Abarbanel, and Mikhail I Rabinovich. Self-organization in the olfactory system: one shot odor recognition in insects. *Biological cybernetics*, 93(6):436–446, 2005. 12, 352
- [7] Tobias C. Potjans and Markus Diesmann. The Cell-Type Specific Cortical Microcircuit: Relating Structure and Activity in a Full-Scale Spiking Network Model. *Cerebral Cortex*, 24(3):785–806, mar 2014. 18
- [8] Nikolai F Rulkov. Modeling of spiking-bursting neural behavior using two-dimensional map. *Physical Review E*, 65(4):041922, 2002. 352
- [9] Marcel Stimberg, Dan F. M. Goodman, and Thomas Nowotny. Brian2genn: a system for accelerating a large variety of spiking neural networks with graphics hardware. *bioRxiv*, 2018. 15
- [10] R. D. Traub and R. Miles. *Neural Networks of the Hippocampus*. Cambridge University Press, New York, 1991. 12, 13, 432

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