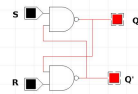
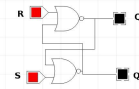
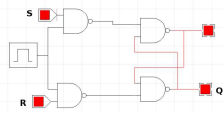
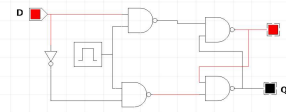
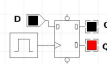
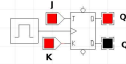
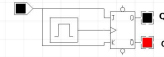
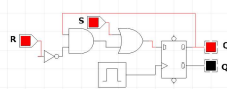
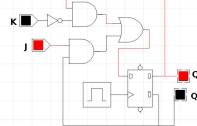
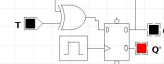
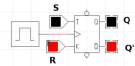
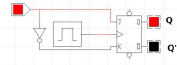
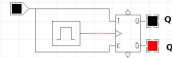
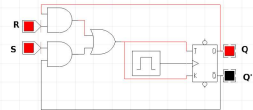
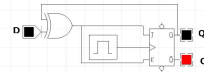


NAND latch**NOR latch****SR flipflop****D flipflop****D flipflop****JK flipflop****T flipflop****D to SR****D to JK****D to T****JK to SR****JK to D****JK to T****T to SR****T to D****T to JK**