

CompTIA A+ Certification Practice Test 3 (Exam 220-901)

► For CPUs without integrated memory controller, which part of the chipset manages communication between the CPU and memory?

☐ ☐ ☐ I/O Controller Hub (ICH)

☐ ☒ ☐ Northbridge (✗ Missed)

☐ ☐ ☐ Fusion Controller Hub (FCH)

☐ ☐ ☐ Southbridge

❏ Your answer to this question is incorrect.

► In order to retain its configuration data, CMOS RAM requires constant supply of electricity which is provided by the:

☐ ☐ ☐ AC adapter

☐ ☐ ☐ Power Supply Unit (PSU)

☐ ☒ ☐ CMOS battery (✗ Missed)

☐ ☐ ☐ Uninterruptible Power Supply (UPS)

☐ ☐ ☐ Power inverter

❏ Your answer to this question is incorrect.

► Which of the answers listed below refers to a source of power for an RTC when the computer is turned off?

☐ ☐ ☐ Power inverter

☐ ☒ ☐ CMOS battery (✗ Missed)

☐ ☐ ☐ Power Supply Unit (PSU)

☐ ☐ ☐ AC adapter

❏ Your answer to this question is incorrect.

► What is the name of the most common connector type used for providing power to various hardware components inside the computer case?

☐ ☐ ☐ RJ-45

☐ ☐ ☐ BNC

☐ ☒ ☐ Molex (✗ Missed)

☐ ☐ ☐ USB

❏ Your answer to this question is incorrect.


► A switch, button, or LED on the front/top panel of a computer case has a connector linked to it that must be attached to the appropriate pins on the:

☐ ☐ ☐ Riser card

☐ ☐ ☐ Expansion slot

☐ ☐ ☐ Power supply

☐ ☒ ☐ Motherboard (✖ Missed)

 Your answer to this question is incorrect.

▶ Which of the memory types listed below are designed to store permanent data that cannot be changed? (Select all that apply)


☐ ☐ ☐ RAM

☐ ☐ ☐ EPROM

☐ ☒ ☐ ROM (✖ Missed)

☐ ☒ ☐ PROM (✖ Missed)


☐ ☐ ☐ EEPROM

 Your answer to this question is incorrect.

▶ Random Access Memory (RAM) falls into the category of the so-called volatile storage media. It requires a continuous flow of electricity to preserve its contents thus all the data stored in RAM lasts only while the power is on.

☐ ☒ ☐ True (✖ Missed)

☐ ☐ ☐ False

 Your answer to this question is incorrect.

▶ Which of the answers listed below refer(s) to the characteristic feature(s) of Static Random Access Memory (SRAM)? (Select all that apply)

☐ ☐ ☐ Non-volatile storage media type

☐ ☒ ☐ Faster than Dynamic Random Access Memory (DRAM) (✖ Missed)

☐ ☐ ☐ Widely used as system's primary storage media (regular RAM modules installed in memory slots on the motherboard)


☐ ☒ ☐ More expensive in comparison to Dynamic Random Access Memory (DRAM) (✖ Missed)

☐ ☐ ☐ Slower than Dynamic Random Access Memory (DRAM)

☐ ☒ ☐ Volatile storage media type (✖ Missed)

☐ ☒ ☐ Utilized for CPU cache memory chips (✖ Missed)

☐ ☐ ☐ Less expensive in comparison to Dynamic Random Access Memory (DRAM)

 Your answer to this question is incorrect.

▶ Which of the following answers describes(s) the characteristics of Dynamic Random Access Memory (DRAM)? (Select all that apply)

☐ ☒ ☐ Volatile storage media type (✖ Missed)

☐ ☐ ☐ Utilized for CPU cache memory chips


☐ ☐ ☐ More expensive in comparison to Static Random Access Memory (SRAM)


☐ ☐ ☐ Non-volatile storage media type

☐ ☒ ☐ Slower than Static Random Access Memory (SRAM) (✖ Missed)


☐ ☒ ☐ Widely used as system's primary storage media (regular RAM modules installed in memory slots on the motherboard) (✖ Missed)

☐ ☐ ☐ Faster than Static Random Access Memory (SRAM)


☐ ☒  Less expensive in comparison to Static Random Access Memory (SRAM) (✖ Missed)

 Your answer to this question is incorrect.


► The term "Synchronous Dynamic Random Access Memory (SDRAM)" refers to a broad category of DRAM modules that rely on the signal sent by the system clock in order to coordinate their functioning with other internal PC components.


☐ ☒  True (✖ Missed)

☐ ☐ ☐ False


 Your answer to this question is incorrect.


► Which of the statements listed below are true? (Select all that apply)

☐ ☒  The speed of the system bus is measured in clock cycles (✖ Missed)


☐ ☒  In computer architecture, the term "word" refers to a group of bits processed as a unit (✖ Missed)

☐ ☐ ☐ The speed of the system bus in modern PCs is measured in GHz (i.e. billions of cycles per second)


☐ ☒  A single clock cycle is an equivalent of a period of time between two adjacent signals sent by the system clock (✖ Missed)

☐ ☒  The speed of the system bus in modern PCs is measured in MHz (i.e. millions of cycles per second) (✖ Missed)


☐ ☐ ☐ Depending on the system specs, a single clock cycle might be an equivalent of multiple ticks of the system clock

 Your answer to this question is incorrect.

► A Single Data Rate Synchronous Dynamic Random Access Memory (SDR SDRAM) can read/write one word of data per clock cycle.

☐ ☒  True (✖ Missed)

☐ ☐ ☐ False

 Your answer to this question is incorrect.


► What is the number of contact pins used in SDR SDRAM modules?

☐ ☐ ☐ 144


☐ ☒  168 (✖ Missed)

☐ ☐ ☐ 184


☐ ☐ ☐ 240

 Your answer to this question is incorrect.

► The placement of a notch on the RAM module contact surface allows for proper alignment of the module with the memory bank on the motherboard and prevents the installation of incompatible memory types.


☐ ☒  True (✖ Missed)

☐ ☐ ☐ False


 Your answer to this question is incorrect.

► What are the characteristic features of the Double Data Rate Synchronous Dynamic Random Access Memory (DDR SDRAM)? (Select 3 answers)

☐ ☒  184 contact pins (✖ Missed)


☐ ☒  Reads/writes 2 words of data per clock cycle (✖ Missed)

☐ ☐ ☐ 168 contact pins

☐ ☒  1 notch on the module contact surface (✖ Missed)

☐ ☐ ☐ Reads/writes 4 words of data per clock cycle


☐ ☐ ☐ 2 notches on the module contact surface

 Your answer to this question is incorrect.


► Which of the answers listed below refers to the industry name used for DDR-333 modules?

☐ ☐ ☐ PC-2100

☐ ☐ ☐ PC-2400

☐ ☒  PC-2700 (✖ Missed)

☐ ☐ ☐ PC-3200

 Your answer to this question is incorrect.


► What is the peak transfer rate of a DDR-266 module?

☐ ☐ ☐ 1600 MB/s


☐ ☒  2133 MB/s (✖ Missed)


☐ ☐ ☐ 2666 MB/s

☐ ☐ ☐ 3200 MB/s

 Your answer to this question is incorrect.

► What are the characteristic features of the Double Data Rate 2 Synchronous Dynamic Random Access Memory (DDR2 SDRAM)? (Select 3 answers)

☐ ☒  Reads/writes 4 words of data per clock cycle (✖ Missed)


☐ ☒  1 notch on the module contact surface (✖ Missed)

☐ ☐ ☐ 204 contact pins

☐ ☐ ☐ Reads/writes 8 words of data per clock cycle

☐ ☒  240 contact pins (✖ Missed)

☐ ☐ ☐ 2 notches on the module contact surface


 Your answer to this question is incorrect.


► Which of the following answers refers to the industry name used for DDR2-1066 modules?

☐ ☐ ☐ PC2-4200

☐ ☐ ☐ PC2-5300

☐ ☐ ☐ PC2-6400

☐ ☒  PC2-8500 (✖ Missed)

 Your answer to this question is incorrect.

► What is the total bandwidth of a DDR2-533 module?

☐ ☐ ☐ 3200 MB/s

☐ ☒ ☐ 4266 MB/s (✖ Missed)

☐ ☐ ☐ 5333 MB/s

☐ ☐ ☐ 6400 MB/s

🚫 Your answer to this question is incorrect.

► What are the characteristic features of the Double Data Rate 3 Synchronous Dynamic Random Access Memory (DDR3 SDRAM)? (Select 3 answers)

☐ ☒ ☐ 1 notch on the module contact surface (✖ Missed)

☐ ☐ ☐ Reads/writes 4 words of data per clock cycle

☐ ☐ ☐ 204 contact pins

☐ ☐ ☐ 2 notches on the module contact surface

☐ ☒ ☐ 240 contact pins (✖ Missed)

☐ ☒ ☐ Reads/writes 8 words of data per clock cycle (✖ Missed)

🚫 Your answer to this question is incorrect.

► Which of the answers listed below refers to the industry name used for DDR3-1333 modules?

☐ ☐ ☐ PC3-6400

☐ ☐ ☐ PC3-8500

☐ ☒ ☐ PC3-10600 (✖ Missed)

☐ ☐ ☐ PC3-12800

🚫 Your answer to this question is incorrect.

► What is the maximum amount of data that can be transferred each second by a DDR3-1600 module?

☐ ☐ ☐ 6400 MB/s

☐ ☐ ☐ 8533 MB/s

☐ ☐ ☐ 10667 MB/s

☐ ☒ ☐ 12800 MB/s (✖ Missed)

🚫 Your answer to this question is incorrect.

► Which of the answers listed below describe(s) the characteristics of Dual In-line Memory Modules (DIMMs)? (Select all that apply)

☐ ☒ ☐ Printed Circuit Board (PCB) type used as a placeholder for memory chips (✖ Missed)

☐ ☐ ☐ 32-bit data path

☐ ☒ ☐ Separate electrical contacts on each side of the module (✖ Missed)

☐ ☐ ☐ Obsolete memory packaging type used only in legacy systems

☐ ☒ ☐ 64-bit data path (✖ Missed)

☐ ☐ ☐ Unified electrical contact surface spanning both sides of the module

☐ ☒ ☐ Used with various SDRAM memory types (SDR, DDR, DDR2, and DDR3) (✖ Missed)

🚫 Your answer to this question is incorrect.

▶ What is the most common memory module form factor type used in laptop computers?

☐ ☒ ☐ SO-DIMM (✖ Missed)

☐ ☐ ☐ DIMM

☐ ☐ ☐ MicroDIMM

☐ ☐ ☐ C-RIMM

🚫 Your answer to this question is incorrect.

Your Final Report

Total marks	44
Total Questions	25
Questions correctly answered	0
Success ratio	0%
Marks secured	0
Percentage secured	0%



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