

## CompTIA A+ Certification Practice Test 2 (Exam 220-901)

▶ Chassis intrusion detection is an option that can be enabled/disabled in the BIOS setup utility (if a BIOS comes equipped with this feature). Coupled with a hardware sensor mounted inside the computer case, this functionality can be used to check if the case was opened and display a notification alert during next boot.

☐ ☒ ☐ True (✗ Missed)

☐ ☐ ☐ False

🚫 Your answer to this question is incorrect.

▶ A standardized specification of a motherboard (including its dimensions, supported power supply types, and layout of components) is known as the motherboard's:

☐ ☐ ☐ Architecture

☐ ☐ ☐ Blueprint

☐ ☒ ☐ Form factor (✗ Missed)

☐ ☐ ☐ Diagram

🚫 Your answer to this question is incorrect.

▶ Which of the following terms are used to describe a motherboard's size and purpose? (Select all that apply)

☐ ☒ ☐ Micro-ATX (✗ Missed)

☐ ☐ ☐ PCI-X

☐ ☒ ☐ ITX (✗ Missed)

☐ ☒ ☐ ATX (✗ Missed)

☐ ☒ ☐ Mini-ITX (✗ Missed)

🚫 Your answer to this question is incorrect.

▶ What is the most common motherboard type used in desktop PCs?

☐ ☐ ☐ ITX

☐ ☐ ☐ LPX

☐ ☒ ☐ ATX (✗ Missed)

☐ ☐ ☐ NLX

🚫 Your answer to this question is incorrect.

▶ Which of the answers listed below refers to a VIA-proprietary low-power consumption SFF type motherboard known for industrial and embedded PC applications?

☐ ☐ ☐ NLX

☐ ☒ ☐ ☐ Mini-ITX (✖ Missed)

☐ ☐ ☐ ATX

☐ ☐ ☐ LPX

☐ ☐ ☐ Mini-ATX

🚩 Your answer to this question is incorrect.

► Low Profile Extended (LPX) and New Low profile eXtended (NLX) are motherboard form factors known for the usage of a riser card (daughterboard) inserted into a slot near the edge of the board. The main advantage of this type of design is that expansion cards that plug into a riser card on the LPX/NLX motherboard are parallel with the board which saves space and allows for a smaller system case.

☐ ☒ ☐ ☐ True (✖ Missed)

☐ ☐ ☐ False

🚩 Your answer to this question is incorrect.

► Which of the following statements are true? (Select 3 answers)

☐ ☒ ☐ ☐ A parallel bus transmits data across multiple wires (✖ Missed)

☐ ☒ ☐ ☐ In computer architecture, the term "bus" describes a system of communication paths used to connect computer components and transfer data between them (✖ Missed)

☐ ☐ ☐ A parallel bus transmits data sequentially one bit at a time over a single communication channel

☐ ☐ ☐ A serial bus transmits data across multiple wires

☐ ☒ ☐ ☐ A serial bus transmits data sequentially one bit at a time over a single communication channel (✖ Missed)

🚩 Your answer to this question is incorrect.

► Peripheral Component Interconnect (PCI) is an example of a parallel expansion bus (and a corresponding slot type) used for attaching hardware devices (in the form of an integrated circuit or expansion card) to the motherboard inside the computer case.

☐ ☒ ☐ ☐ True (✖ Missed)

☐ ☐ ☐ False

🚩 Your answer to this question is incorrect.

► What are the characteristic features of conventional PCI? (Select all that apply)

☐ ☐ ☐ Serial interface

☐ ☒ ☐ ☐ 32-bit bus width (✖ Missed)

☐ ☐ ☐ Maximum throughput of up to 1064 MB/s

☐ ☒ ☐ ☐ 64-bit bus width (✖ Missed)






☐ ☒ ☐ ☐ Parallel interface (✖ Missed)





☐ ☒ ☐ ☐ Maximum throughput of up to 533 MB/s (✖ Missed)


🚩 Your answer to this question is incorrect.




► The Peripheral Component Interconnect eXtended (PCI-X) is an advanced version of PCI which: (Select all that apply)


☐ ☒ ☐ ☐ Doubles the bandwidth of PCI (✖ Missed)


<input type="checkbox"/> <input checked="" type="checkbox"/>  Provides much faster data transfer rates in comparison to PCI ( <input checked="" type="checkbox"/> Missed)
<input type="checkbox"/> <input checked="" type="checkbox"/>  Offers backward compatibility with PCI devices ( <input checked="" type="checkbox"/> Missed)
<input type="checkbox"/> <input type="checkbox"/> <input type="checkbox"/> Supersedes all previous standards, including PCIe
<input type="checkbox"/> <input checked="" type="checkbox"/>  Didn't get much of a market success due to the development and widespread adoption of the PCIe standard ( <input checked="" type="checkbox"/> Missed)
<input type="checkbox"/> <input checked="" type="checkbox"/>  Was intended for devices with higher bandwidth requirements such as servers and high-end workstations ( <input checked="" type="checkbox"/> Missed)
 Your answer to this question is incorrect.

► Which of the following answers describe(s) the feature(s) of the PCI-X standard? (Select all that apply)
<input type="checkbox"/> <input type="checkbox"/> <input type="checkbox"/> 32-bit bus width
<input type="checkbox"/> <input checked="" type="checkbox"/>  Parallel interface ( <input checked="" type="checkbox"/> Missed)
<input type="checkbox"/> <input checked="" type="checkbox"/>  Maximum throughput of up to 1064 MB/s ( <input checked="" type="checkbox"/> Missed)
<input type="checkbox"/> <input checked="" type="checkbox"/>  64-bit bus width ( <input checked="" type="checkbox"/> Missed)
<input type="checkbox"/> <input type="checkbox"/> <input type="checkbox"/> Maximum throughput of up to 533 MB/s
<input type="checkbox"/> <input type="checkbox"/> <input type="checkbox"/> Serial interface
 Your answer to this question is incorrect.


► Which of the answers listed below refers to a standard that relies on serial communication?
<input type="radio"/> <input type="radio"/> <input type="radio"/> ISA
<input type="radio"/> <input type="radio"/> <input type="radio"/> PCI-X
<input type="radio"/> <input type="radio"/> <input type="radio"/> IEEE 1284
<input type="radio"/> <input type="radio"/> <input type="radio"/> PCI
<input type="radio"/> <input type="radio"/> <input type="radio"/> PATA
<input type="radio"/> <input checked="" type="radio"/>  PCIe ( <input checked="" type="checkbox"/> Missed)
 Your answer to this question is incorrect.

► Motherboard slots that enable installation of dedicated graphics controllers include: (Select 2 answers)
<input type="checkbox"/> <input type="checkbox"/> <input type="checkbox"/> PGA
<input type="checkbox"/> <input checked="" type="checkbox"/>  AGP ( <input checked="" type="checkbox"/> Missed)
<input type="checkbox"/> <input type="checkbox"/> <input type="checkbox"/> CNR slot
<input type="checkbox"/> <input type="checkbox"/> <input type="checkbox"/> ATX connector
<input type="checkbox"/> <input checked="" type="checkbox"/>  PCIe ( <input checked="" type="checkbox"/> Missed)
 Your answer to this question is incorrect.


► Peripheral Component Interconnect Express (abbr. as PCIe) is a high-speed, serial expansion bus designed as a replacement for: (Select all that apply)
<input type="checkbox"/> <input checked="" type="checkbox"/>  PCI ( <input checked="" type="checkbox"/> Missed)
<input type="checkbox"/> <input type="checkbox"/> <input type="checkbox"/> SATA
<input type="checkbox"/> <input type="checkbox"/> <input type="checkbox"/> USB

☐ ☒  PCI-X (✖ Missed)

☐ ☒  AGP (✖ Missed)

 Your answer to this question is incorrect.


► In PCIe architecture, a single full-duplex point-to-point serial communication path consisting of two pairs of wires (where one pair is used for transmitting data and the other pair for receiving data) is known as:

☐ ☒  Lane (✖ Missed)


☐ ☐ ☐ Dual rail

☐ ☐ ☐ Dual channel


☐ ☐ ☐ Duplexing assembly

 Your answer to this question is incorrect.

► The PCIe labeling (×1, ×4, ×8, ×12, ×16 and ×32) refers to the number of communication links (lanes) available in different versions of the PCIe interface. The base value (×1) relates to the speed of a single lane expressed in MB/s. The multiplier (x) allows for easy calculation of the maximum throughput of the entire interface for a given PCIe variant.

☐ ☒  True (✖ Missed)

☐ ☐ ☐ False

 Your answer to this question is incorrect.

► Which of the values listed below refers to the speed of a single-lane (×1) PCIe v1.x link?


☐ ☐ ☐ 125 MB/s

☐ ☒  250 MB/s (✖ Missed)

☐ ☐ ☐ 500 MB/s

☐ ☐ ☐ 985 MB/s

☐ ☐ ☐ 1969 MB/s

 Your answer to this question is incorrect.

► A single-lane (×1) PCIe v2.x link offers maximum throughput of up to:


☐ ☐ ☐ 125 MB/s

☐ ☐ ☐ 250 MB/s

☐ ☒  500 MB/s (✖ Missed)

☐ ☐ ☐ 985 MB/s

☐ ☐ ☐ 1969 MB/s

 Your answer to this question is incorrect.

► Which of the following answers lists the speed of a single-lane (×1) PCIe v3.x link?


☐ ☐ ☐ 125 MB/s

☐ ☐ ☐ 250 MB/s

☐ ☐ ☐ 500 MB/s

☐ ☒  985 MB/s (✖ Missed)

☐ ☐ ☐ 1969 MB/s

 Your answer to this question is incorrect.

► A type of PCI slot for expansion cards installed in laptops characterized by a much smaller form factor when compared to its desktop counterpart is called:


☐ ☒  miniPCI (✖ Missed)

☐ ☐ ☐ PCIe

☐ ☐ ☐ Mini-SD

☐ ☐ ☐ PCI-X

☐ ☐ ☐ Mini-ITX

 Your answer to this question is incorrect.


► Which of the following slot types allows for adding primary storage modules to the motherboard?

☐ ☐ ☐ USB

☐ ☐ ☐ SATA

☐ ☒  RAM (✖ Missed)

☐ ☐ ☐ PATA

 Your answer to this question is incorrect.

► Which of the answers listed below refer(s) to the features of a motherboard's CPU socket? (Select all that apply)

☐ ☒  PGA (✖ Missed)


☐ ☐ ☐ TPM

☐ ☒  LGA (✖ Missed)

☐ ☐ ☐ FRU

☐ ☒  ZIF (✖ Missed)

☐ ☒  PGA2 (✖ Missed)

 Your answer to this question is incorrect.

► Which of the following integrated circuit types defines the core functionality and capabilities of a motherboard?


☐ ☒  Chipset (✖ Missed)

☐ ☐ ☐ Northbridge

☐ ☐ ☐ I/O Controller Hub (ICH)

☐ ☐ ☐ Southbridge

☐ ☐ ☐ Fusion Controller Hub (FCH)

 Your answer to this question is incorrect.

► In a northbridge/southbridge chipset computer architecture, the function of southbridge is to provide high-speed data links between CPU, RAM, and PCIe graphics controllers.

☐ ☐ ☐ True

☐ ☒ ☐ False (✖ Missed)

🚩 Your answer to this question is incorrect.

► The term "northbridge" refers to a part of the computer chipset that controls peripheral buses used for connecting lower-speed I/O devices such as hard drives, keyboards, mice, PCI devices, as well as embedded hardware including BIOS, integrated graphics/audio, USB, or Ethernet interfaces.

☐ ☐ ☐ True

☐ ☒ ☐ False (✖ Missed)

🚩 Your answer to this question is incorrect.

## Your Final Report

Total marks	45
Total Questions	25
Questions correctly answered	0
Success ratio	0%
Marks secured	0
Percentage secured	0%



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