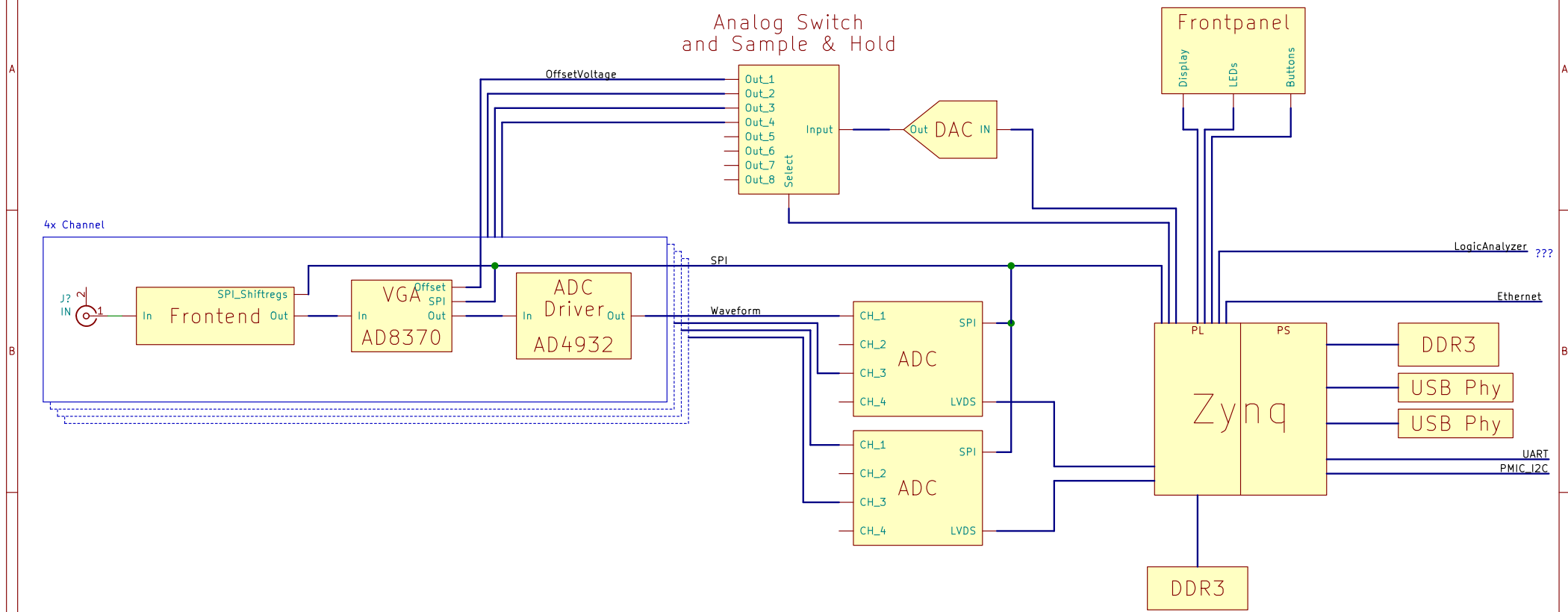


NOTE: Channels on ADC and offset sample and hold outputs may be different, I didn't check these on Hardware



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