

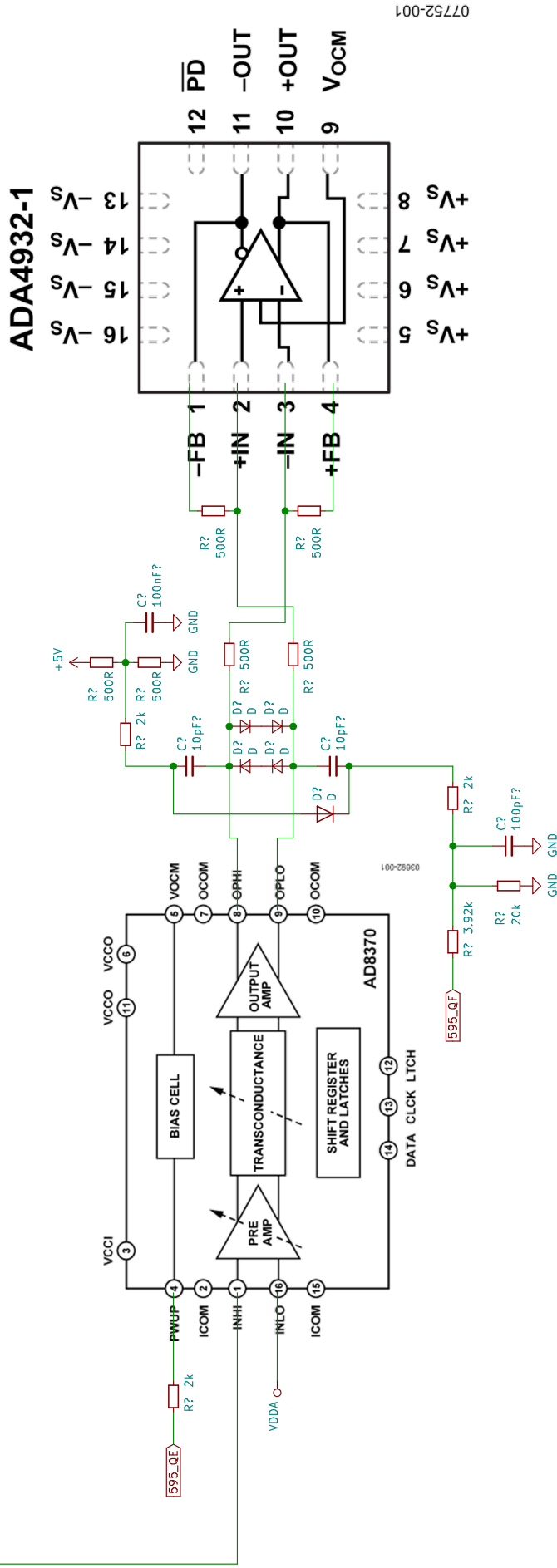
595_QB: First divider, 10:1

595_QC: Second divider, 10:1

595_QD: AC coupling, low = AC, high = DC

595_QE: PGA enable, active high

595_QF: BW limit, high = full, low = 20MHz



Reverse engineered by Niklas Fauth

Sheet: /
File: sigilent_AFE.sch

Title: Siglient SDS1202X-E AFE

Size: A3	Date: 2018-12-05
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Size: AS	Date: 2016-12-03
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