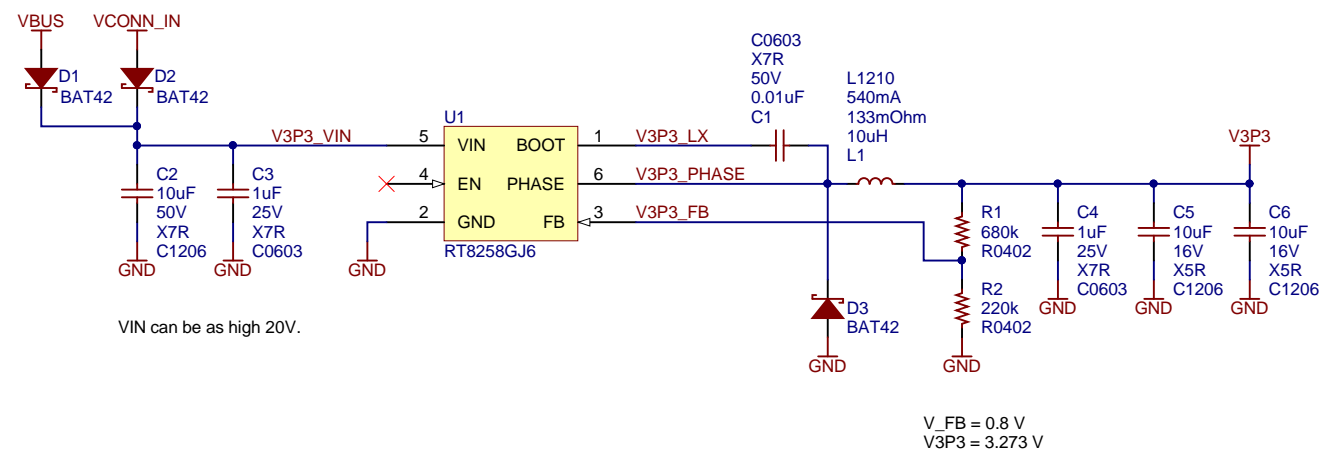
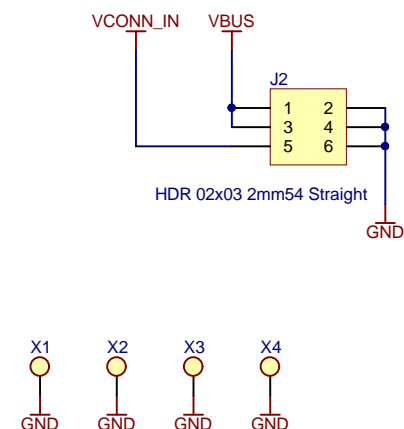
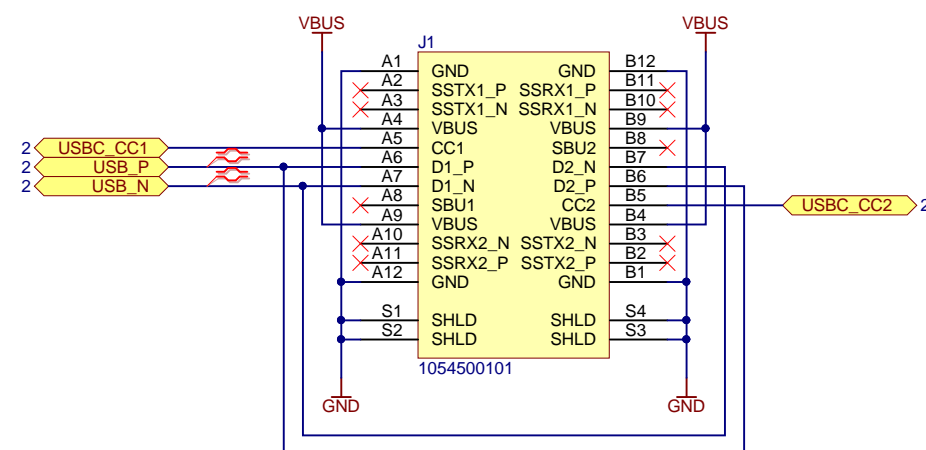
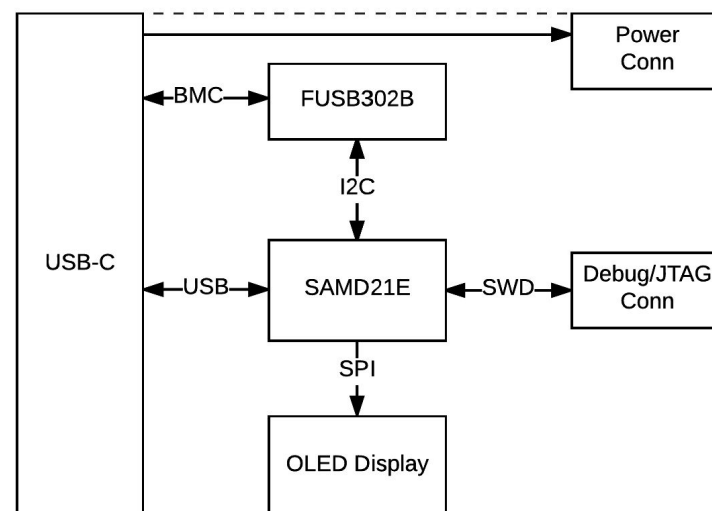
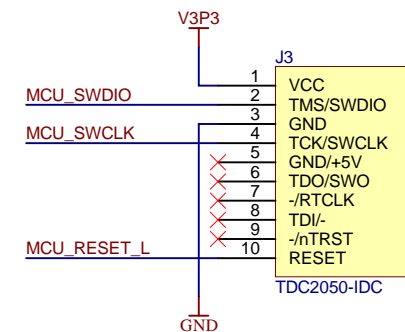
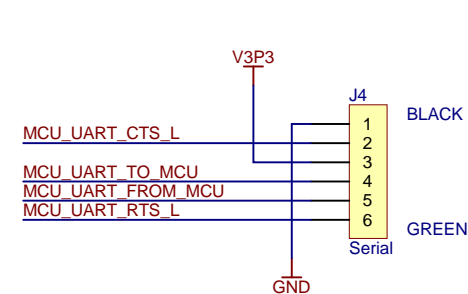
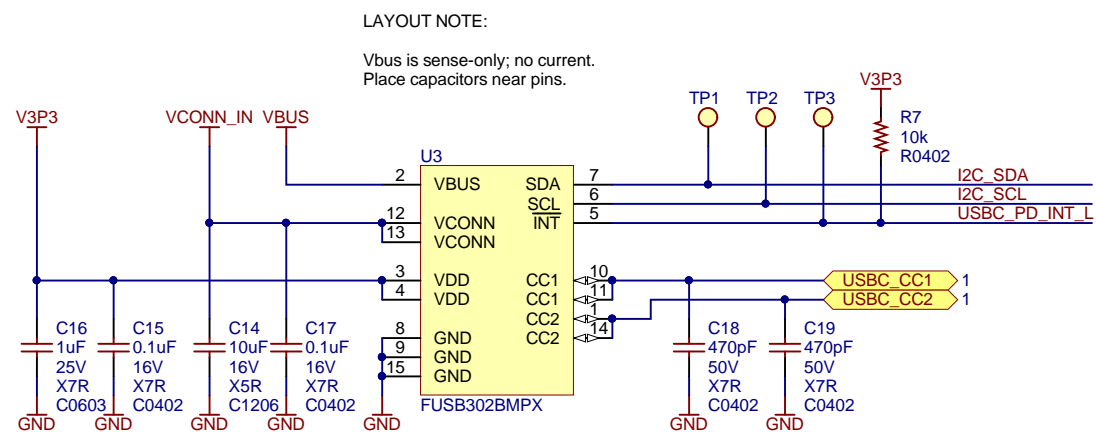
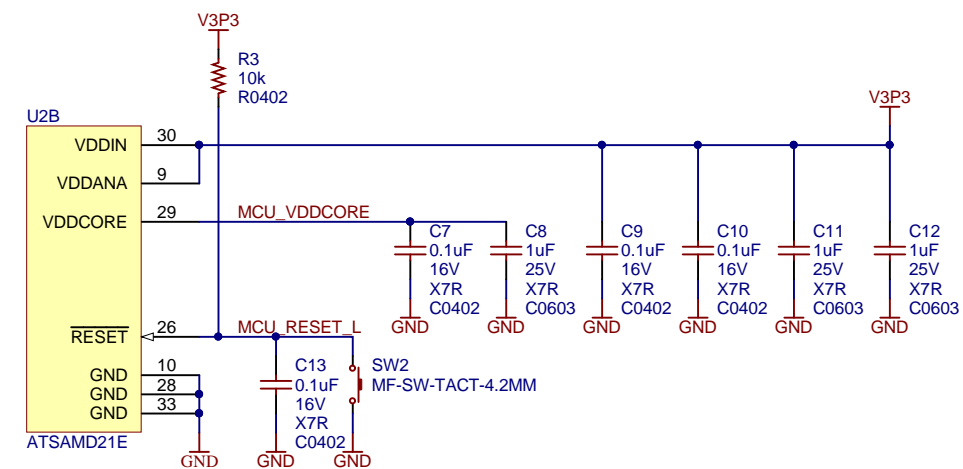
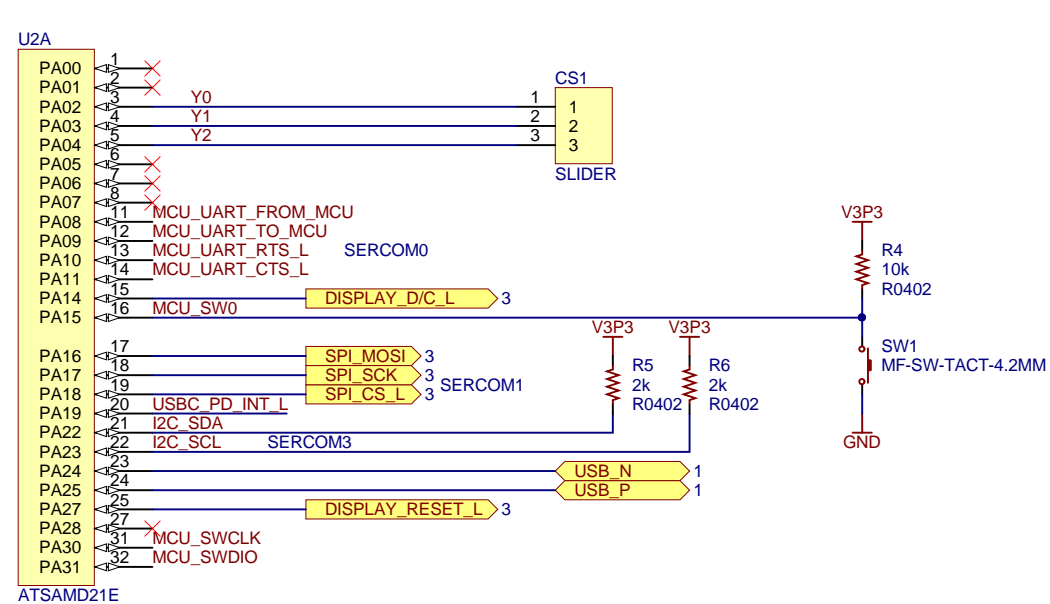
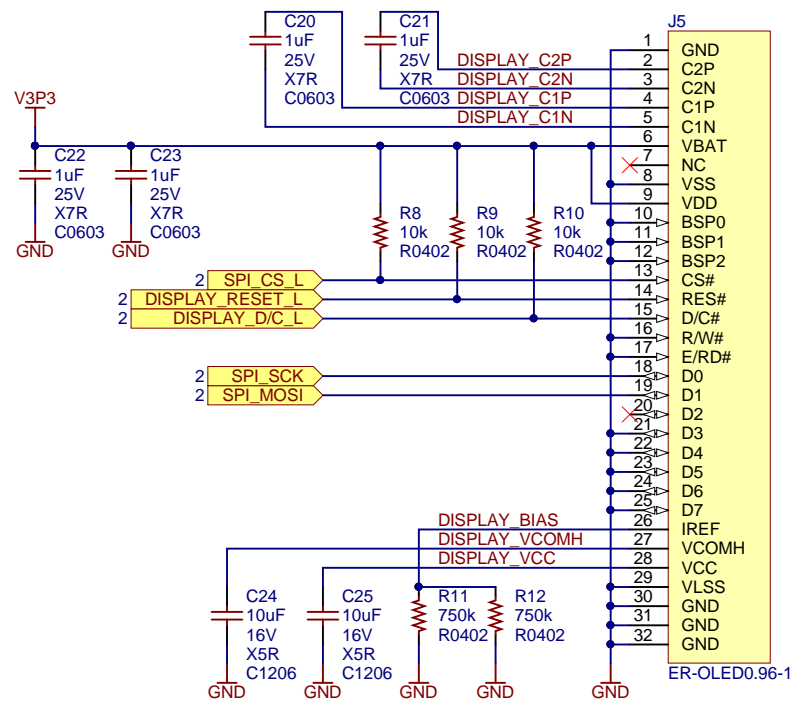


USB-C Explorer Block Diagram







BSP[2..0] = 0 => 4-wire SPI

Section 8.1.3 of the SSD1306
datasheet calls for D3 thru D7,
E/RD#, R/W# can be tied to GND.
D2 should be left floating.

I_REF is recommended to be 12.5 +/- 2 uA.
V_IREF = VCC - 2.5
When using internal boost, VCC = 7.25V.
Two 750k resistors are used to match
MacroFab House Parts and save cost.