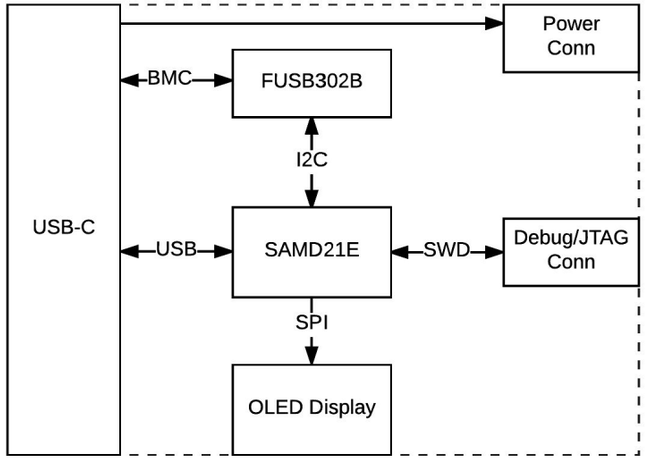
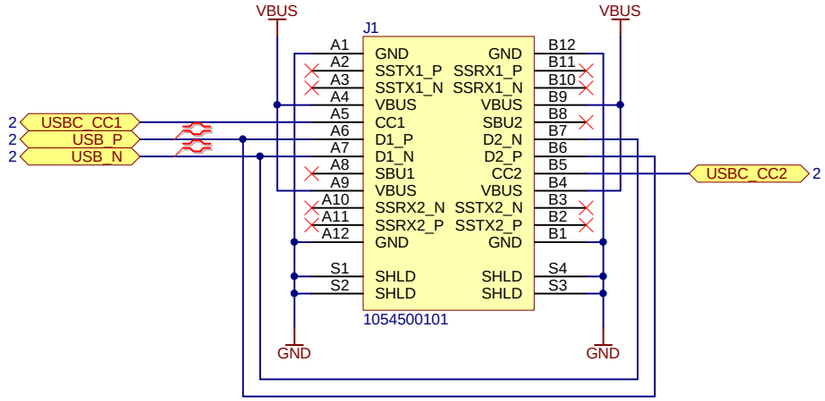
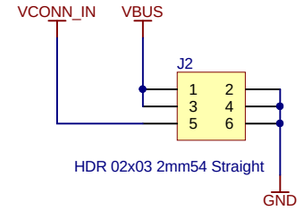
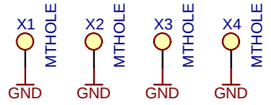
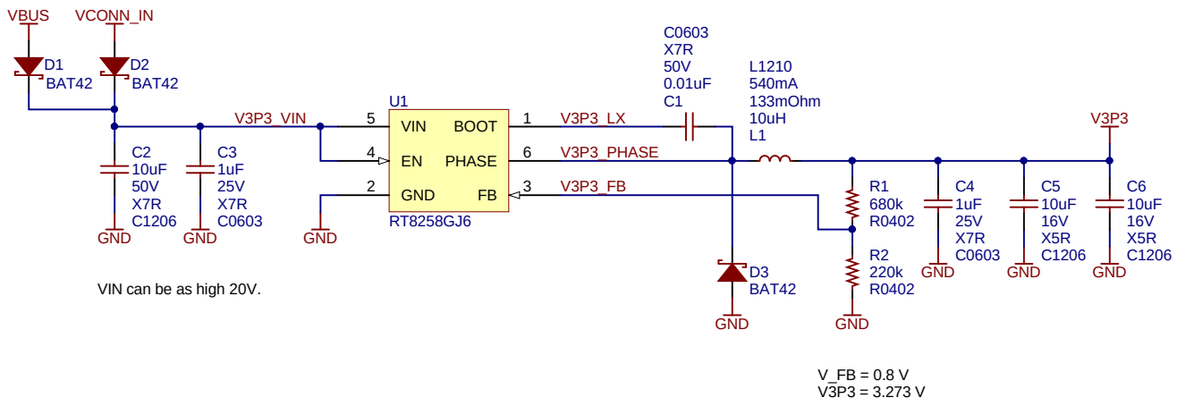
USB-C Explorer Block Diagram











Project:	USB-C Explorer
Document:	Connectors and Power
Revision:	1
Drawn By:	Jason Cerundolo
Date:	9/20/2017 Time: 02:08:22 Sheet 1 of 3
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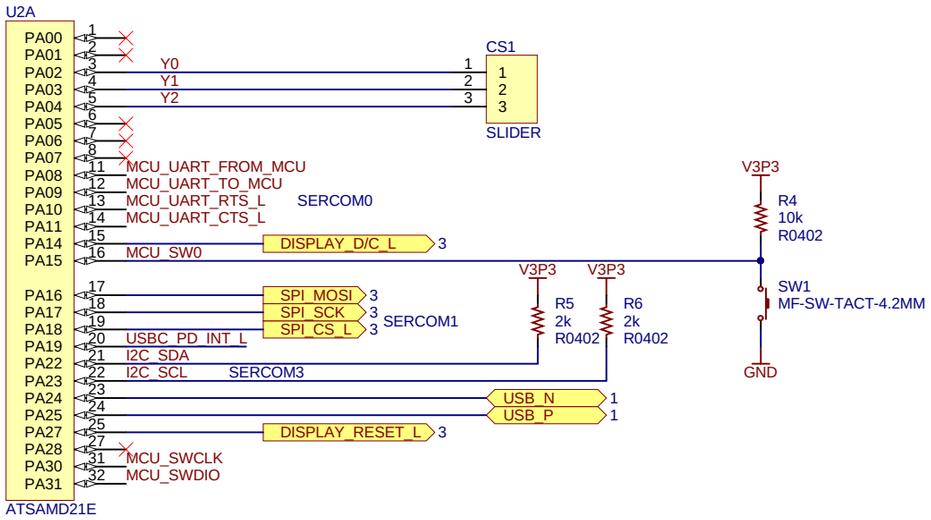

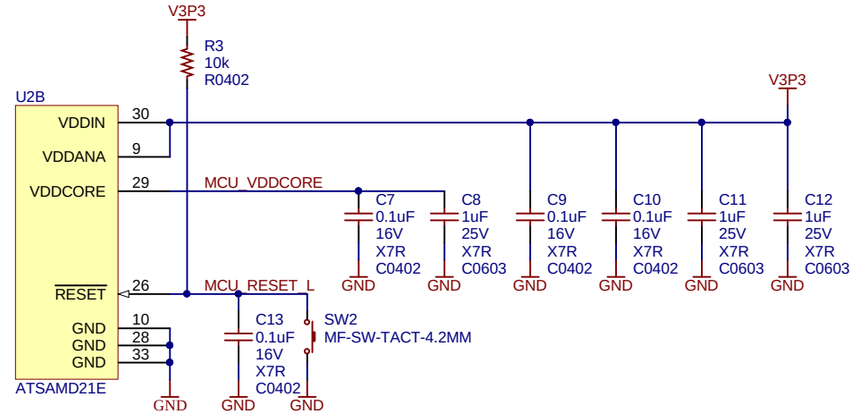

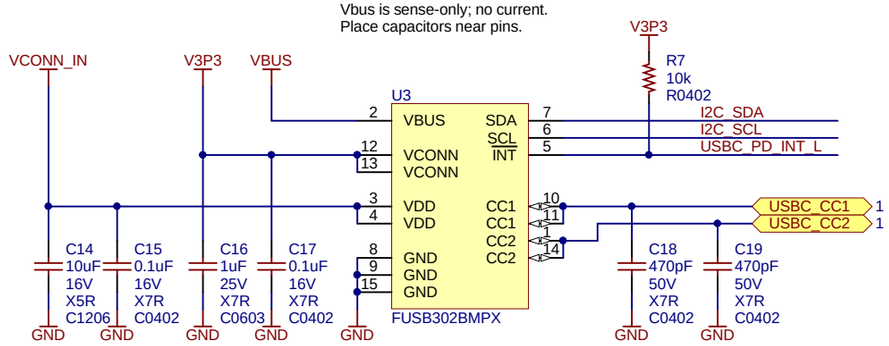

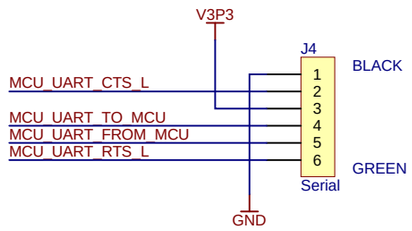

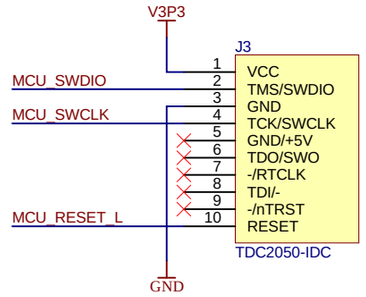

Project:	USB-C Explorer
Document:	Microcontroller
Revision:	1
Drawn By:	Jason Cerundolo
Date:	9/20/2017 Time: 02:08:23 Sheet 2 of 3

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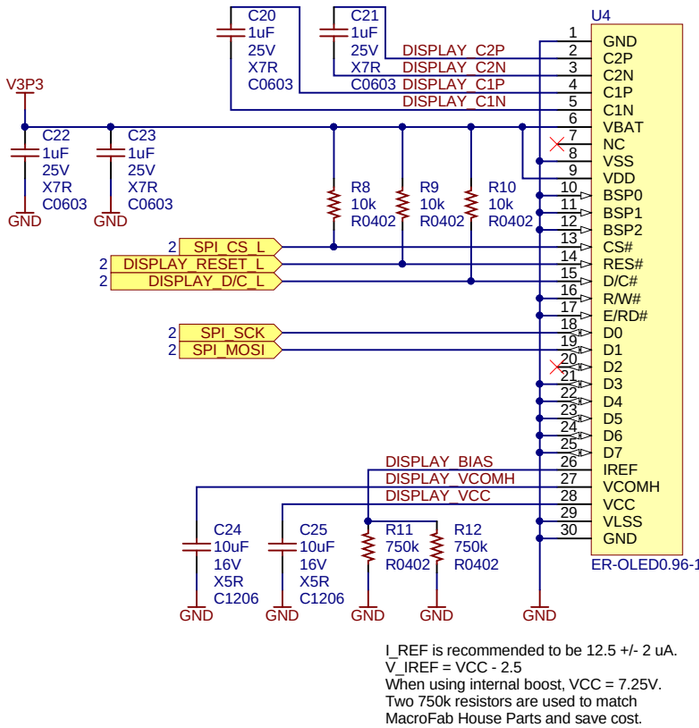

BSP[2..0] = 0 => 4-wire SPI

Section 8.1.3 of the SSD1306 datasheet calls for D3 thru D7, E/RD#, R/W# can be tied to GND. D2 should be left floating.

I_REF is recommended to be 12.5 +/- 2 uA.
V_IREF = VCC - 2.5
When using internal boost, VCC = 7.25V.
Two 750k resistors are used to match MacroFab House Parts and save cost.

Project:	USB-C Explorer
Document:	Display
Revision:	1
Drawn By:	Jason Cerundolo
Date:	9/20/2017 Time: 02:08:23 Sheet 3 of 3
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