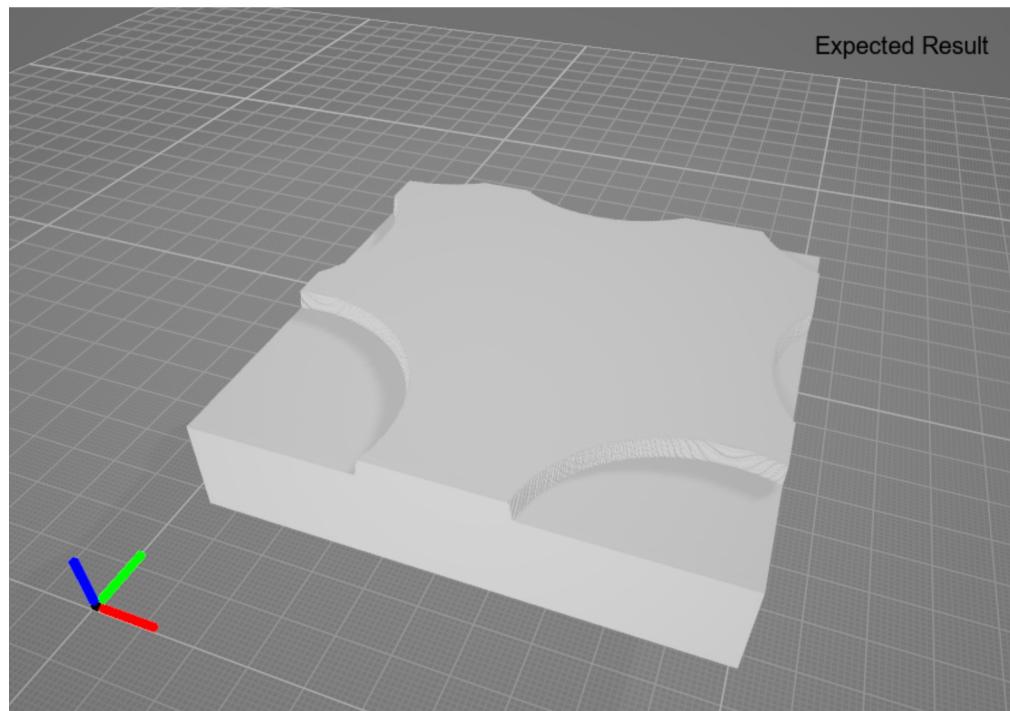
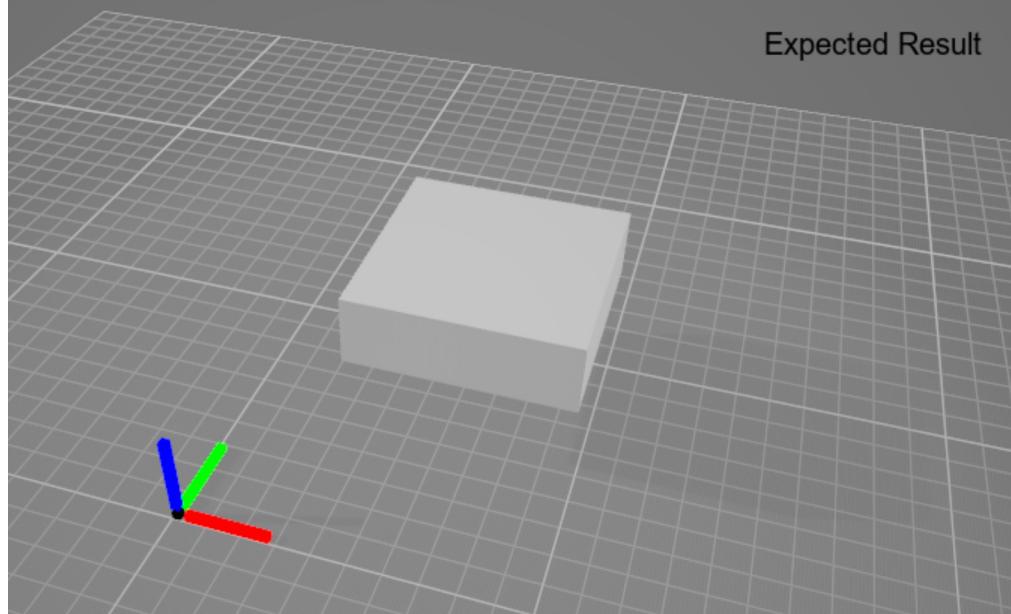


P_DPX_3200_02



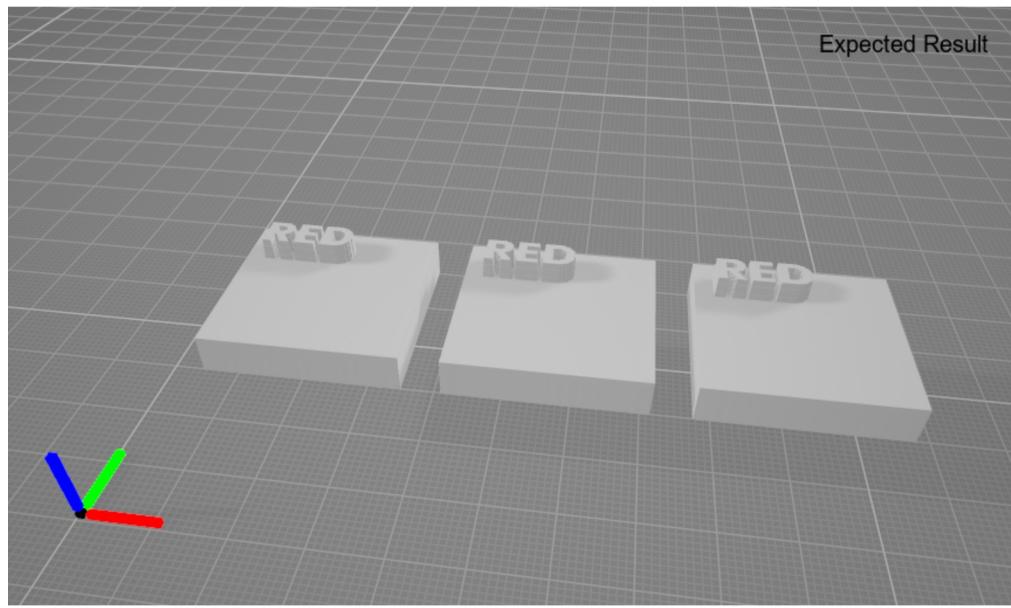
P_DPX_3200_03

Expected Result

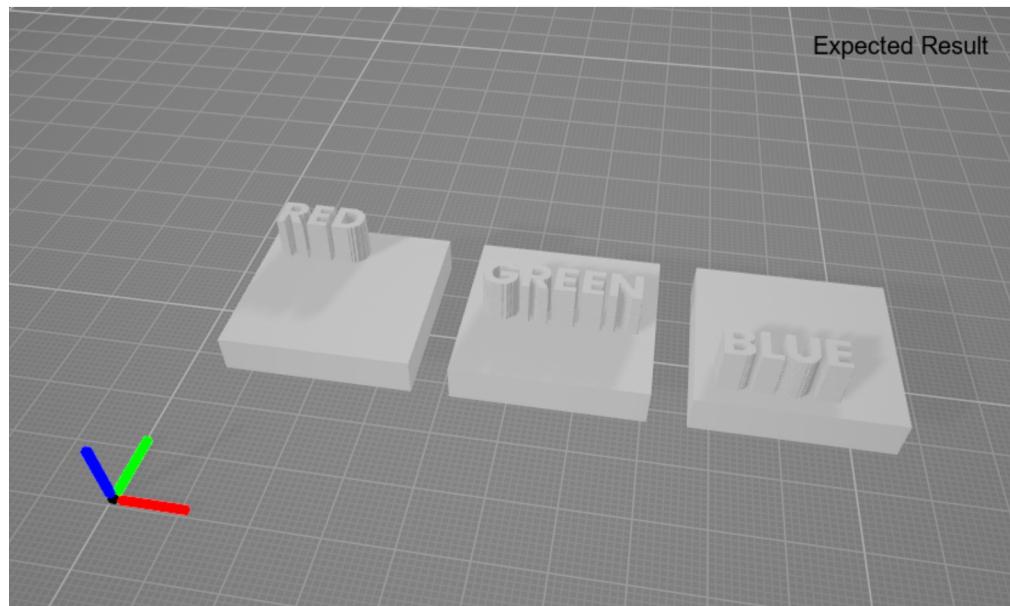


P_DPX_3200_04

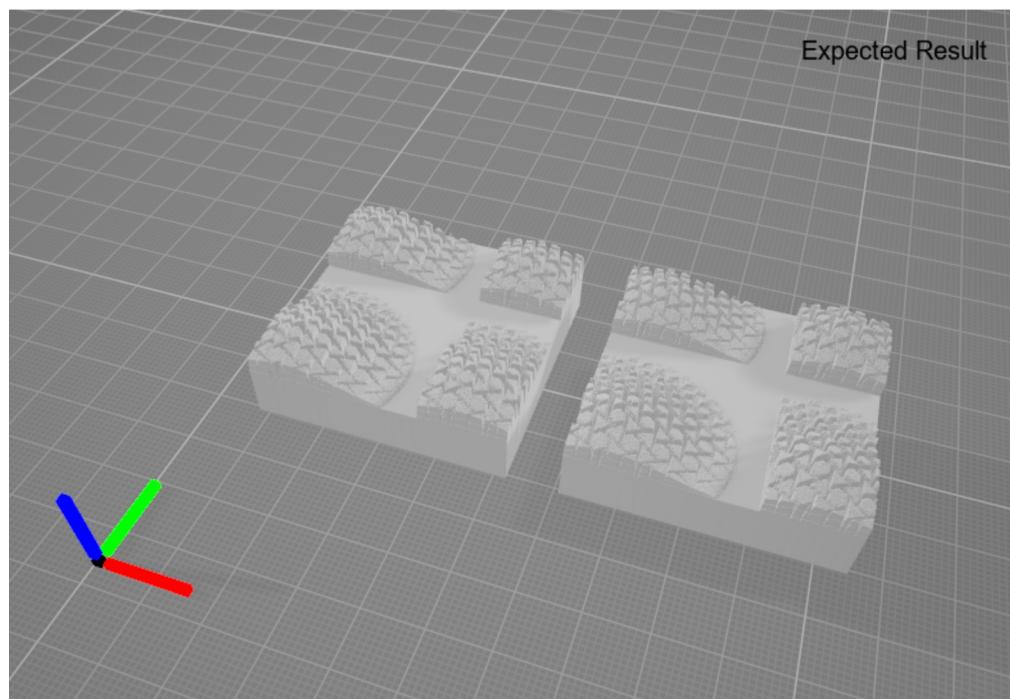
Expected Result



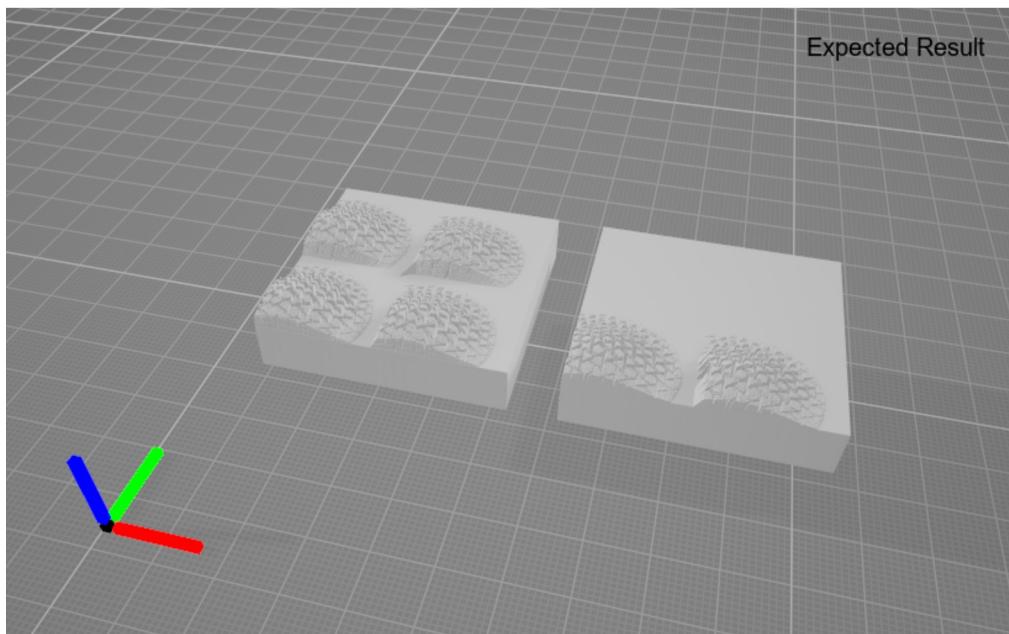
P_DPX_3200_05



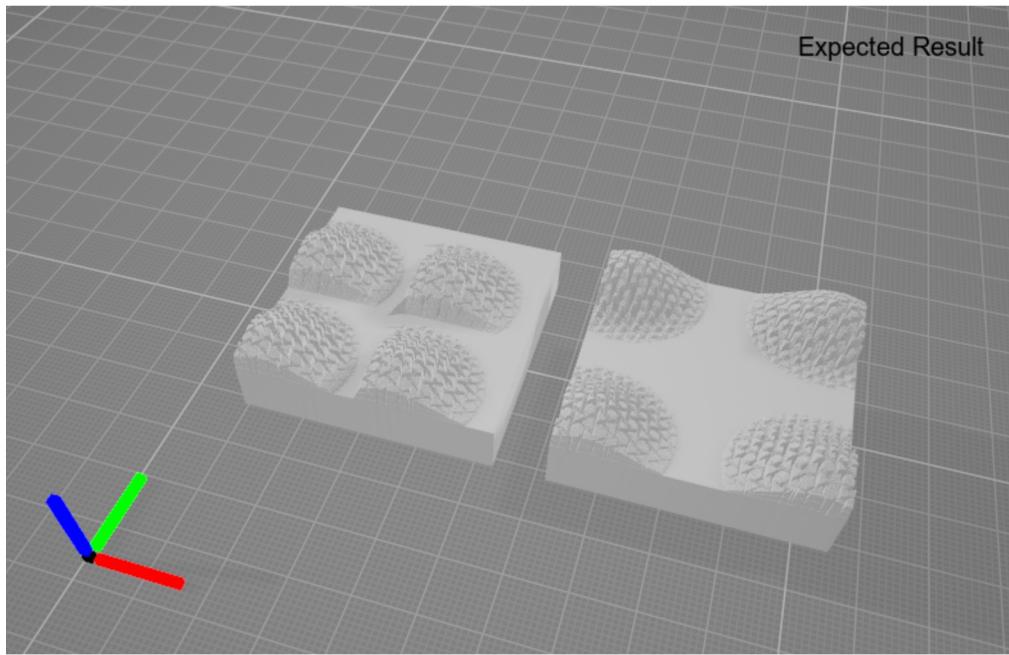
P_DPX_3200_06



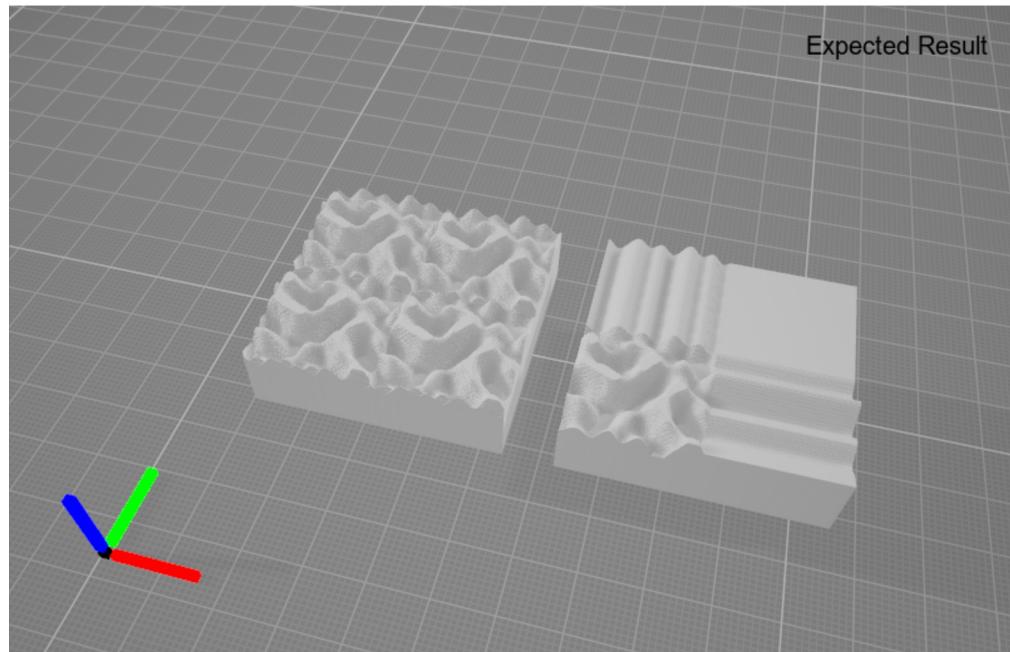
P_DPX_3200_07



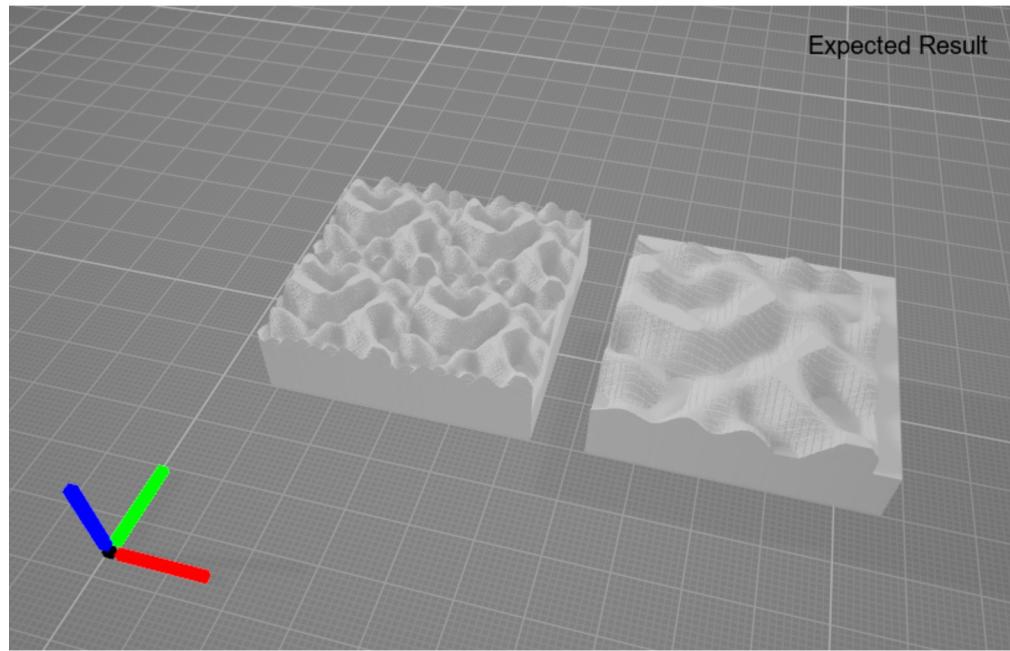
P_DPX_3200_08



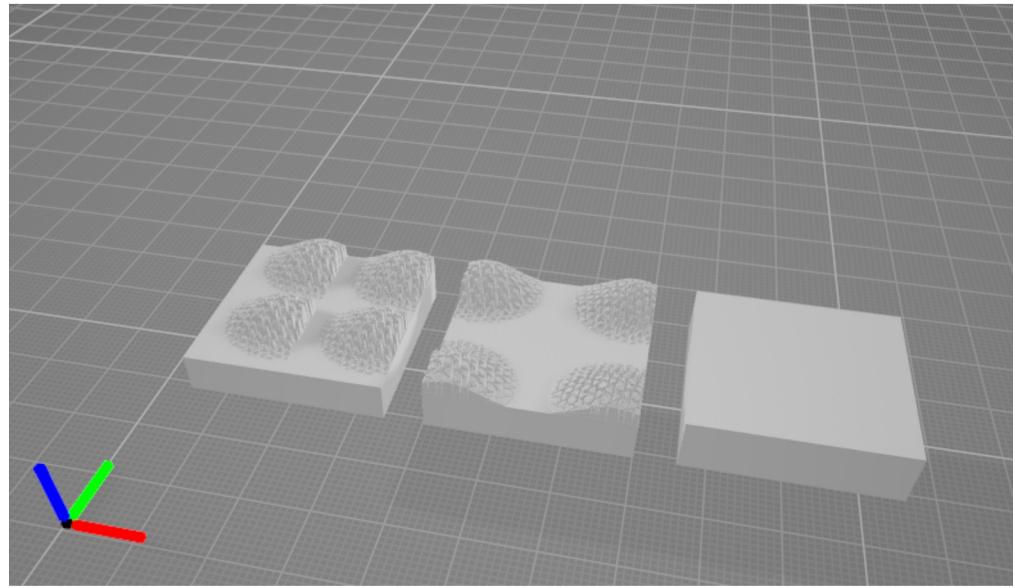
P_DPX_3200_09



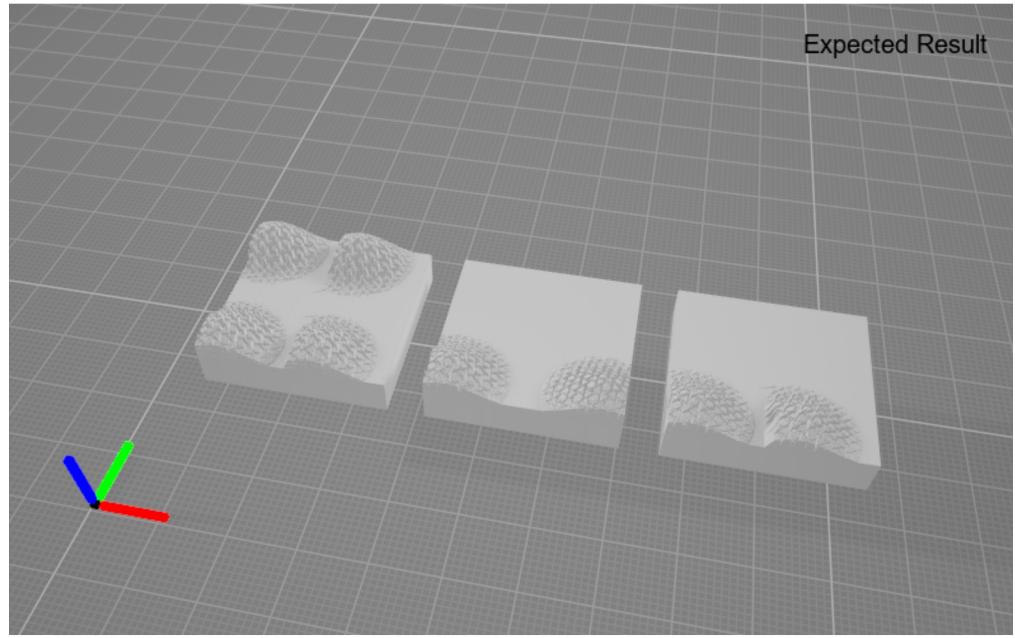
P_DPX_3200_10



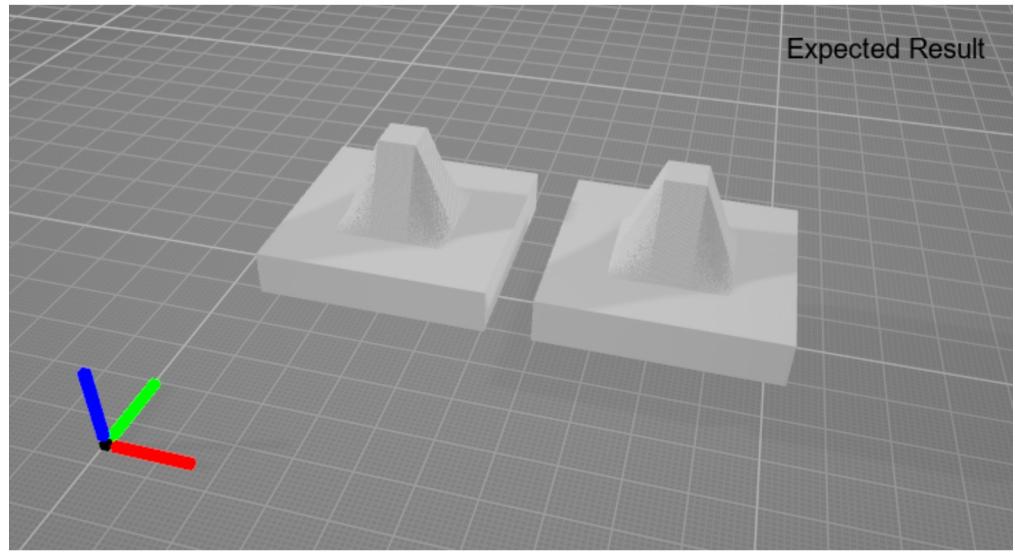
P_DPX_3200_11



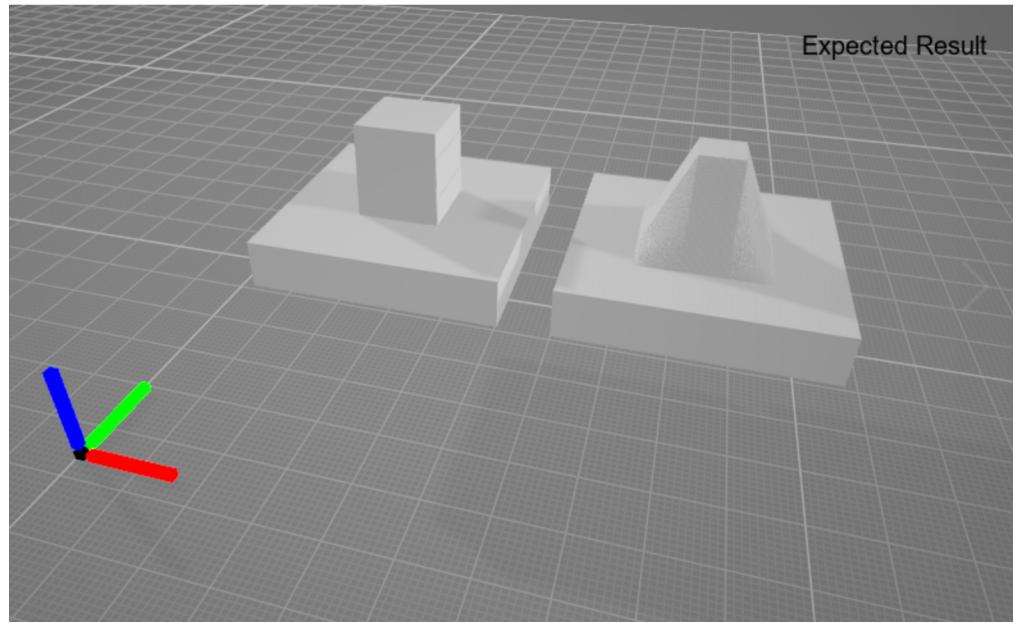
P_DPX_3200_12



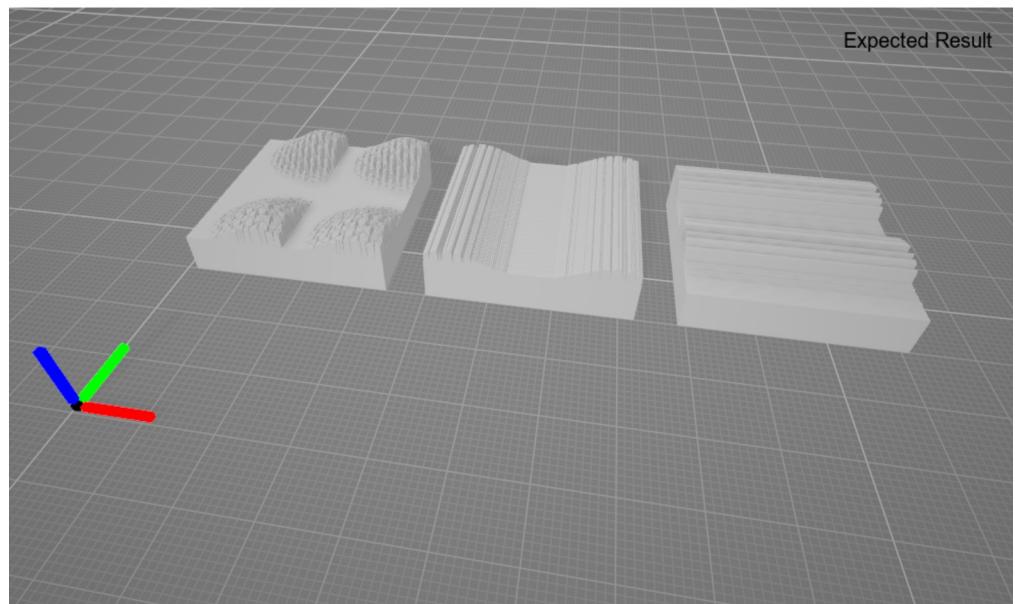
P_DPX_3200_13



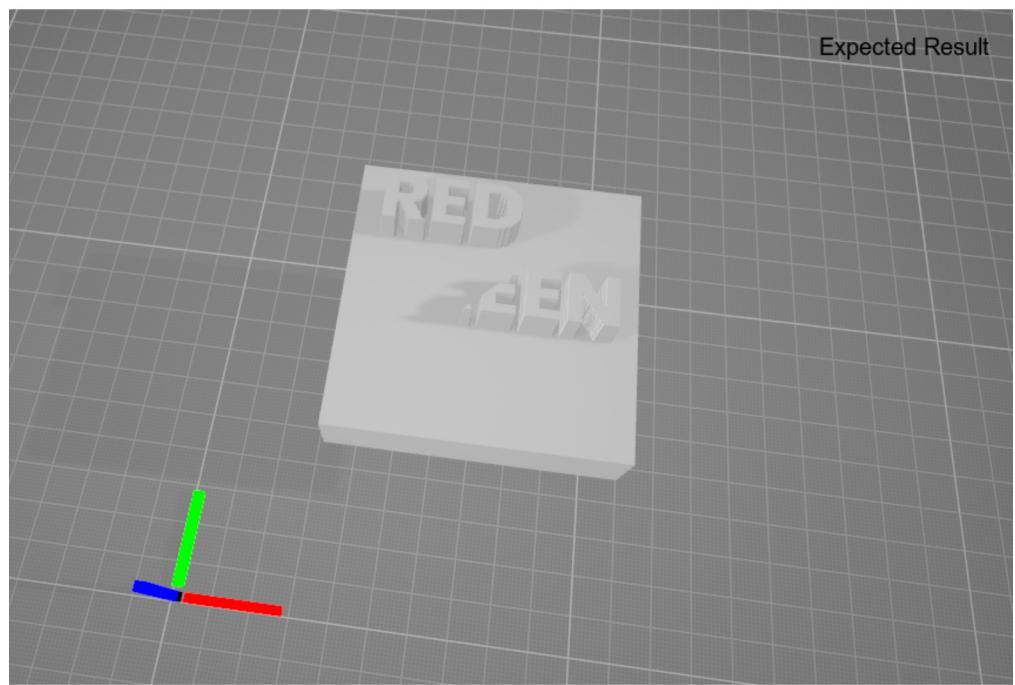
P_DPX_3200_14



P_DPX_3200_15

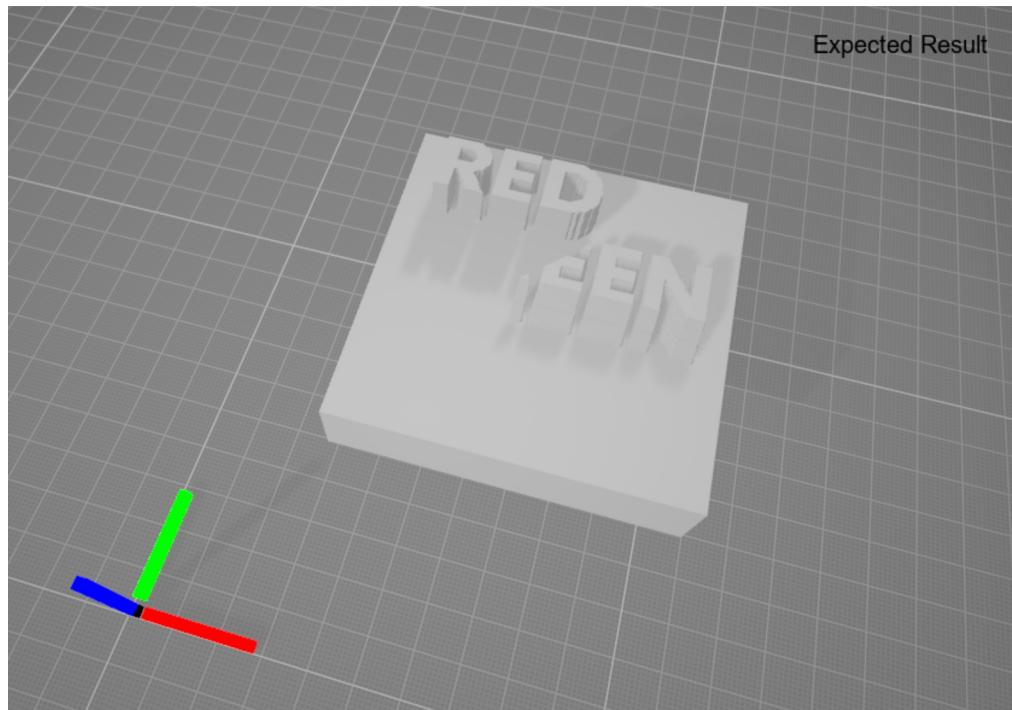


P_DPX_3200_16



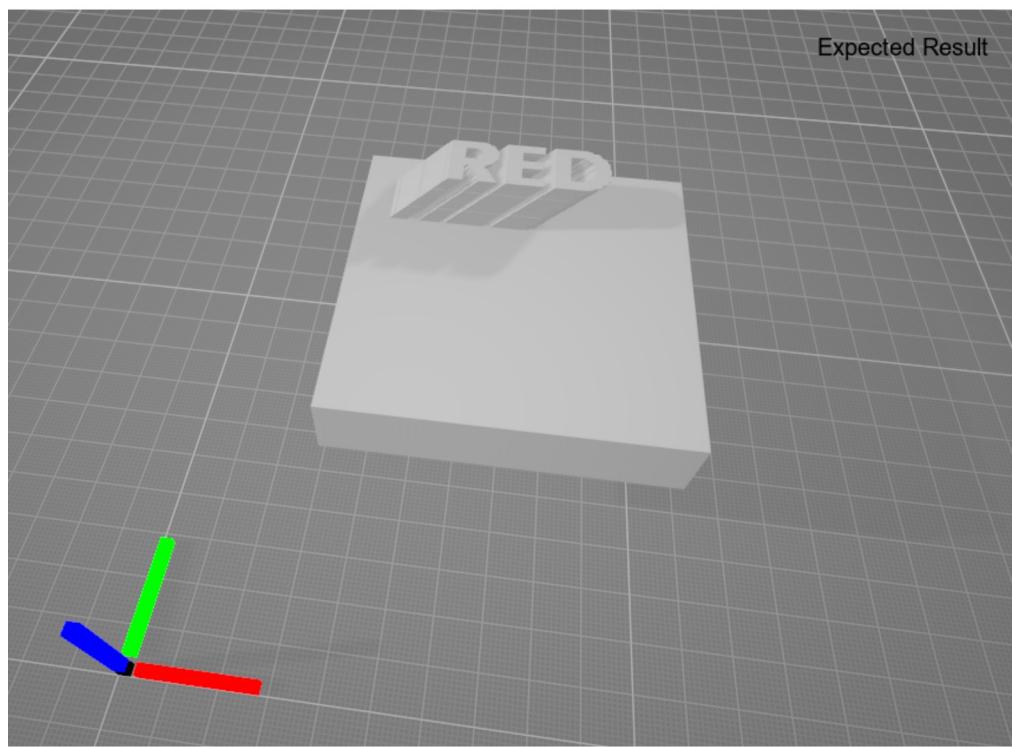
P_DPX_3200_17

Expected Result



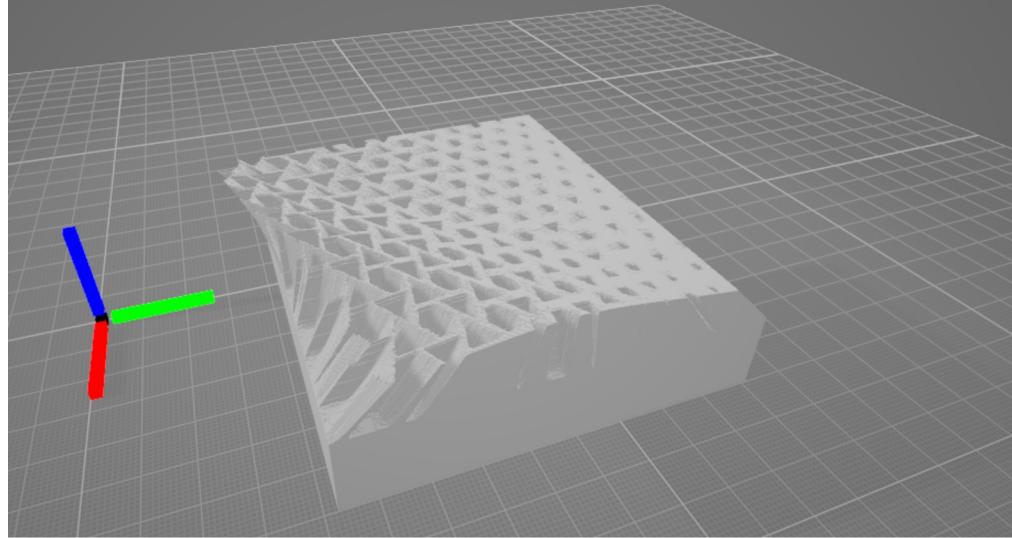
P_DPX_3202_01

Expected Result



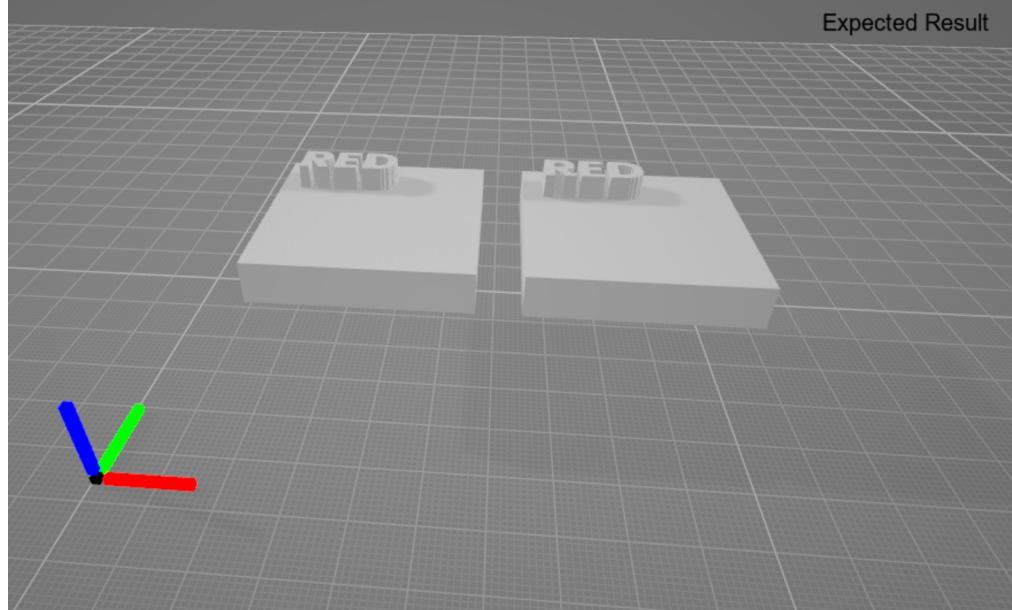
P_DPX_3204_01

Expected Result



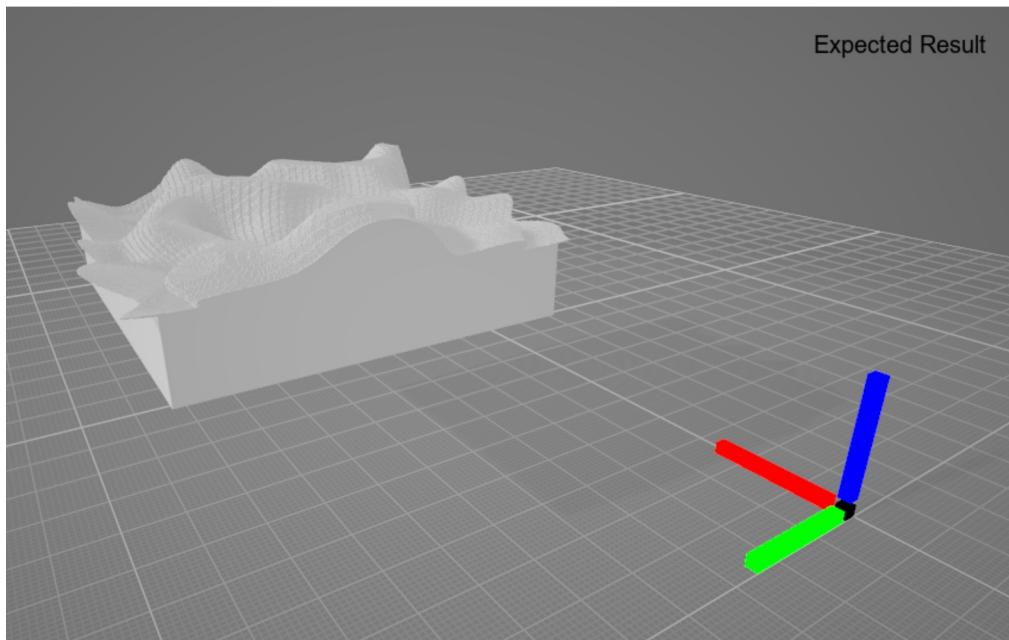
P_DPX_3204_02

Expected Result



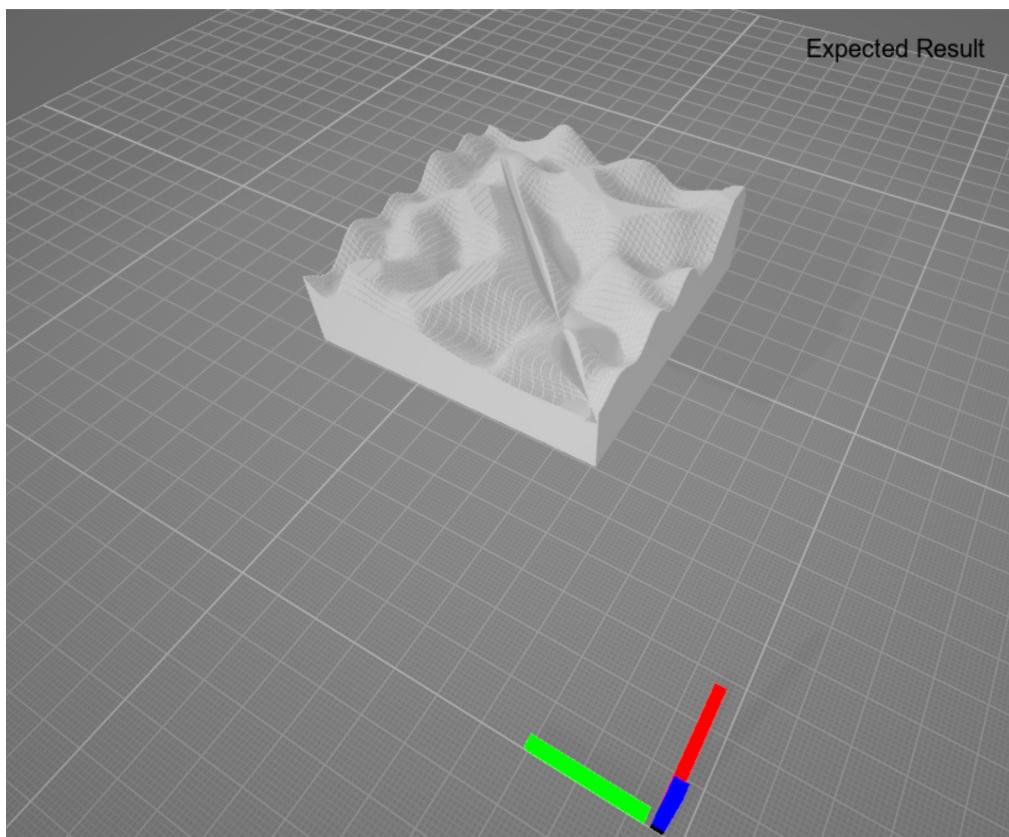
P_DPX_3204_03

Expected Result



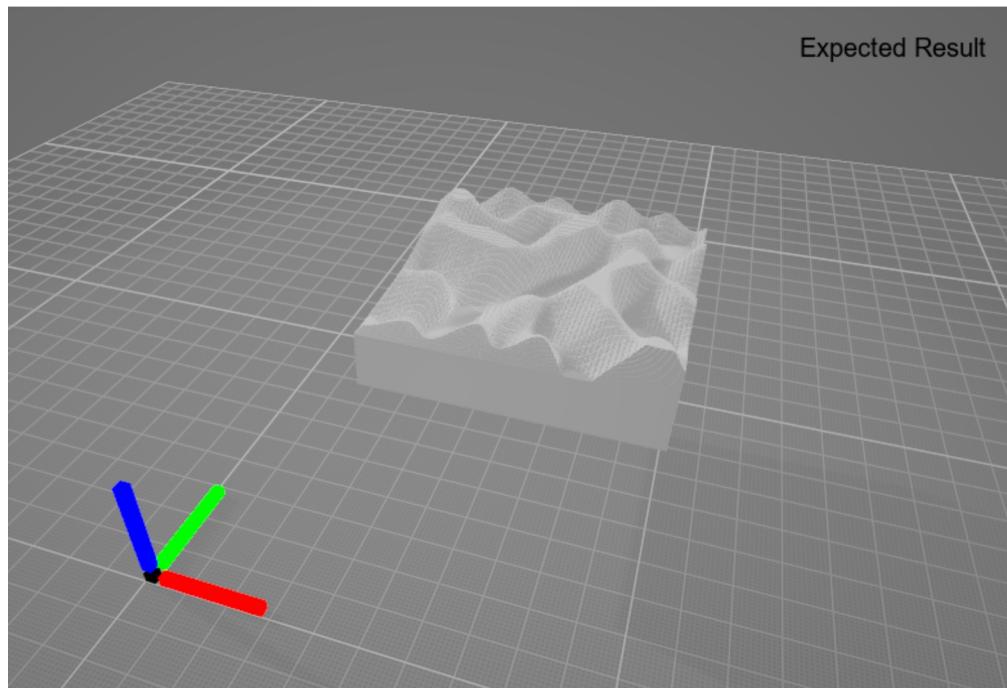
P_DPX_3204_04

Expected Result



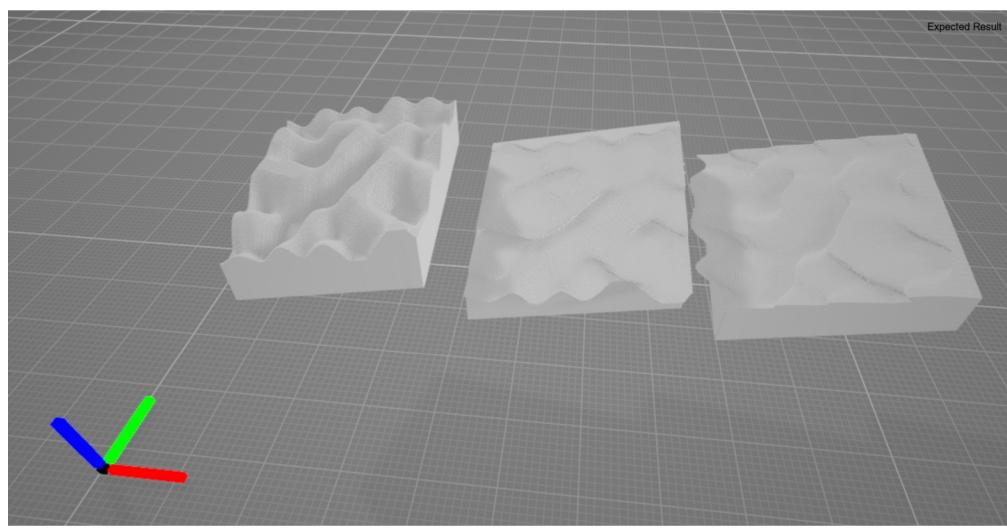
P_DPX_3204_05

Expected Result

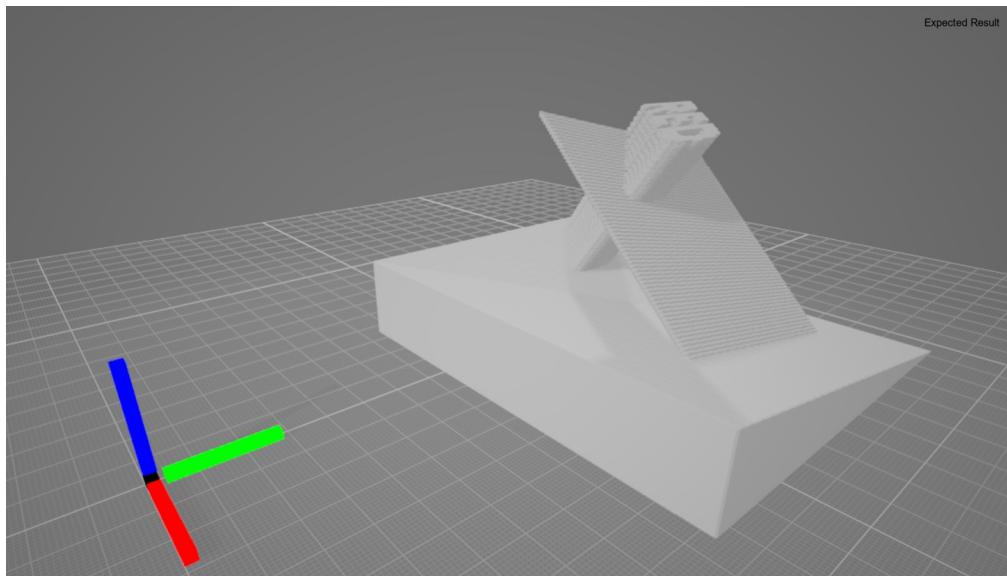


P_DPX_3204_06

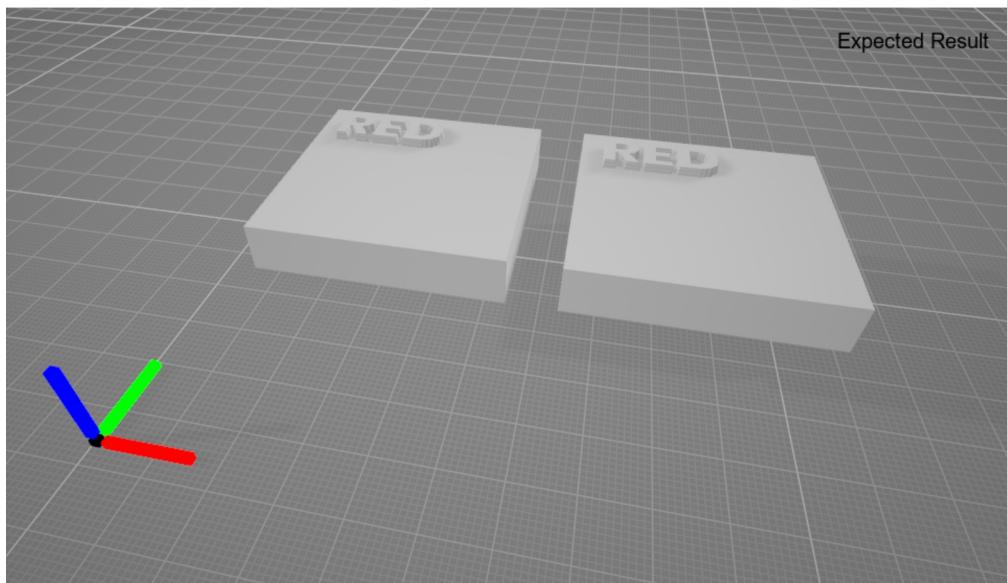
Expected Result



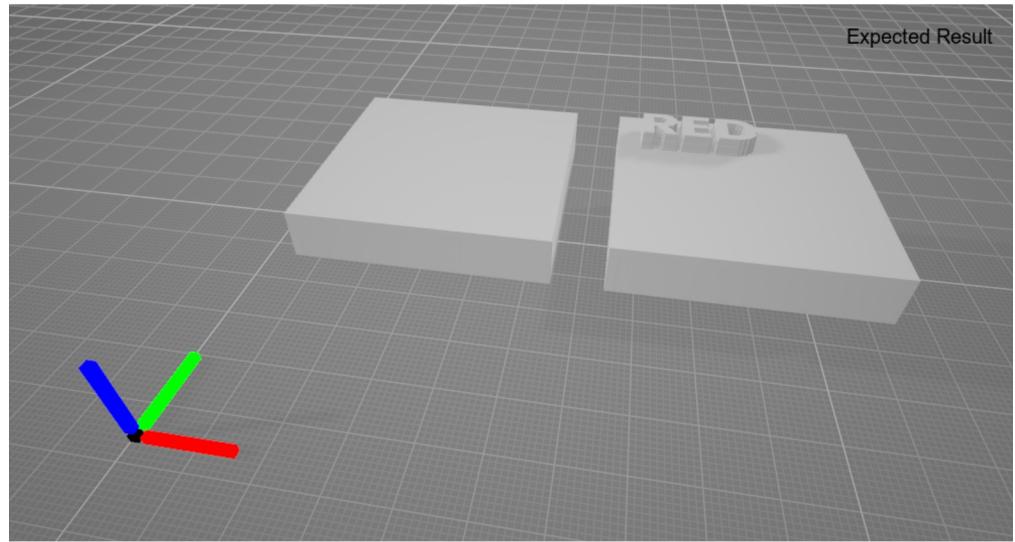
P_DPX_3204_07



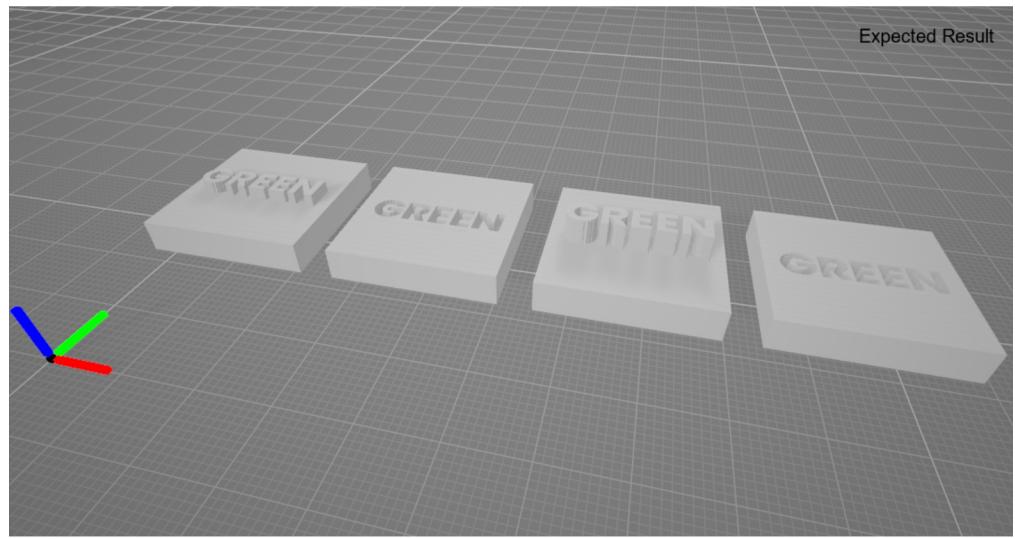
P_DPX_3204_08



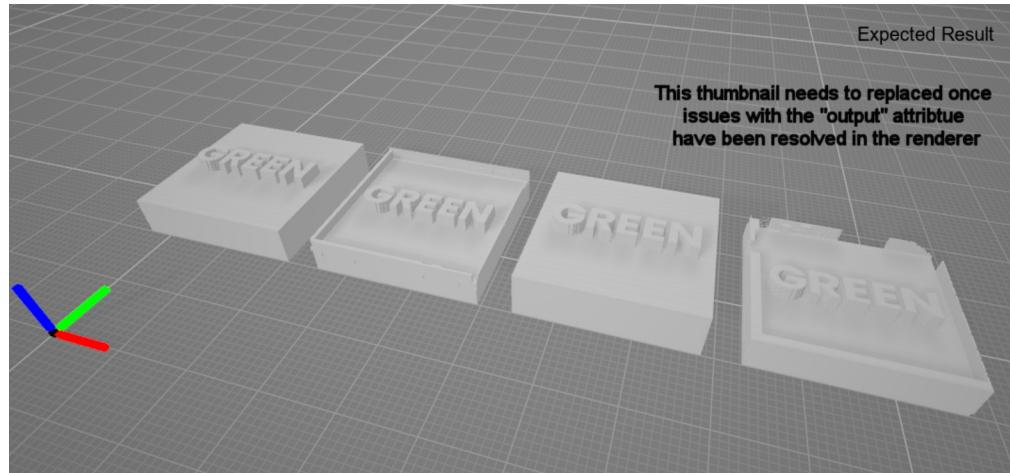
P_DPX_3206_01



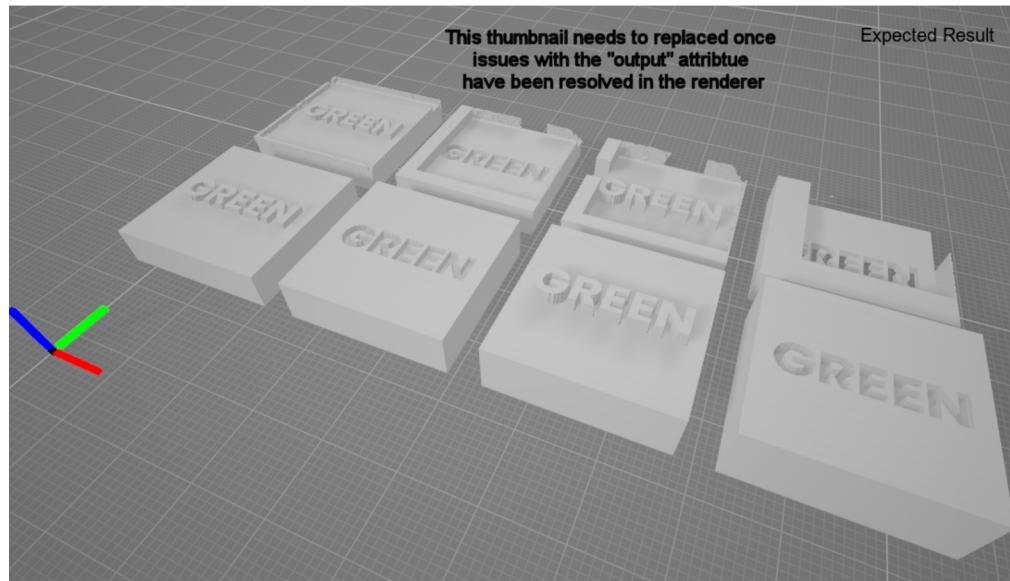
P_DPX_3206_02



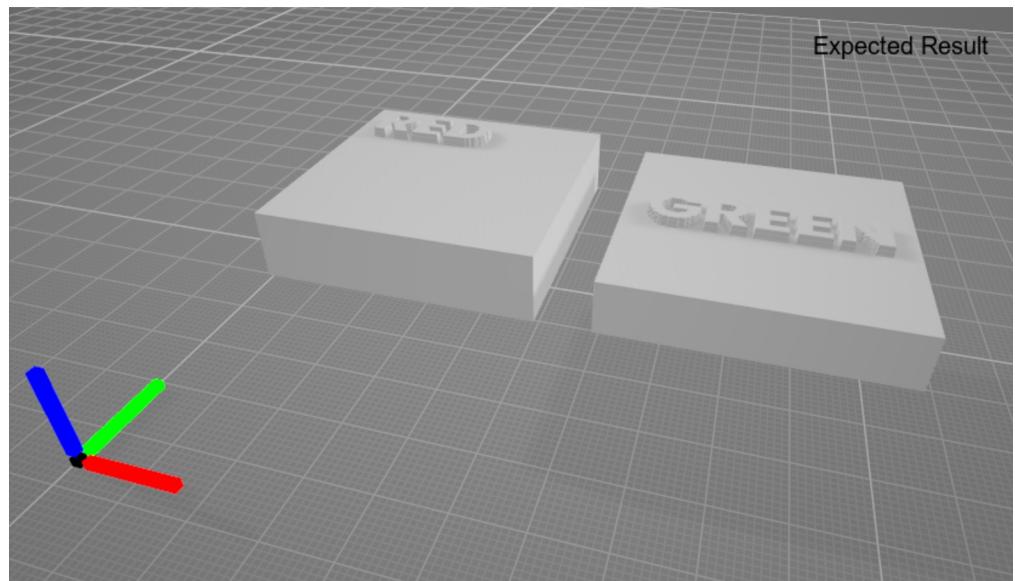
P_DPX_3206_03



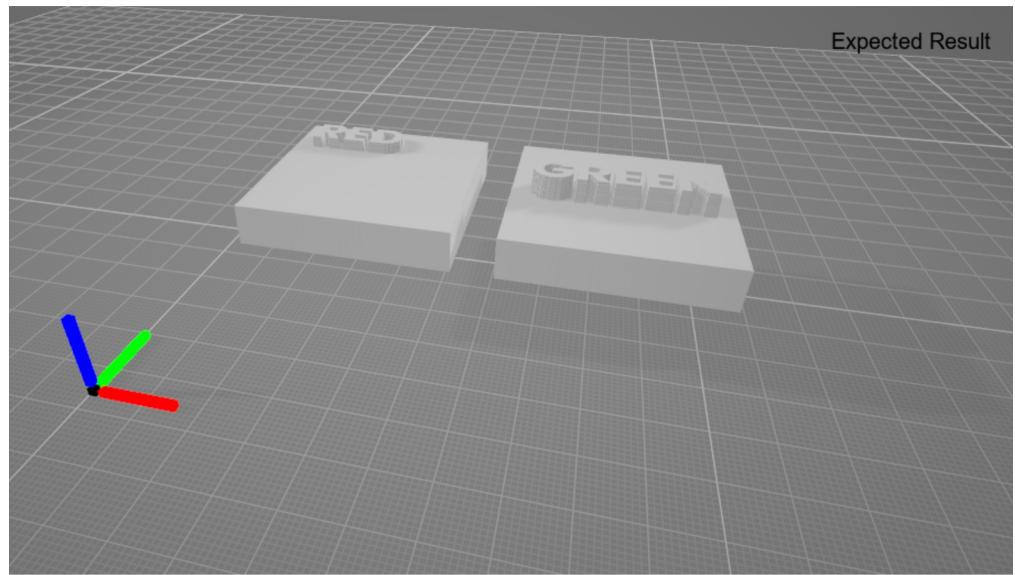
P_DPX_3206_04



P_DPX_3206_05



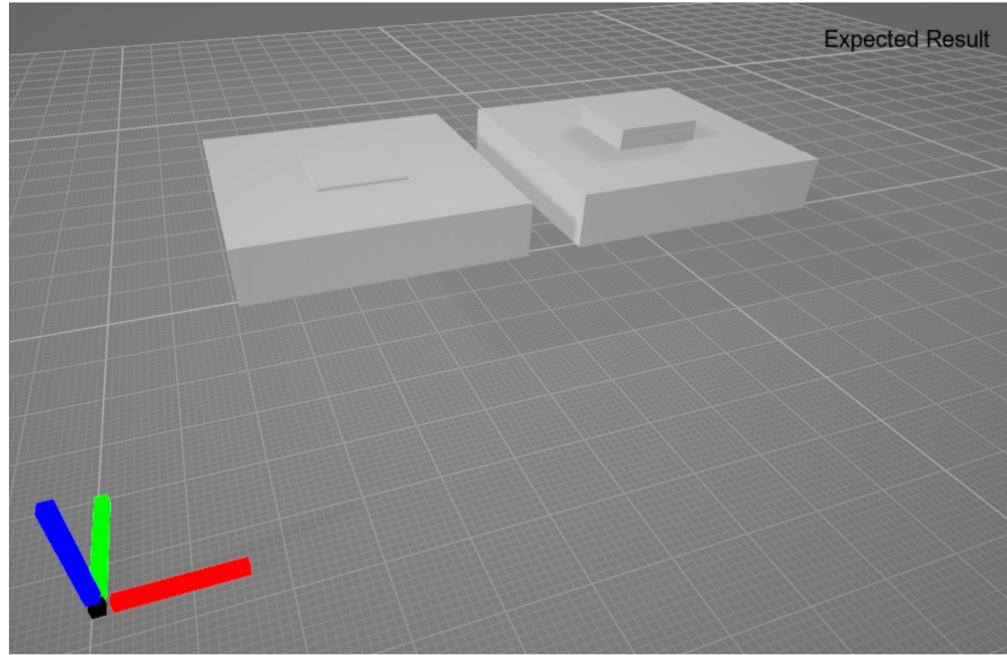
P_DPX_3206_06



P_DPX_3206_07

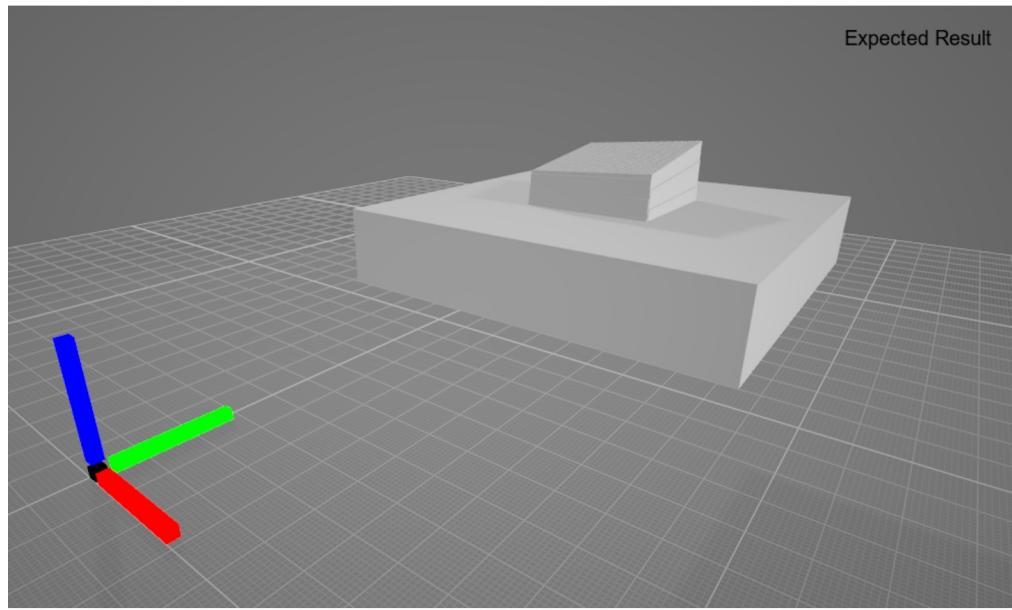


P_DPX_3206_08



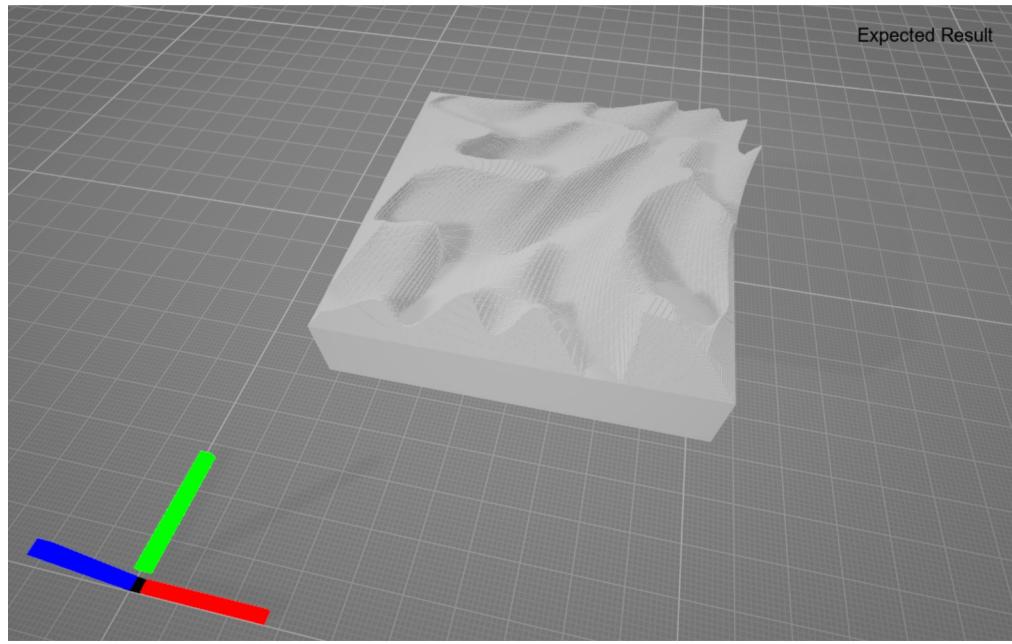
P_DPX_3208_01

Expected Result



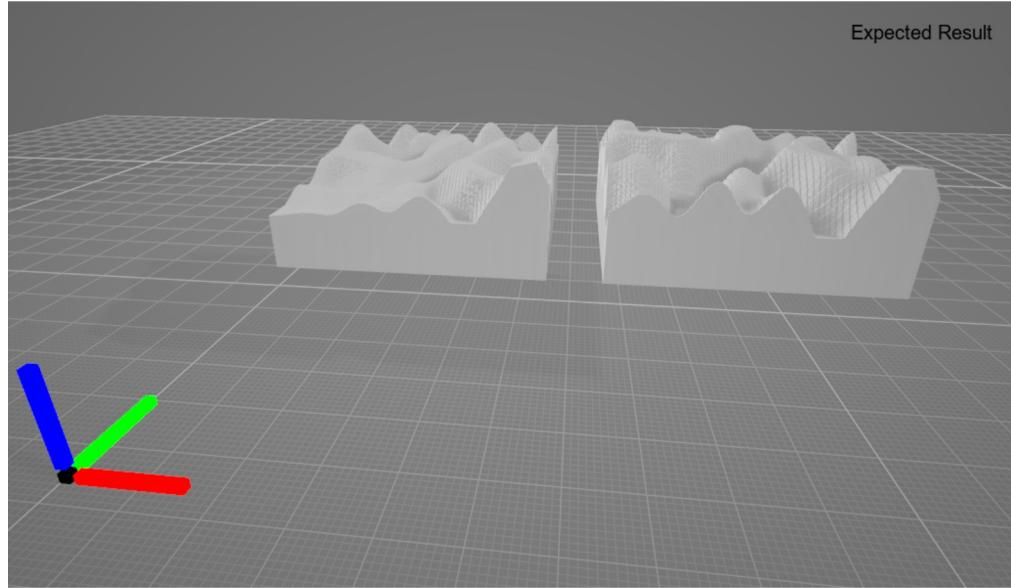
P_DPX_3208_02

Expected Result



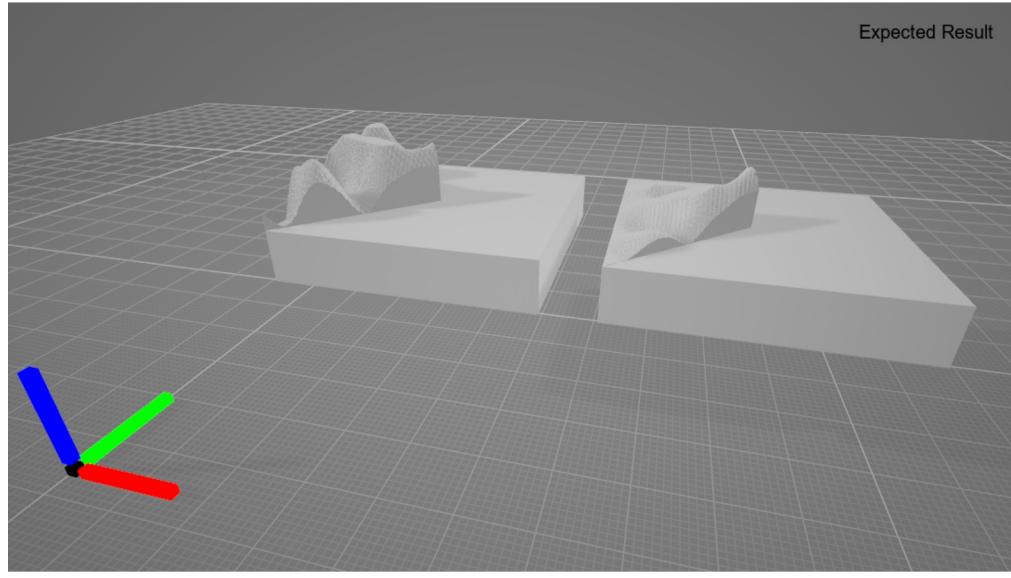
P_DPX_3208_03

Expected Result



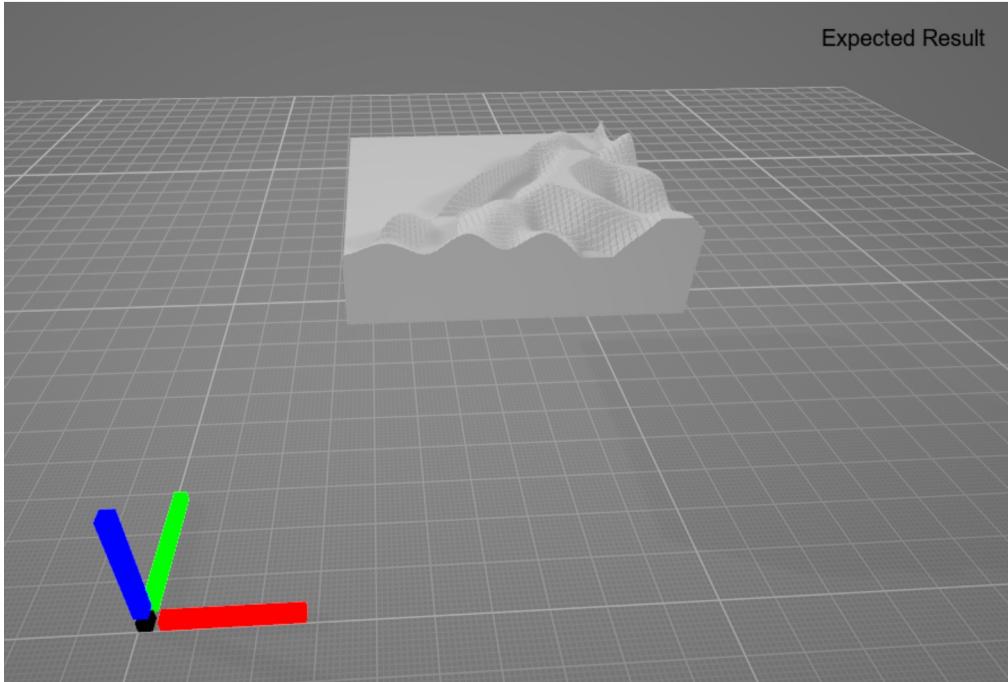
P_DPX_3208_04

Expected Result



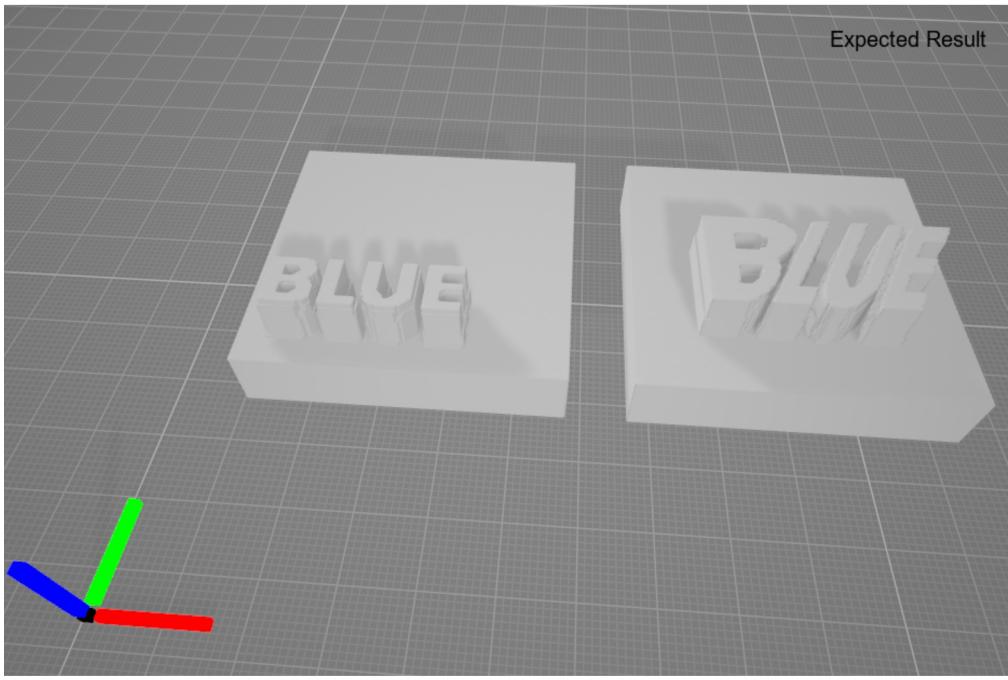
P_DPX_3208_05

Expected Result

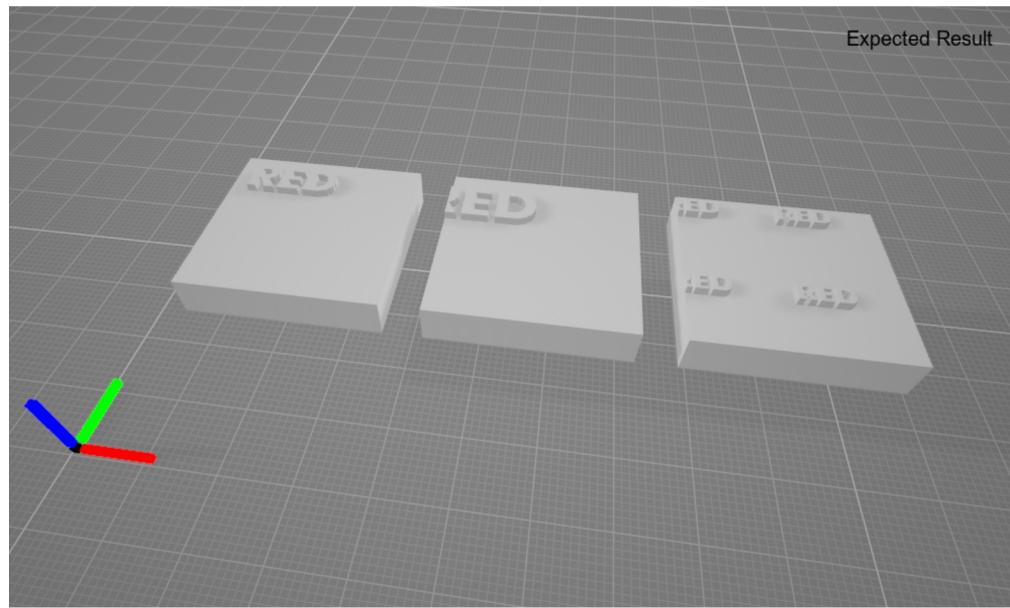


P_DPX_3208_06

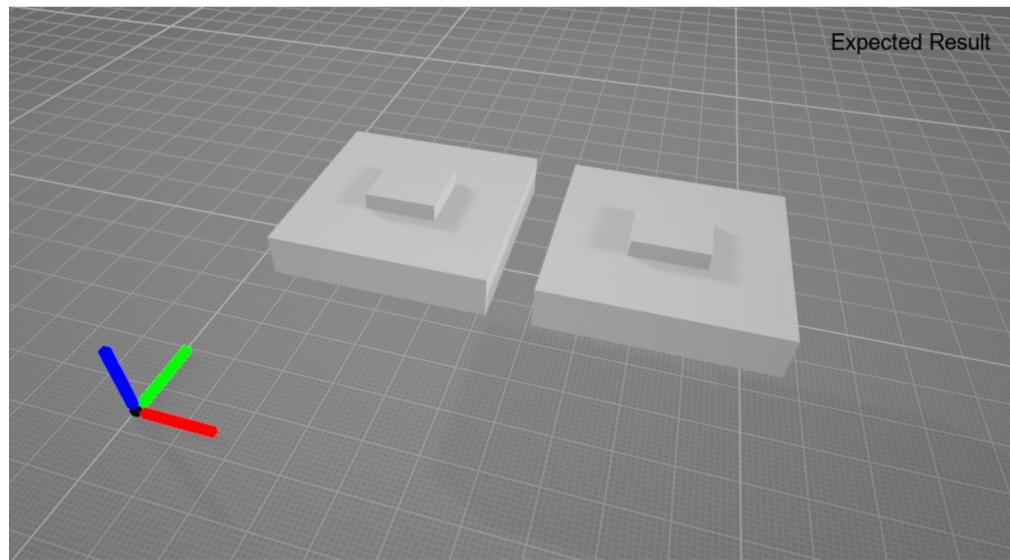
Expected Result



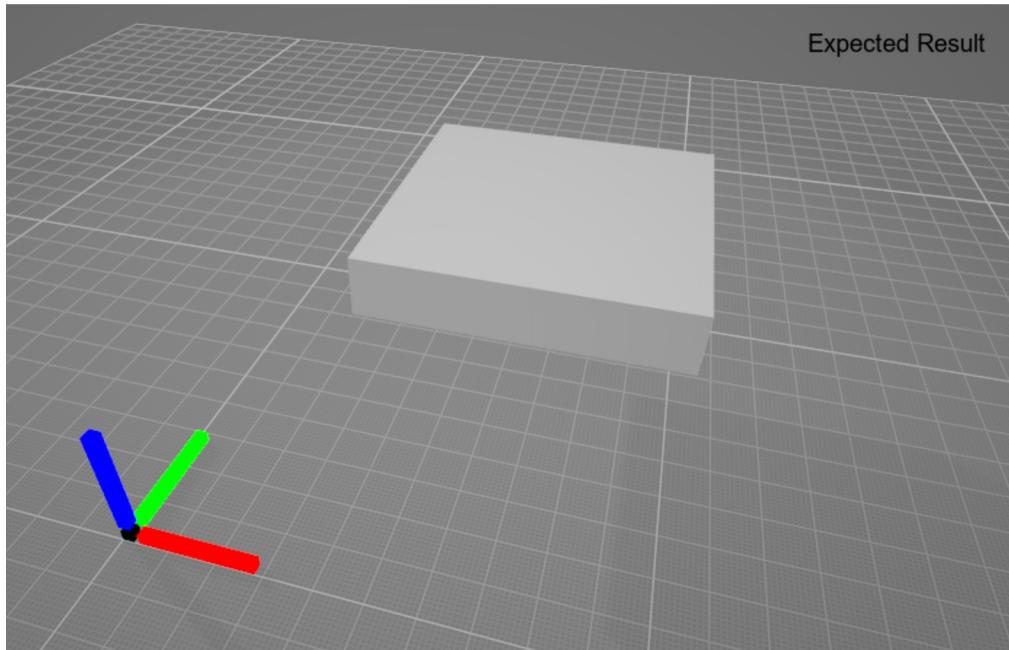
P_DPX_3208_07



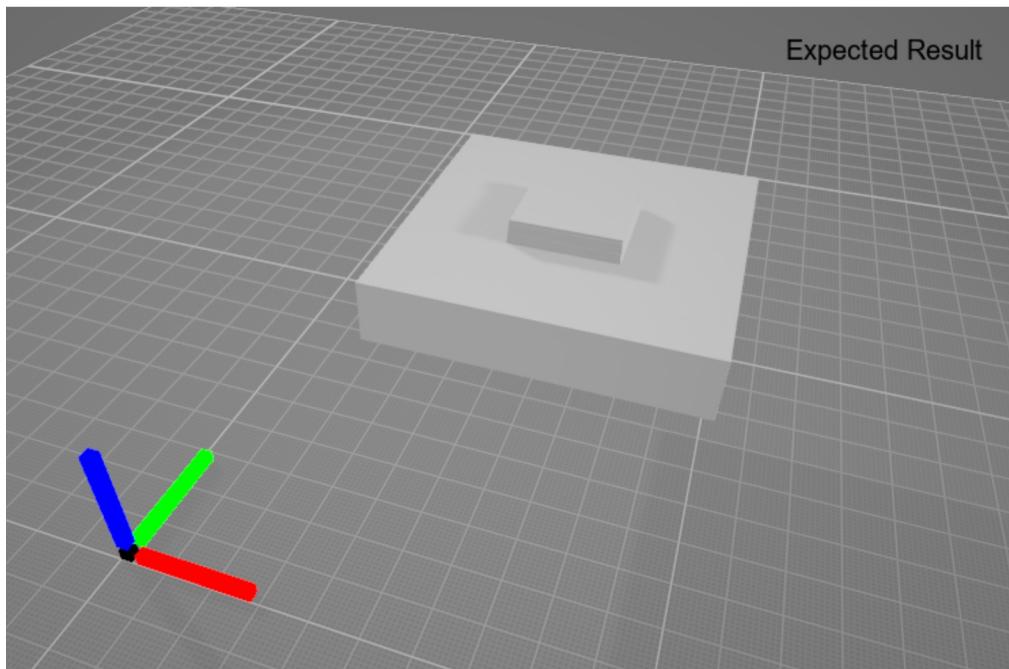
P_DPX_3208_08



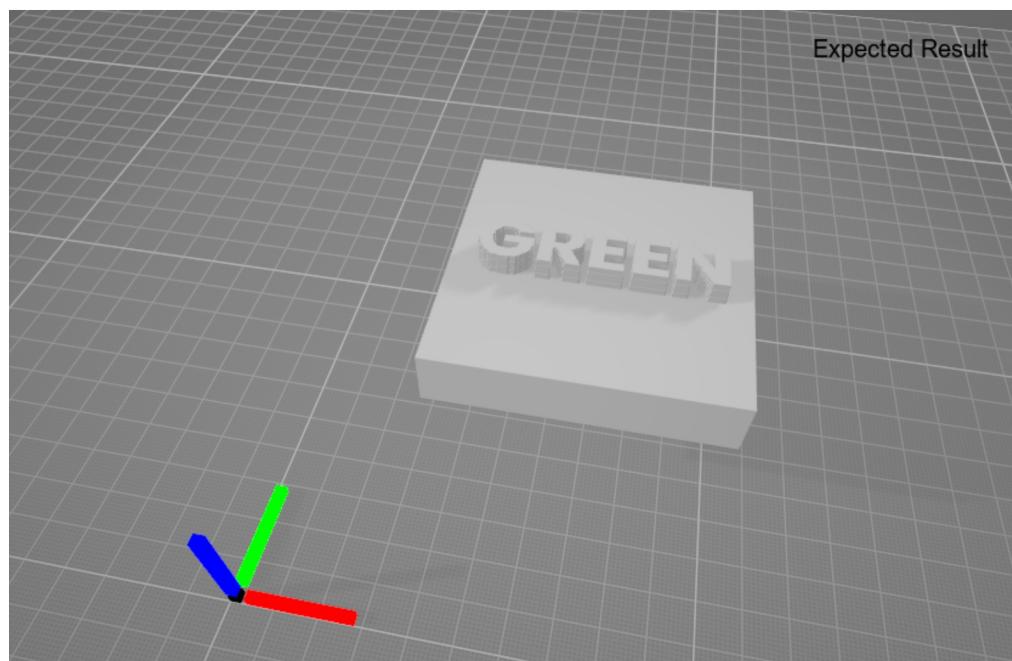
P_DPX_3210_01



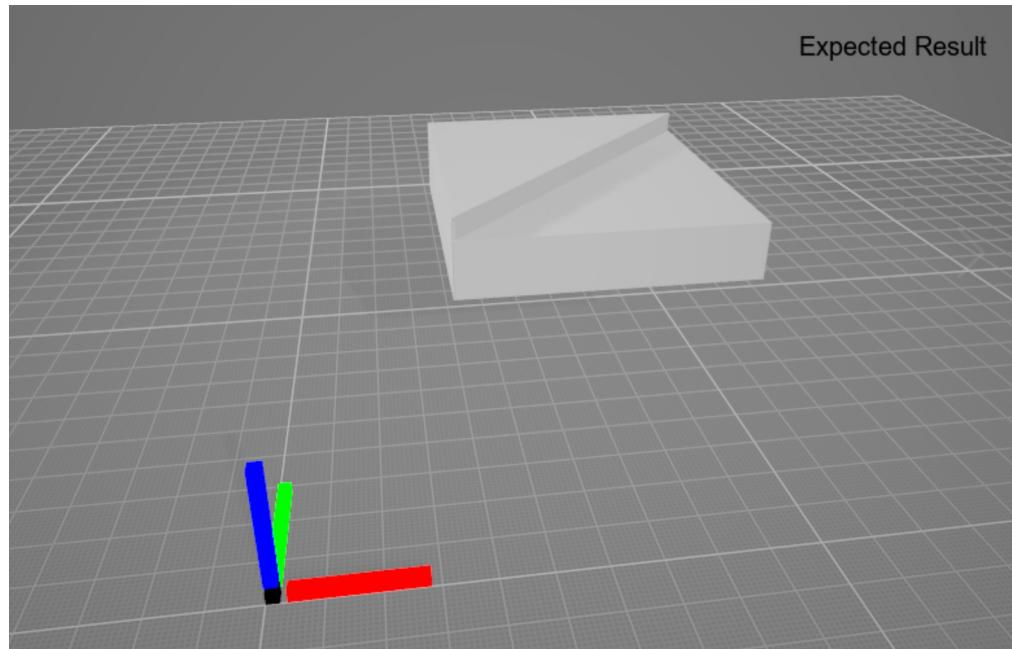
P_DPX_3212_01



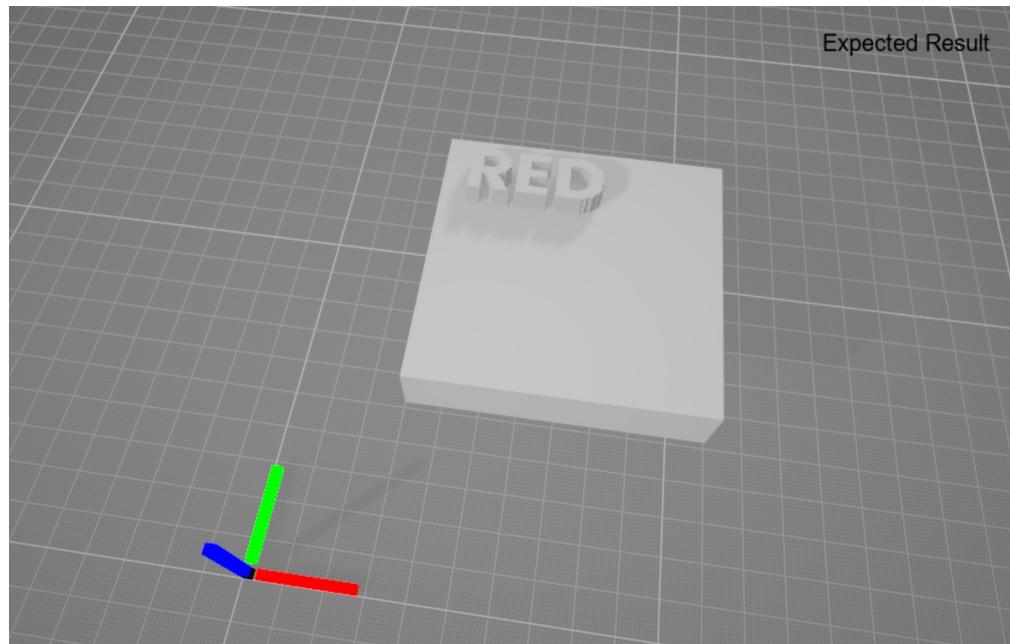
P_DPX_3212_02



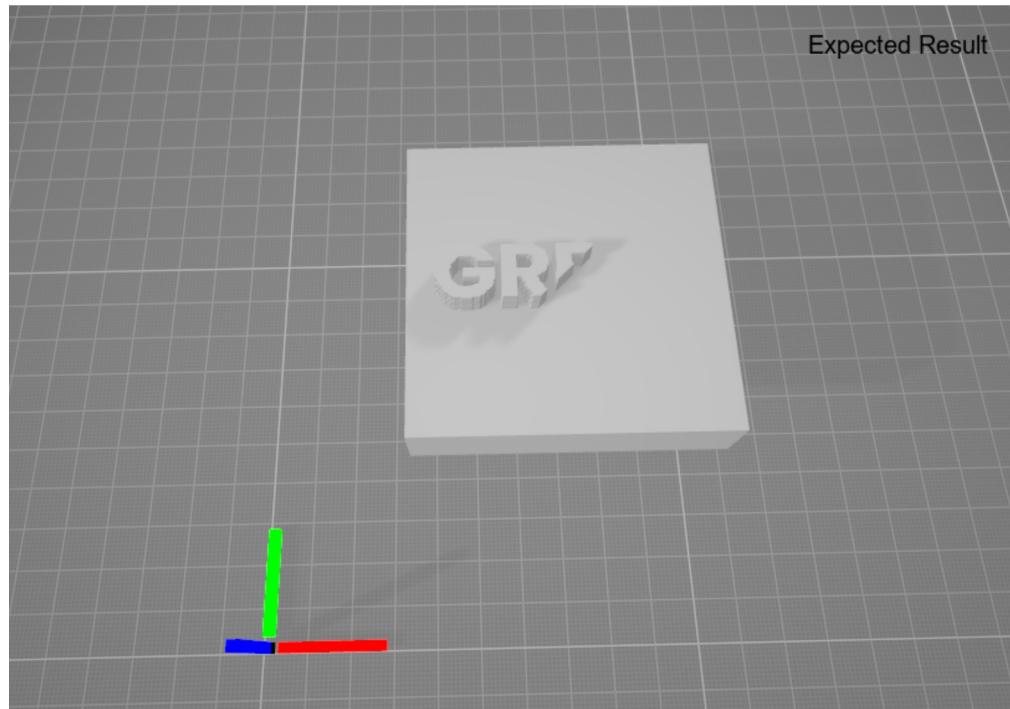
P_DPX_3212_03



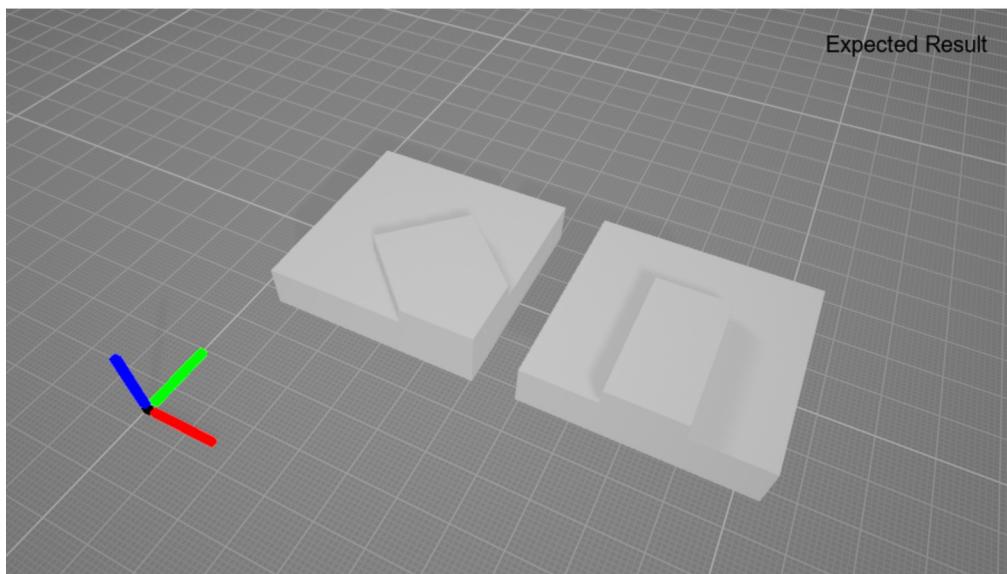
P_DPX_3212_05



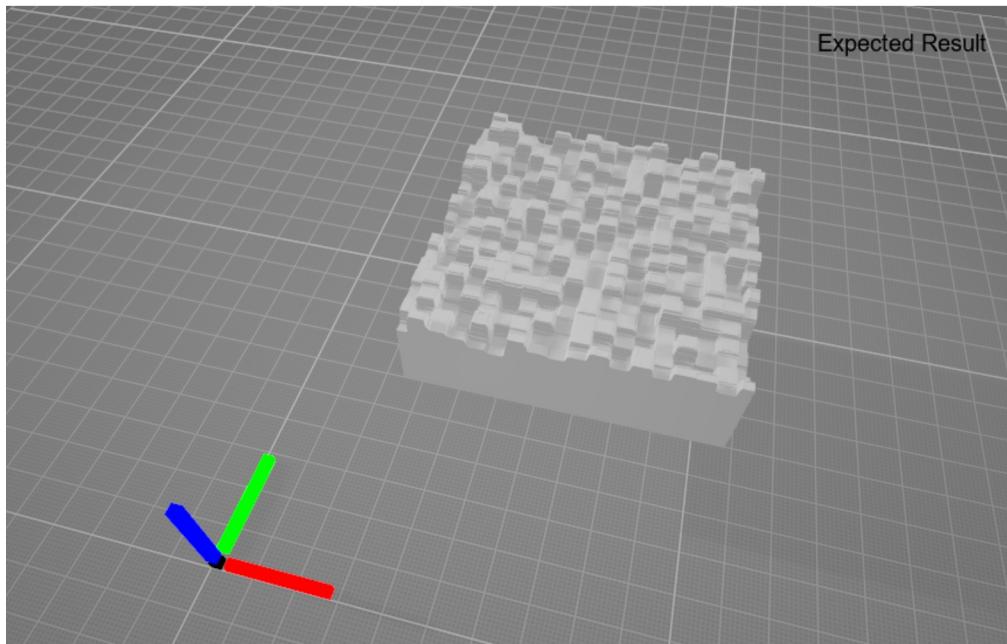
P_DPX_3214_01



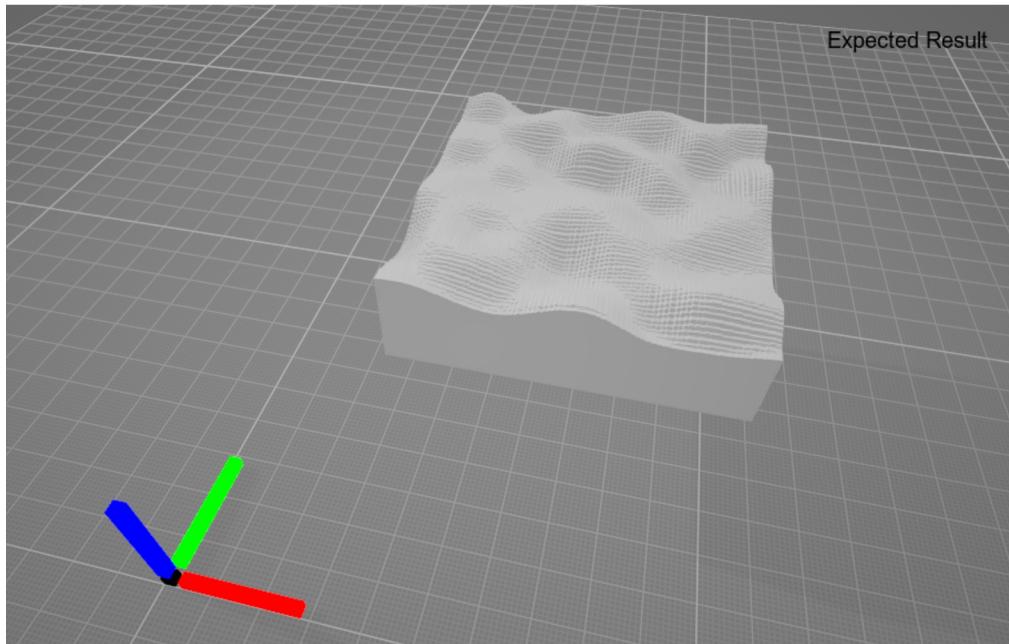
P_DPX_3214_02



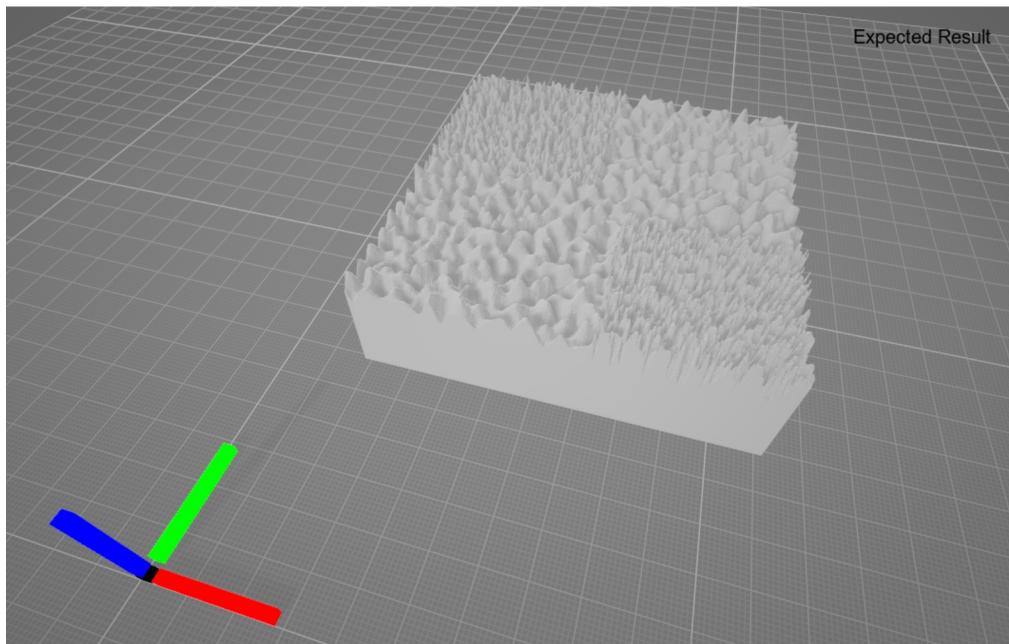
P_DPX_3214_03



P_DPX_3216_01

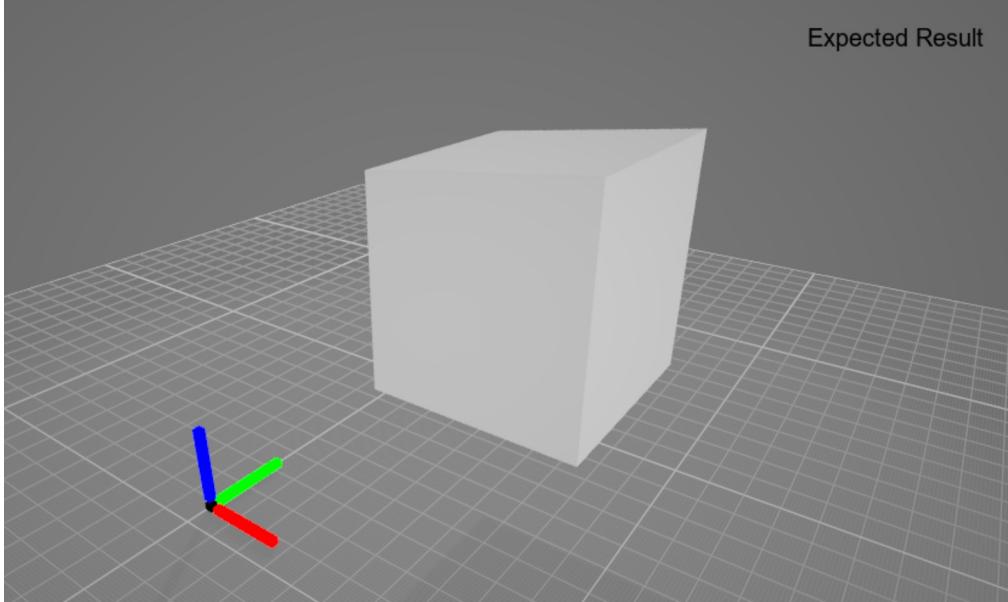


P_DPX_3216_02



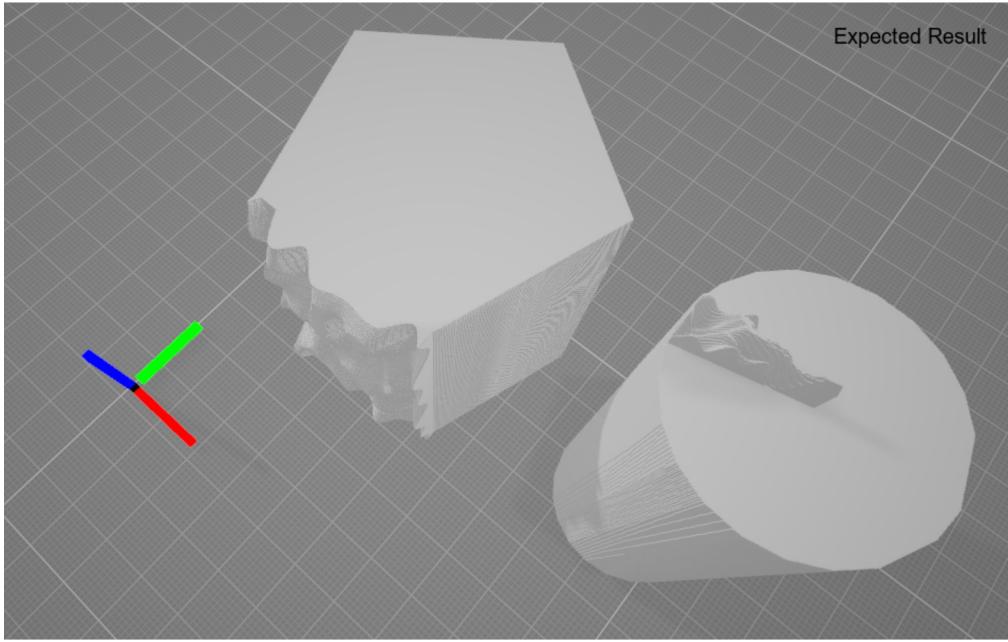
P_DPX_3216_03

Expected Result

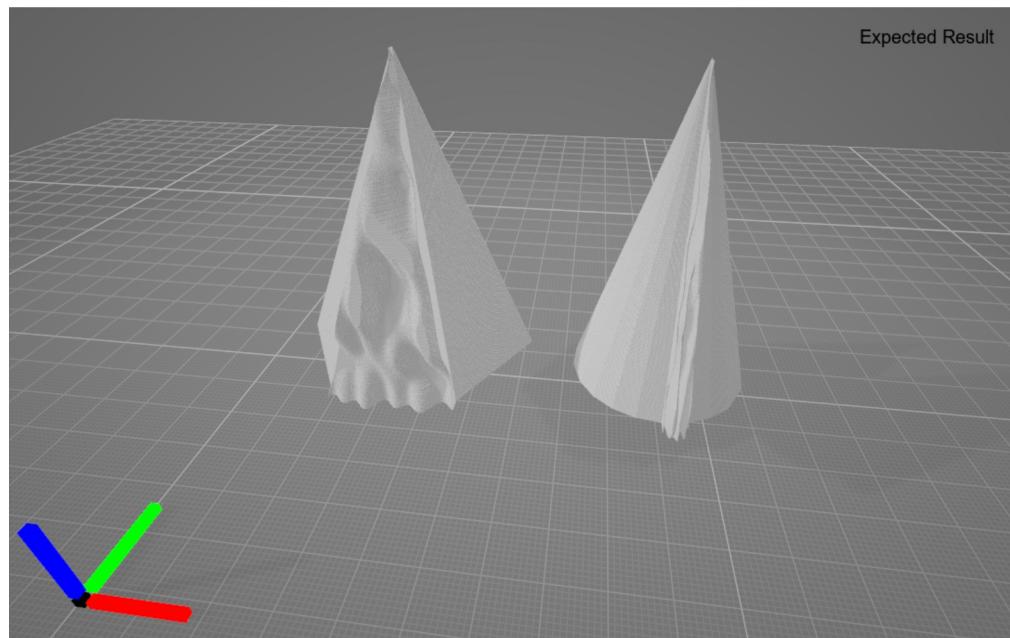


P_DPX_3216_04

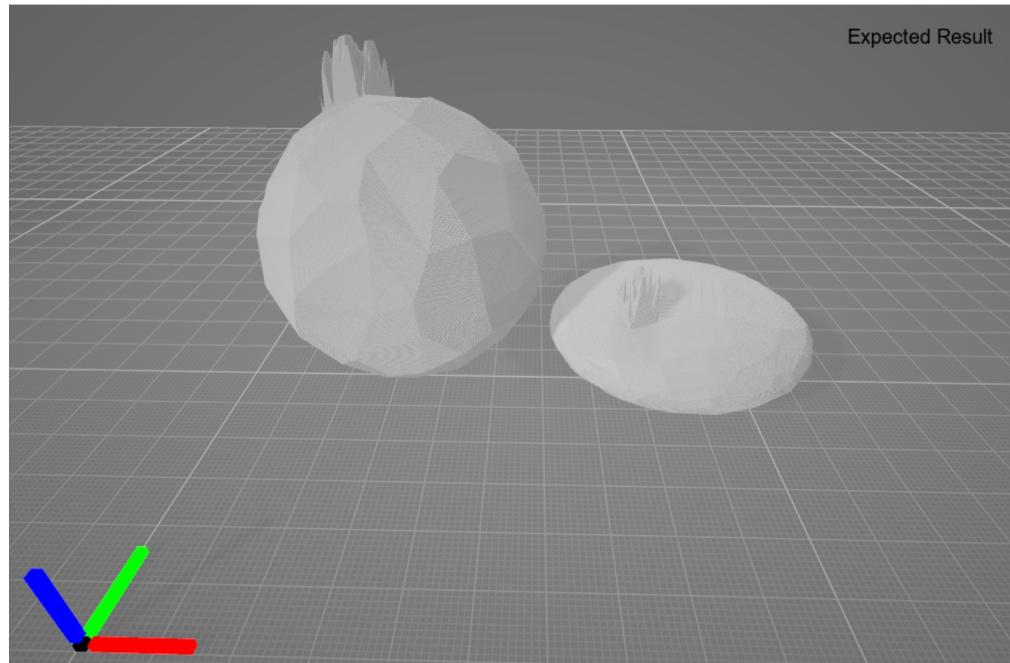
Expected Result



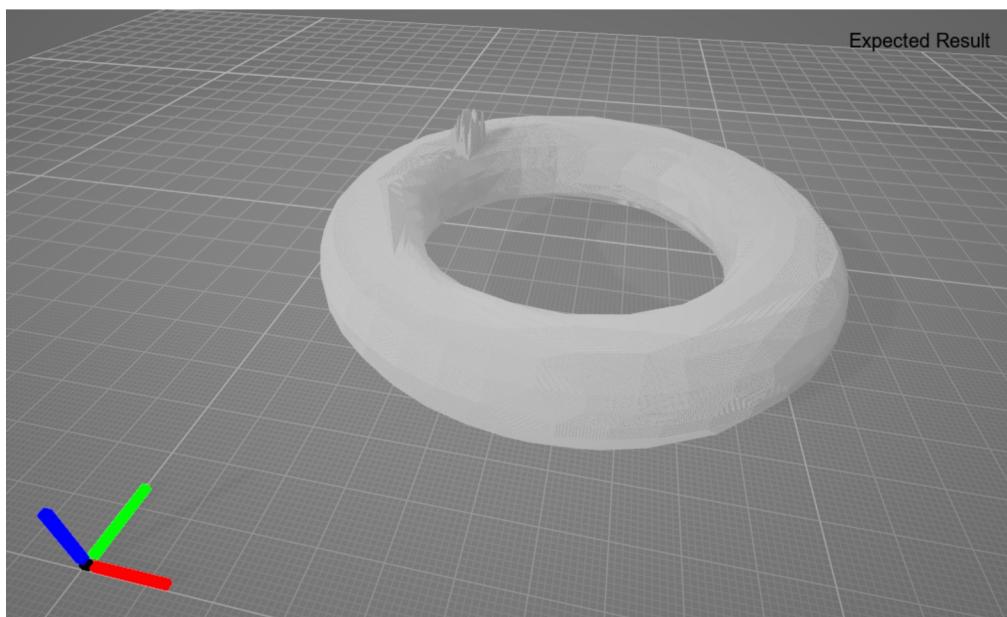
P_DPX_3218_01



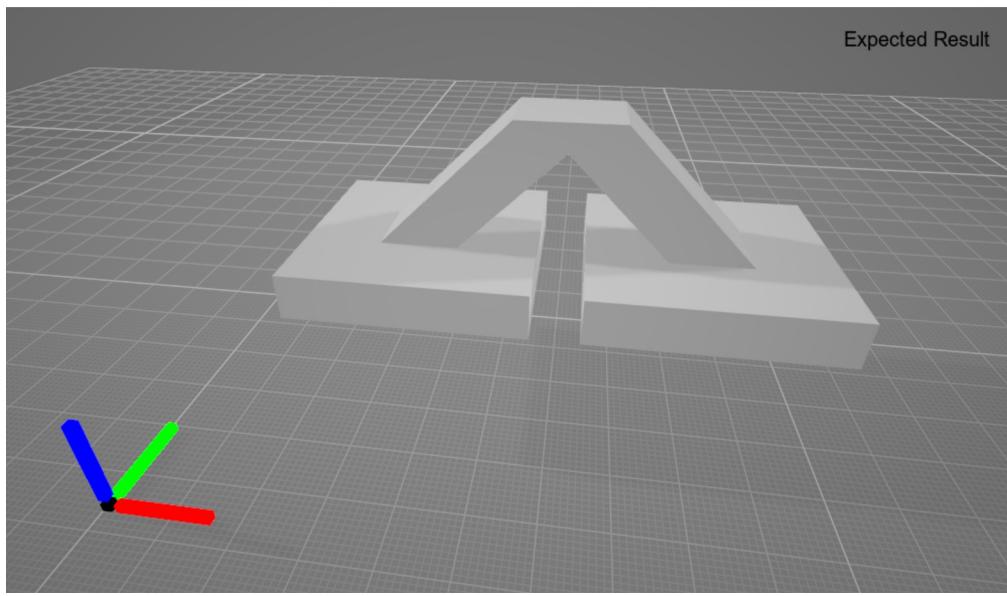
P_DPX_3218_02



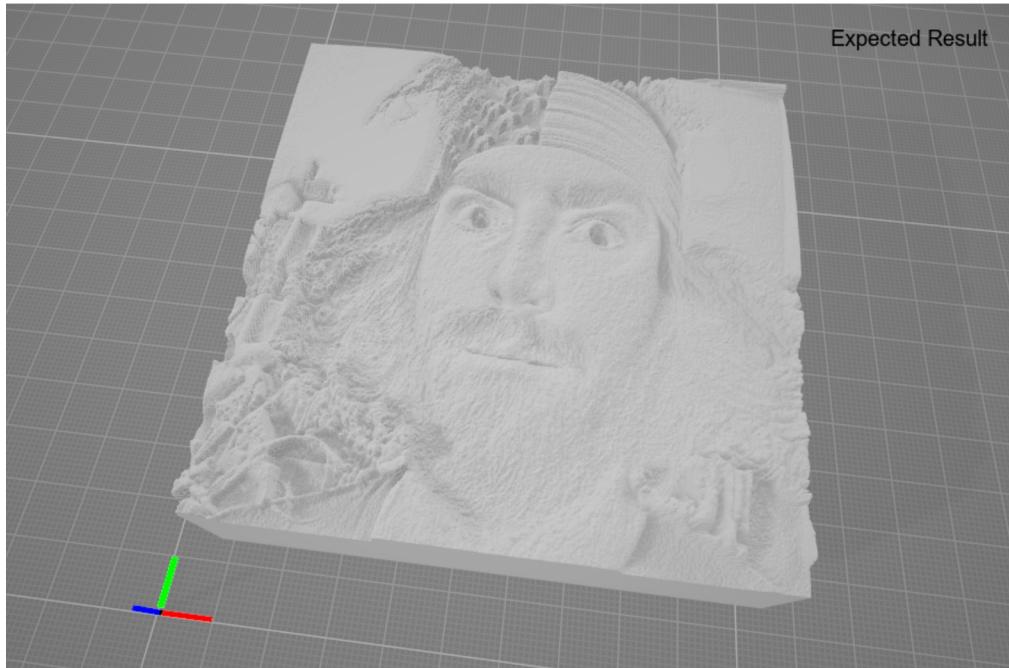
P_DPX_3218_03



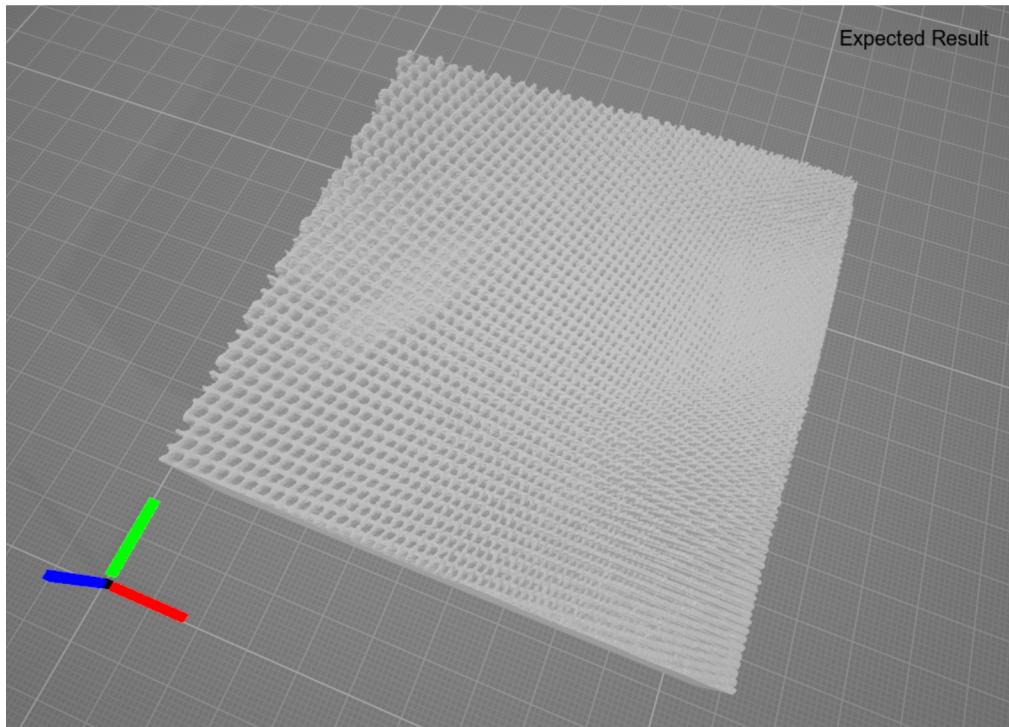
P_DPX_3218_04



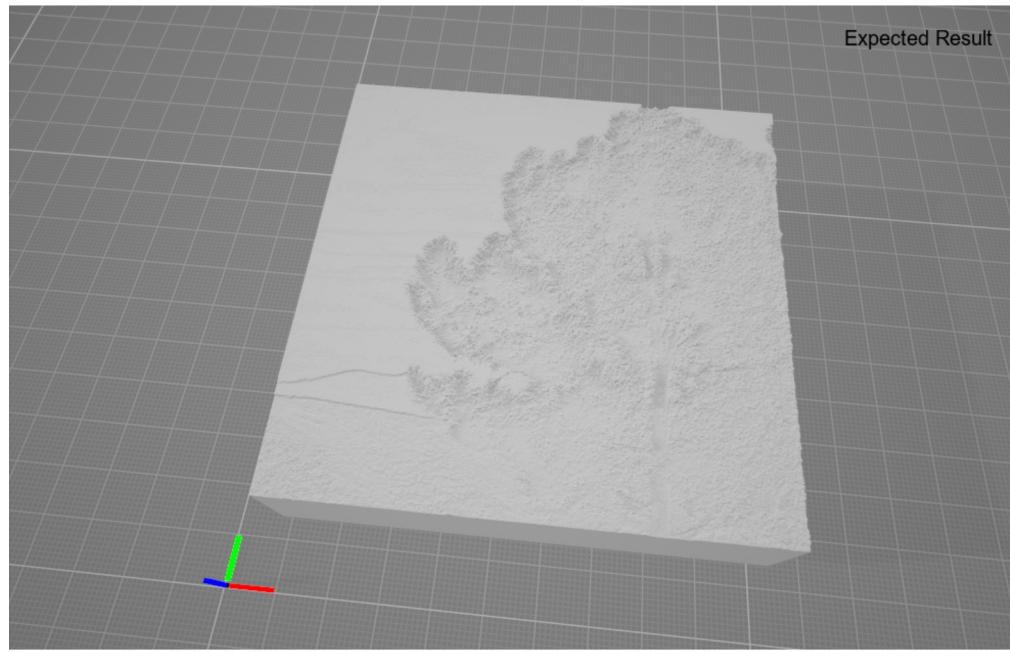
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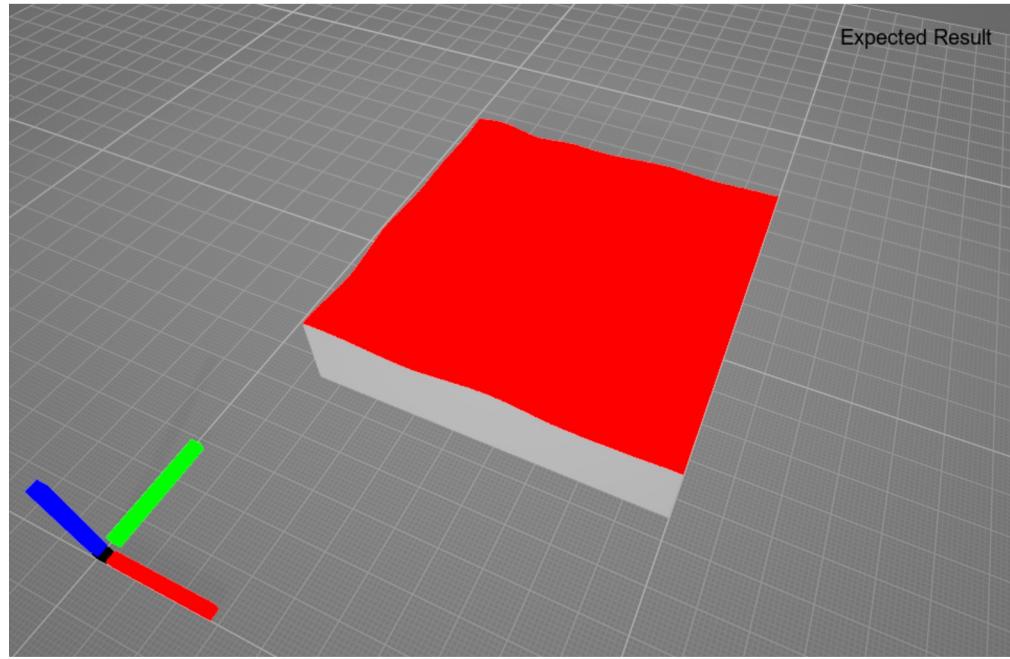
P_DPX_3220_01



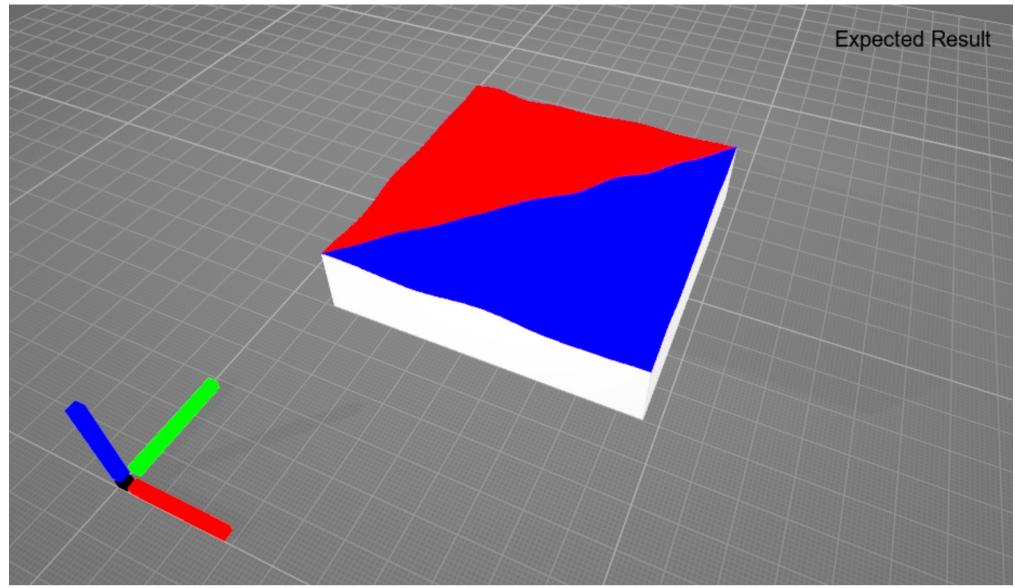
P_DPX_3220_02



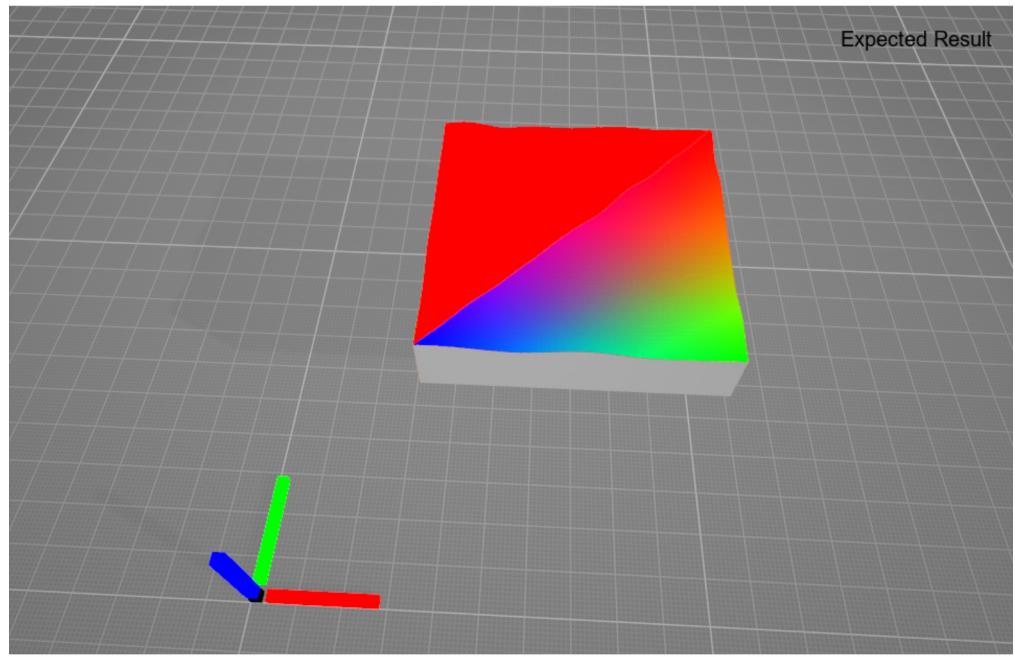
P_DPX_3220_03



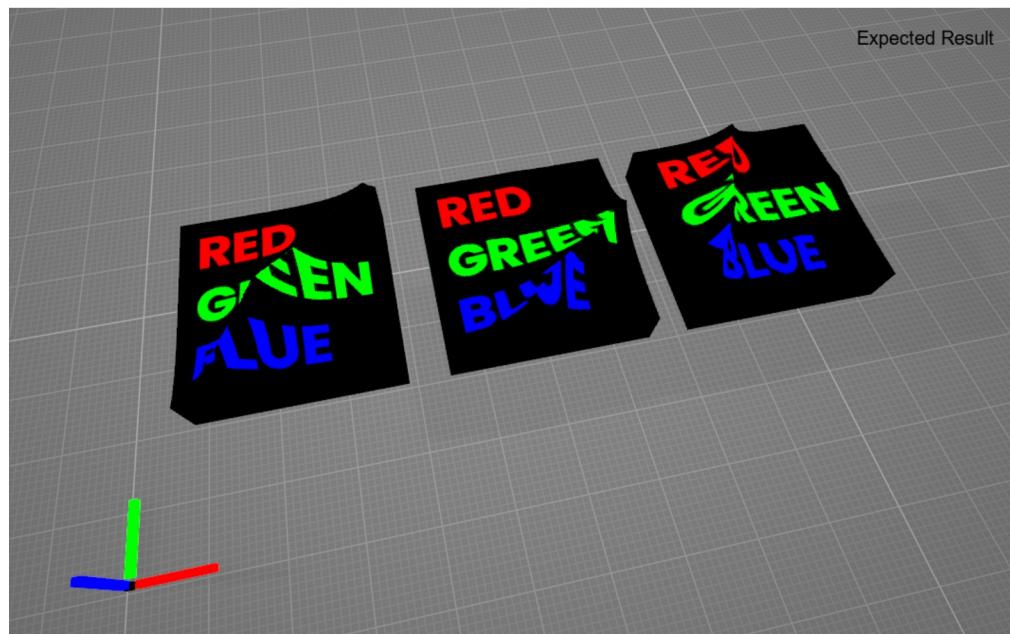
P_DPX_3222_01_material



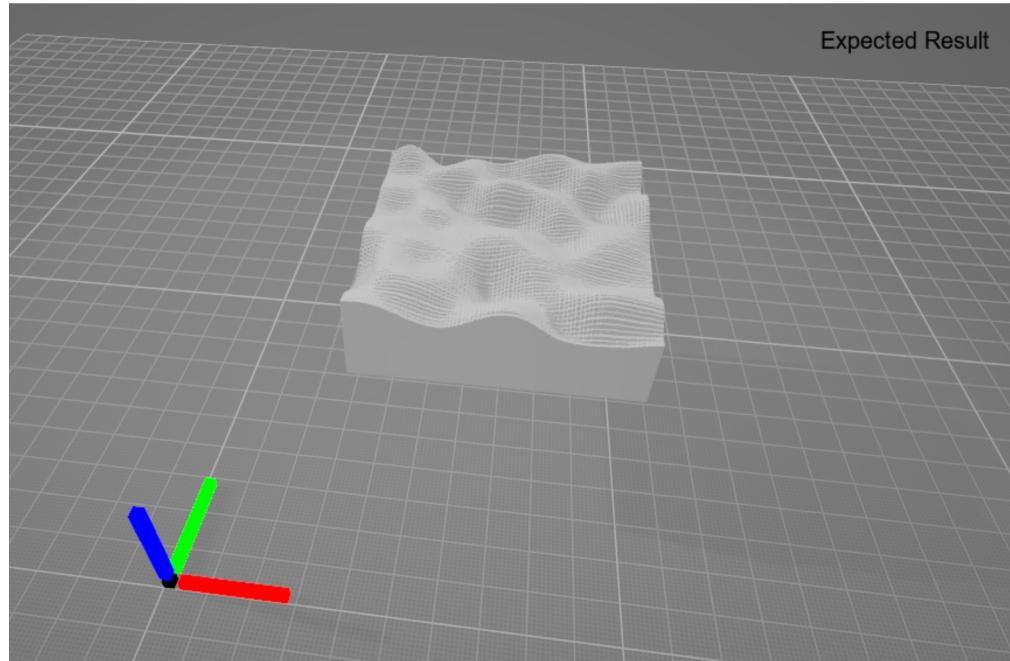
P_DPX_3222_02_material



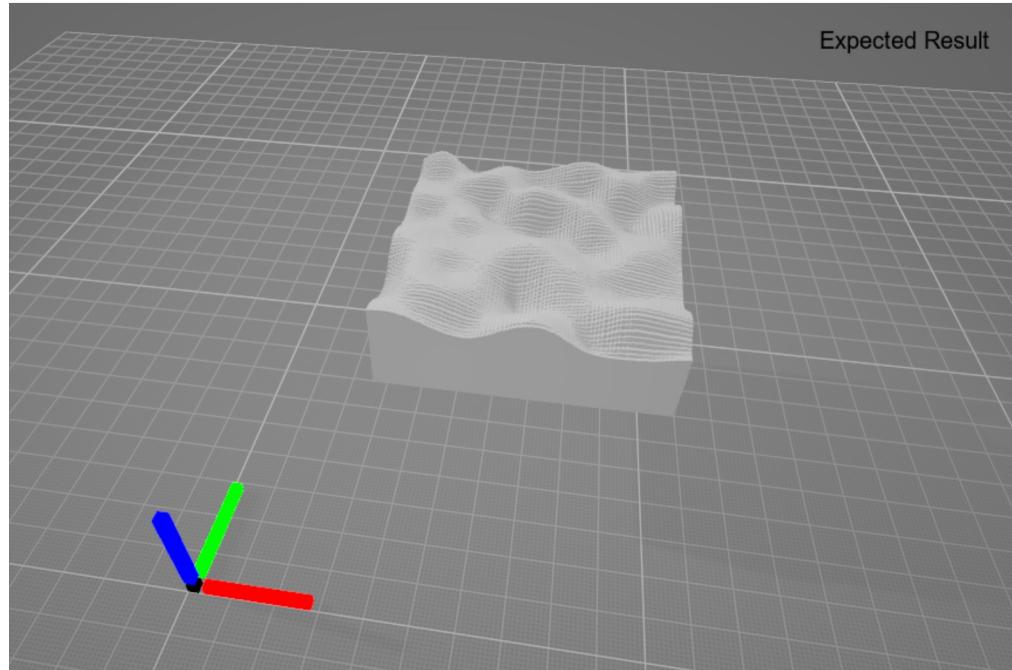
P_DPX_3222_03_material



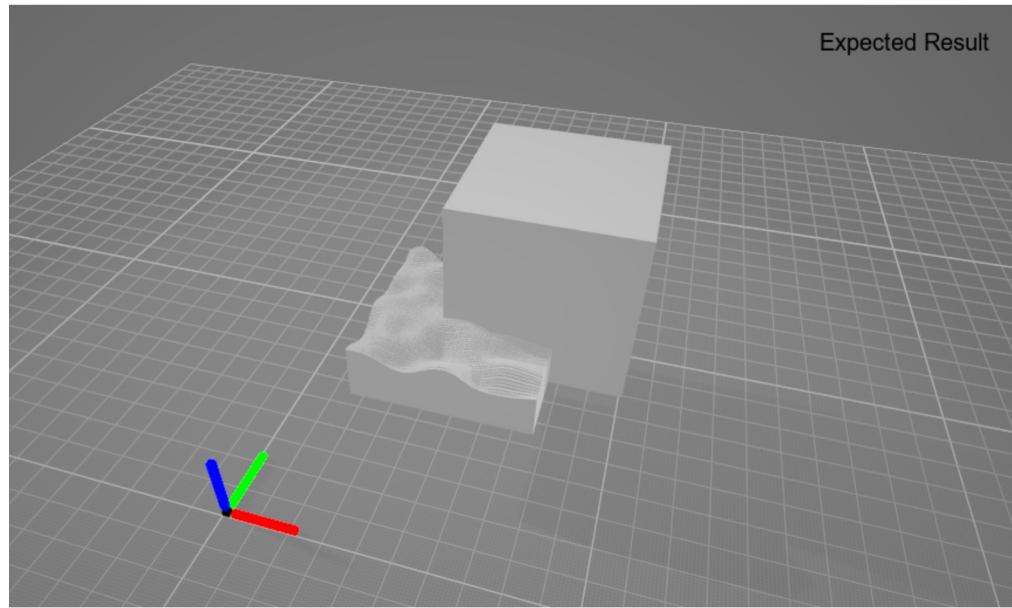
P_DPX_3222_04_material



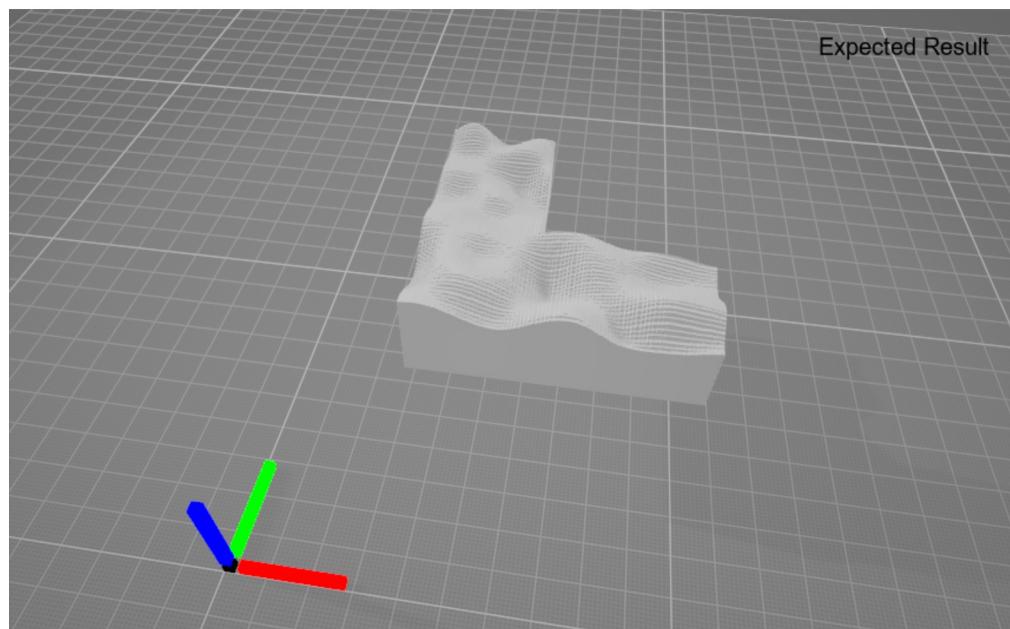
P_DPX_3224_01_production



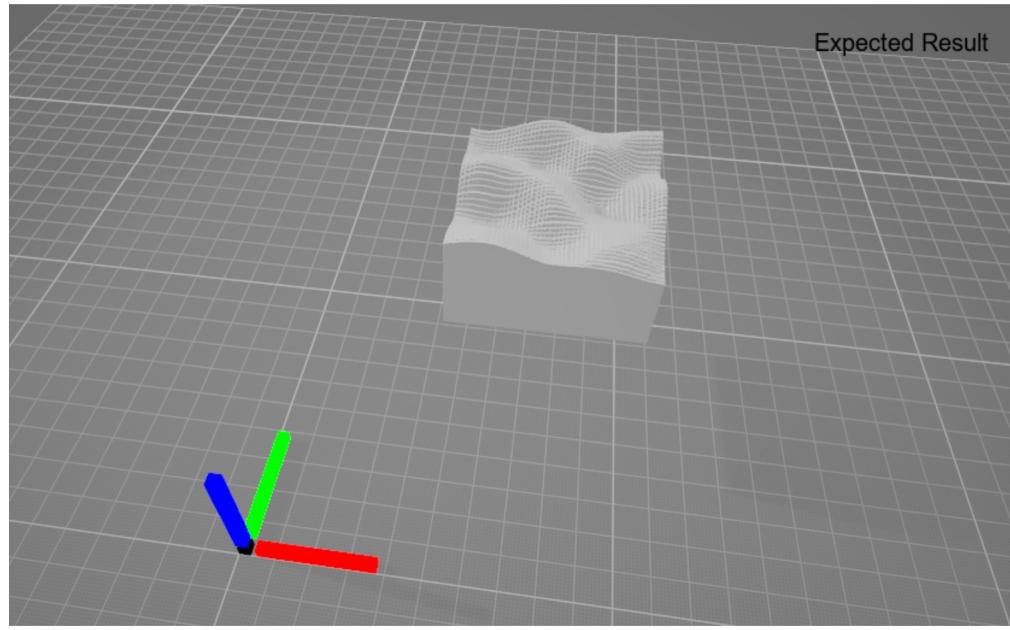
P_DPX_3224_02_production



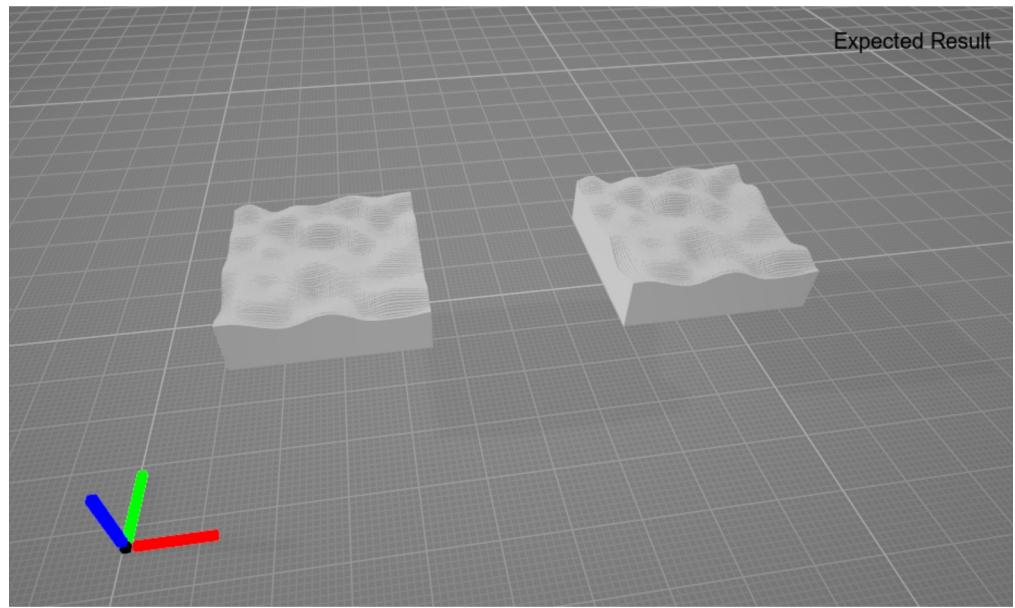
P_DPX_3226_01_boolean



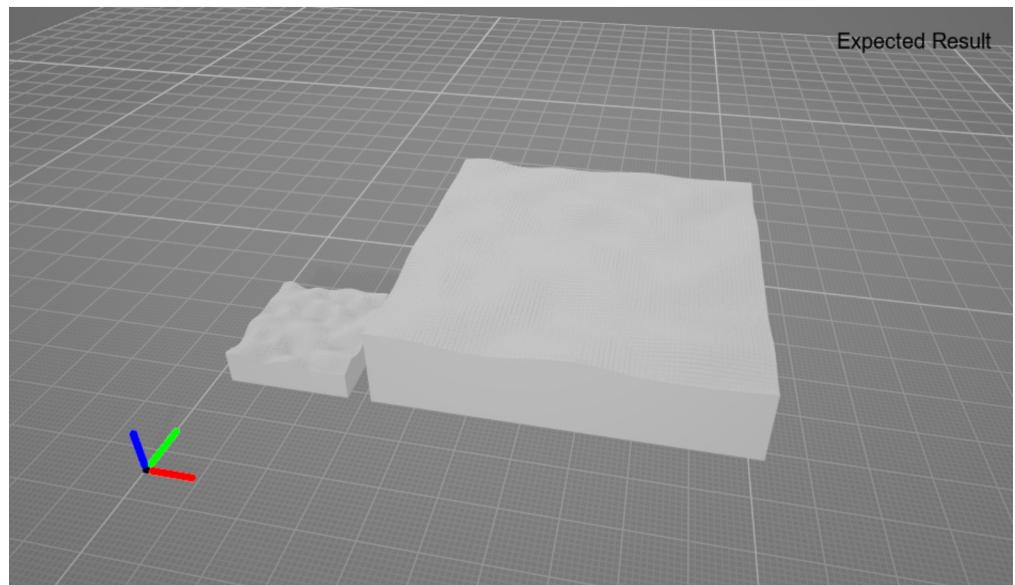
P_DPX_3226_02_boolean



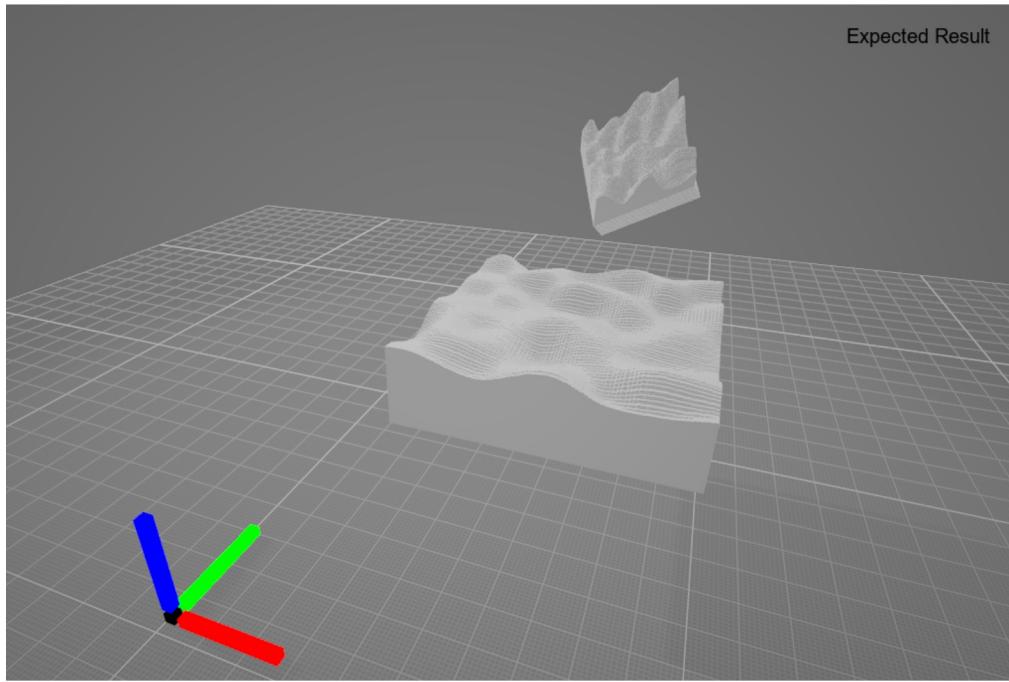
P_DPX_3226_03_boolean



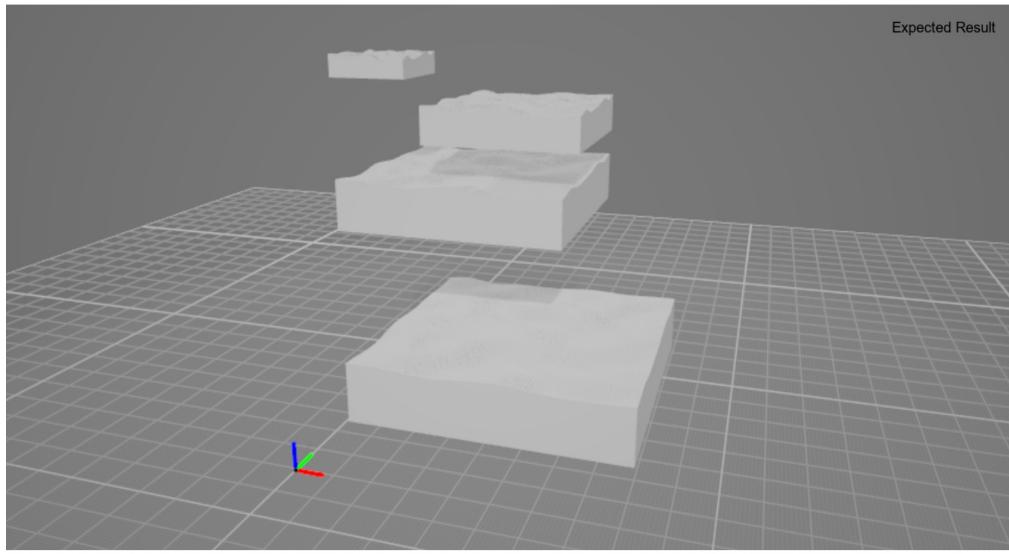
P_DPX_3228_01



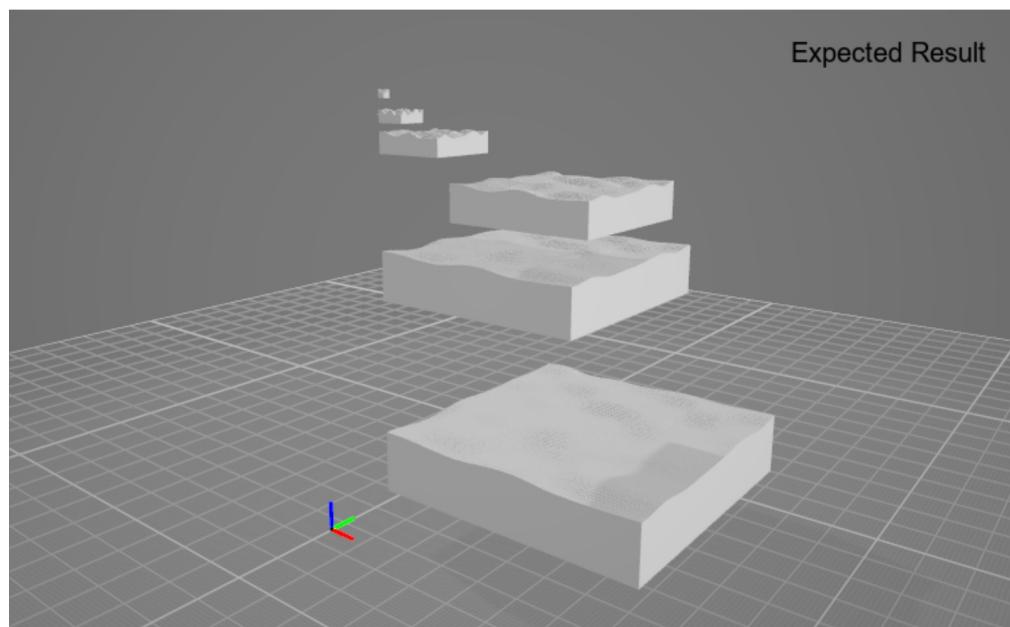
P_DPX_3228_02



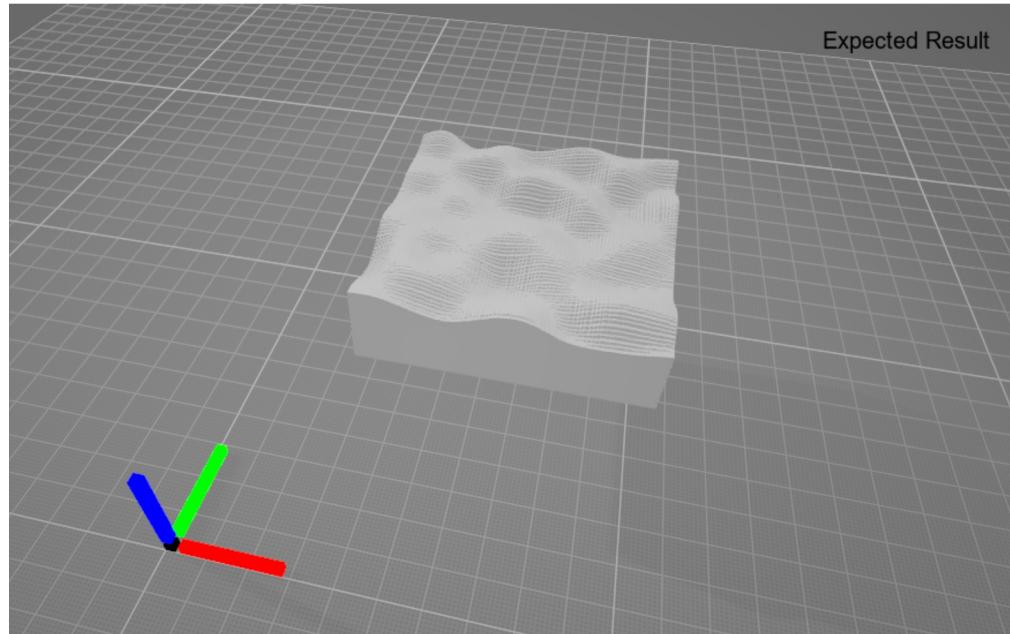
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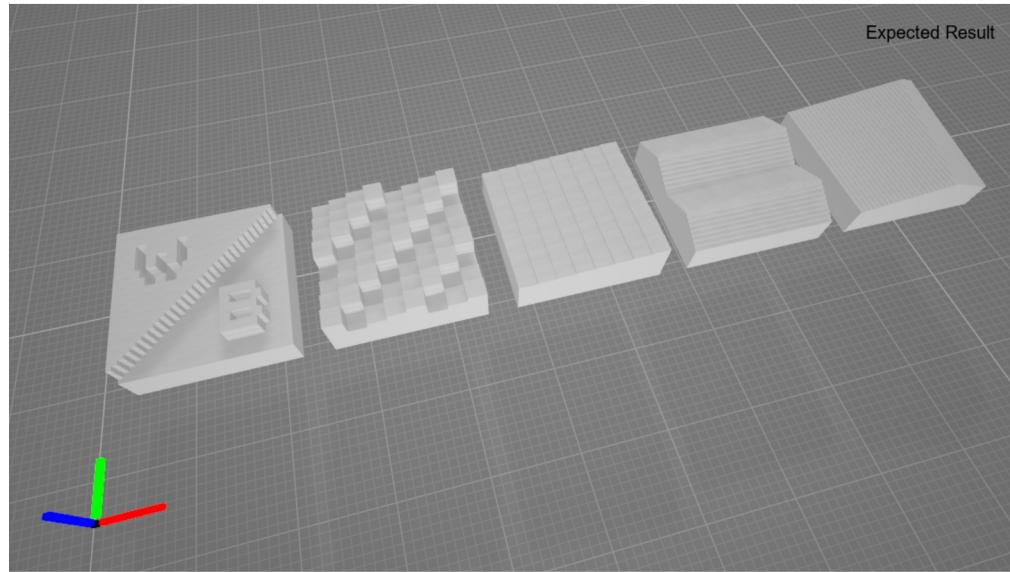
P_DPX_3228_04



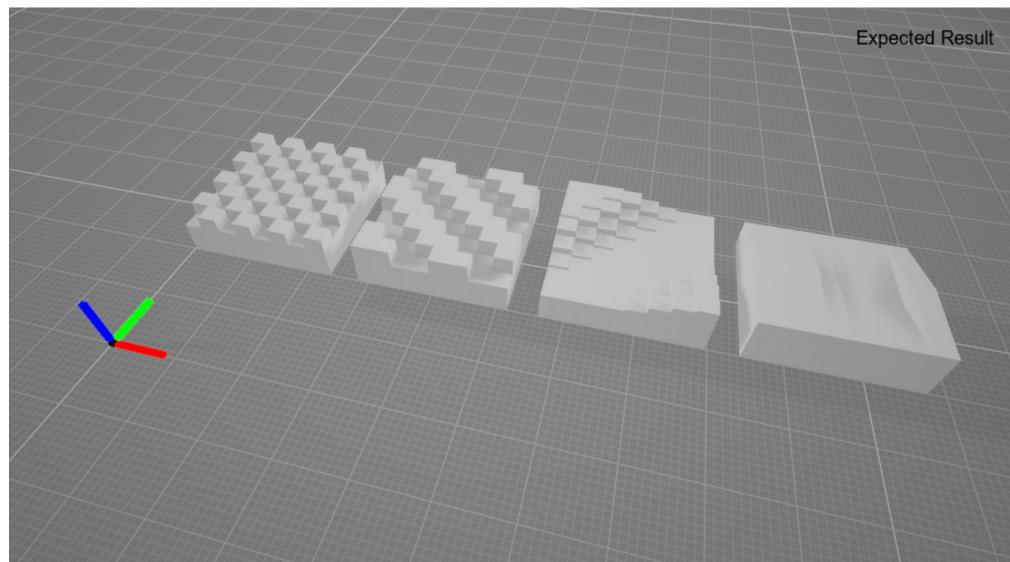
P_DPX_3228_05



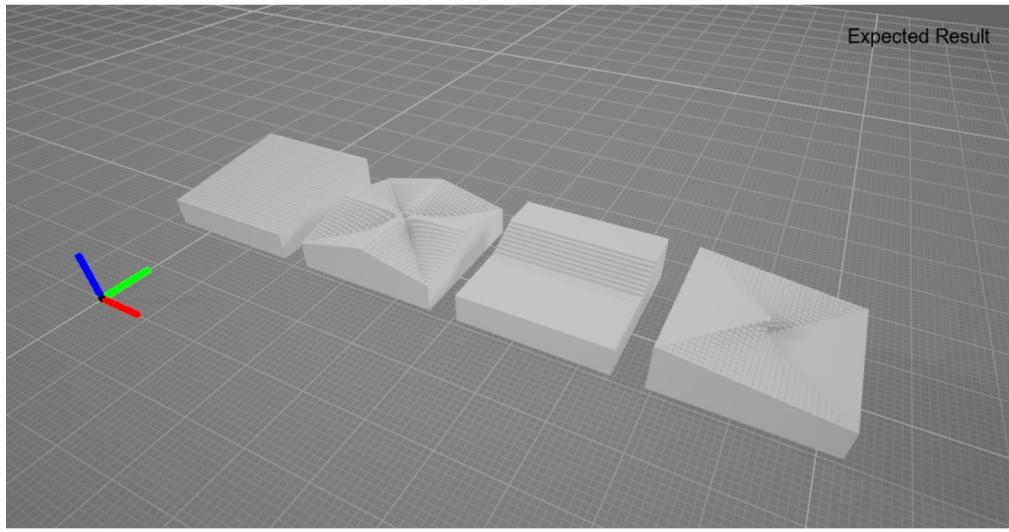
P_DPX_3230_01



P_DPX_3230_02



P_DPX_3230_03



P_DPX_3230_04