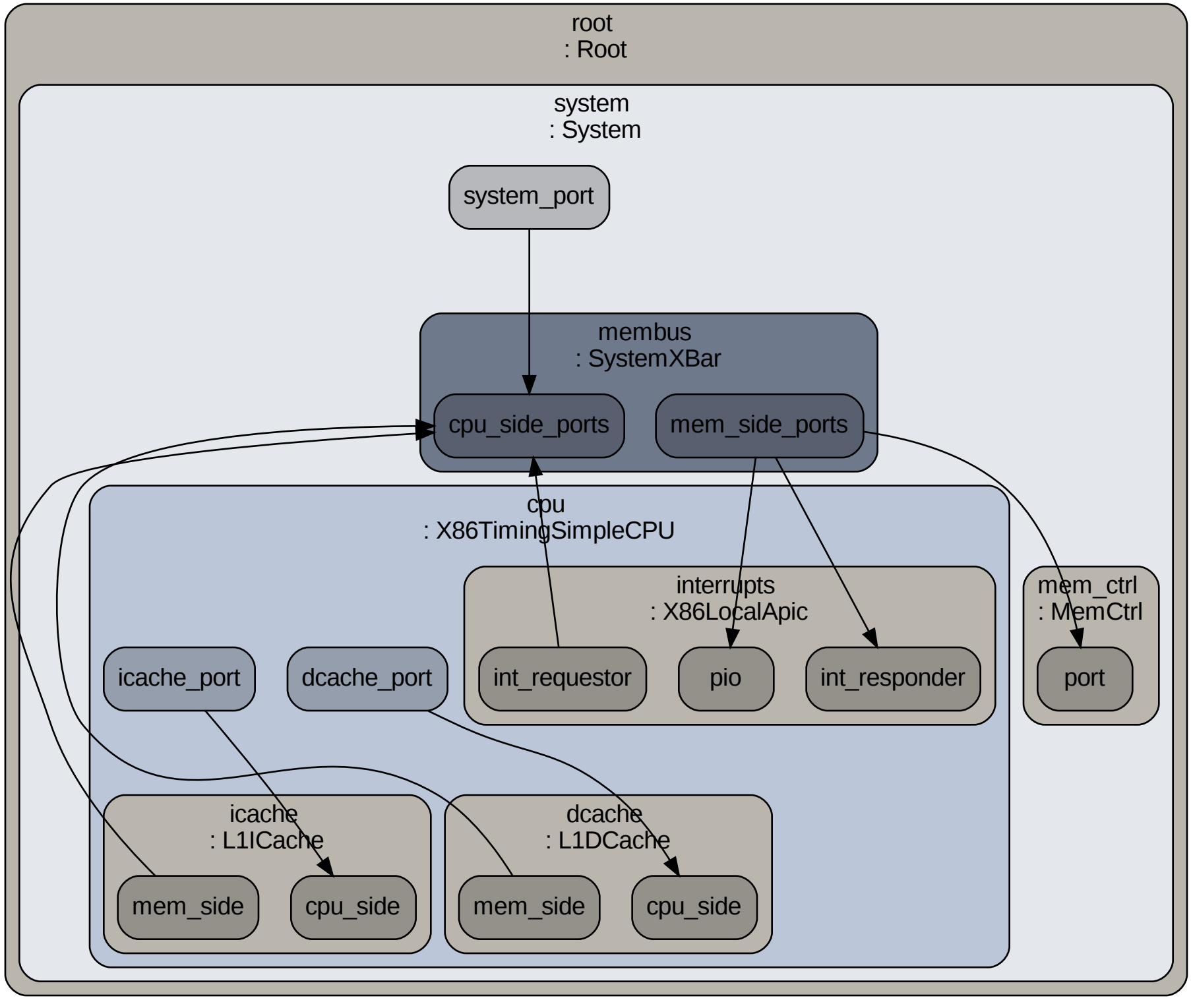
root
: Root
system
: System
system_port
membus
: SystemXBar
cpu_side_ports
mem_side_ports
cpu
: X86TimingSimpleCPU
interrupts
: X86LocalApic
int_requestor
pio
int_responder
mem_ctrl
: MemCtrl
port
icache_port
dcache_port
icache
: L1ICache
mem_side
cpu_side
dcache
: L1DCache
mem_side
cpu_side