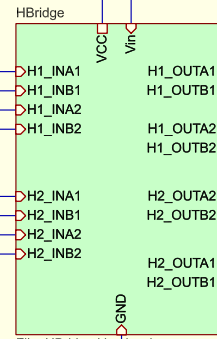
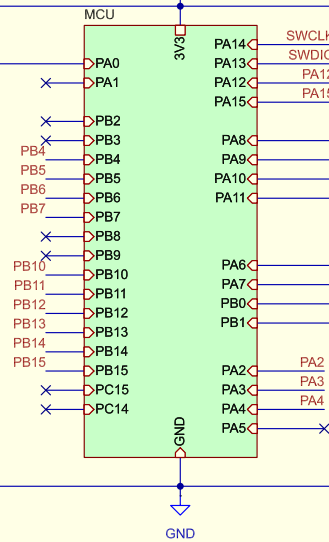
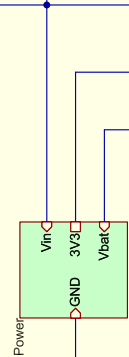
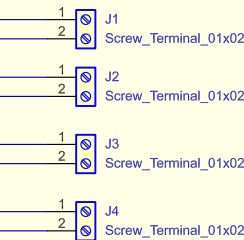


PWR_FLAG



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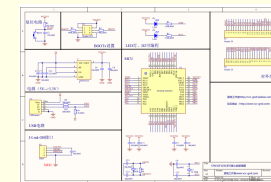
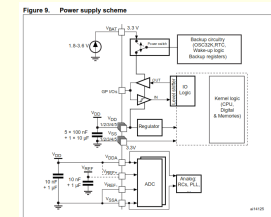
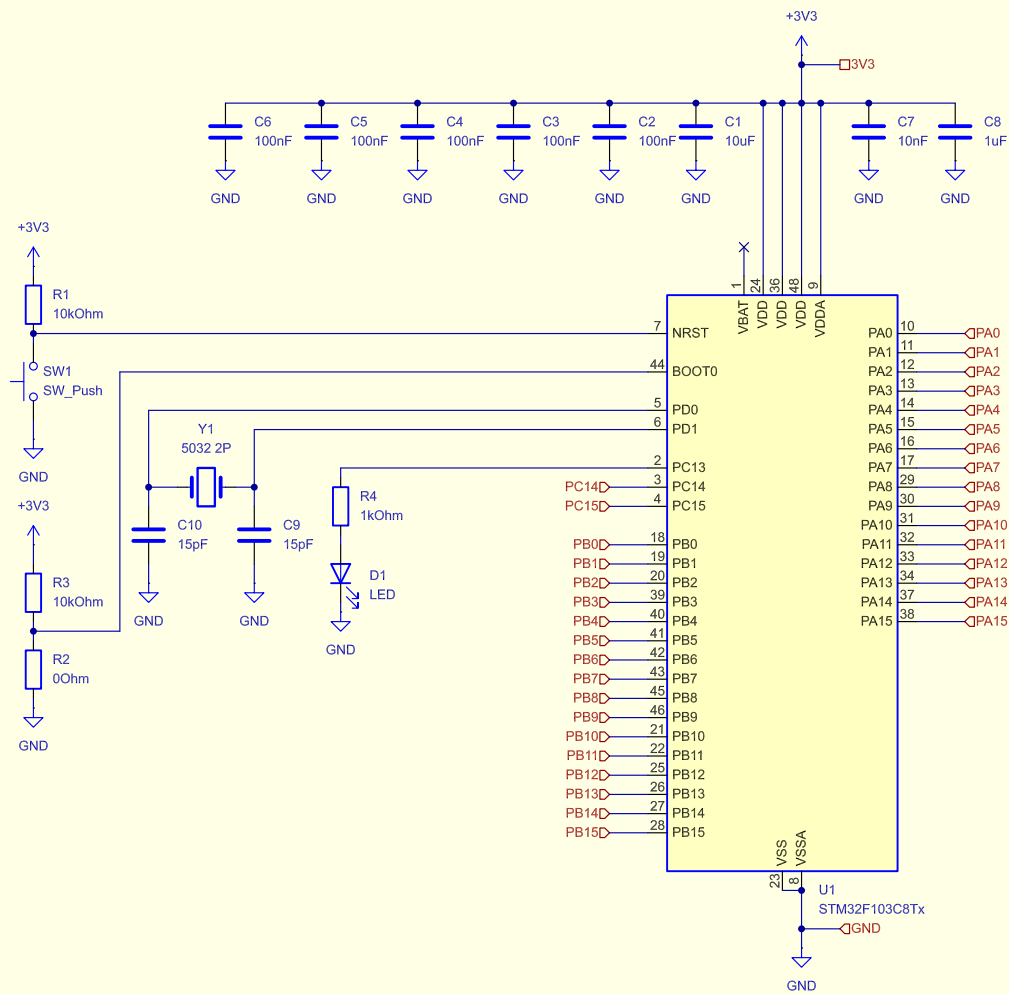
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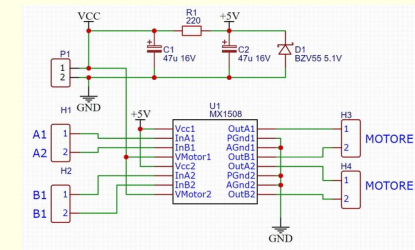
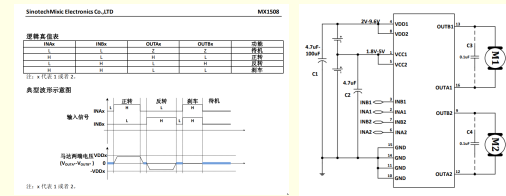
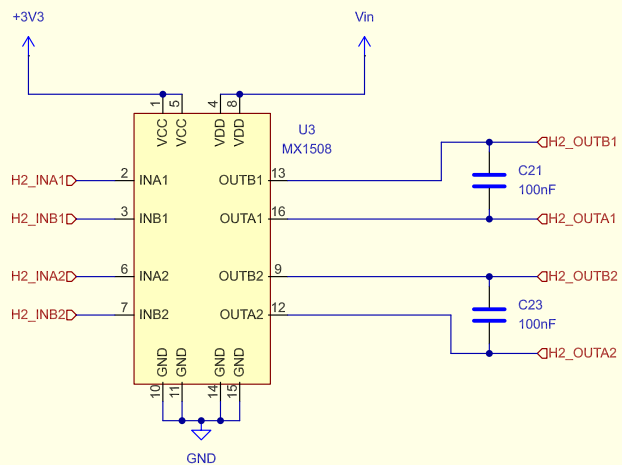
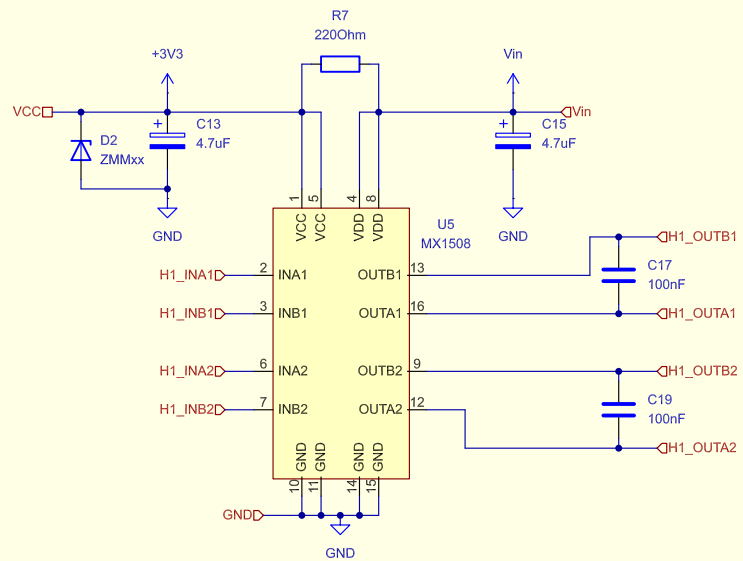
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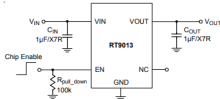
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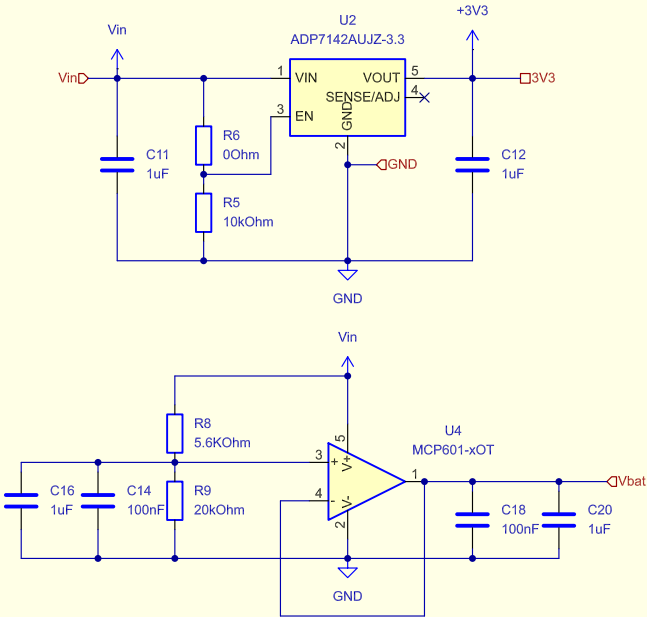


Typical Application Circuit



Functional Pin Description

Pin Number		Pin Name		Pin Function
SC-82	SOT-23-6 / SC-70-6	WDFN-6L 2x2		
3	5	4	VOUT	Regulator Output.
—	4	5, 6	NC	No Internal Connection.
2	2	2, 7 (Exposed Pad)	GND	Common Ground. The exposed pad must be soldered to a large PCB and connected to GND for maximum power dissipation.
1	3	1	EN	Enable Input Logic, Active High. When the EN goes to a logic low, the device will be shutdown mode.
4	1	3	VIN	Supply Input.



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