

```

/

#pragma vector = TIMER0_A0_VECTOR //PRZERWANIE TIMER 0
__interrupt void CCR0_ISR(void){

    POM2++;

    if(POM2==10)
    {
        switch (POM)
        {
            case 0:
            {
                P1OUT |= GREEN;
                break;
            }
            case 1:
            case 2:
            case 3:
            {
                P1OUT ^= GREEN;
                P1OUT ^= RED;
                break;
            }
            default:
            {
                POM=0;
                TA0CTL &= ~MC_3;
                P1OUT &= ~GREEN;
                P1OUT &= ~RED;
                POM2=0;
            }
        }

        POM++;
        POM2=0;
    }
}

#pragma vector = TIMER1_A0_VECTOR //PRZERWANIE TIMER 1
__interrupt void CCR1_ISR(void){
    POM2++;
    if (POM2==10)
    {
        for(POM; POM<=8; POM++)
        {
            P1OUT ^= RED;
            delay_cycles(200000);
        }

        TA1CTL &= ~MC_3;
    }
}
:}

```