

Sheet: Motor X
X_END >> EN CIPO >> CIPO
X_STEP >> STEP COPI >> COPI
X_DIR >> DIR SCK >> SCK
CS_UART << X_CS_UART
File: motor.sch

Sheet: Motor AUX
A_END >> EN CIPO >> CIPO
A_STEP >> STEP COPI >> COPI
A_DIR >> DIR SCK >> SCK
CS_UART << A_CS_UART
File: dual_motors.sch

Sheet: Motor Z
Z_END >> EN CIPO >> CIPO
Z_STEP >> STEP COPI >> COPI
Z_DIR >> DIR SCK >> SCK
CS_UART << Z_CS_UART
File: motor.sch

Sheet: Motor Y
Y_END >> EN CIPO >> CIPO
Y_STEP >> STEP COPI >> COPI
Y_DIR >> DIR SCK >> SCK
CS_UART << Y_CS_UART
File: dual_motors.sch

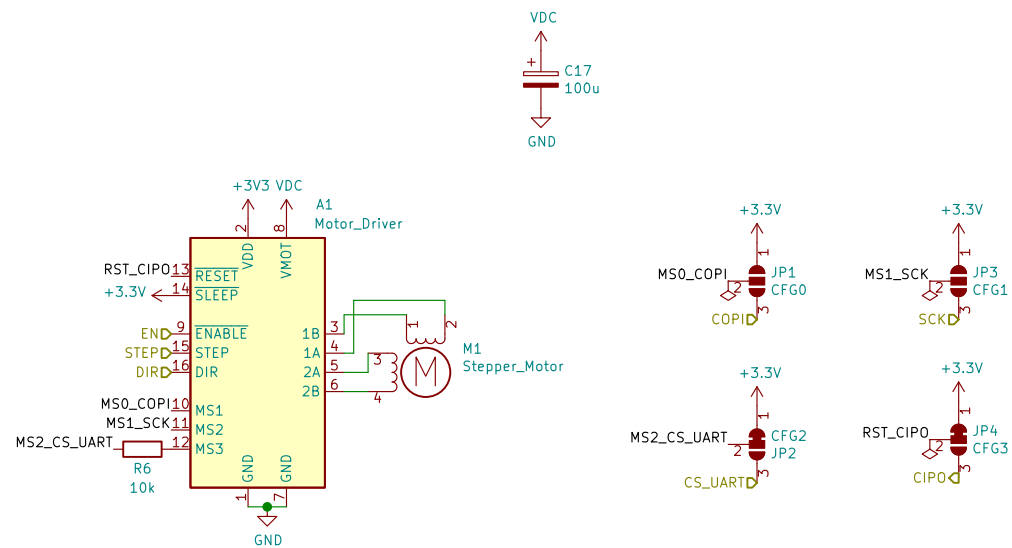
Sheet: Motor Left
L_END >> EN CIPO >> CIPO
L_STEP >> STEP COPI >> COPI
L_DIR >> DIR SCK >> SCK
CS_UART << L_CS_UART
File: motor.sch

Sheet: Motor Right
R_END >> EN CIPO >> CIPO
R_STEP >> STEP COPI >> COPI
R_DIR >> DIR SCK >> SCK
CS_UART << R_CS_UART
File: motor.sch

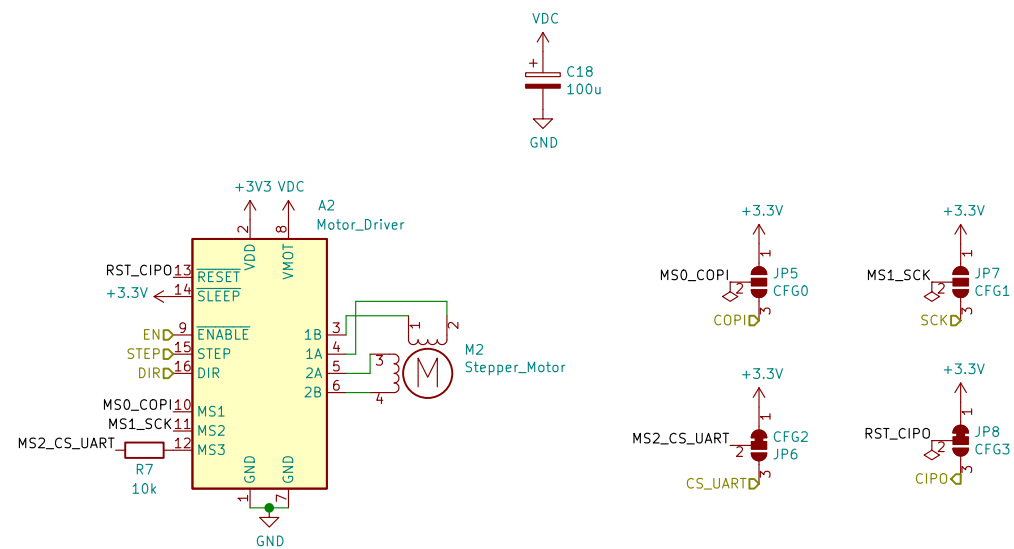
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File: motor_control.sch

Title: Motors

Size: A4	Date: 2021-02-19	Rev: 003
KiCad E.D.A. eeschema 5.1.9+dfsg1-1bpo10+1		Id: 2/16



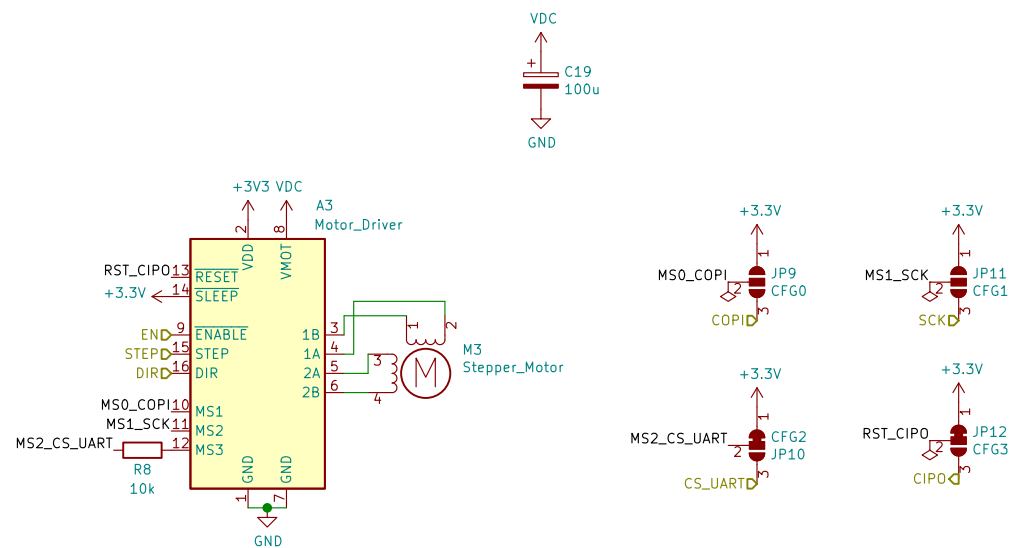
Sheet: /motor_control/Motor X/ File: motor.sch		
Title: Motor		
Size: A4	Date: 2021-02-19	Rev: 003
KiCad E.D.A. eeschema 5.1.9+dfsg1-1bpo10+1		Id: 3/16



Sheet: /motor_control/Motor Z/
File: motor.sch

Title: Motor

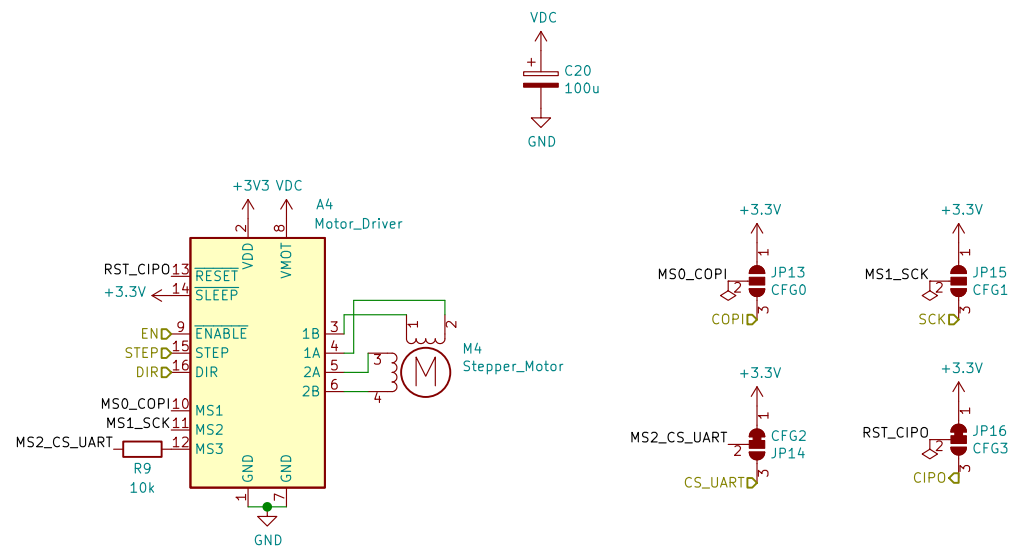
Size: A4	Date: 2021-02-19	Rev: 003
KiCad E.D.A. eeschema 5.1.9+dfsg1-1bpo10+1		Id: 4/16



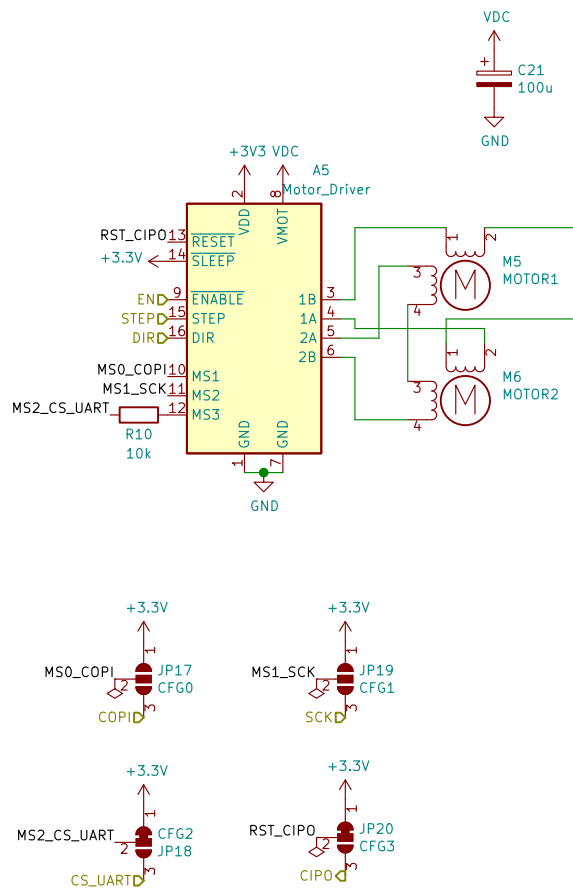
Sheet: /motor_control/Motor Left/
File: motor.sch

Title: Motor

Size: A4	Date: 2021-02-19	Rev: 003
KiCad E.D.A. eeschema 5.1.9+dfsg1-1bpo10+1		Id: 5/16



Sheet: /motor_control/Motor Right/ File: motor.sch	
Title: Motor	
Size: A4	Date: 2021-02-19
KiCad E.D.A. eeschema 5.1.9+dfsg1-1bpo10+1	Rev: 003 Id: 6/16



Sheet: /motor_control/Motor AUX/
File: dual_motors.sch

Title:

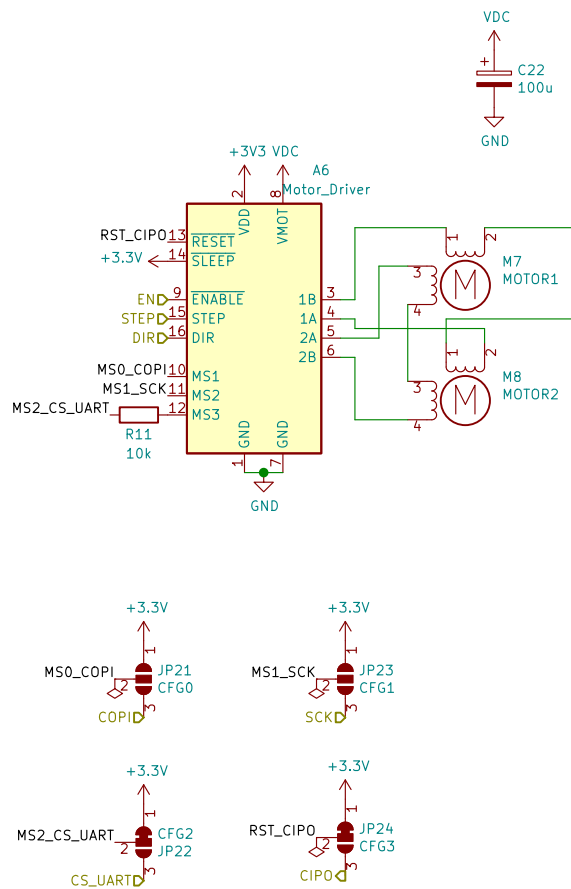
Size: A4

Date:

KiCad E.D.A. eeschema 5.1.9+dfsg1-1bpo10+1

Rev:

Id: 7/16



Sheet: /motor_control/Motor Y/
File: dual_motors.sch

Title:

Size: A4

Date:

KiCad E.D.A. eeschema 5.1.9+dfsg1-1bpo10+1

Rev:

Id: 8/16

GND 3 J2
+3.3V 2 X_LIMIT
X_LIMIT 1

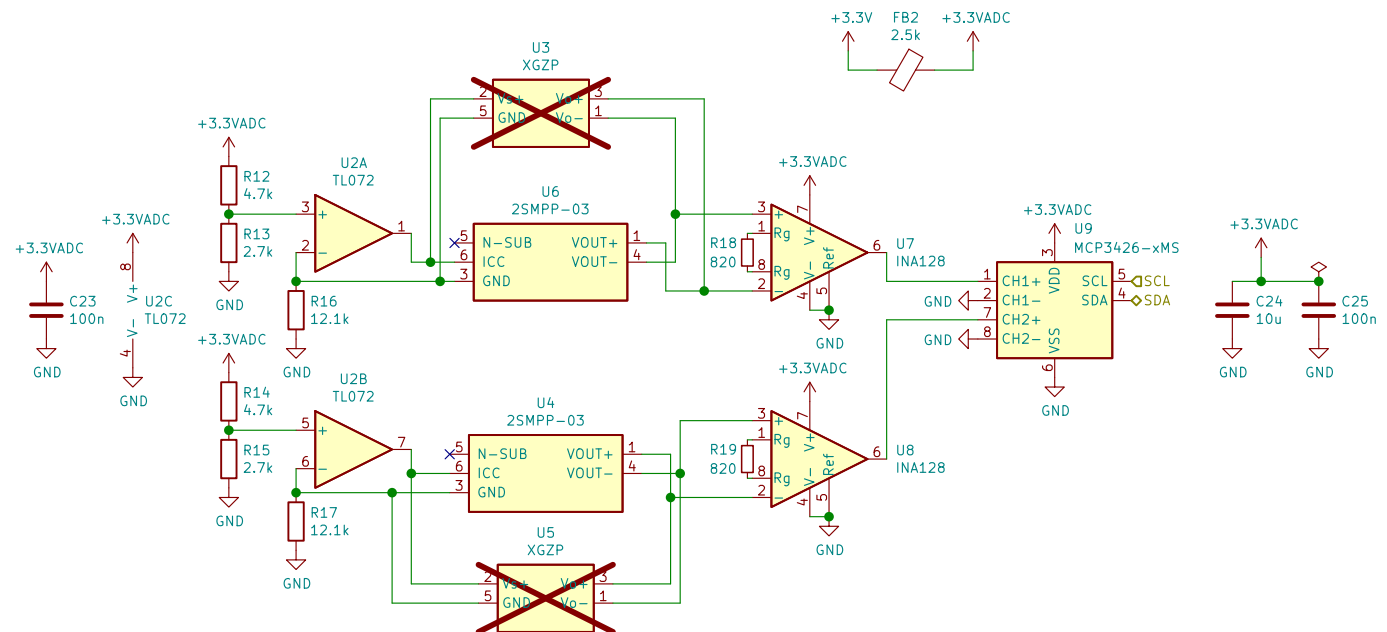
GND 3 J3
+3.3V 2 Y_LIMIT
Y_LIMIT 1

GND 3 J4
+3.3V 2 Z_LIMIT
Z_LIMIT 1

GND 3 J5
+3.3V 2 L_LIMIT
L_LIMIT 1

GND 3 J6
+3.3V 2 R_LIMIT
R_LIMIT 1

GND 3 J7
+3.3V 2 A_LIMIT
A_LIMIT 1



Sheet: /misc_input/
File: misc_input.sch

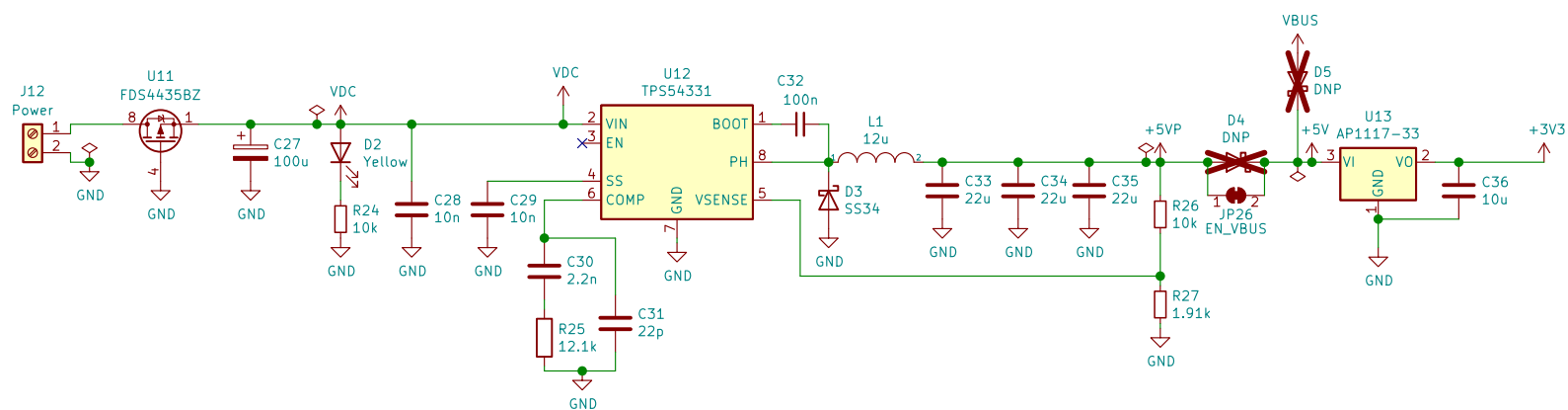
Title: Index Mobo

Size: A4 Date: 2021-02-19
KiCad E.D.A. eeschema 5.1.9+dfsg1-1bpo10+1

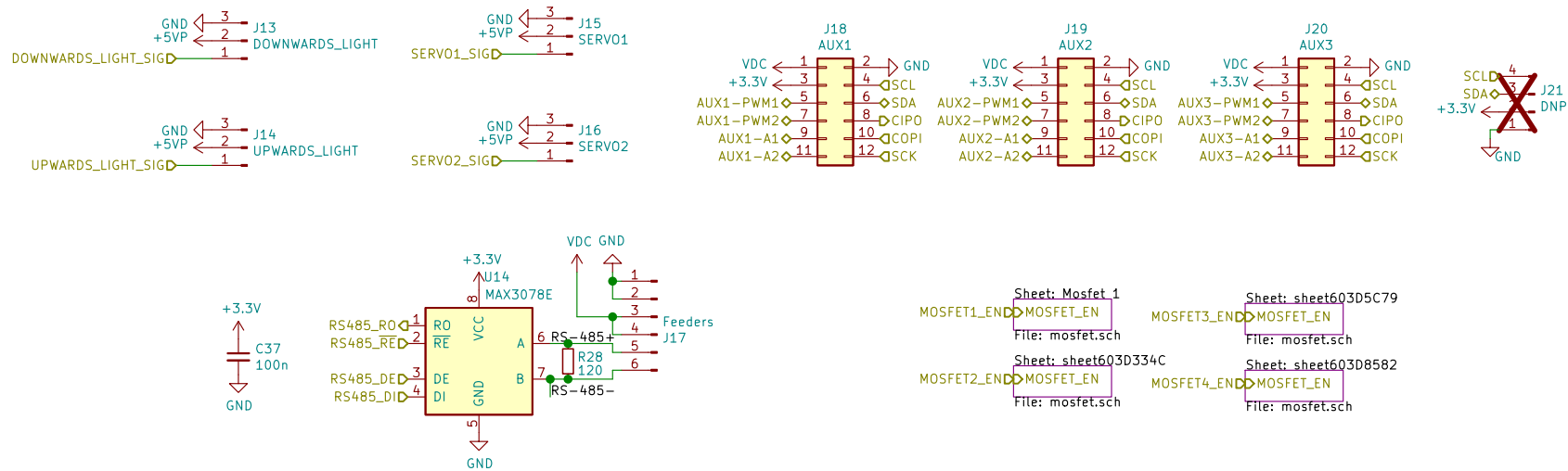
Rev: 003
Id: 9/16



Sheet: /usb/ File: usb.sch		
Title: Index Mobo		
Size: A4	Date: 2021-02-19	Rev: 003
KiCad E.D.A. eeschema 5.1.9+dfsg1-1bpo10+1		Id: 10/16



Sheet: /power/	
File: power.sch	
Title: Power	
Size: A4	Date: 2021-02-19
KiCad E.D.A. eeschema 5.1.9+dfsg1-1bpo10+1	Rev: 003
Id: 11/16	



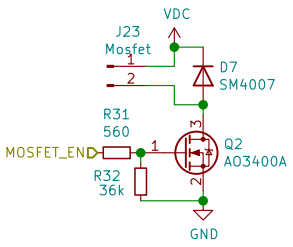
Sheet: /misc_output/
File: misc_output.sch

Title: Output

Size: A4	Date: 2021-02-19	Rev: 003
KiCad E.D.A. eeschema 5.1.9+dfsg1-1bpo10+1		Id: 12/16



Sheet: /misc_output/Mosfet 1/ File: mosfet.sch		
Title: Mosfet		
Size: A4	Date: 2021-02-19	Rev: 003
KiCad E.D.A. eeschema 5.1.9+dfsg1-1bpo10+1		Id: 13/16



Sheet: /misc_output/sheet603D334C/
File: mosfet.sch

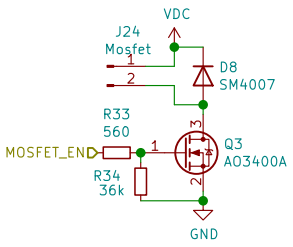
Title: Mosfet

Size: A4 Date: 2021-02-19

KiCad E.D.A. eeschema 5.1.9+dfsg1-1bpo10+1

Rev: 003

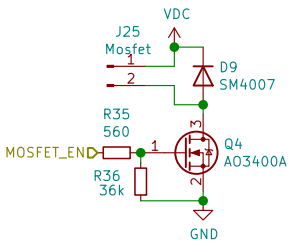
Id: 14/16



Sheet: /misc_output/sheet603D5C79/
File: mosfet.sch

Title: Mosfet

Size: A4	Date: 2021-02-19	Rev: 003
KiCad E.D.A. eeschema 5.1.9+dfsg1-1bpo10+1		Id: 15/16



Sheet: /misc_output/sheet603D8582/
File: mosfet.sch

Title: Mosfet

Size: A4	Date: 2021-02-19	Rev: 003
KiCad E.D.A. eeschema 5.1.9+dfsg1-1bpo10+1		Id: 16/16