

Sheet: Motor X  
X\_END >> EN    CIPO >> CIPO  
X\_STEP >> STEP    COPI >> COPI  
X\_DIR >> DIR    SCK >> SCK  
CS\_UART << X\_CS\_UART  
File: motor.sch

Sheet: Motor AUX  
A\_END >> EN    CIPO >> CIPO  
A\_STEP >> STEP    COPI >> COPI  
A\_DIR >> DIR    SCK >> SCK  
CS\_UART << A\_CS\_UART  
File: dual\_motors.sch

Sheet: Motor Z  
Z\_END >> EN    CIPO >> CIPO  
Z\_STEP >> STEP    COPI >> COPI  
Z\_DIR >> DIR    SCK >> SCK  
CS\_UART << Z\_CS\_UART  
File: motor.sch

Sheet: Motor Y  
Y\_END >> EN    CIPO >> CIPO  
Y\_STEP >> STEP    COPI >> COPI  
Y\_DIR >> DIR    SCK >> SCK  
CS\_UART << Y\_CS\_UART  
File: dual\_motors.sch

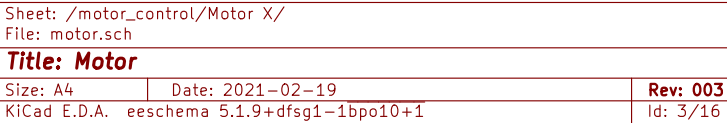
Sheet: Motor Left  
L\_END >> EN    CIPO >> CIPO  
L\_STEP >> STEP    COPI >> COPI  
L\_DIR >> DIR    SCK >> SCK  
CS\_UART << L\_CS\_UART  
File: motor.sch

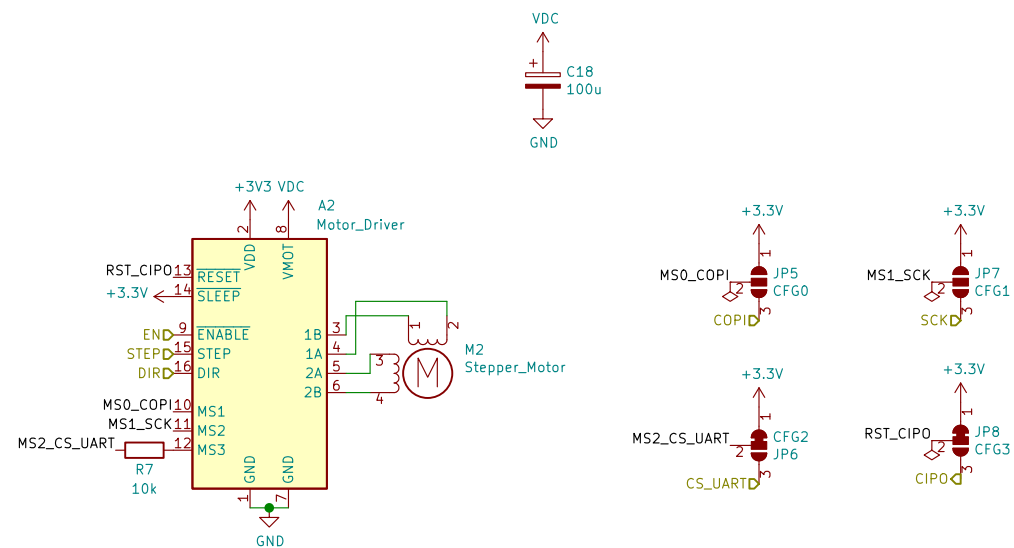
Sheet: Motor Right  
R\_END >> EN    CIPO >> CIPO  
R\_STEP >> STEP    COPI >> COPI  
R\_DIR >> DIR    SCK >> SCK  
CS\_UART << R\_CS\_UART  
File: motor.sch

Sheet: /motor\_control/  
File: motor\_control.sch

**Title: Motors**

Size: A4	Date: 2021-02-19	Rev: 003
KiCad E.D.A. eeschema 5.1.9+dfsg1-1bpo10+1		Id: 2/16

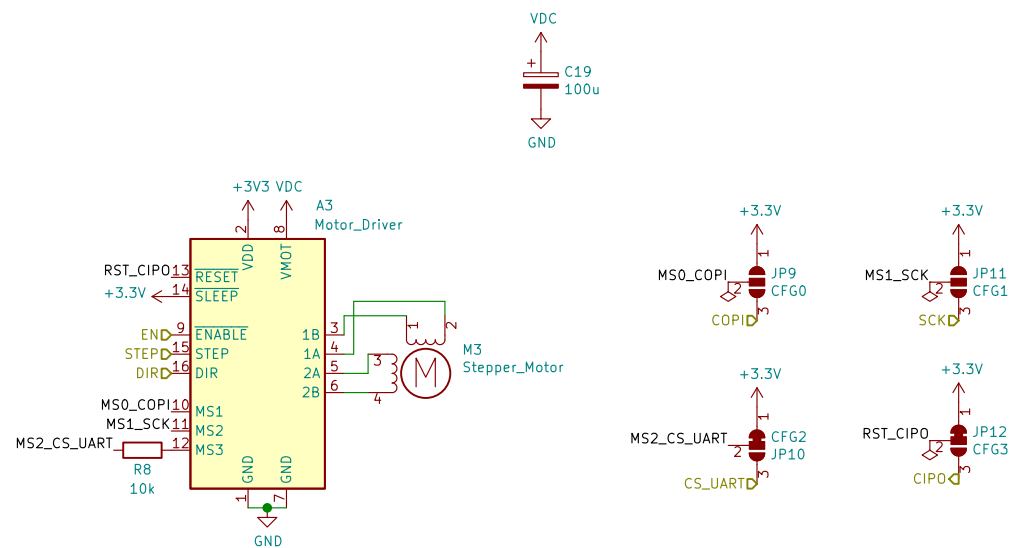




Sheet: /motor\_control/Motor Z/  
File: motor.sch

# Title: Motor

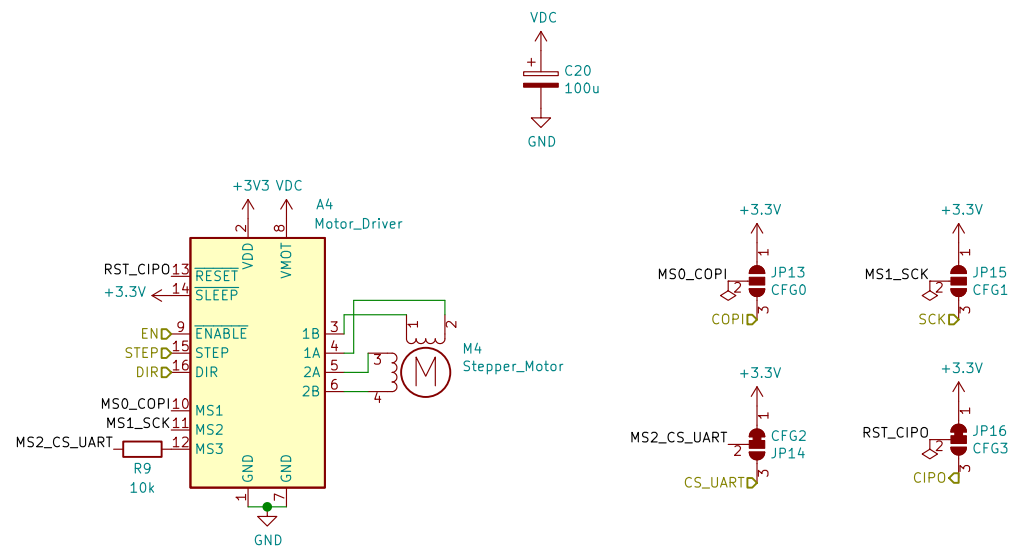
Size: A4	Date: 2021-02-19	Rev: 003
KiCad E.D.A. eeschema 5.1.9+dfsg1-1bpo10+1	Id: 4/16	



Sheet: /motor\_control/Motor Left/  
File: motor.sch

**Title: Motor**

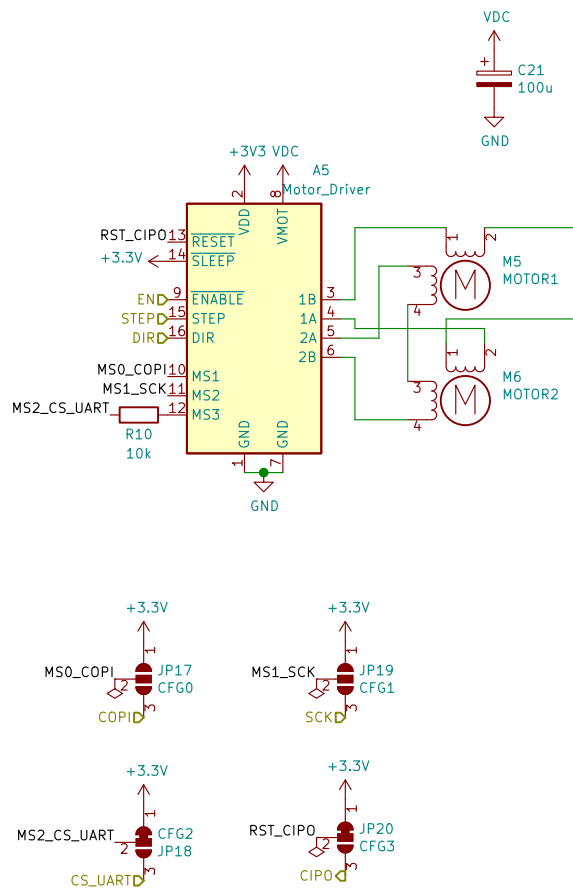
Size: A4	Date: 2021-02-19	Rev: 003
KiCad E.D.A. eeschema 5.1.9+dfsg1-1bpo10+1		Id: 5/16



Sheet: /motor\_control/Motor Right/  
File: motor.sch

**Title: Motor**

Size: A4	Date: 2021-02-19	Rev: 003
KiCad E.D.A. eeschema 5.1.9+dfsg1-1bpo10+1		Id: 6/16



Sheet: /motor\_control/Motor AUX/  
File: dual\_motors.sch

# **Title:**

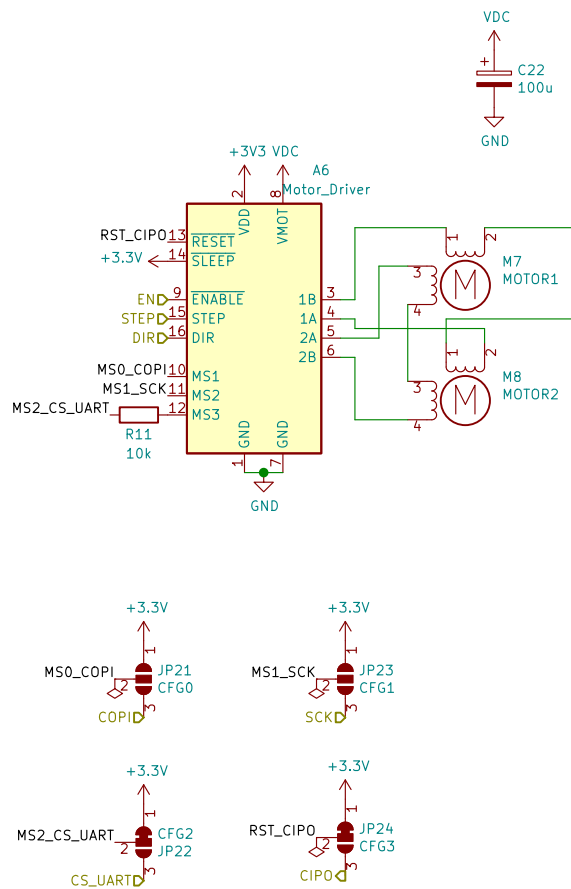
Size: A4

Date:

KiCad E.D.A. eeschema 5.1.9+dfsg1-1bpo10+1

**Rev:**

Id: 7/16



Sheet: /motor\_control/Motor Y/  
File: dual\_motors.sch

**Title:**

Size: A4

Date:

KiCad E.D.A. eeschema 5.1.9+dfsg1-1bpo10+1

**Rev:**

Id: 8/16



GND 3 J2  
+3.3V 2 X\_LIMIT  
X\_LIMIT 1

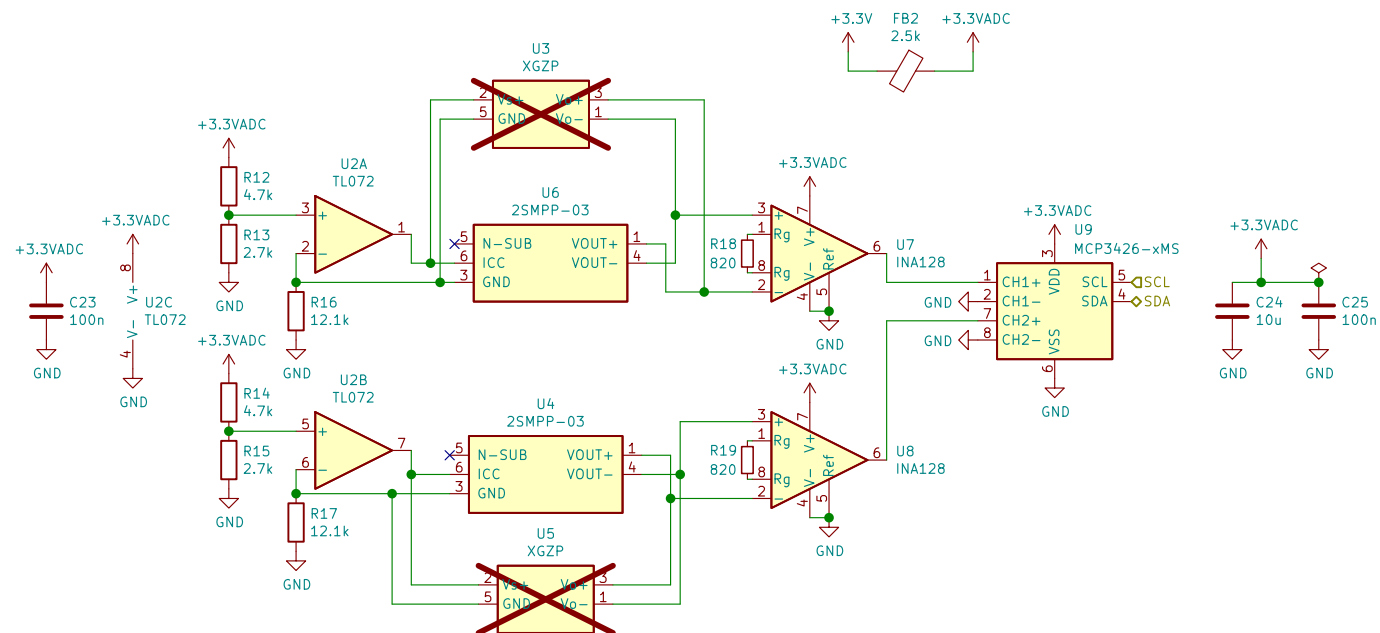
GND 3 J3  
+3.3V 2 Y\_LIMIT  
Y\_LIMIT 1

GND 3 J4  
+3.3V 2 Z\_LIMIT  
Z\_LIMIT 1

GND 3 J5  
+3.3V 2 L\_LIMIT  
L\_LIMIT 1

GND 3 J6  
+3.3V 2 R\_LIMIT  
R\_LIMIT 1

GND 3 J7  
+3.3V 2 A\_LIMIT  
A\_LIMIT 1



Sheet: /misc\_input/  
File: misc\_input.sch

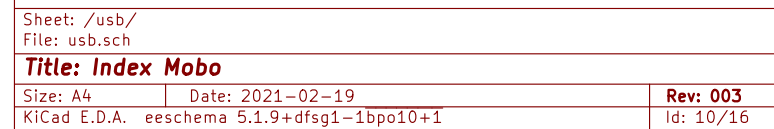
**Title: Index Mobo**

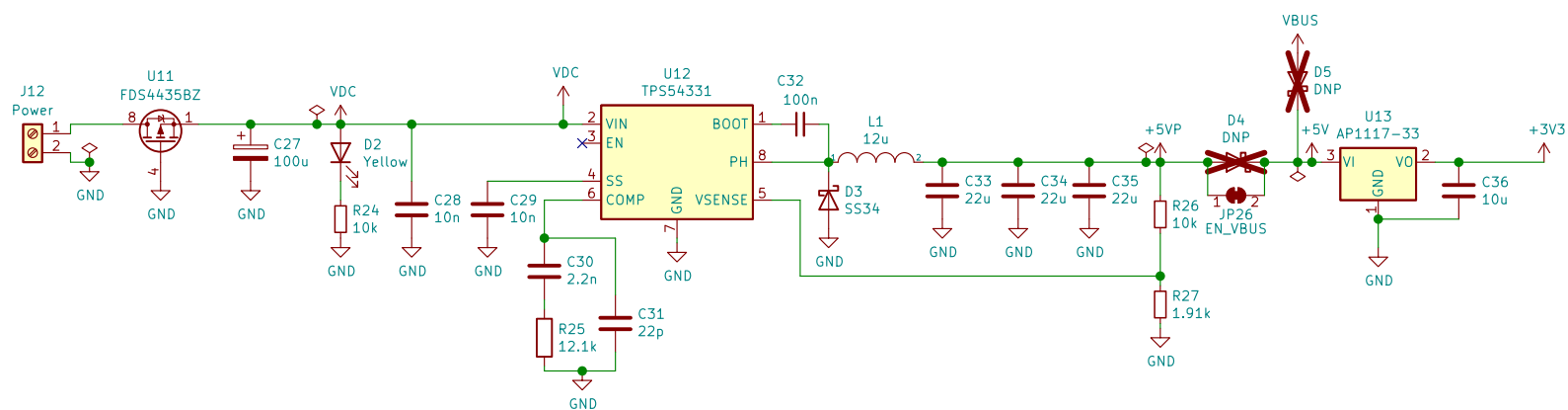
Size: A4 Date: 2021-02-19

KiCad E.D.A. eeschema 5.1.9+dfsg1-1bpo10+1

**Rev: 003**

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Sheet: /power/  
File: power.sch

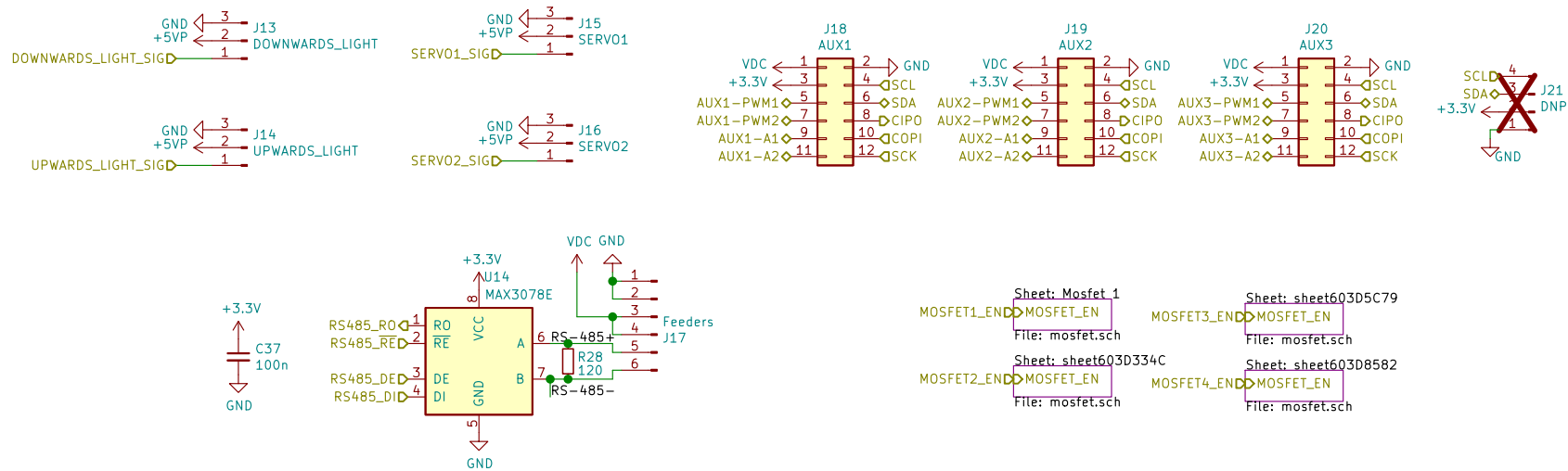
# **Title: Power**

Size: A4 Date: 2021-02-19

KiCad E.D.A. eeschema 5.1.9+dfsg1-1bpo10+1

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Sheet: Mosfet 1  
File: mosfet.sch  
MOSFET1\_END > MOSFET\_EN

Sheet: sheet603D334C  
File: mosfet.sch  
MOSFET2\_END > MOSFET\_EN

Sheet: sheet603D5C79  
File: mosfet.sch  
MOSFET3\_END > MOSFET\_EN

Sheet: sheet603D8582  
File: mosfet.sch  
MOSFET4\_END > MOSFET\_EN

Sheet: /misc\_output/  
File: misc\_output.sch

# Title: Output

Size: A4  
KiCad E.D.A. eeschema 5.1.9+dfsg1-1bpo10+1

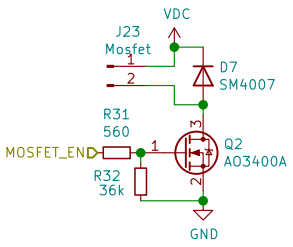
Date: 2021-02-19

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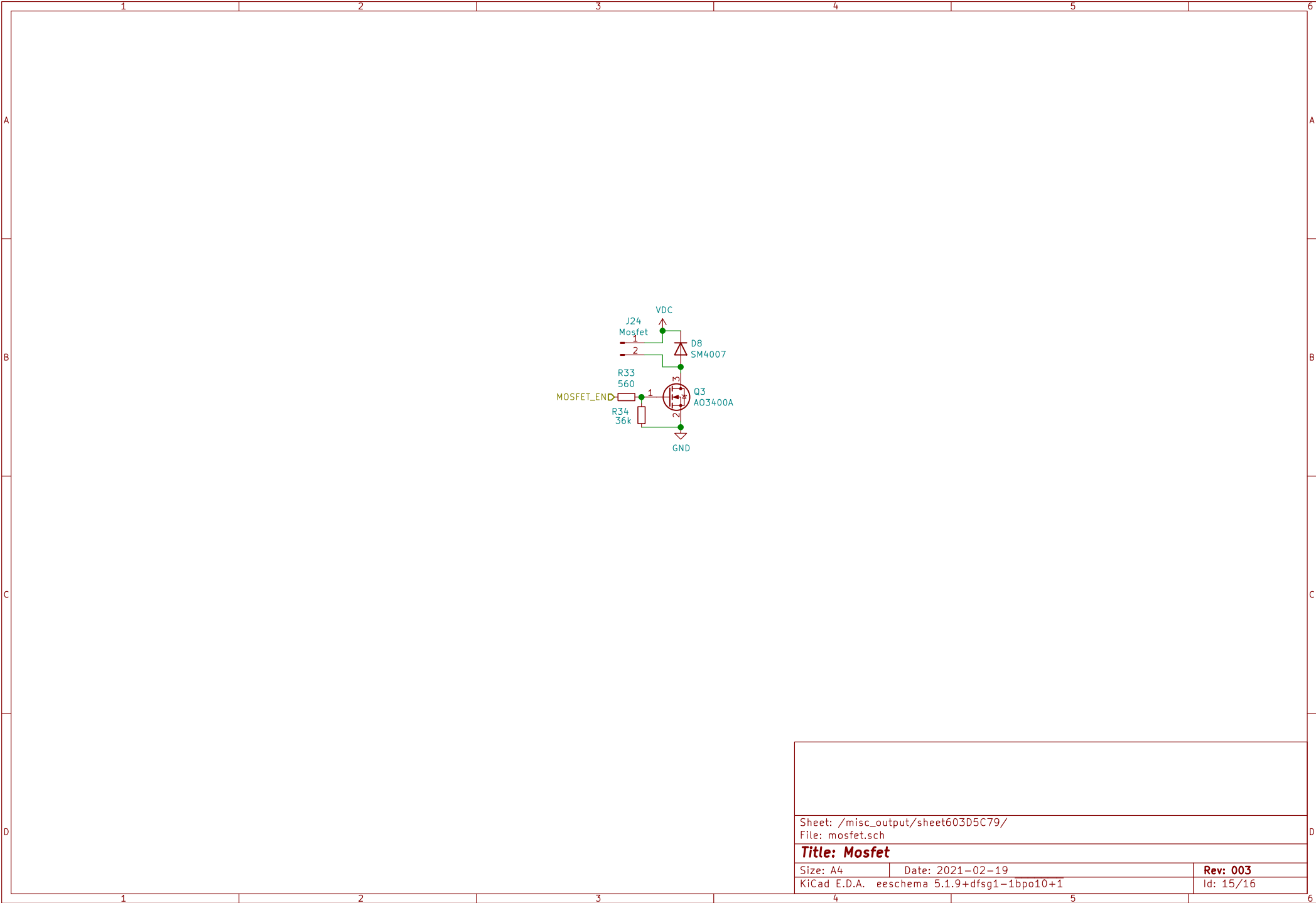
Sheet: /misc_output/Mosfet 1/ File: mosfet.sch		
Title: Mosfet		
Size: A4	Date: 2021-02-19	Rev: 003
KiCad E.D.A. eeschema 5.1.9+dfsg1-1bpo10+1		Id: 13/16



Sheet: /misc\_output/sheet603D334C/  
File: mosfet.sch

**Title: Mosfet**

Size: A4	Date: 2021-02-19	Rev: 003
KiCad E.D.A. eeschema 5.1.9+dfsg1-1bpo10+1		Id: 14/16



Sheet: /misc_output/sheet603D5C79/ File: mosfet.sch		
Title: Mosfet		
Size: A4	Date: 2021-02-19	Rev: 003
KiCad E.D.A. eeschema 5.1.9+dfsg1-1bpo10+1		Id: 15/16

