Alhambra II V1.0A

Open FPGA Board



Maximun Supply Voltage: 5.5V



Operating Supply Voltage: 3.5 - 5.5V

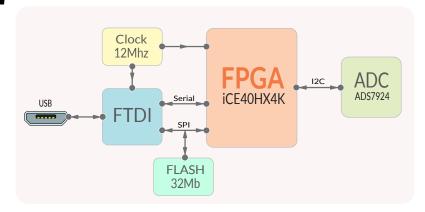


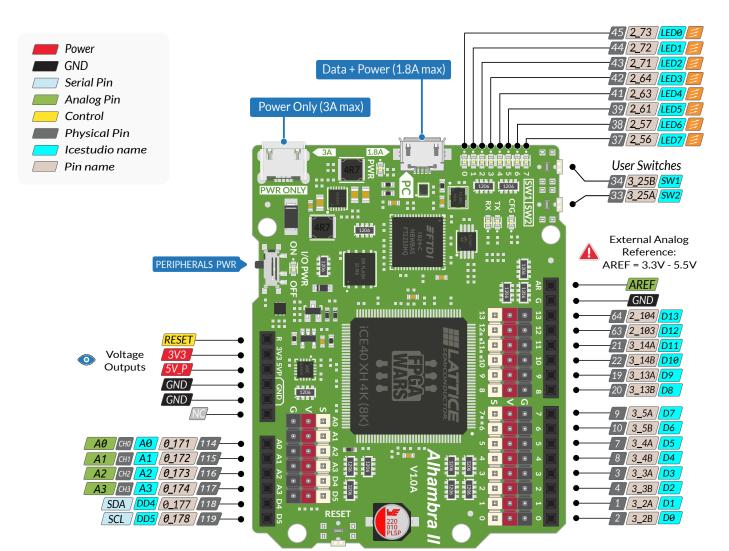
3.3V GPIO, 5V Compatible



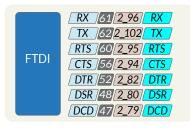
Each GPIO Includes a 200 ohm Serial Resistor

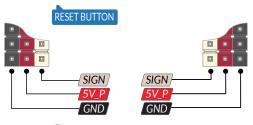
ADC Internal to External Ref. Automatic Switching





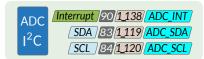






Max 4.8A for all 5V_P Pins

CLK Clock 12Mhz 49 2_81







@EladioDM





