Assembler User Guide

2015

Mips Processor Project

# Supported Instructions and their Formats:

add rd rt rs

and rd rt rs

addi rt rs immediate

lw rt immediate (rs)

sw rt immediate (rs)

sll rd rt shiftAmount

andi rt rs immediate

beq rs rt offset

nor rd rt rs

slt rd rs rt

jal offset

jr rs

# Important Notes:

Register names:

$zero

$at

$v0 .. $v1

$a0 .. $a3

$t0 .. $t9

$s0 .. $s7

$gp

$sp

$fp

$ra

# 3.0Example

Machine code is stored at file named “assembler\_output.txt”

