ADI Engineering RCCVE Bootrom Software Release Notes

Contents

The RCC-VE platform is based on the Intel Intel AtomTM C2000 processor (previously referred to as Rangeley/Avoton). The C2000 is a multi-core (up to 4 for RCC-VE) Intel Atom based SOC product featuring high levels of I/O integration and an Intel QuickAssist hardware acceleration engine, targeting for the routers and security communications market segment.

This document serves as the release notes for ADI RCC-VE bootrom that resides in the 8M SPI flash. The SPI flash can be updated using either an external programmer or the flashrom Linux utility provided by ADI. Please refer to RCC-VE Platform User's Manual for details.

1 RELEASE ADI_RCCVE-00.00.00.01

Release Date : 8/28/2014

The FSP and microcode version used in this release:
RANGELEY_FSP_POSTGOLD_001_20131218.

1.1 New Features

This is the initial release of the RCC-VE bootrom software.

The RCC-VE bootrom is an Intel Firmware Support Package (FSP) and Open Source coreboot (http://coreboot.org) based boot loader. It performs basic initialization for the C2000 SoC and its memory subsystem. It uses seabios as payload and supports booting OS from the on-board eMMC, external SATA or mSATA drive, or external USB drive. sgabios VGA emulator is loaded as option rom to redirect VGA output to console.

1.2 Bug Fixes

None.

1.3 Known Issues & Limitations

1.3.1 Hardware Reset Does Not Work

The CPU hard reset does not work in this release. Pushing the reset button results in system hang. Only power cycling the board could recover the system. This issue is being investigated.

1.3.2 Issue with Linux Installation CD

It has been observed in ADI lab that the boot process stalls when booting from a Linux installation CD connected via SATA port. The issue is being investigated.